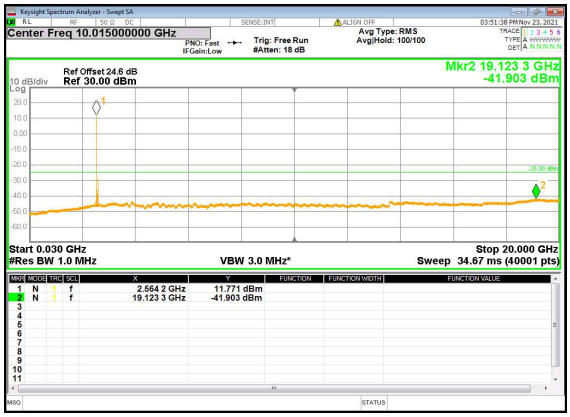
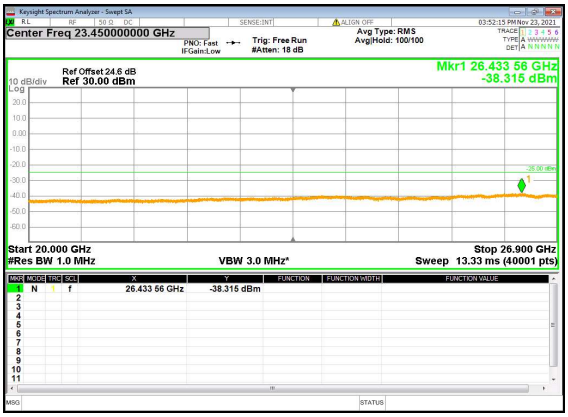
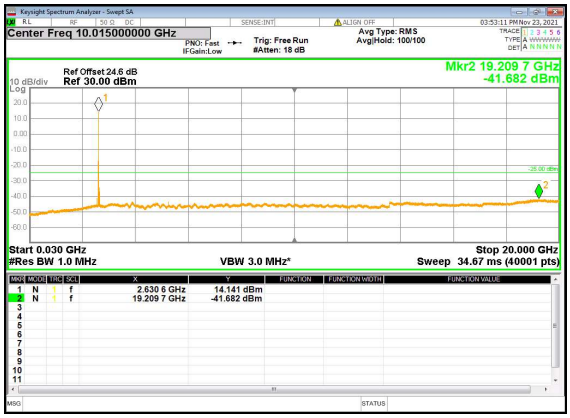
N41(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N41(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



N41(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



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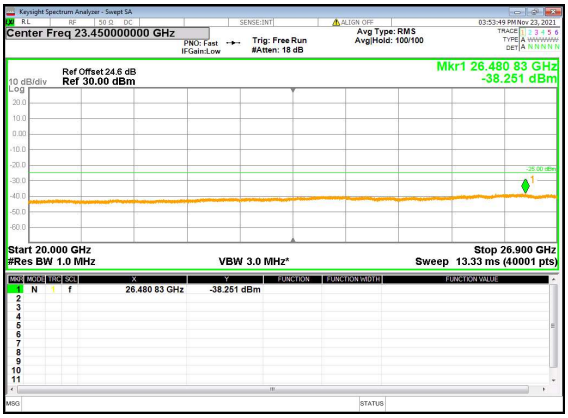

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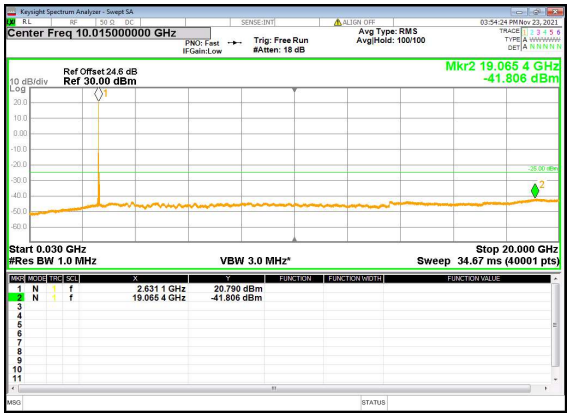

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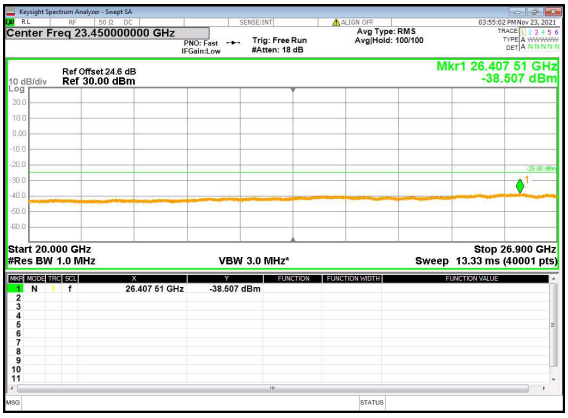
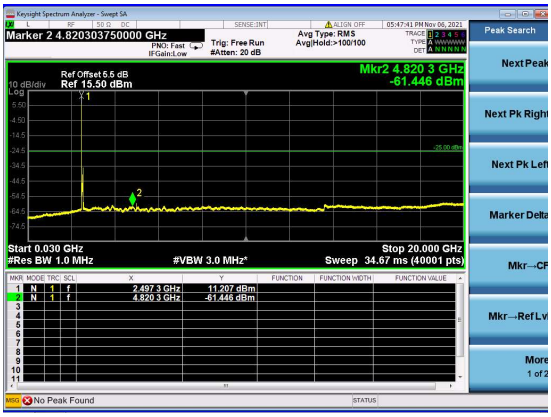
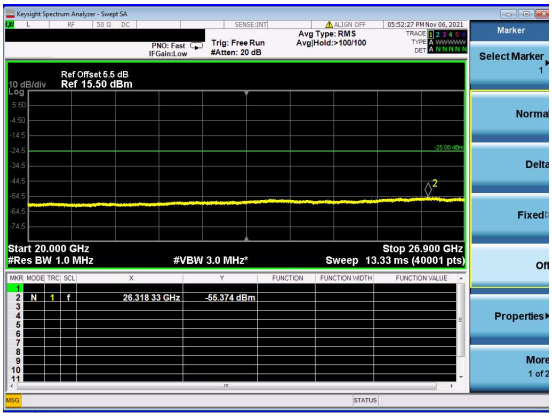


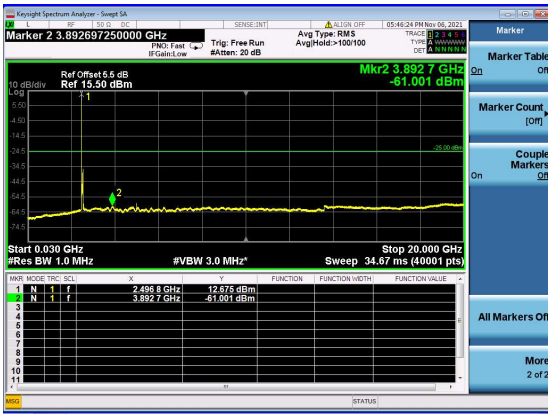
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



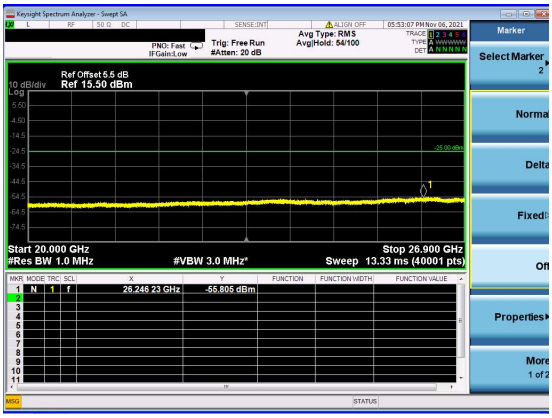
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



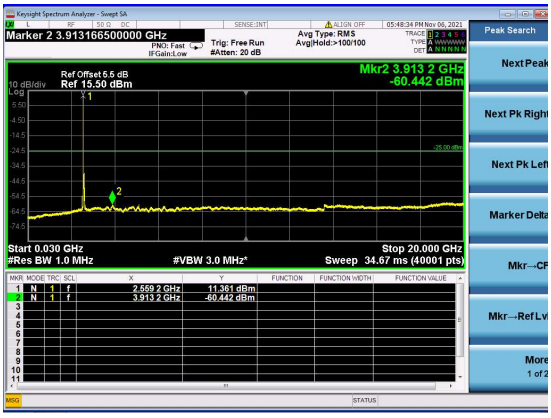
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



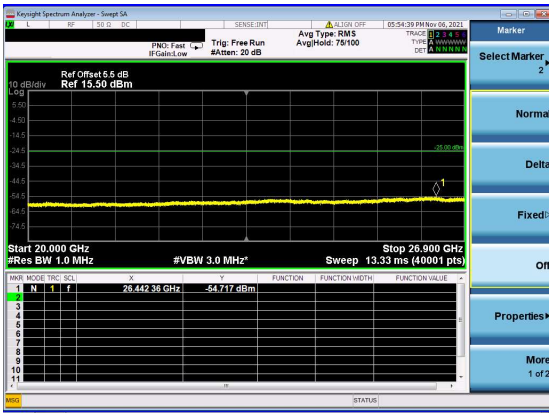
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



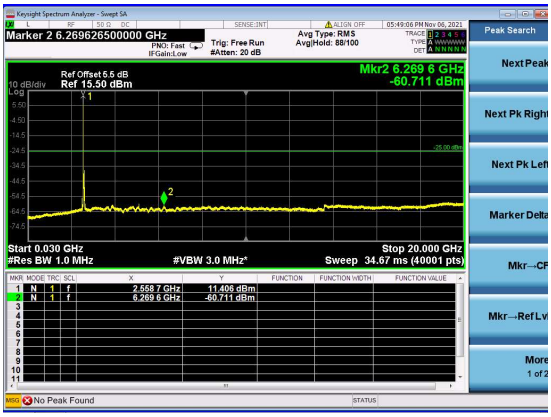
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



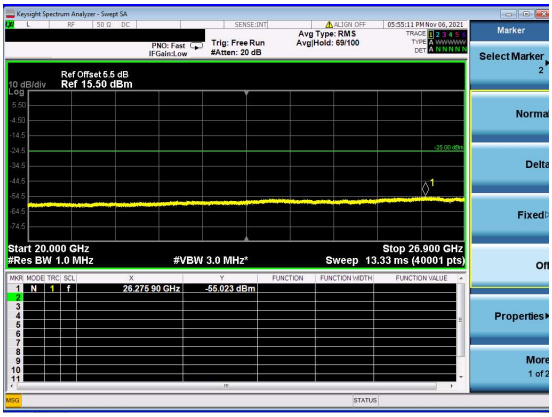
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



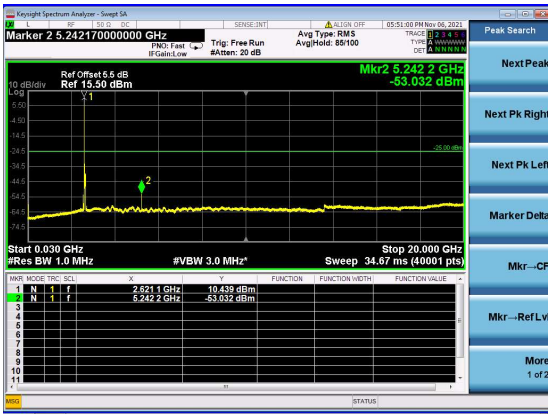
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



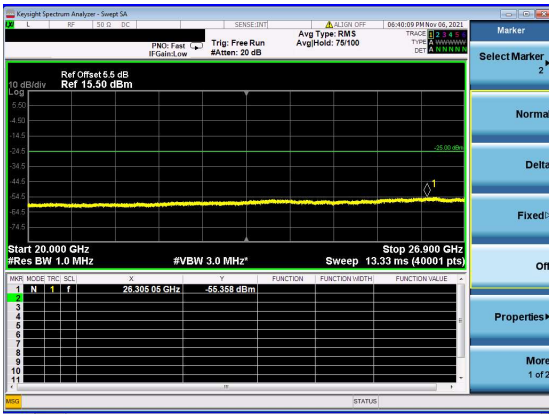
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



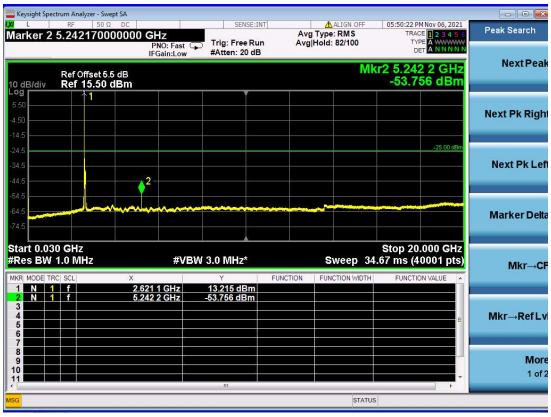
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



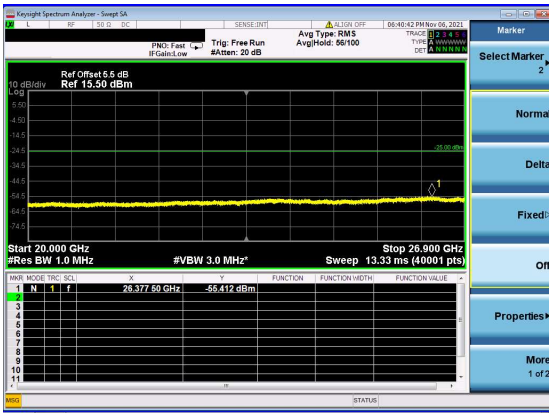
N41(70M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



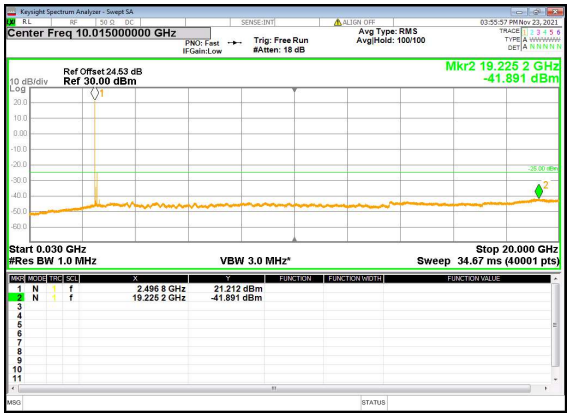
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



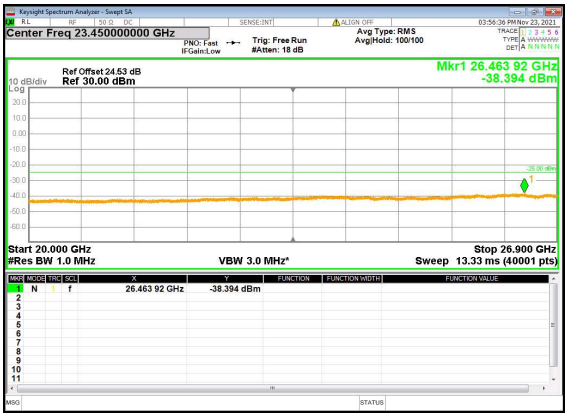
N41(70M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



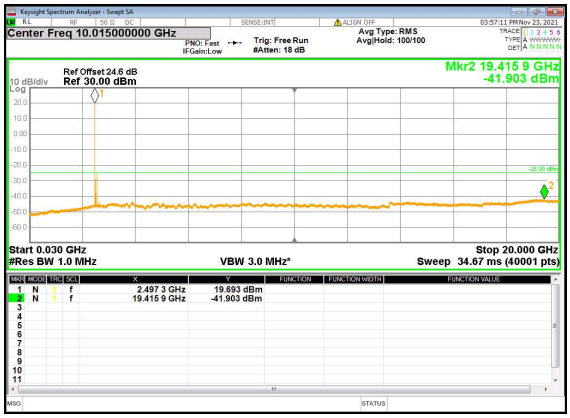
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



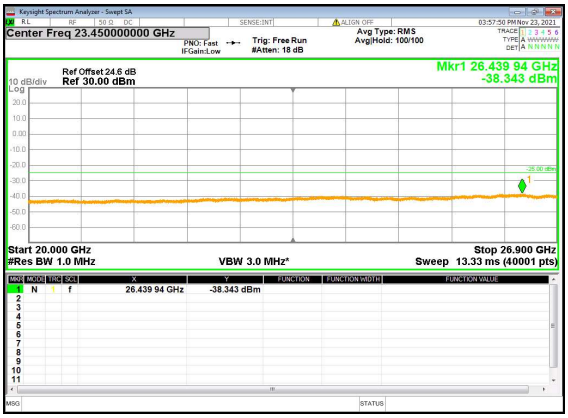
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



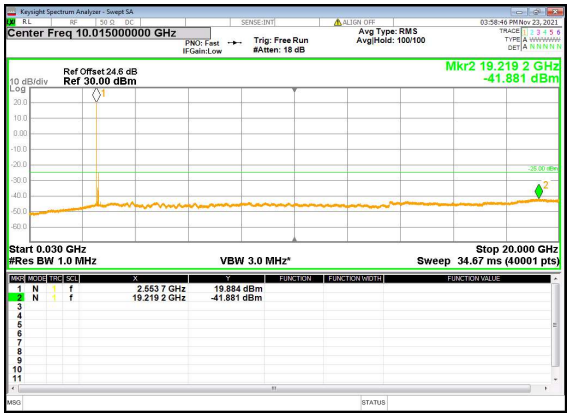
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



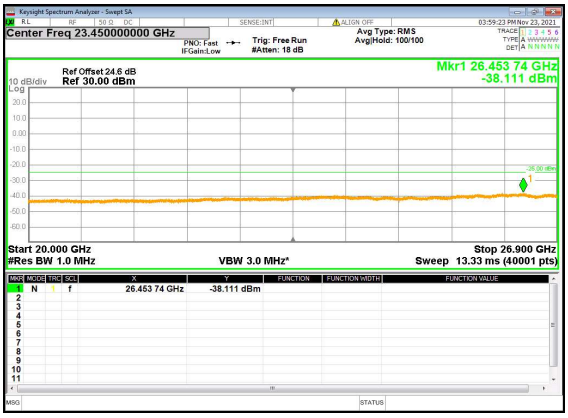
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



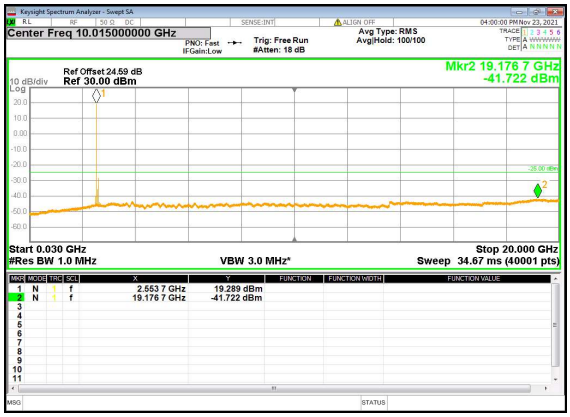
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



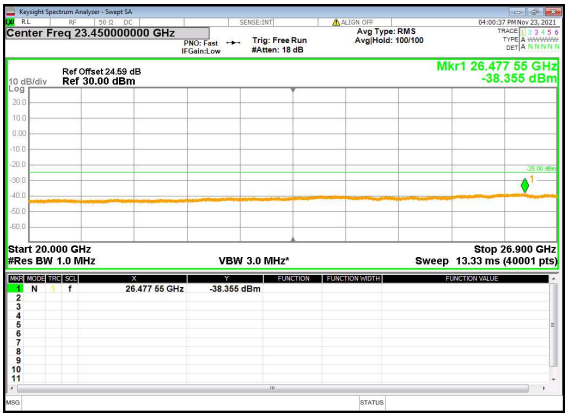
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



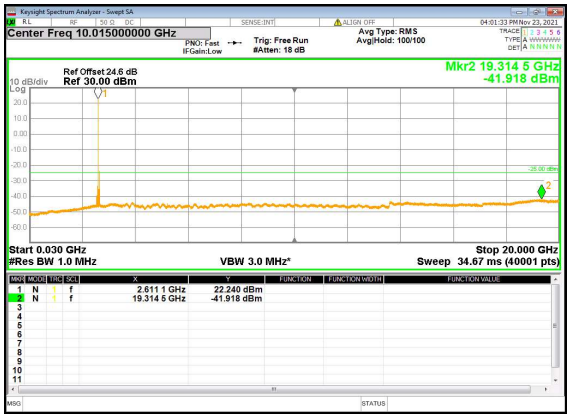
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



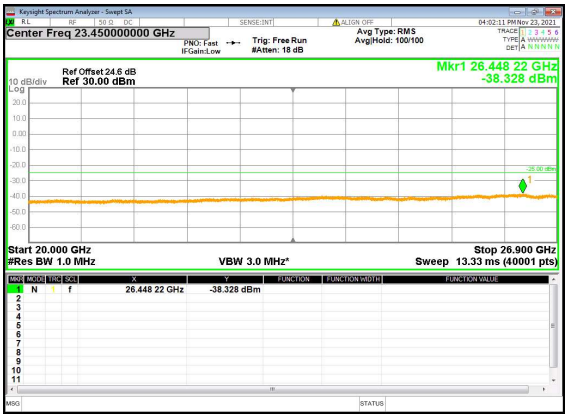
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



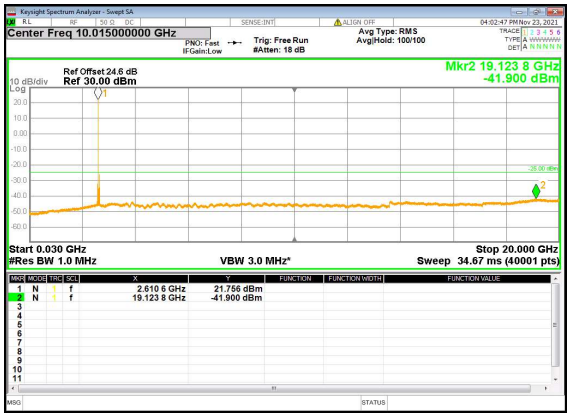
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



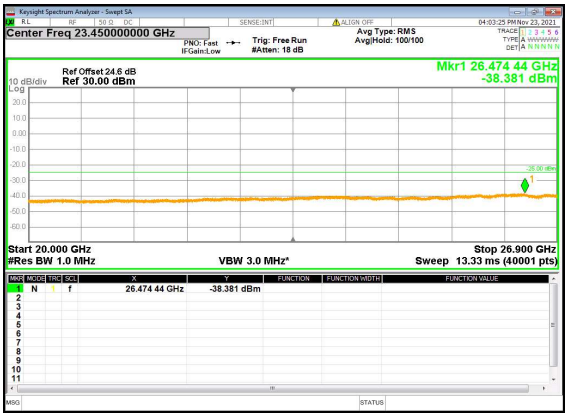
N41(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



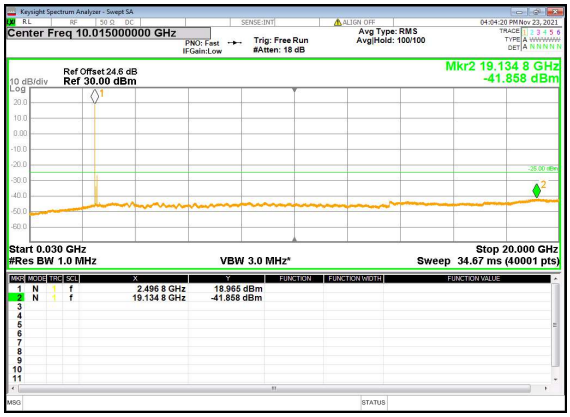
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



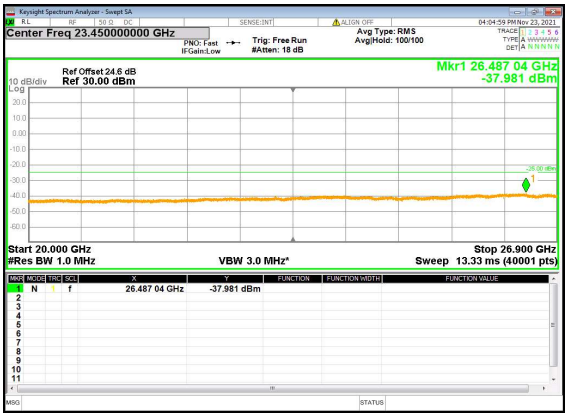
N41(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_High_CH



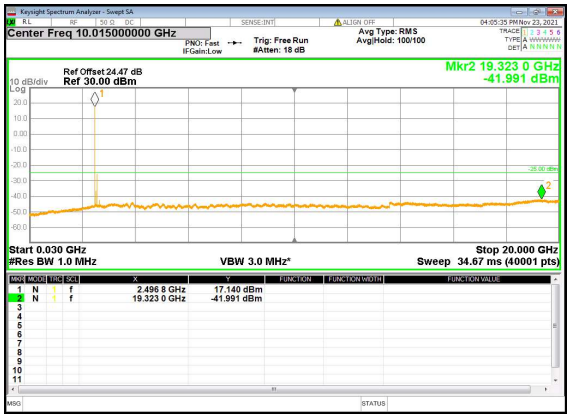
N41(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



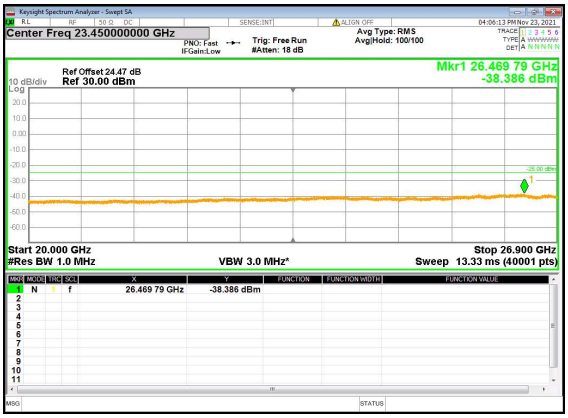
N41(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



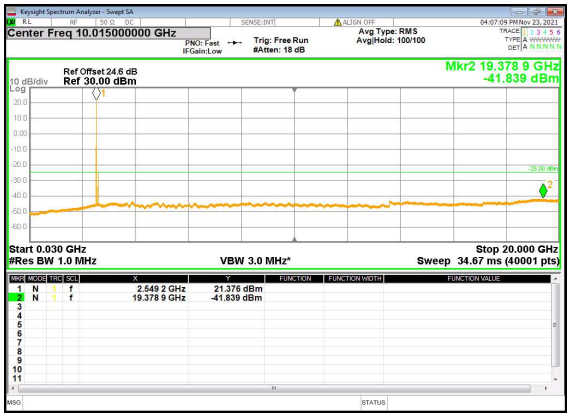
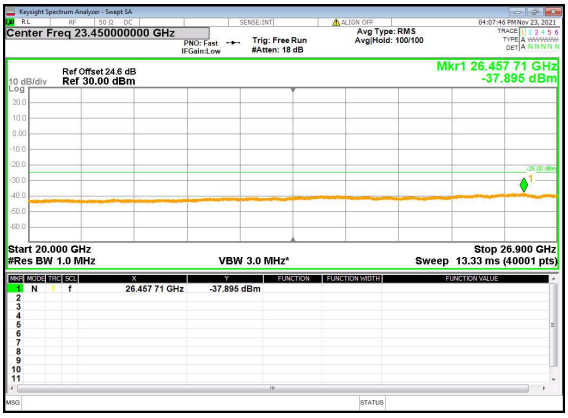
N41(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N41(90M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



N41(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



N41(90M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH