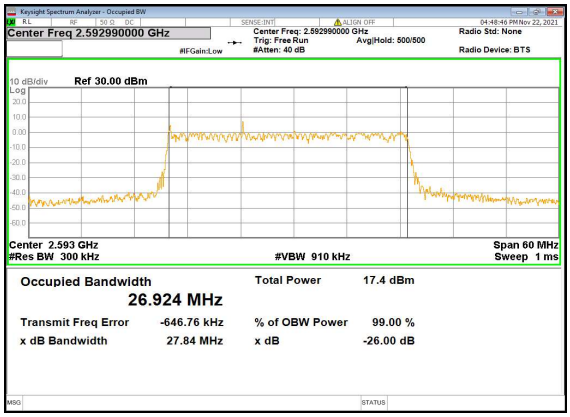
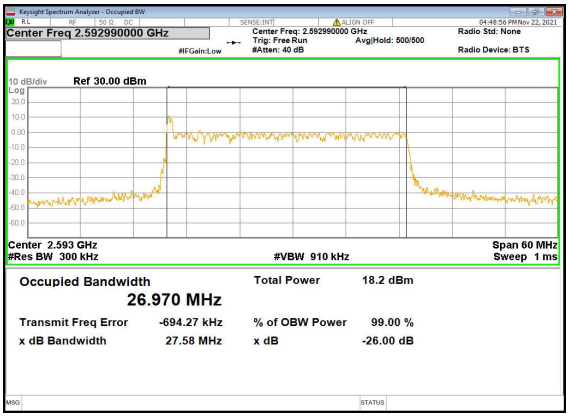
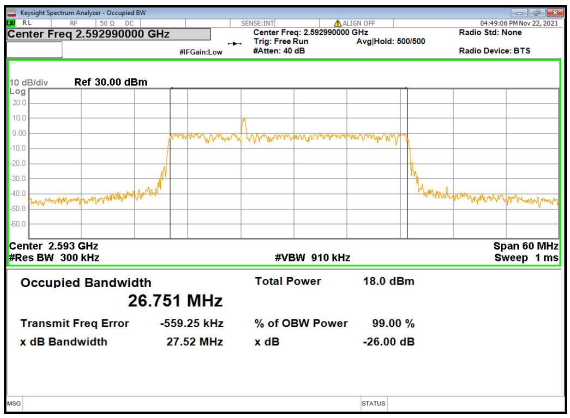
N41(30M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



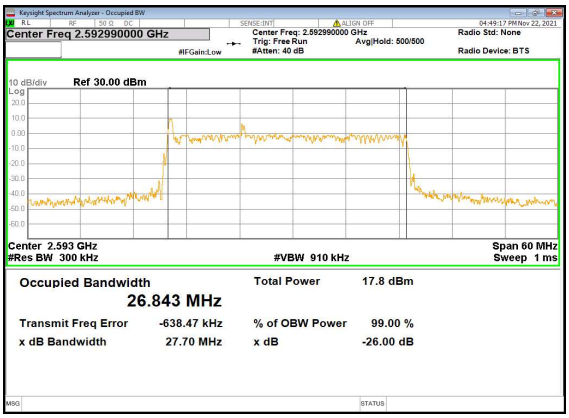
N41(30M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



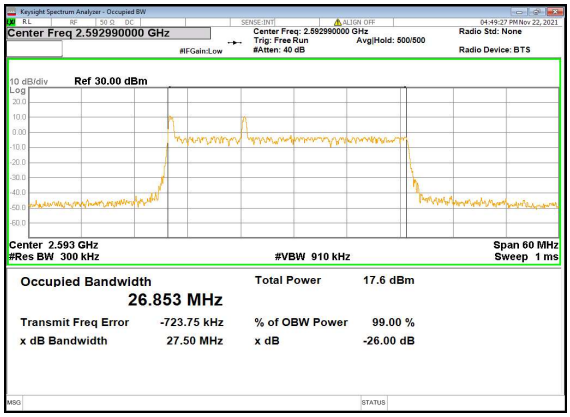
N41(30M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



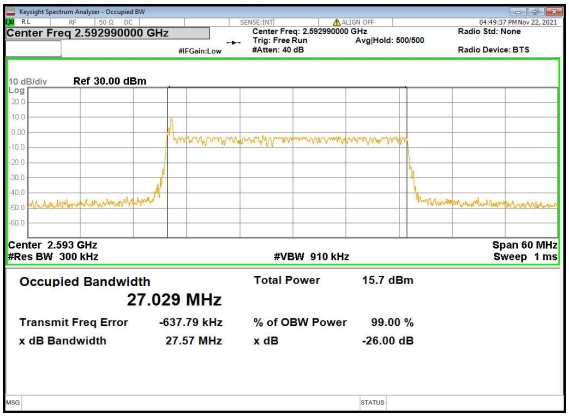
N41(30M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



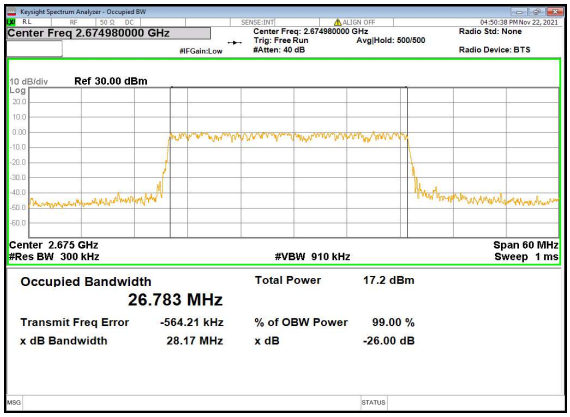
N41(30M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



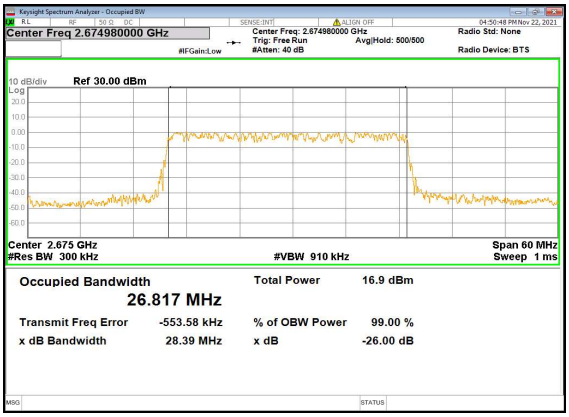
N41(30M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



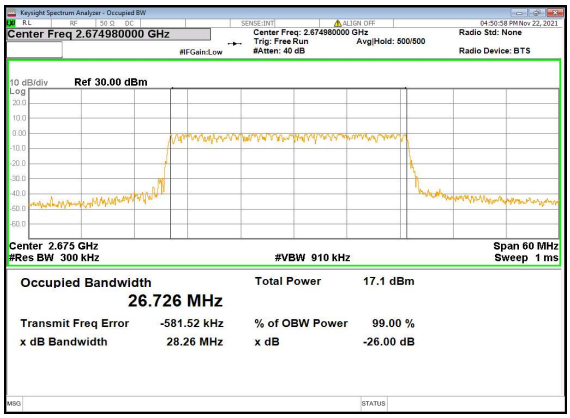
N41(30M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



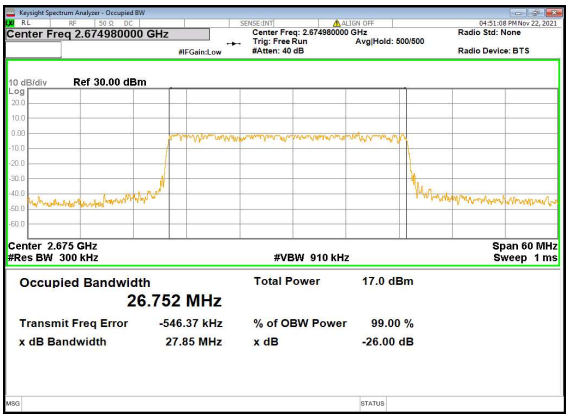
N41(30M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



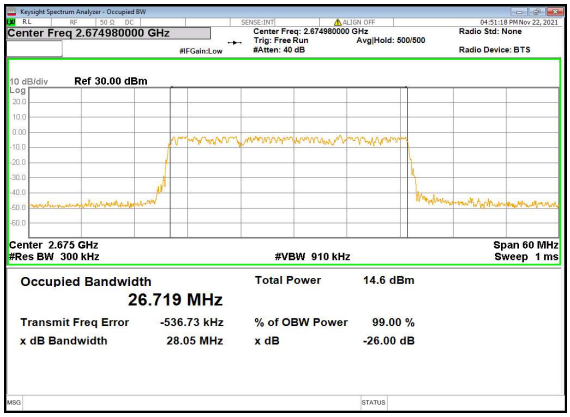
N41(30M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH



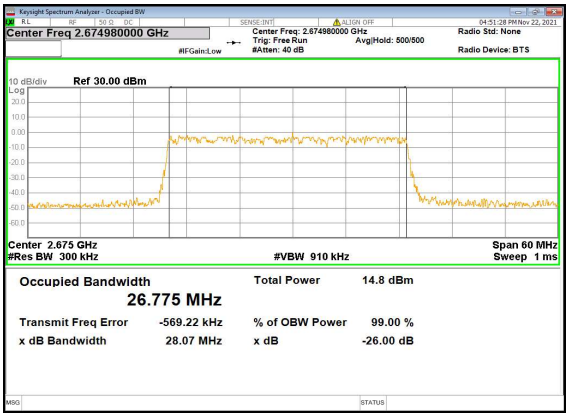
N41(30M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH



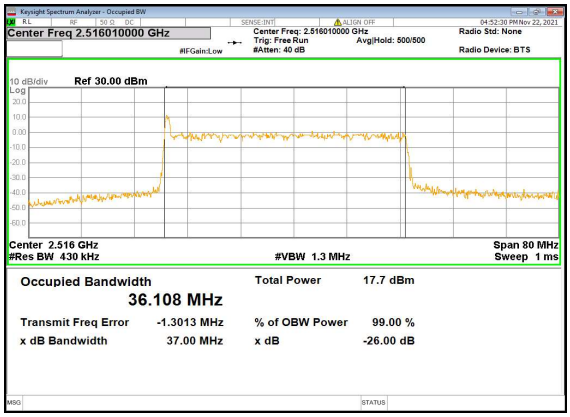
N41(30M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



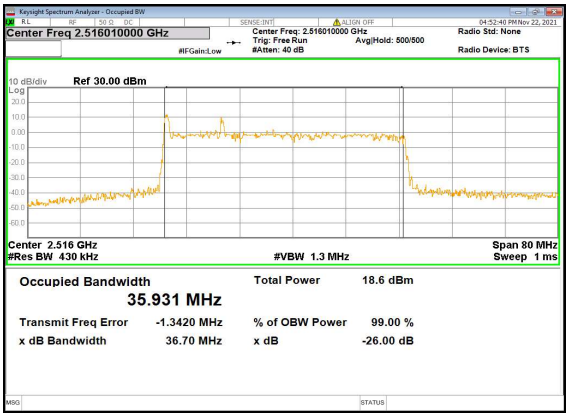
N41(30M)_CP-OFDM_QPSK_Outer_Full_High_CH



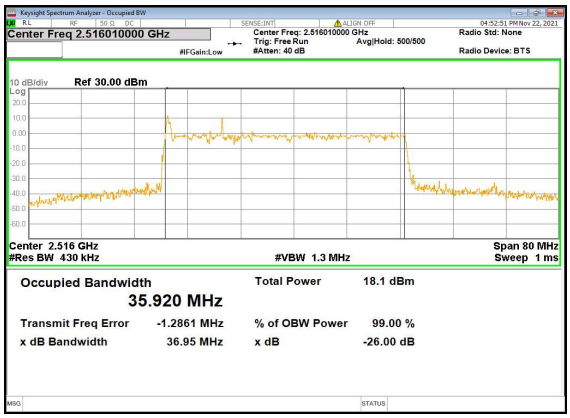
N41(40M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



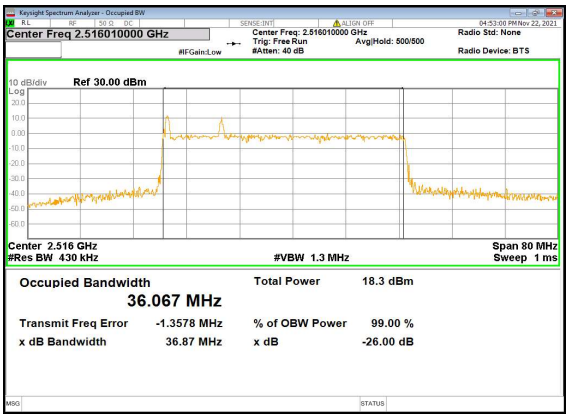
N41(40M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



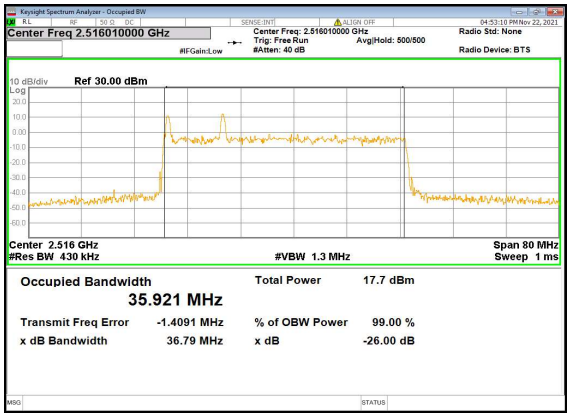
N41(40M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



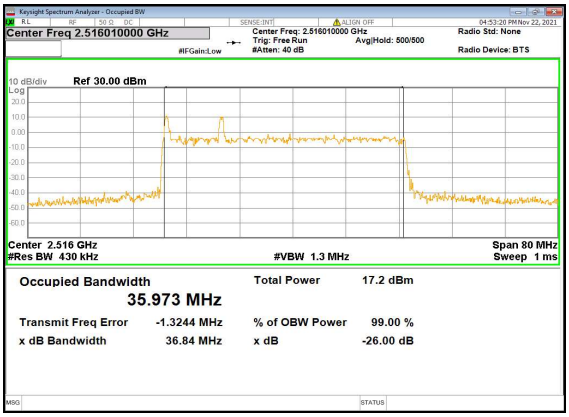
N41(40M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



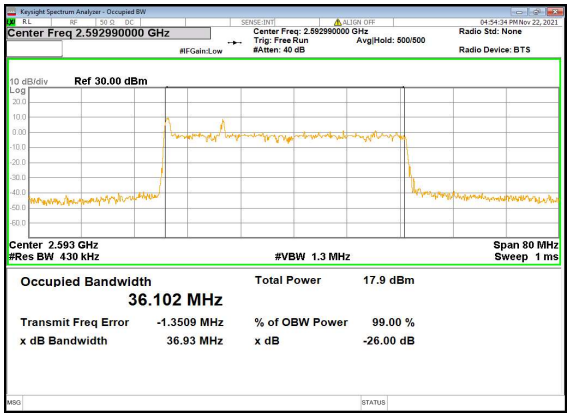
N41(40M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



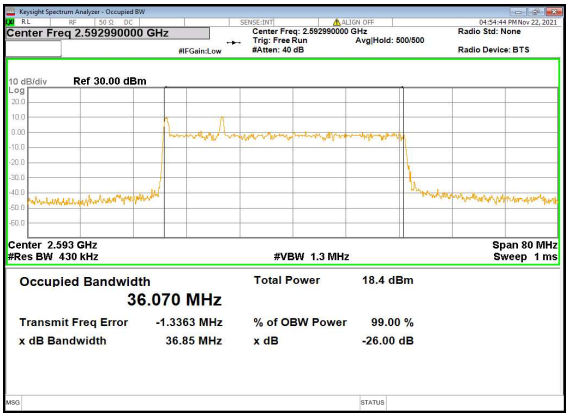
N41(40M)_CP-OFDM_QPSK_Outer_Full_Low_CH



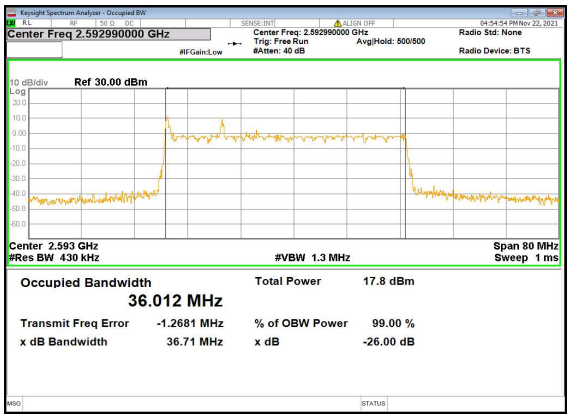
N41(40M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



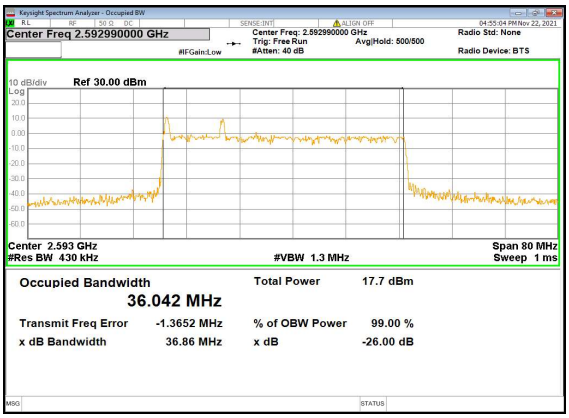
N41(40M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



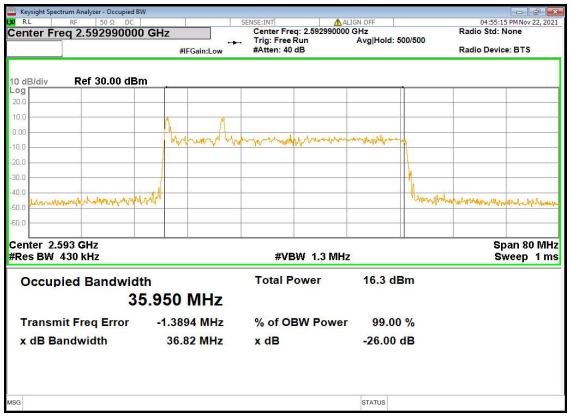
N41(40M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



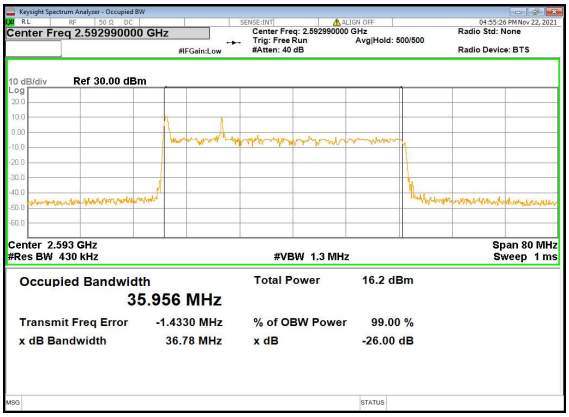
N41(40M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



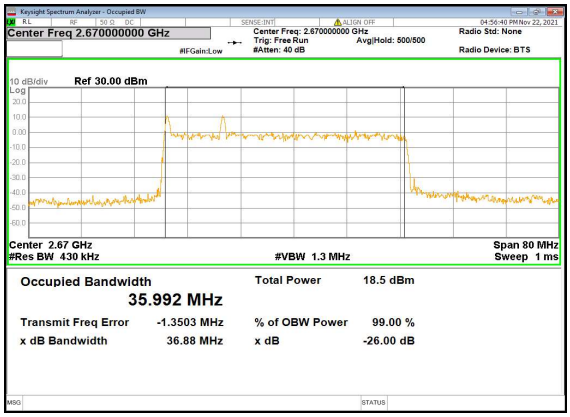
N41(40M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



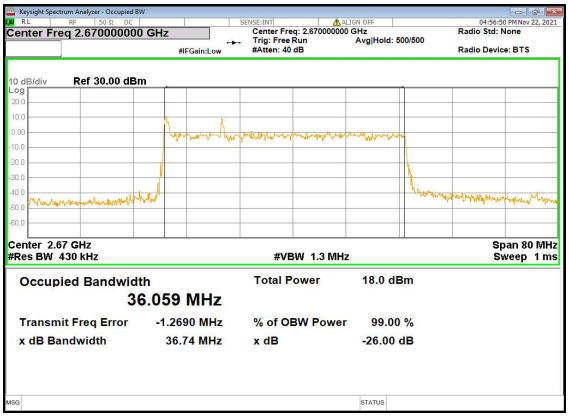
N41(40M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



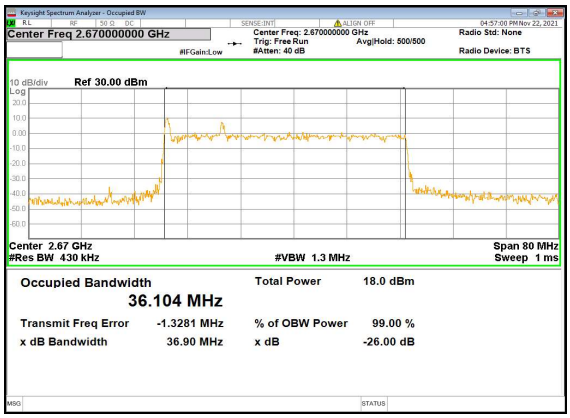
N41(40M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



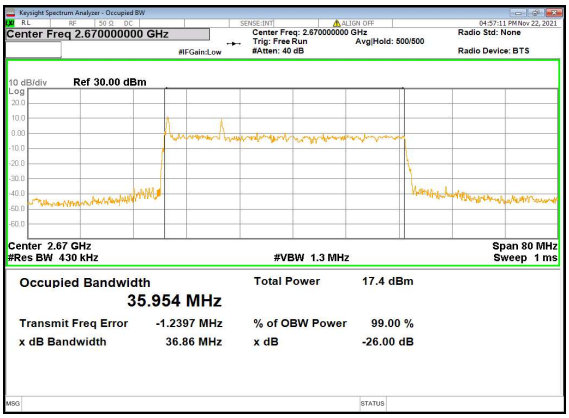
N41(40M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



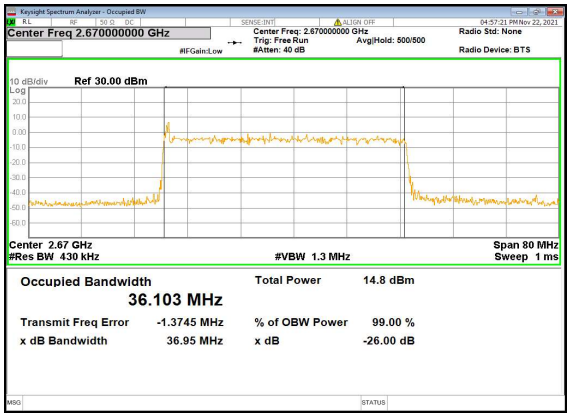
N41(40M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH



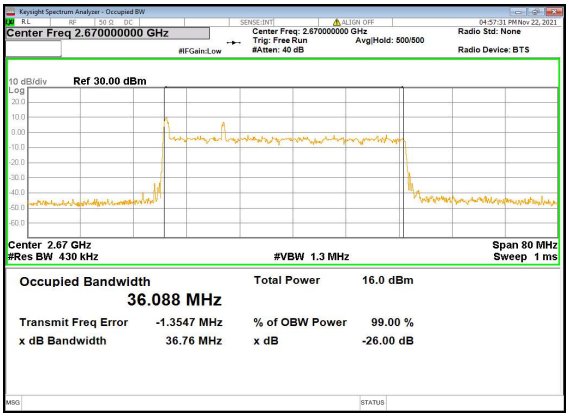
N41(40M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH



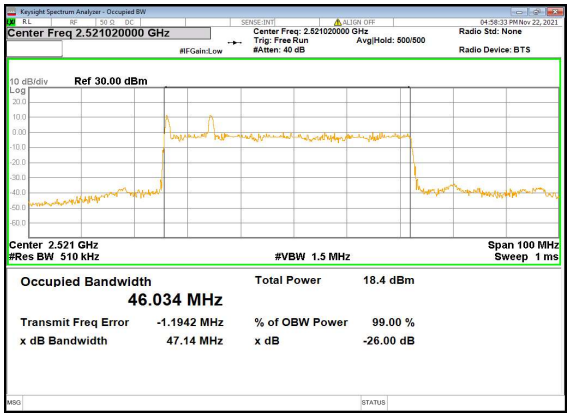
N41(40M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



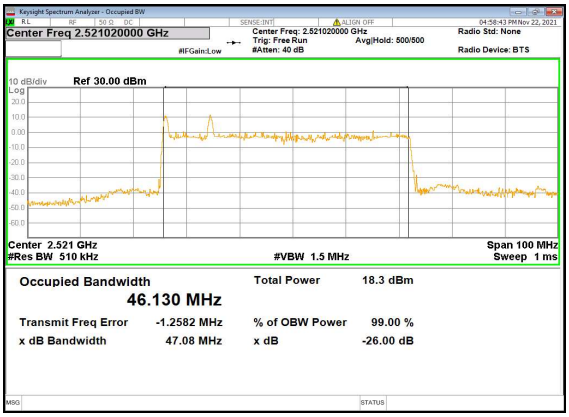
N41(40M)_CP-OFDM_QPSK_Outer_Full_High_CH



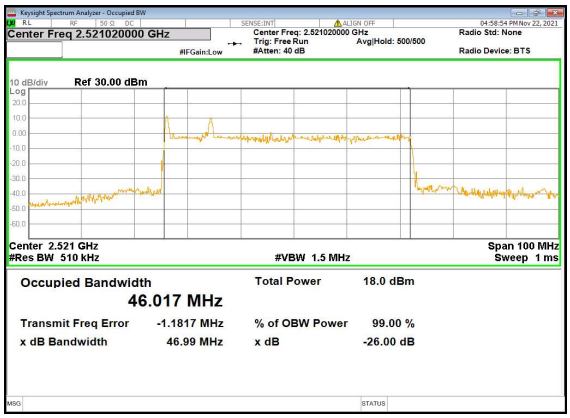
N41(50M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



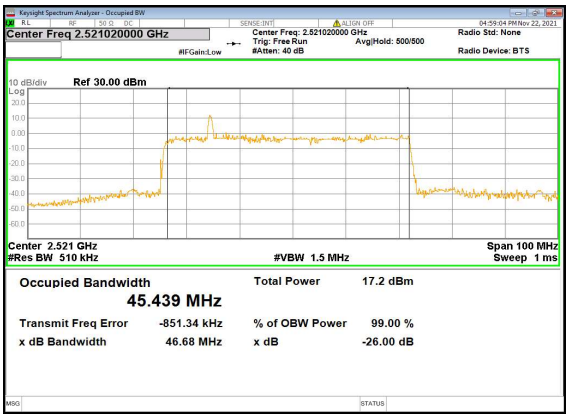
N41(50M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



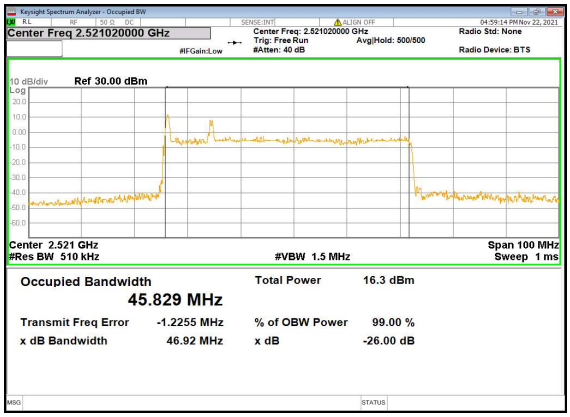
N41(50M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



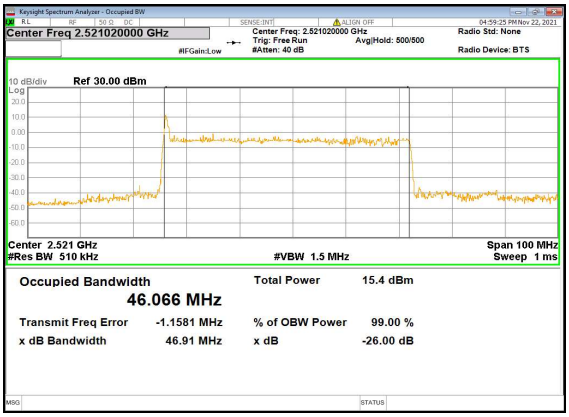
N41(50M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



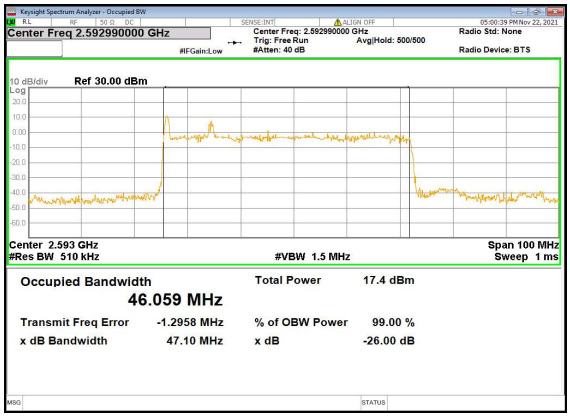
N41(50M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



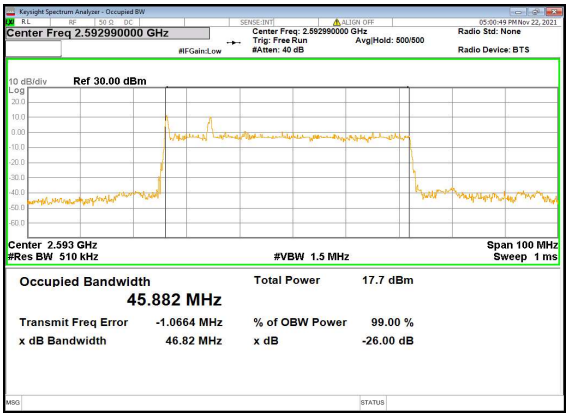
N41(50M)_CP-OFDM_QPSK_Outer_Full_Low_CH



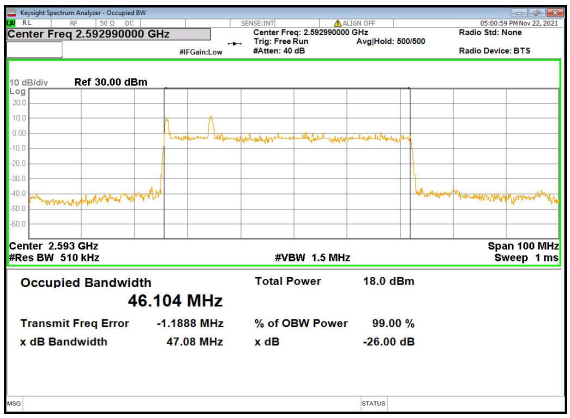
N41(50M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



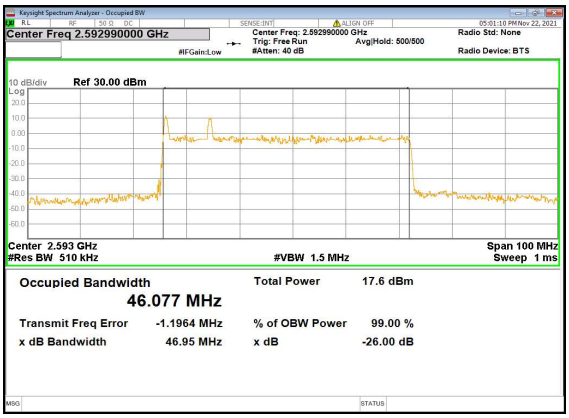
N41(50M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



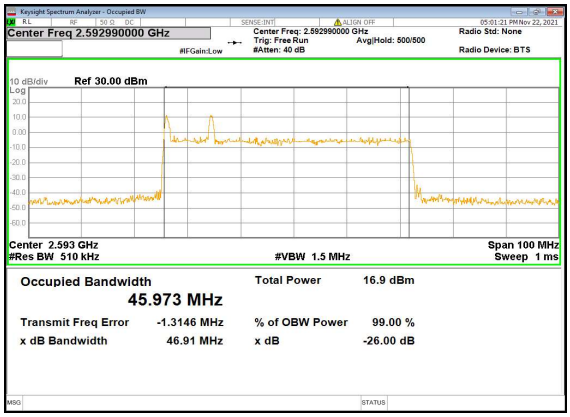
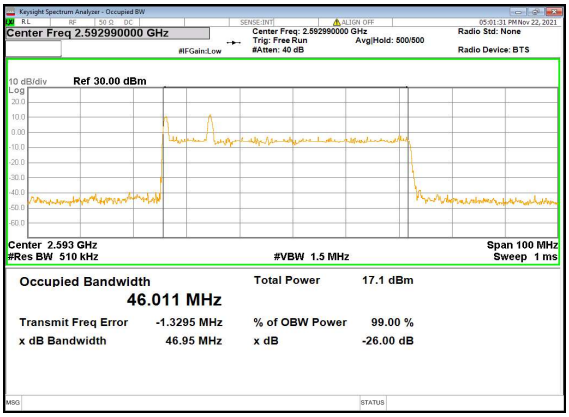
N41(50M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



N41(50M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



N41(50M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



N41(50M)_CP-OFDM_QPSK_Outer_Full_Mid_CH