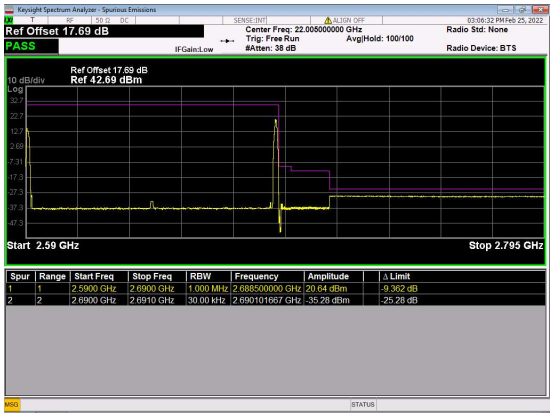
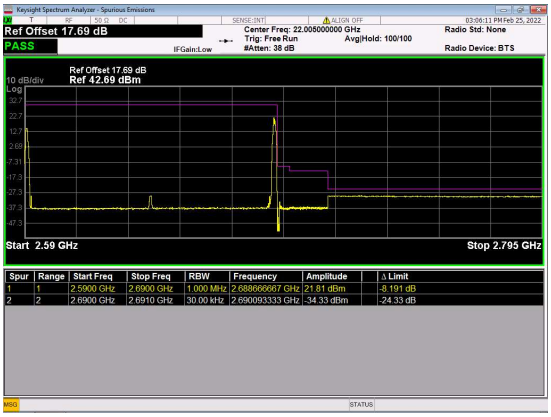
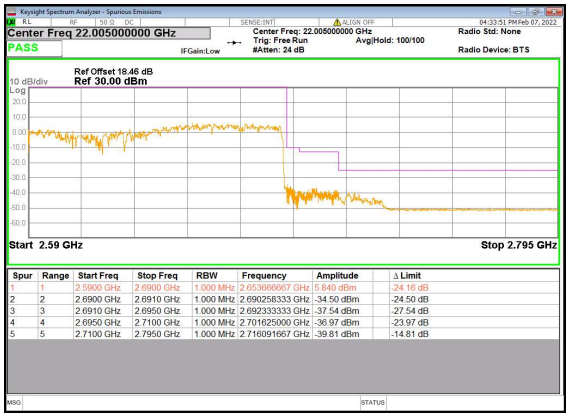
n41(100M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



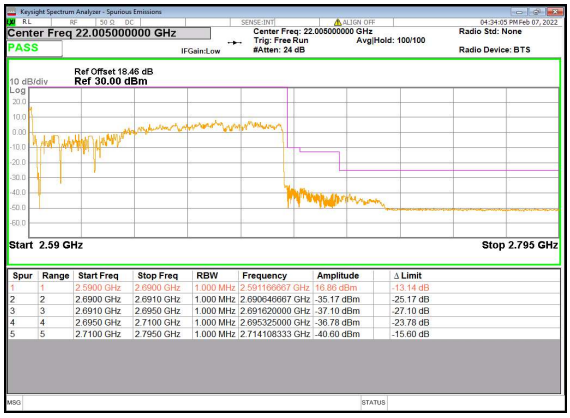
n41(100M)_DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



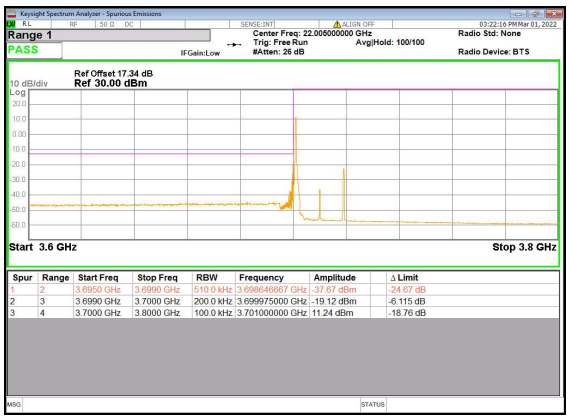
n41(100M)_DFT-s-OFDM_BPSK_Outer_Full_High_CH



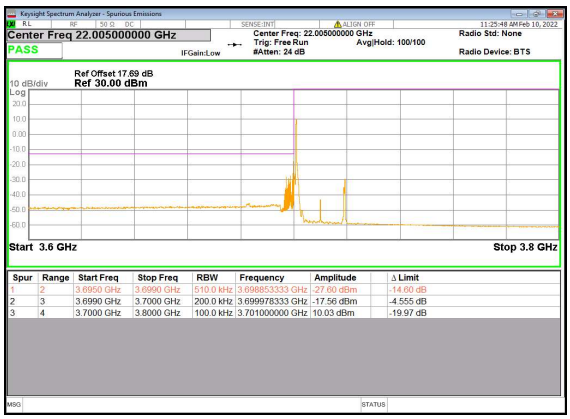
n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



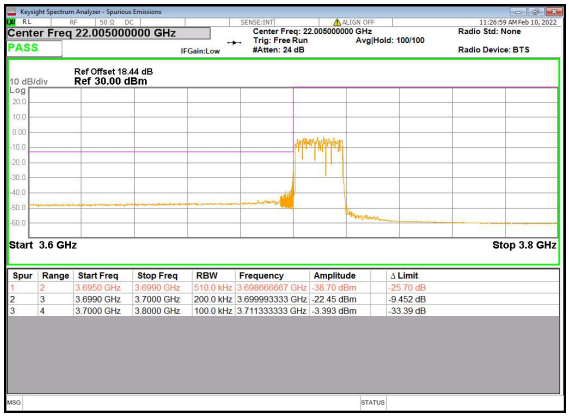
n77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



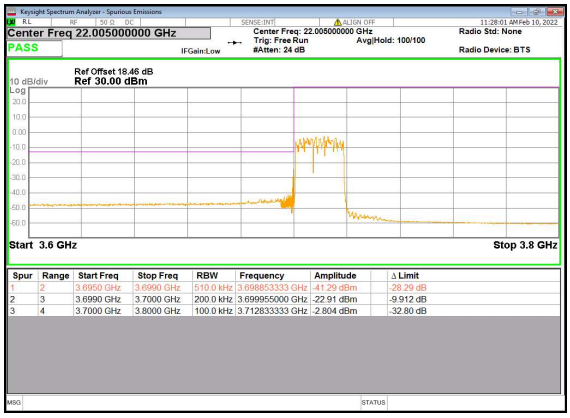
n77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



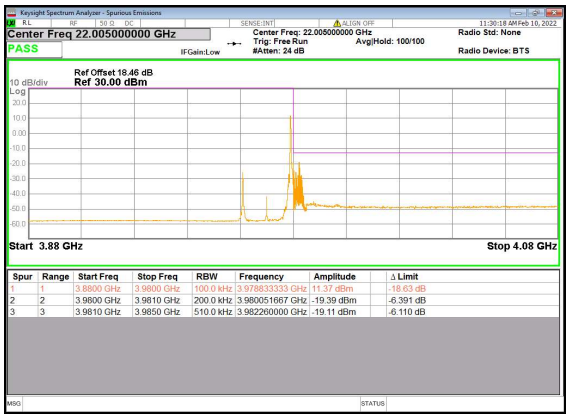
n77(20M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH



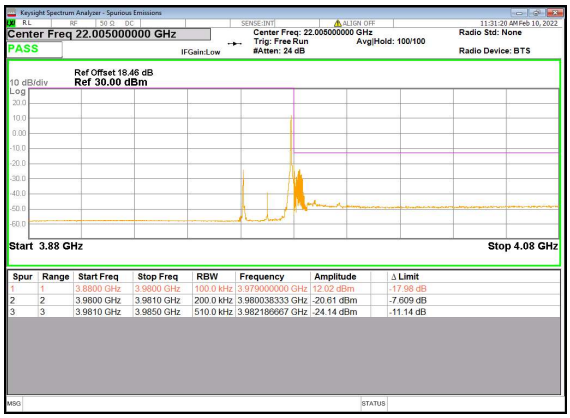
n77(20M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



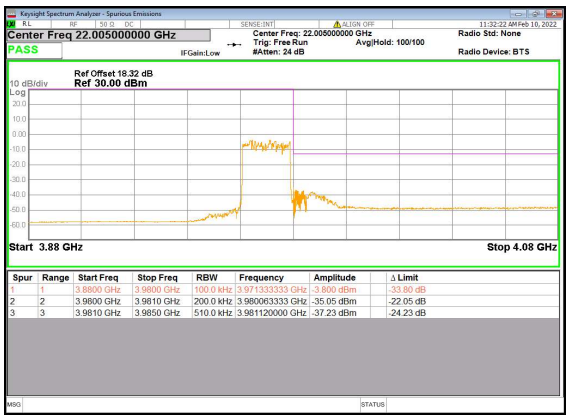
n77(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



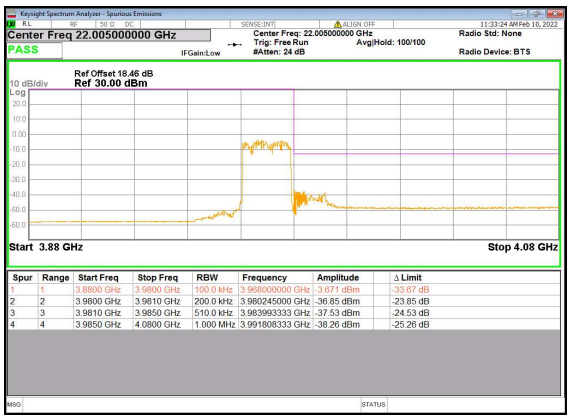
n77(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



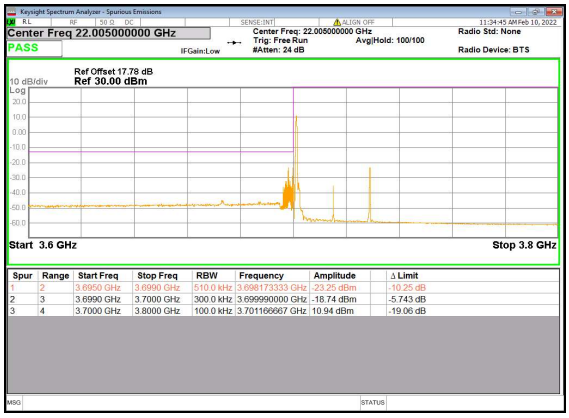
n77(20M)_DFT-s-OFDM_BPSK_Outer_Full_High_CH



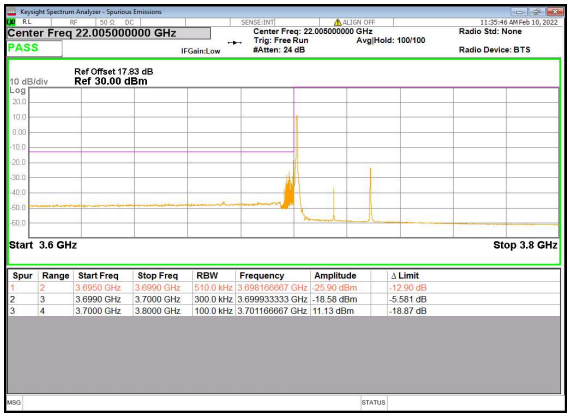
n77(20M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



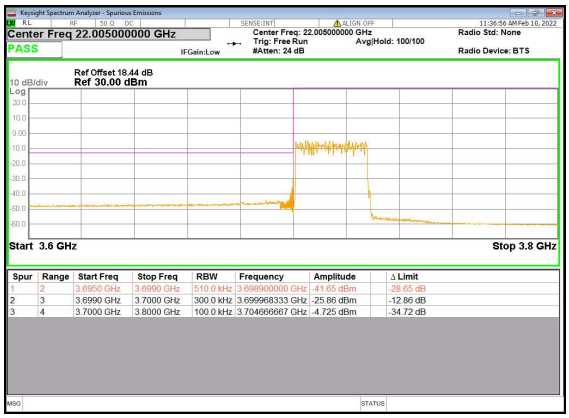
n77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



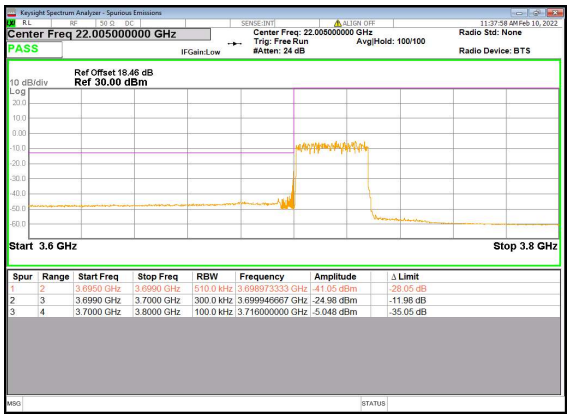
n77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



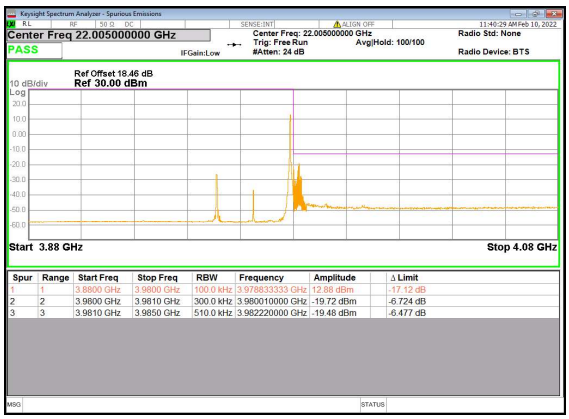
n77(30M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH



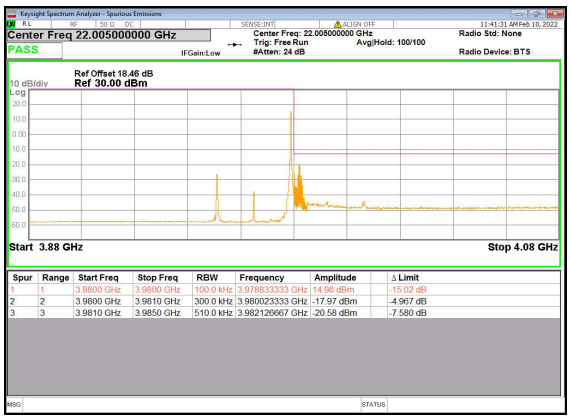
n77(30M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



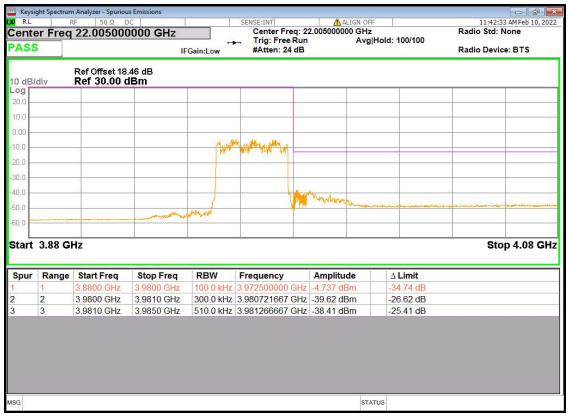
n77(30M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



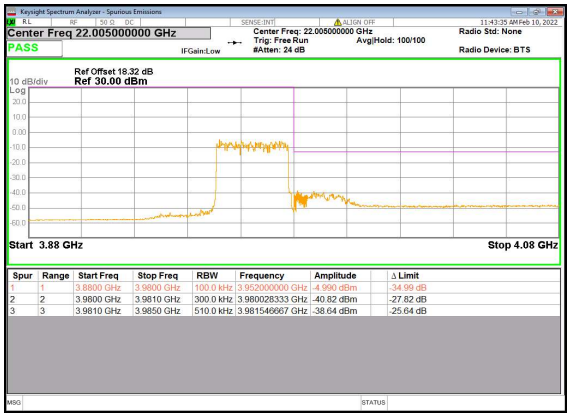
n77(30M)_DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



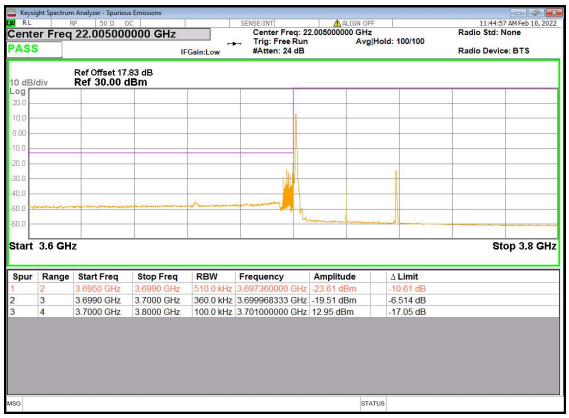
n77(30M)_DFT-s-OFDM_BPSK_Outer_Full_High_CH



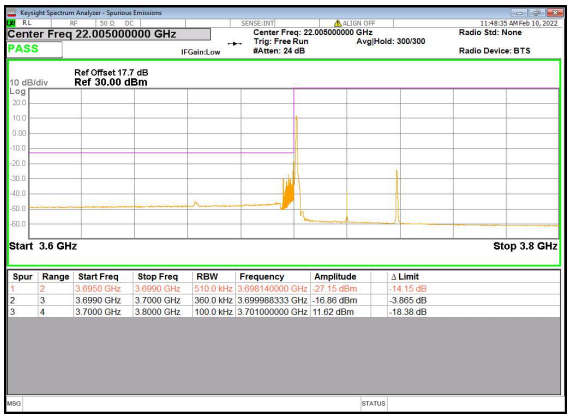
n77(30M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



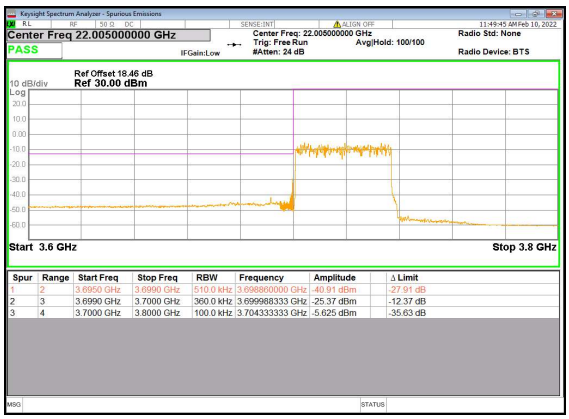
n77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



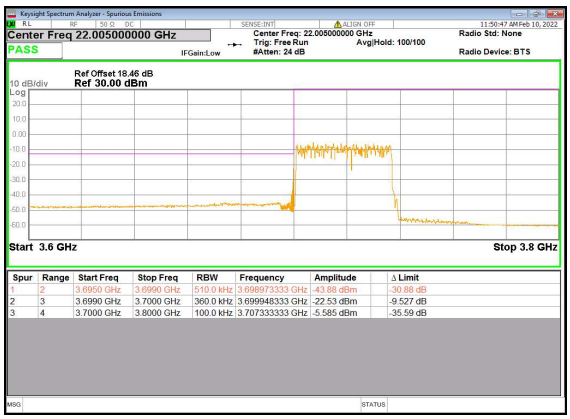
n77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



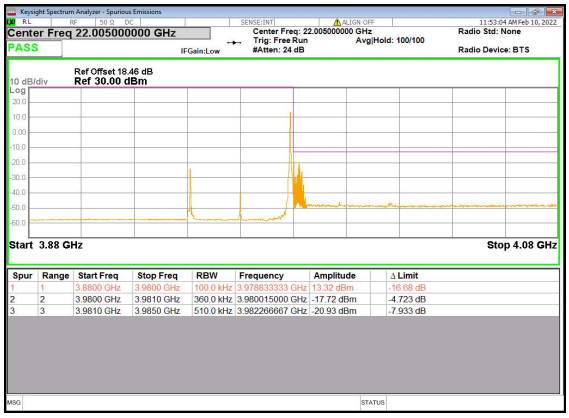
n77(40M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH



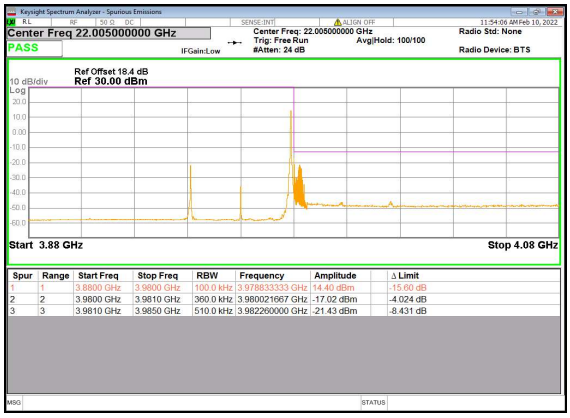
n77(40M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



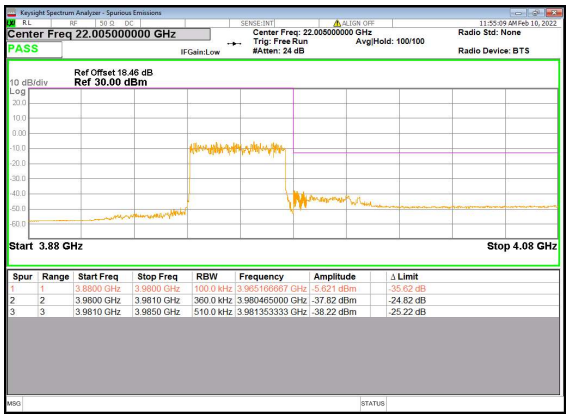
n77(40M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



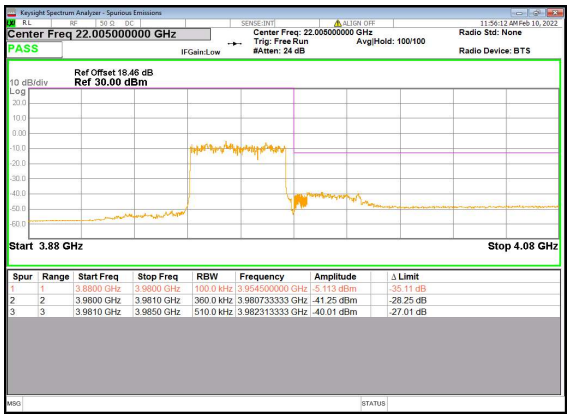
n77(40M)_DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



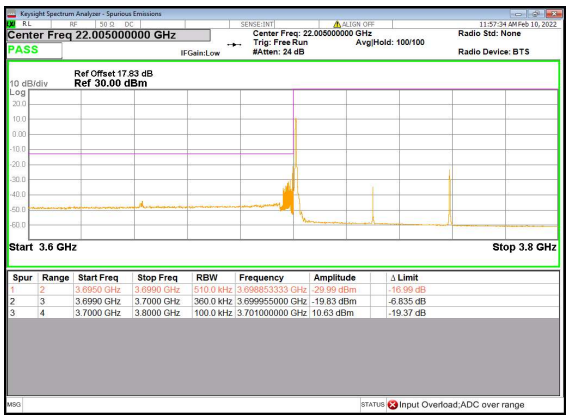
n77(40M)_DFT-s-OFDM_BPSK_Outer_Full_High_CH



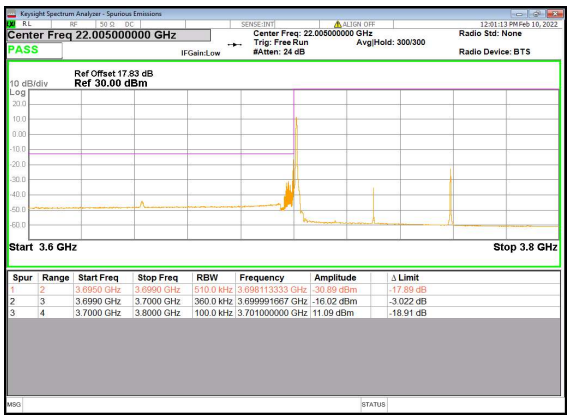
n77(40M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



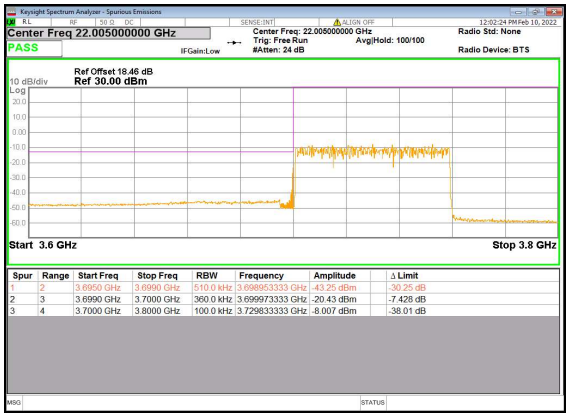
n77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



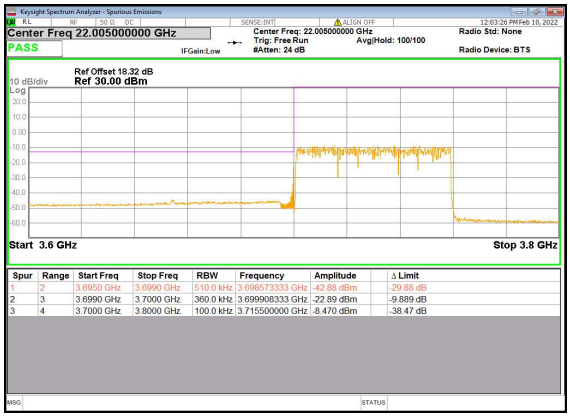
n77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



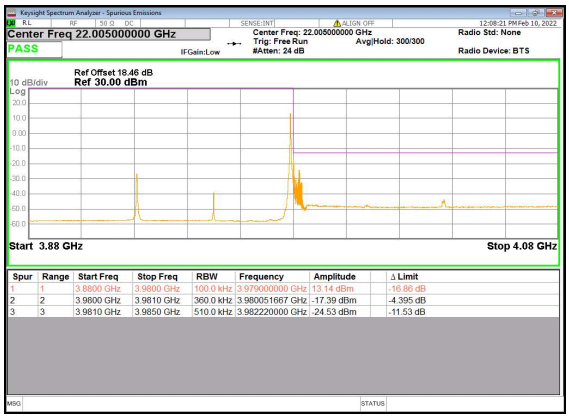
n77(60M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH



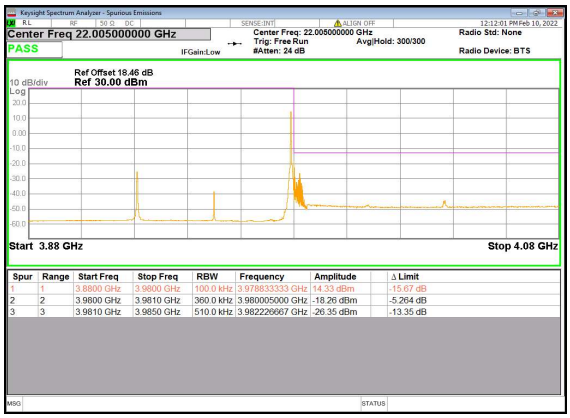
n77(60M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



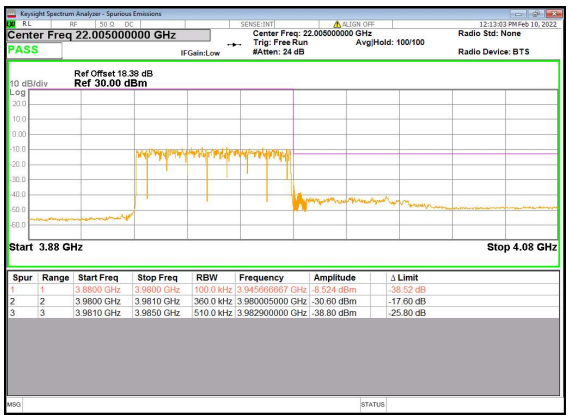
n77(60M)_DFT-s-OFDM_BPSK_Edge_1RB_Right_High_CH



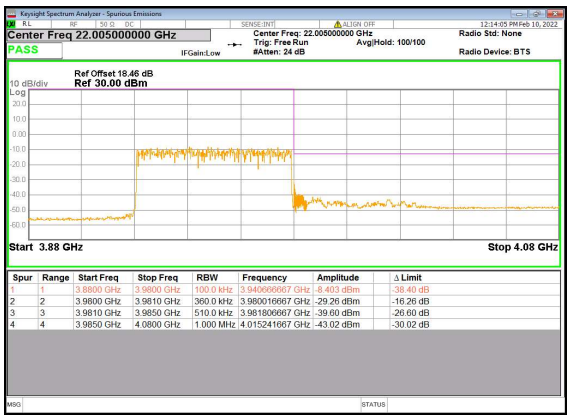
n77(60M)_DFT-s-OFDM_QPSK_Edge_1RB_Right_High_CH



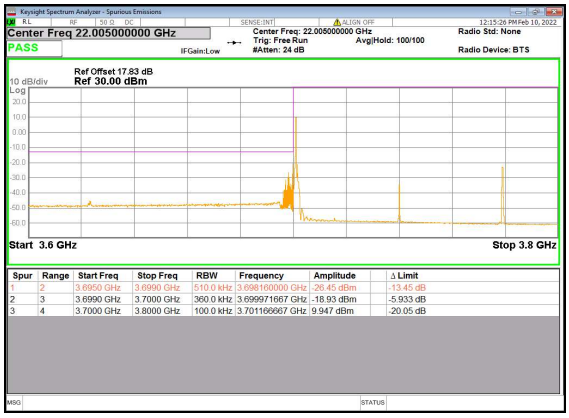
n77(60M)_DFT-s-OFDM_BPSK_Outer_Full_High_CH



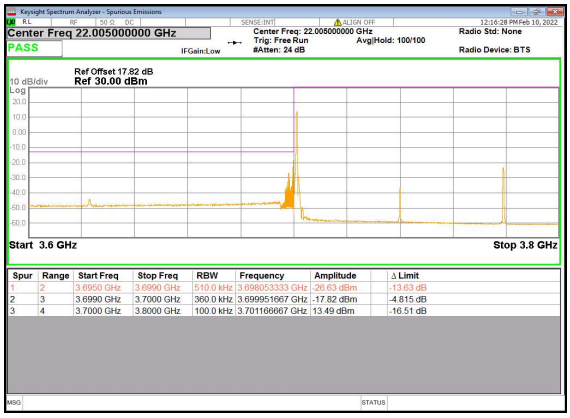
n77(60M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



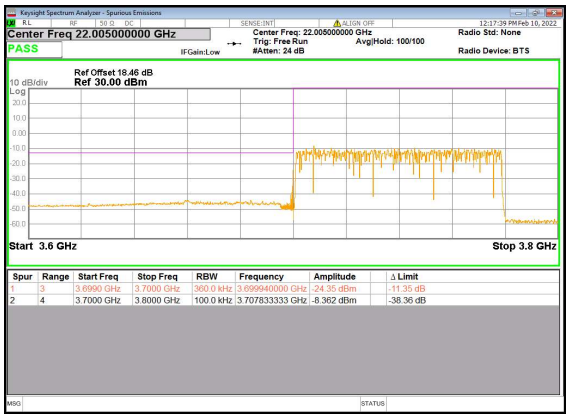
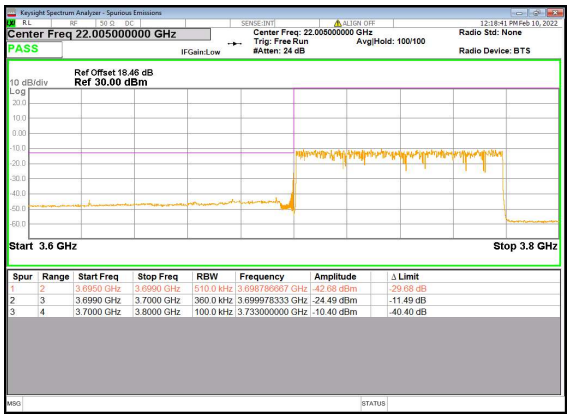
n77(80M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



n77(80M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



n77(80M)_DFT-s-OFDM_BPSK_Outer_Full_Low_CH



n77(80M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH