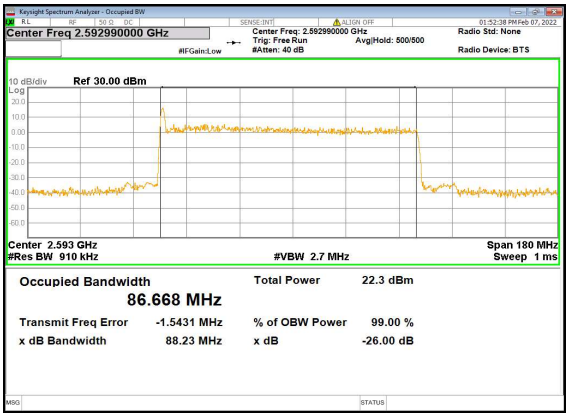
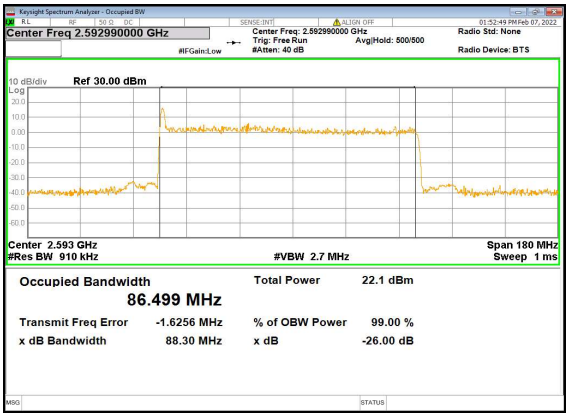
n41(90M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH

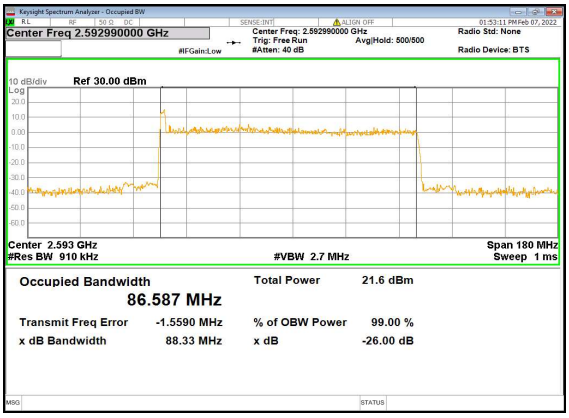


n41(90M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH

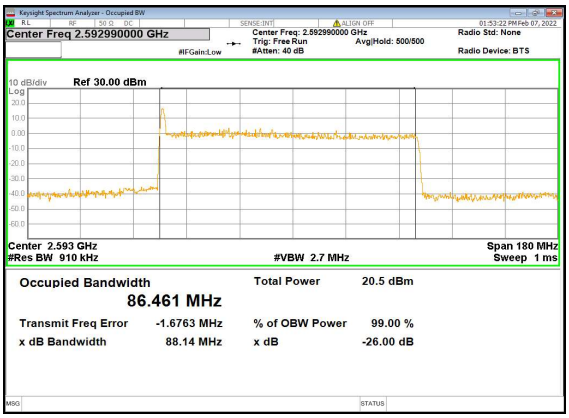


n41(90M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH

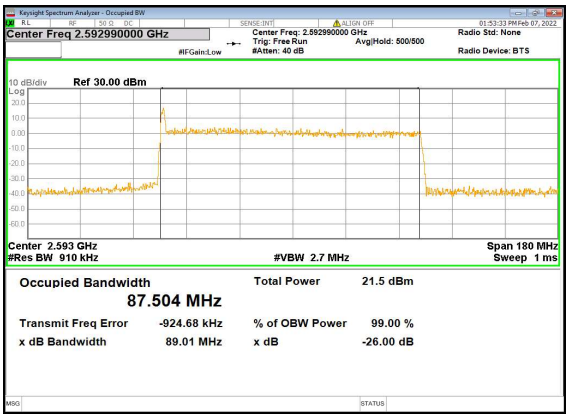
n41(90M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



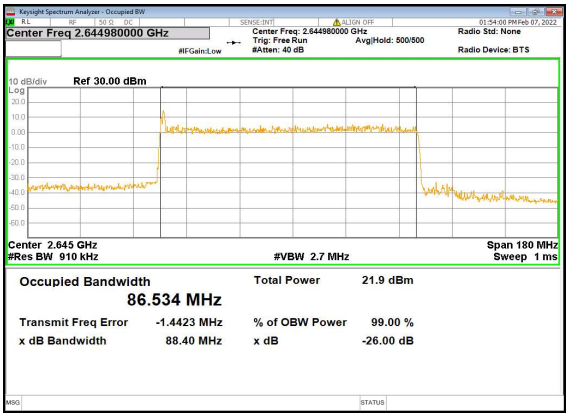
n41(90M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



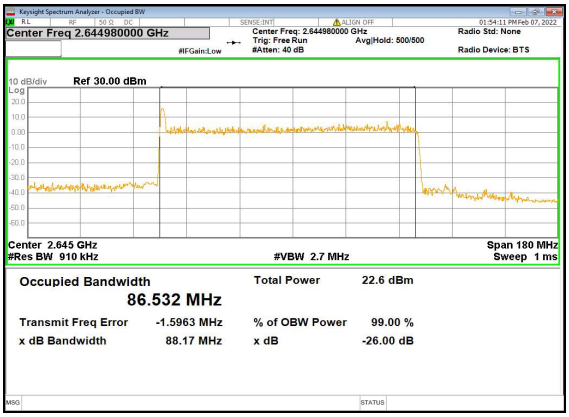
n41(90M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



n41(90M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH

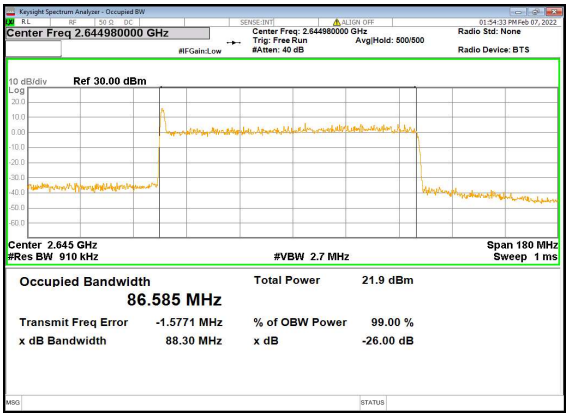


n41(90M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH

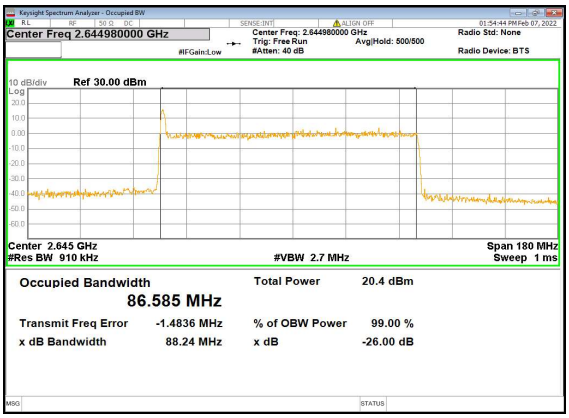


n41(90M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH

n41(90M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH

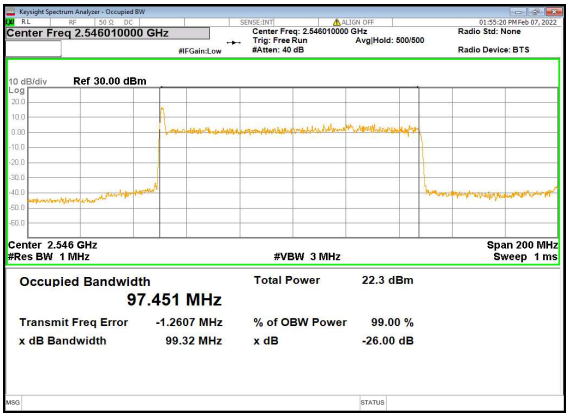


n41(90M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH

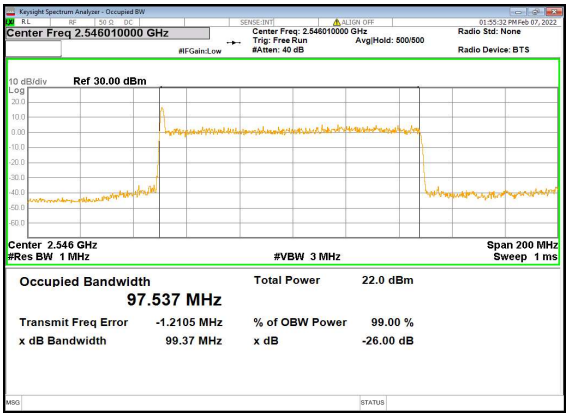


n41(90M)_CP-OFDM_QPSK_Outer_Full_High_CH

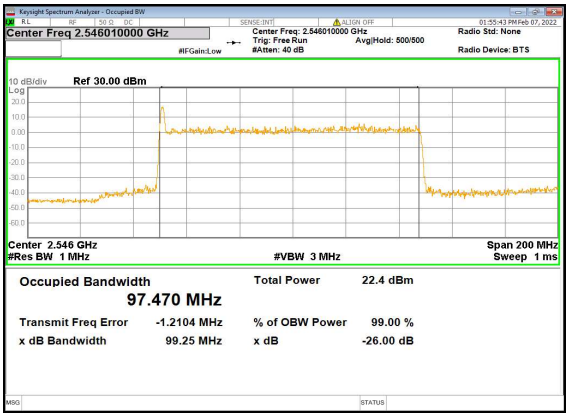
n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



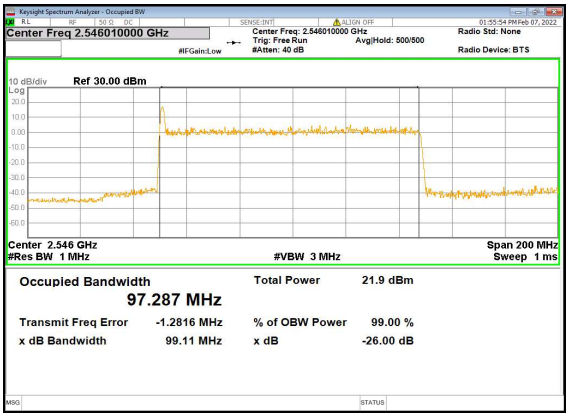
n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



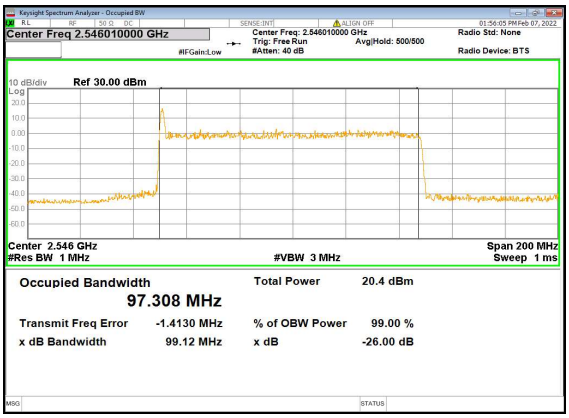
n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



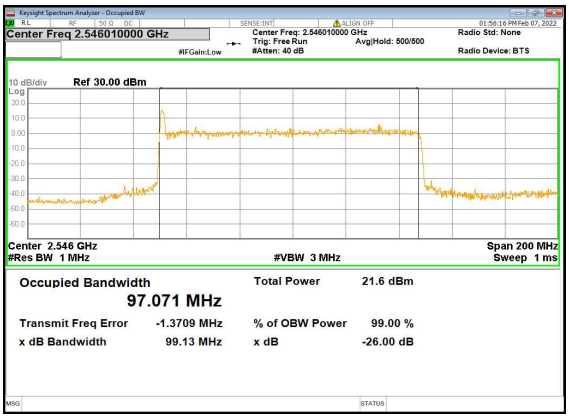
n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



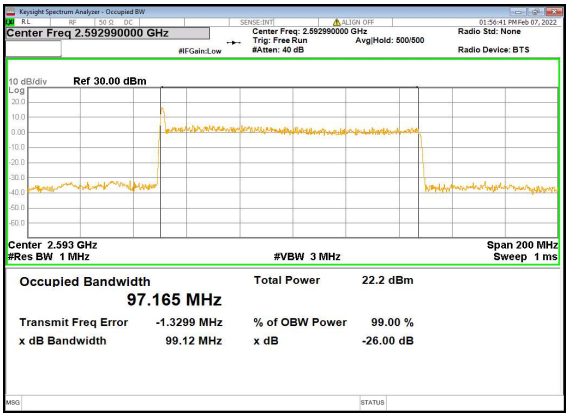
n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



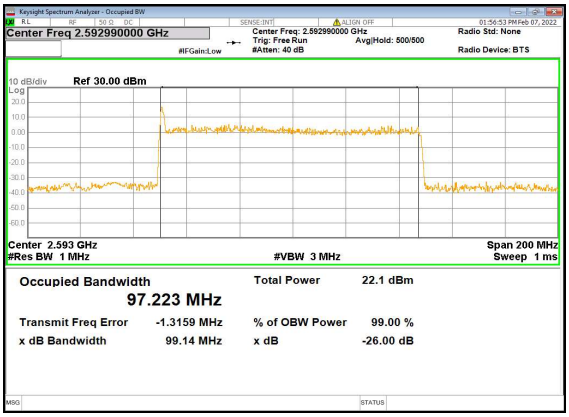
n41(100M)_CP-OFDM_QPSK_Outer_Full_Low_CH



n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH

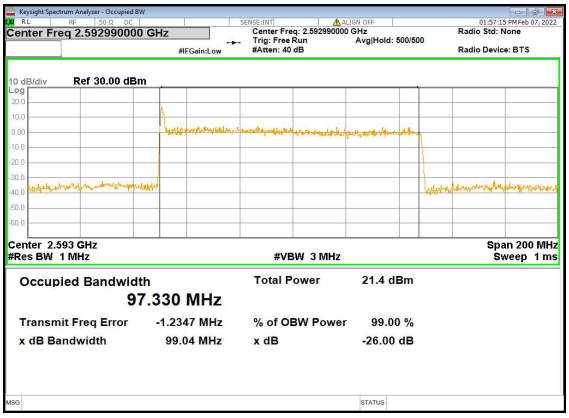


n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH

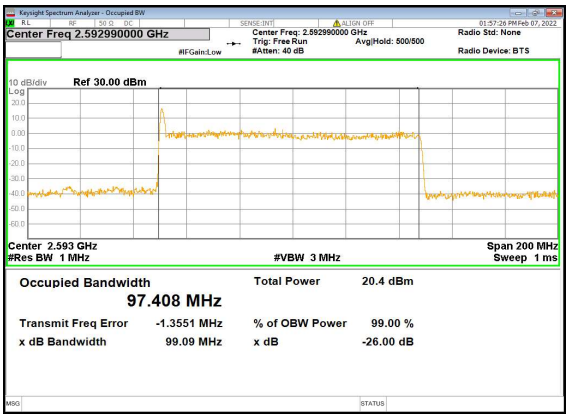


n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH

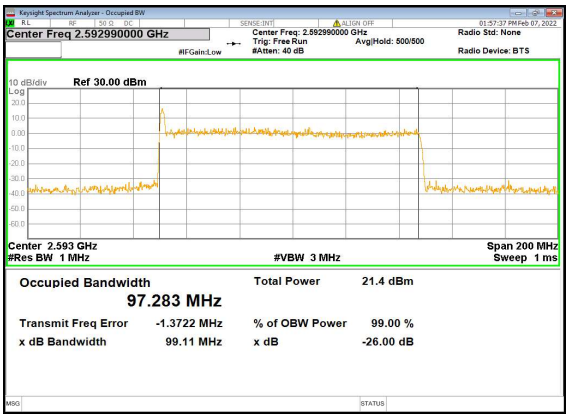
n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



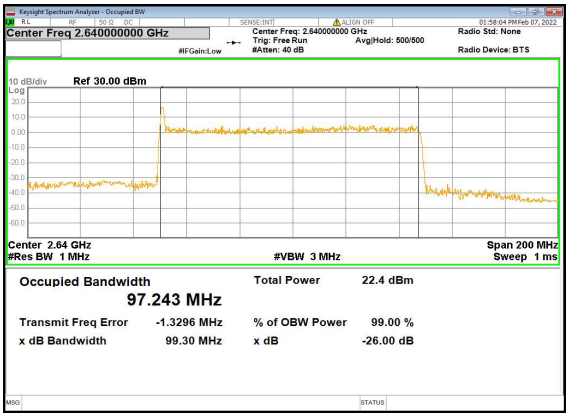
n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



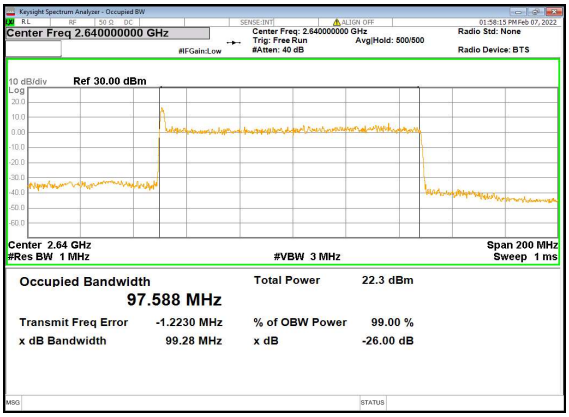
n41(100M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



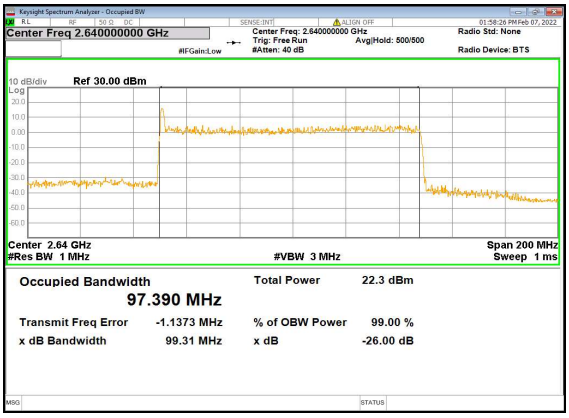
n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH

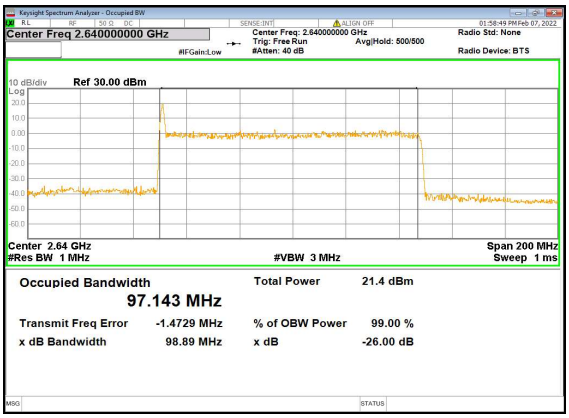


n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH

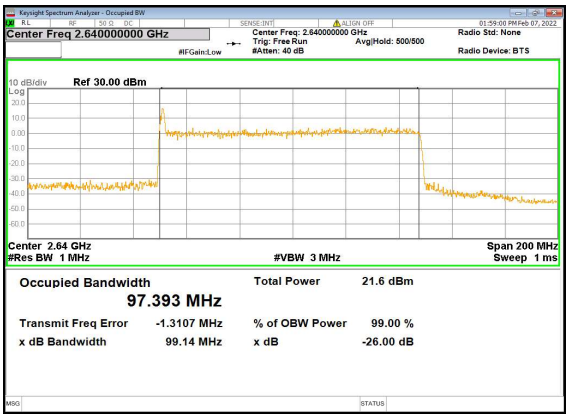


n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH

n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH

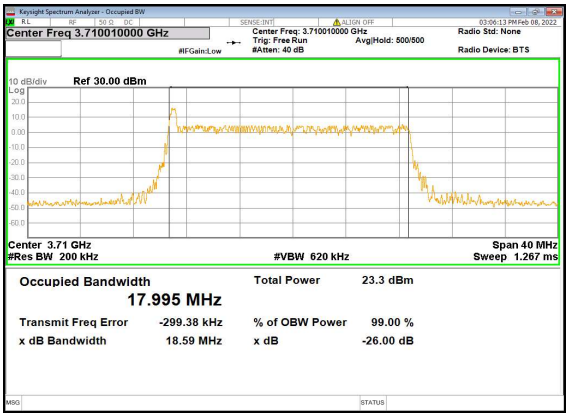


n41(100M)_CP-OFDM_QPSK_Outer_Full_High_CH

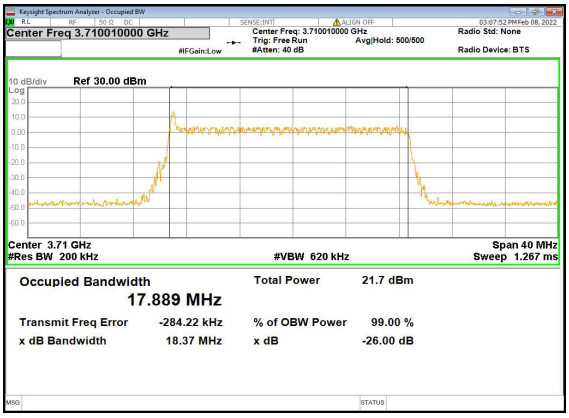


n77(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH

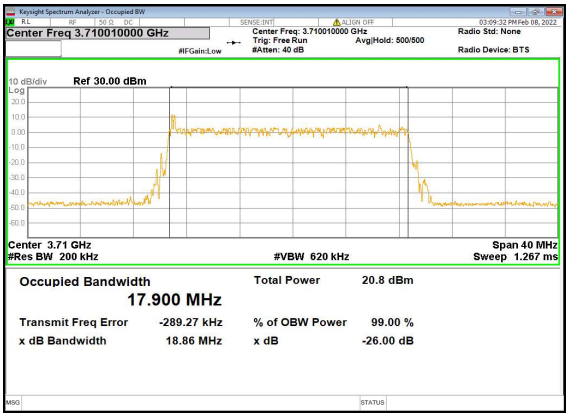
n77(20M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



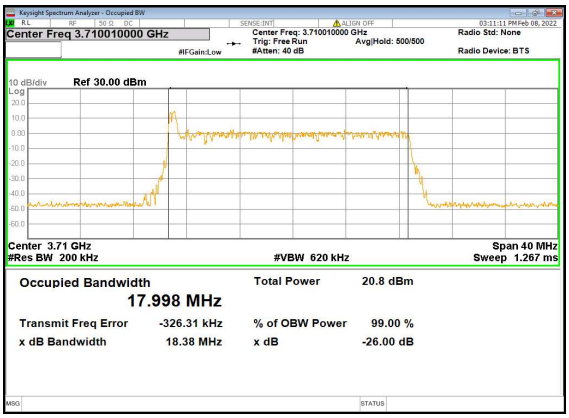
n77(20M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



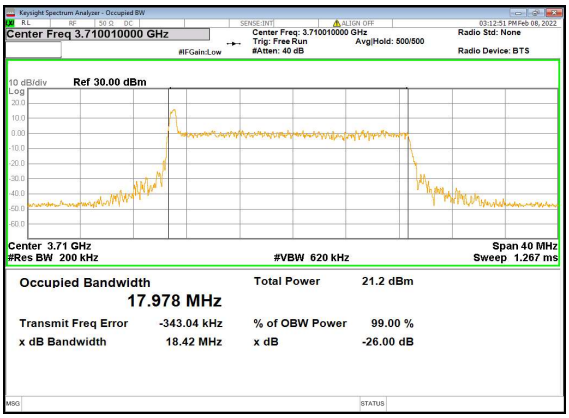
n77(20M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



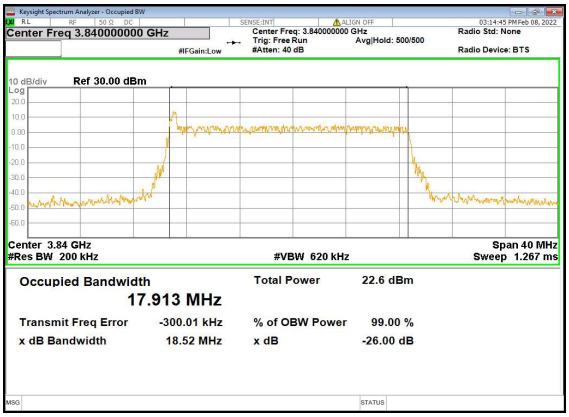
n77(20M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



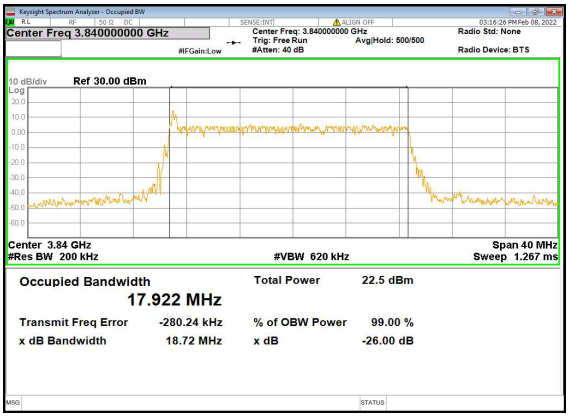
n77(20M)_CP-OFDM_QPSK_Outer_Full_Low_CH



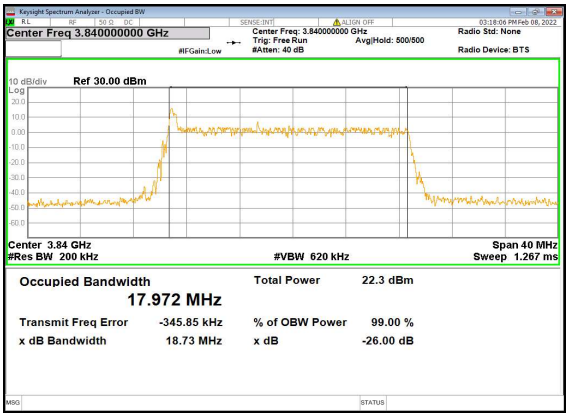
n77(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



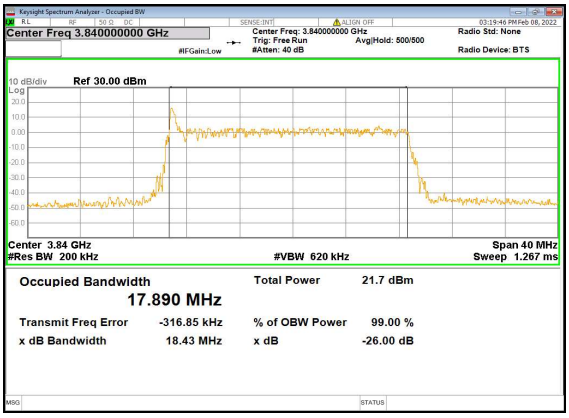
n77(20M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



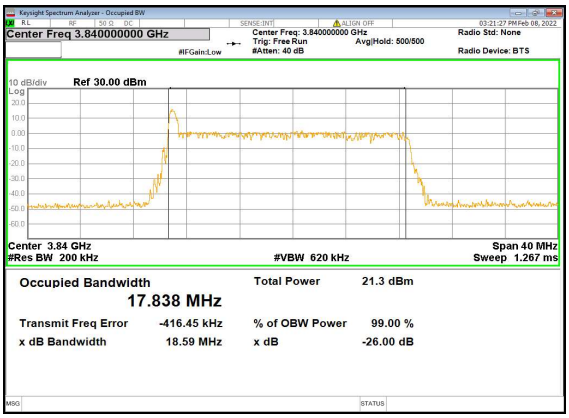
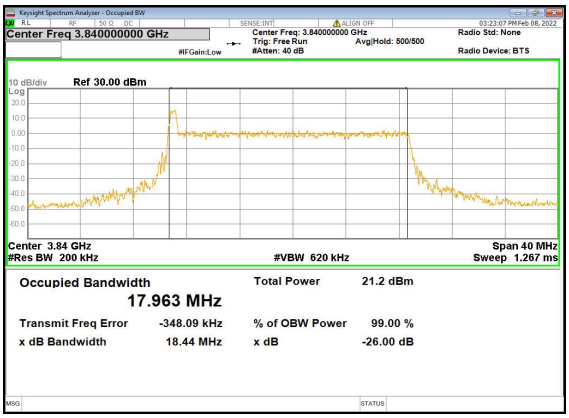
n77(20M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



n77(20M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



n77(20M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



n77(20M)_CP-OFDM_QPSK_Outer_Full_Mid_CH