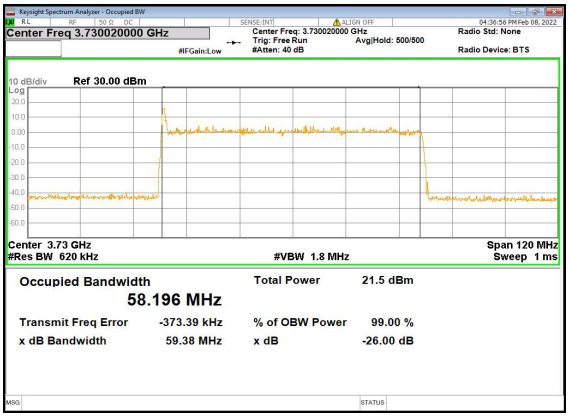
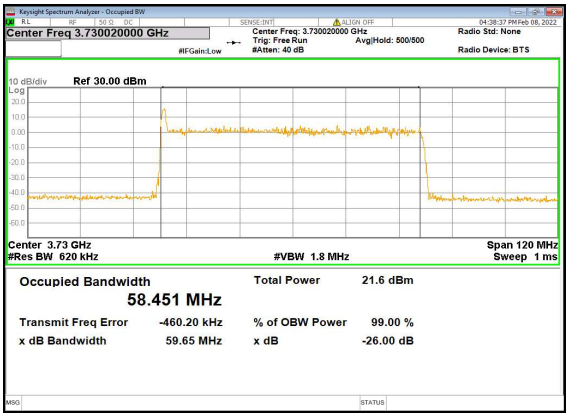
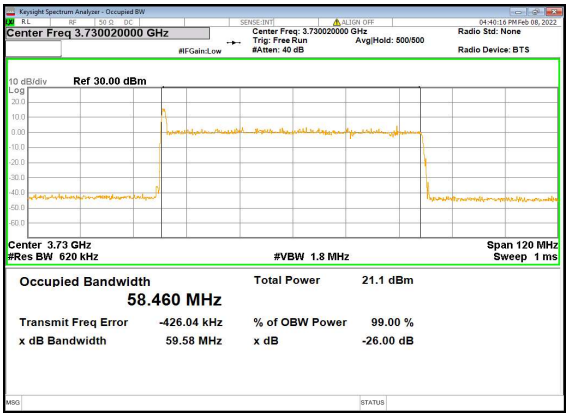
n77(60M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



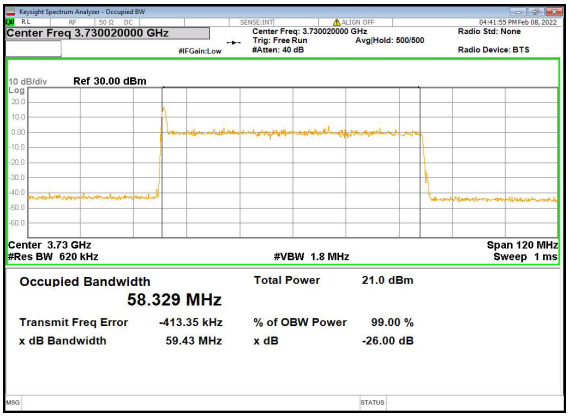
n77(60M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



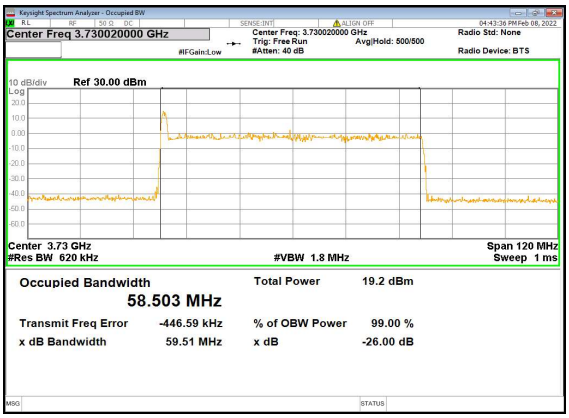
n77(60M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



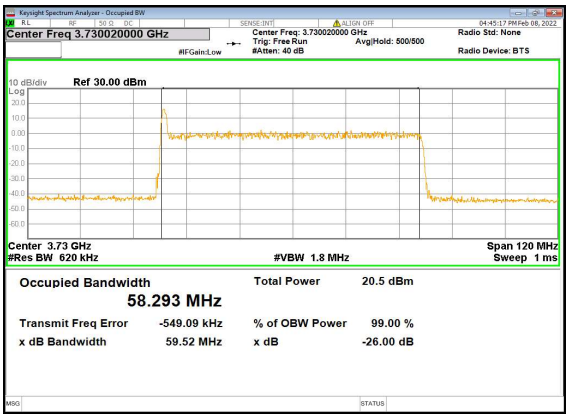
n77(60M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



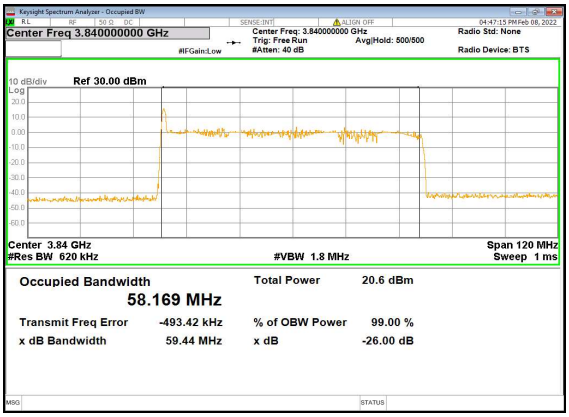
n77(60M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



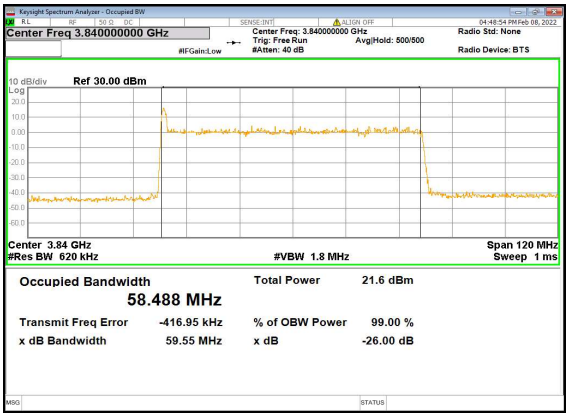
n77(60M)_CP-OFDM_QPSK_Outer_Full_Low_CH



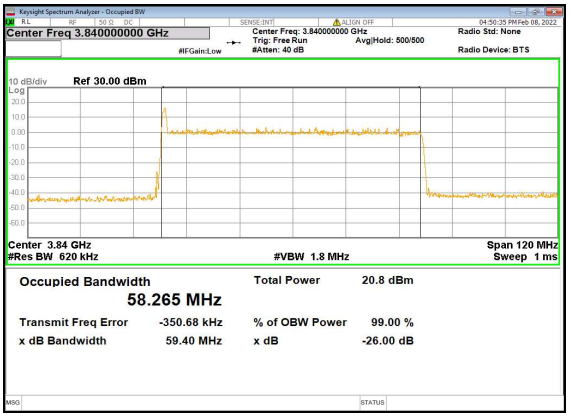
n77(60M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



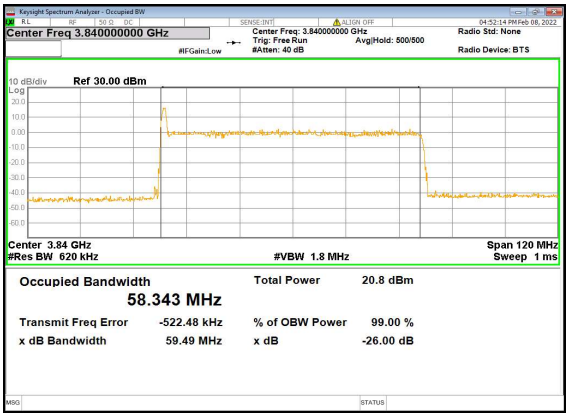
n77(60M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



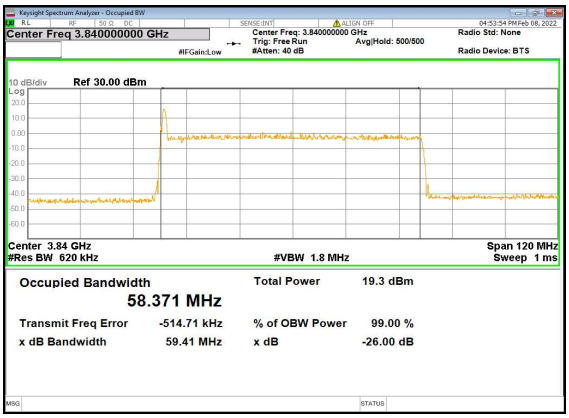
n77(60M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



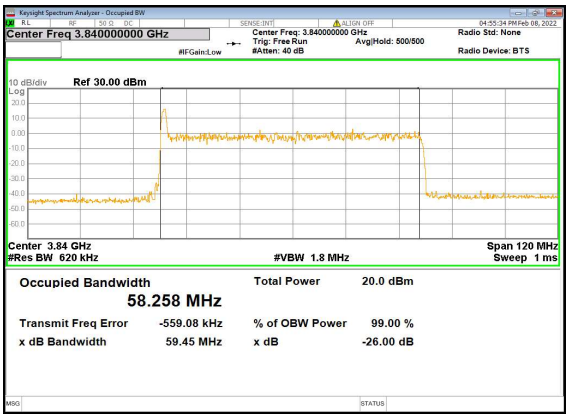
n77(60M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



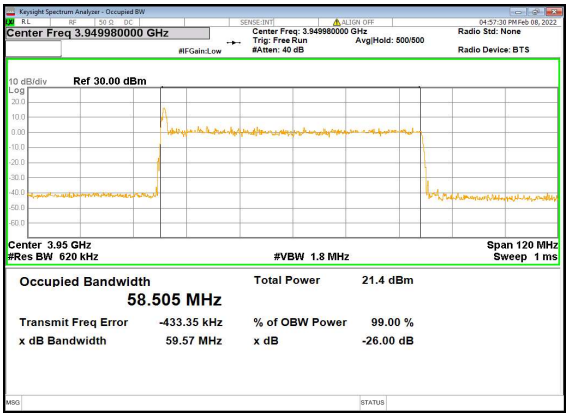
n77(60M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



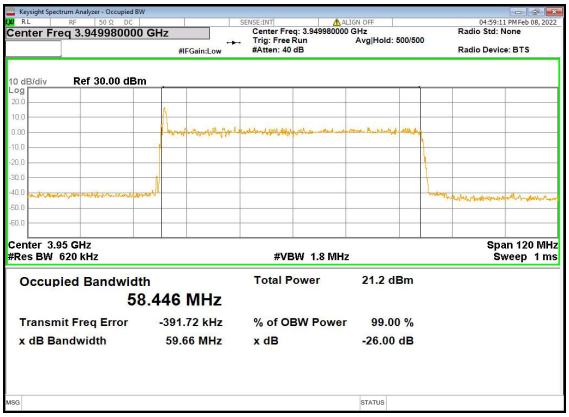
n77(60M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



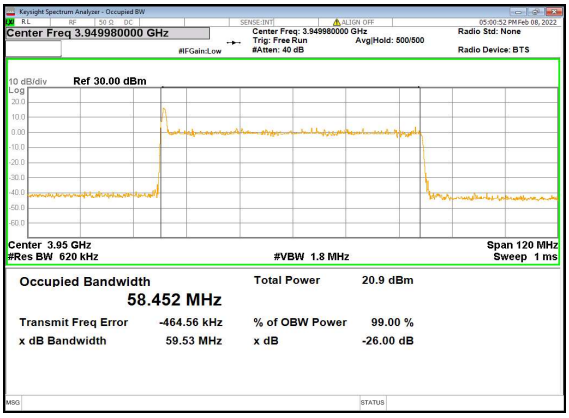
n77(60M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



n77(60M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



n77(60M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH



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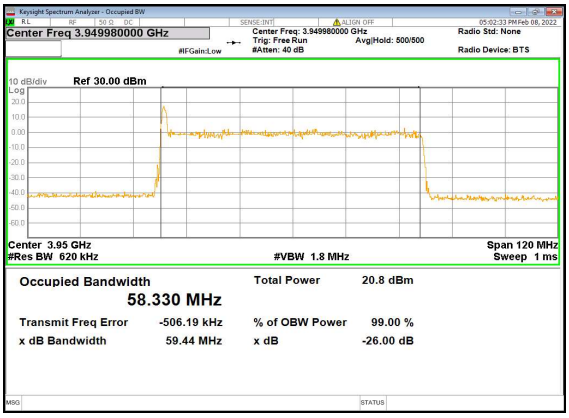
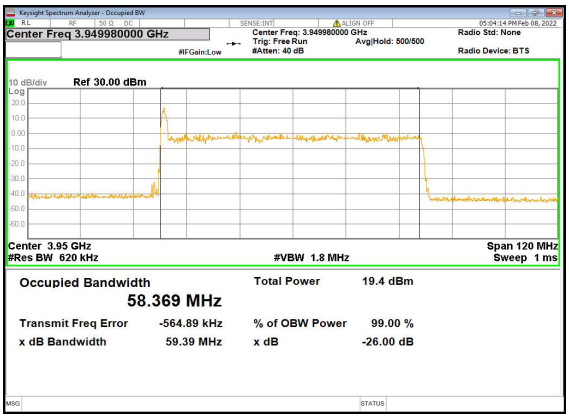
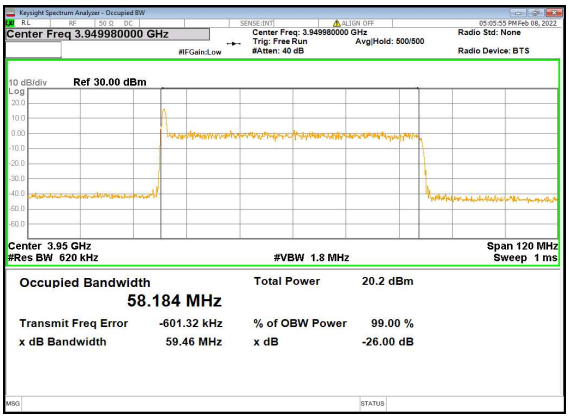


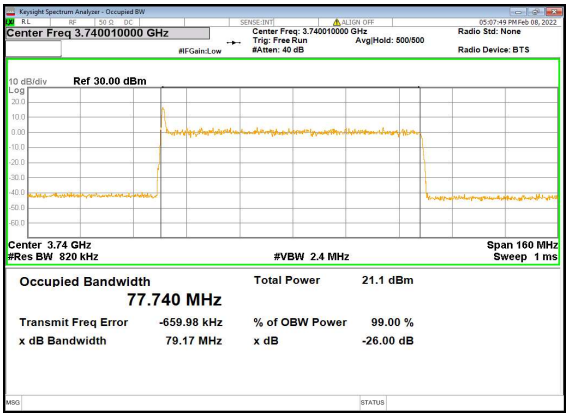
n77(60M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



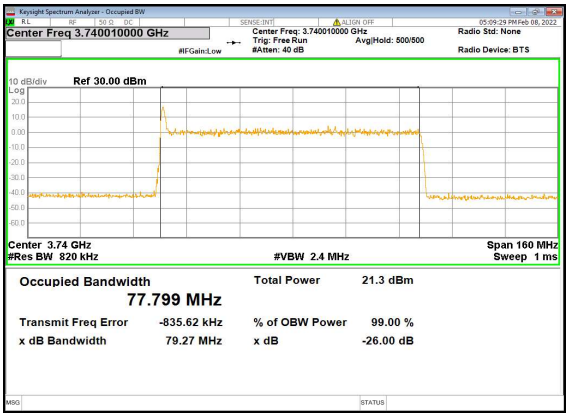
n77(60M)_CP-OFDM_QPSK_Outer_Full_High_CH



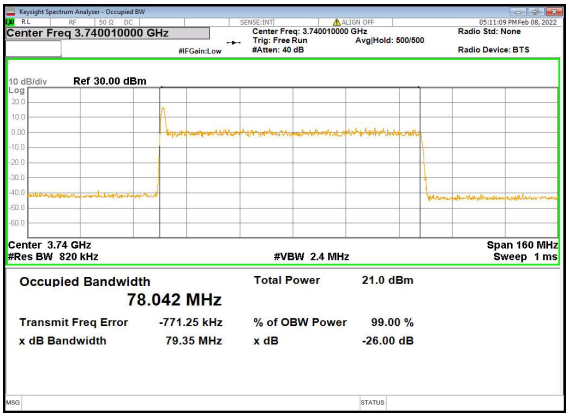
n77(80M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



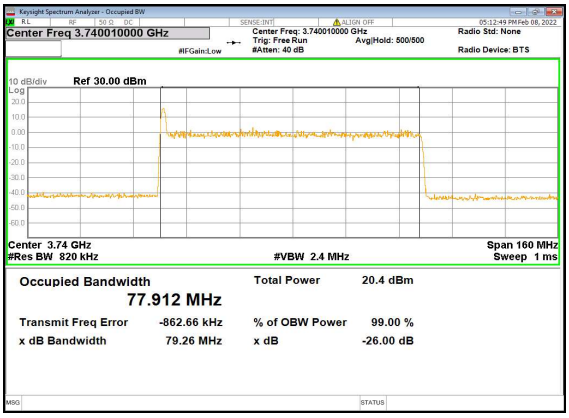
n77(80M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



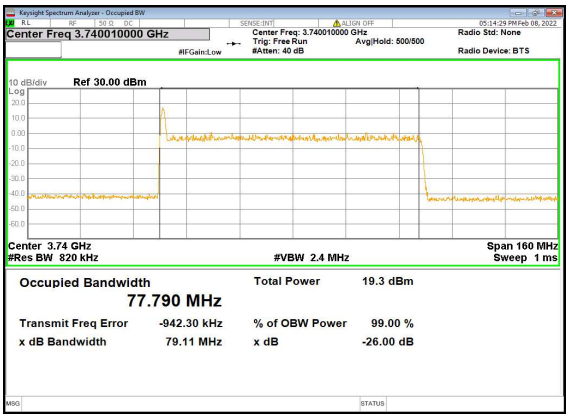
n77(80M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



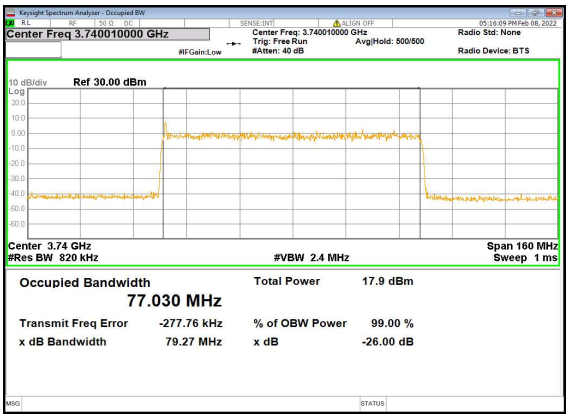
n77(80M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



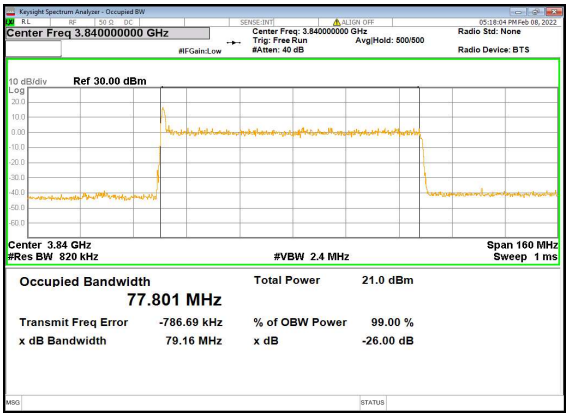
n77(80M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



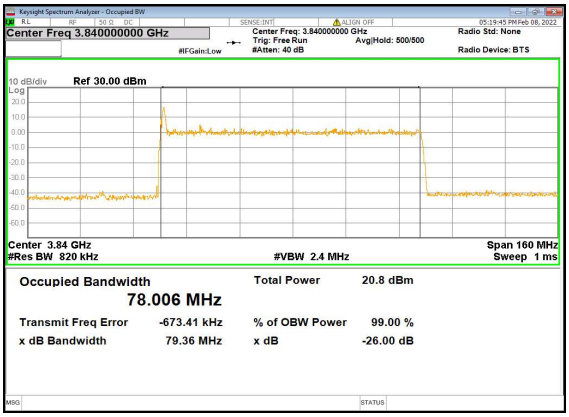
n77(80M)_CP-OFDM_QPSK_Outer_Full_Low_CH



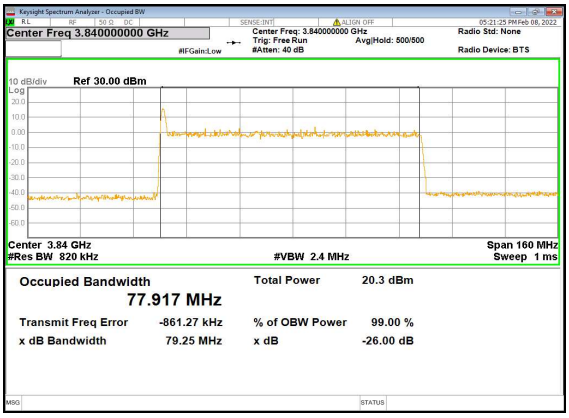
n77(80M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



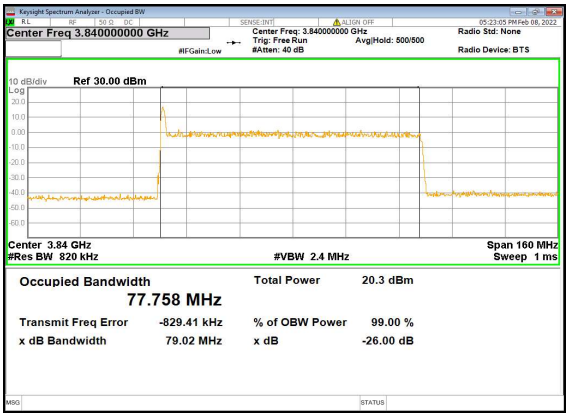
n77(80M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



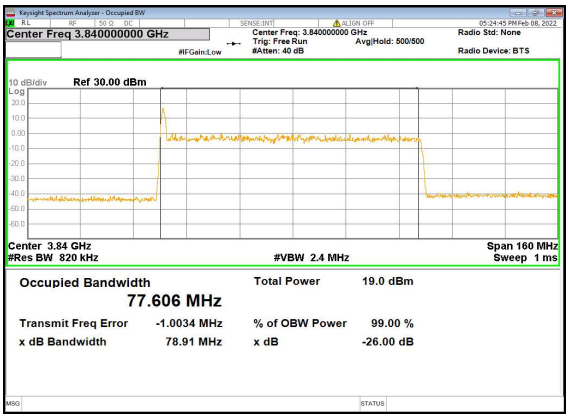
n77(80M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



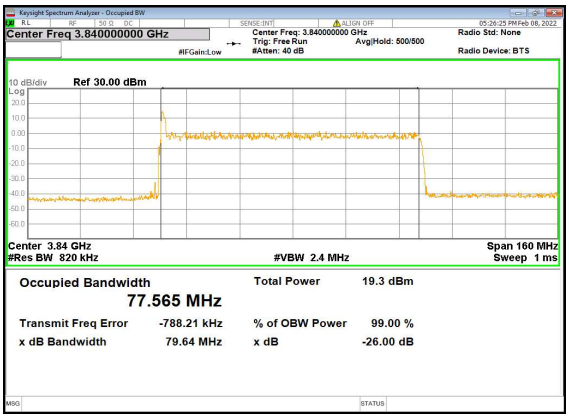
n77(80M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



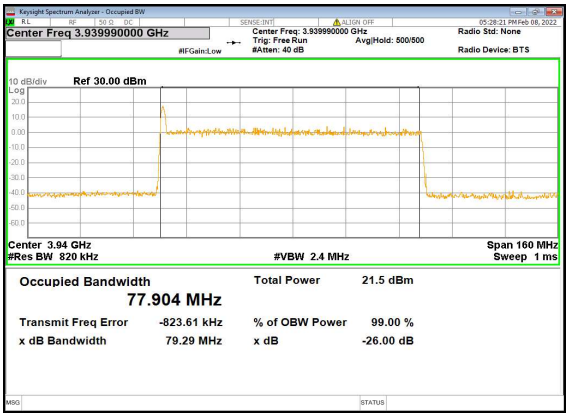
n77(80M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



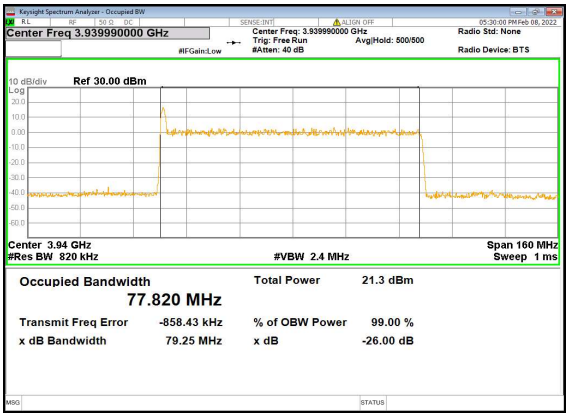
n77(80M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



n77(80M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH

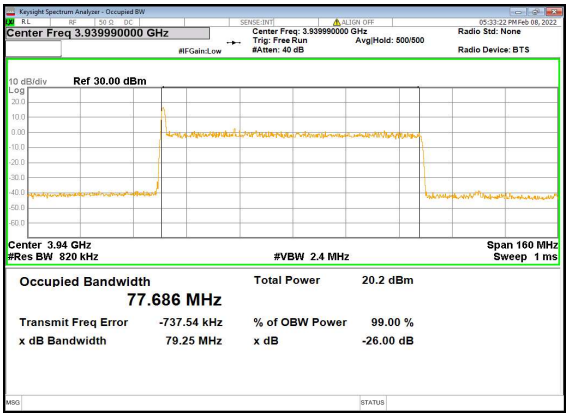


n77(80M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH

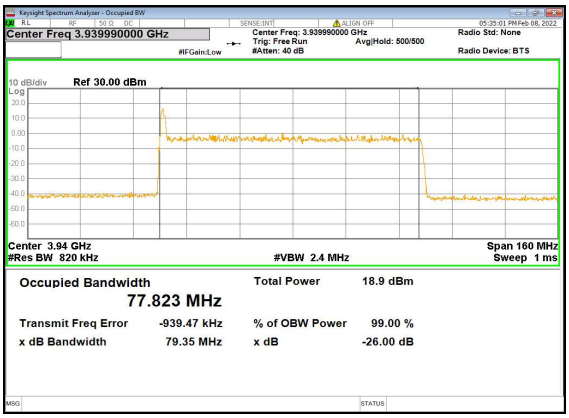


n77(80M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH

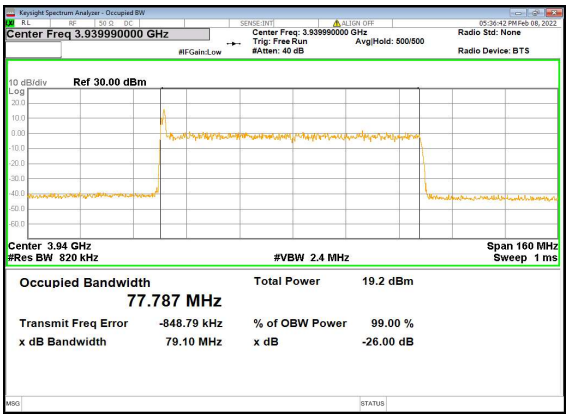
n77(80M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH



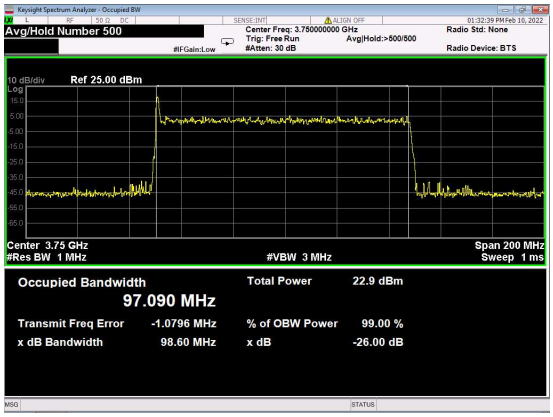
n77(80M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



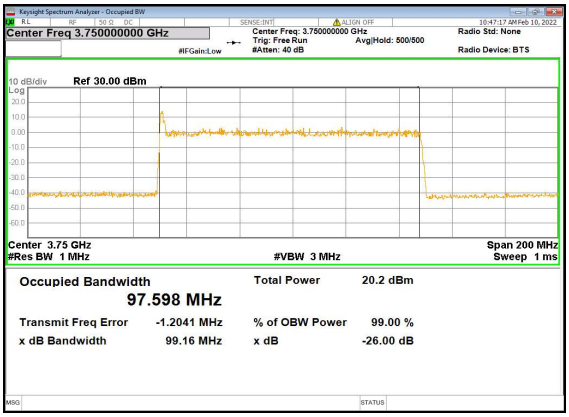
n77(80M)_CP-OFDM_QPSK_Outer_Full_High_CH



n77(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH

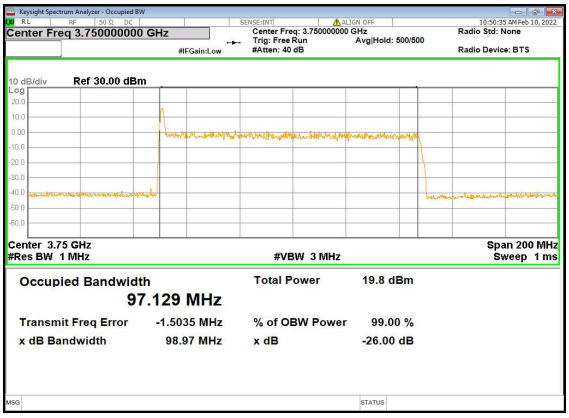


n77(100M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH

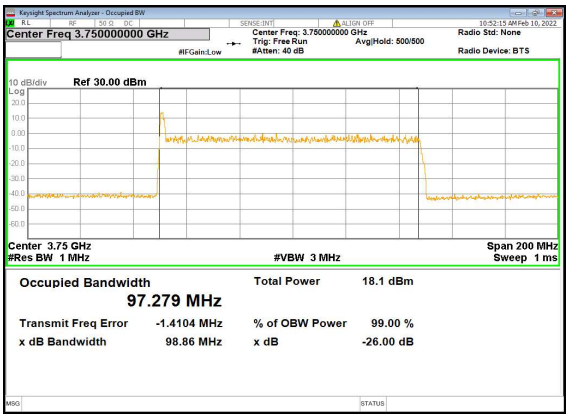
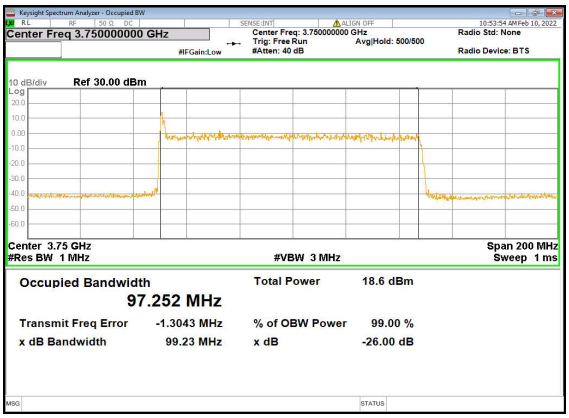


n77(100M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH

n77(100M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



n77(100M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



n77(100M)_CP-OFDM_QPSK_Outer_Full_Low_CH