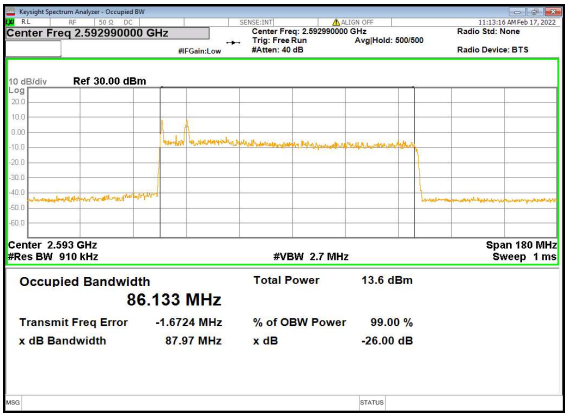
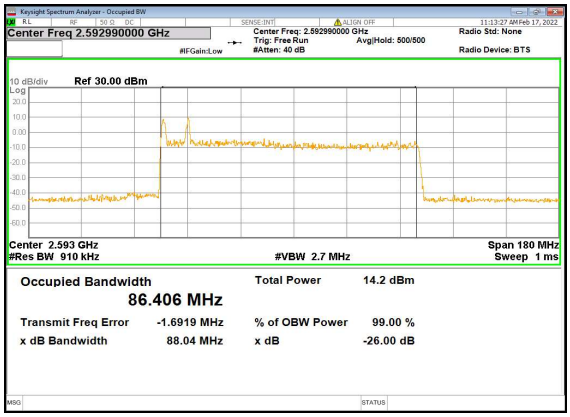
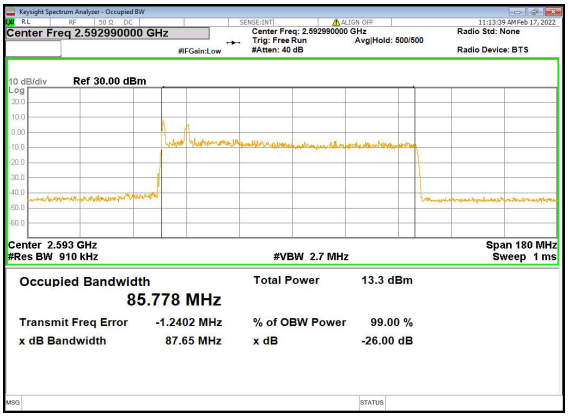
B2_n41(90M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



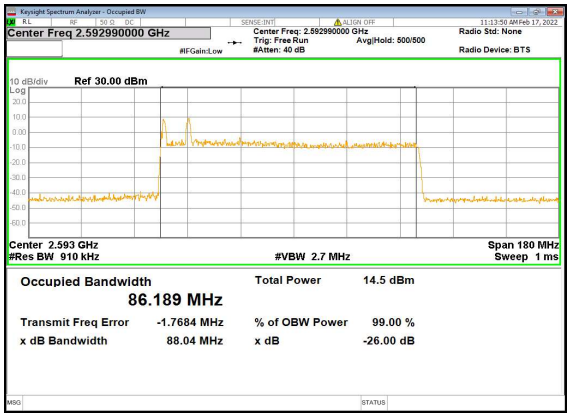
B2_n41(90M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



B2_n41(90M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH

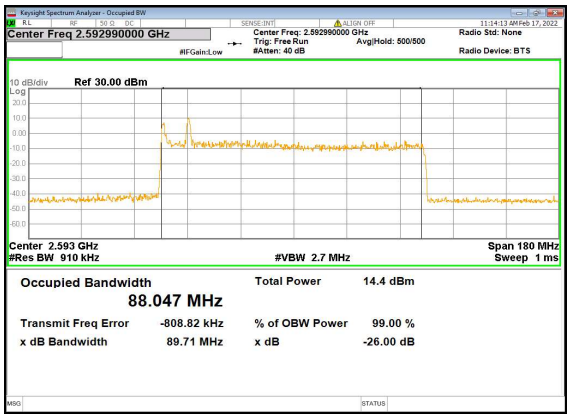


B2_n41(90M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH

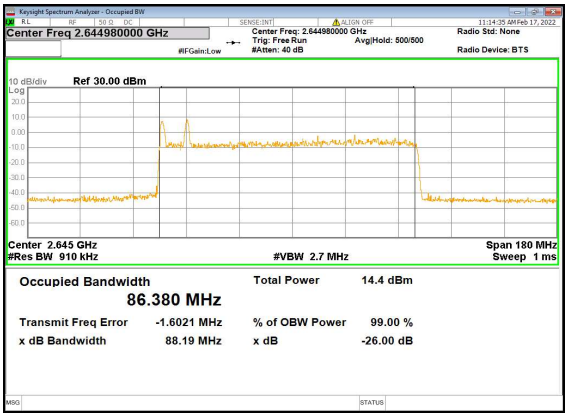


B2_n41(90M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH

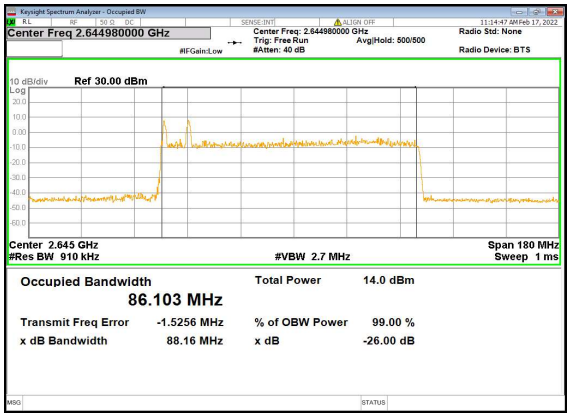
B2_n41(90M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



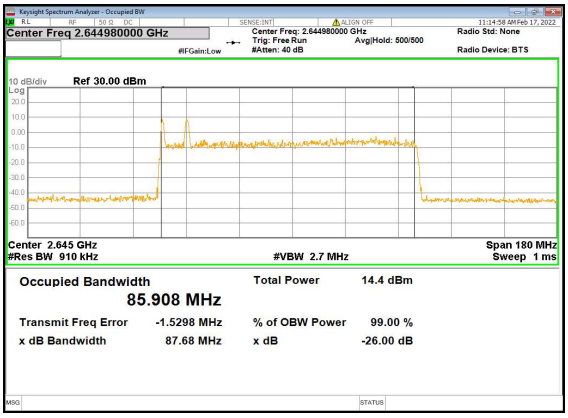
B2_n41(90M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



B2_n41(90M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



B2_n41(90M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH



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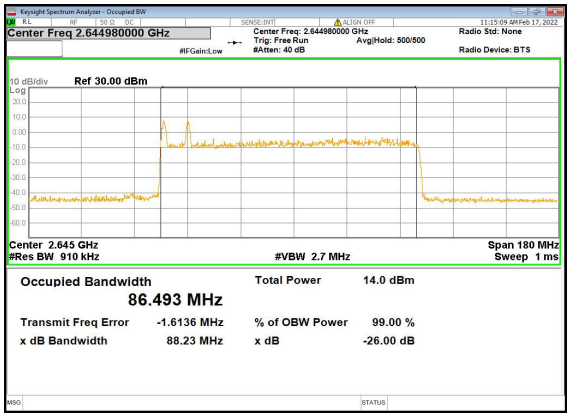
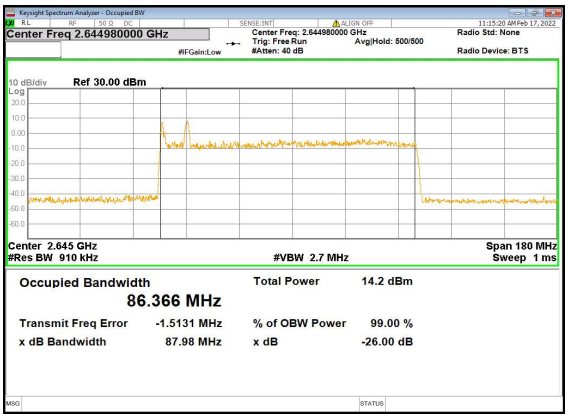
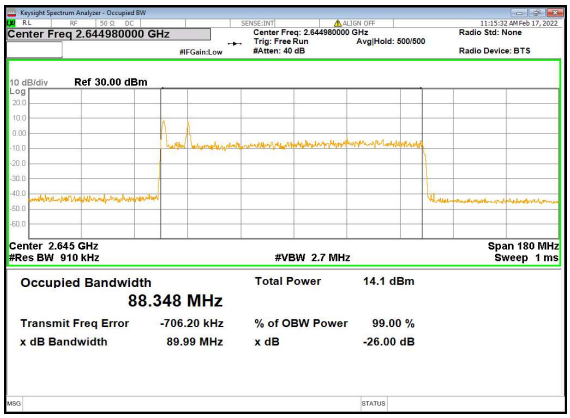


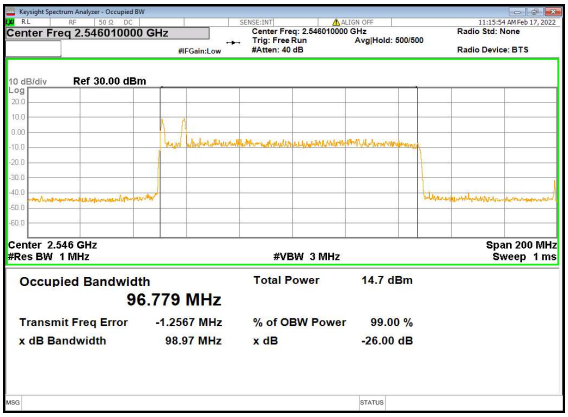
B2_n41(90M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



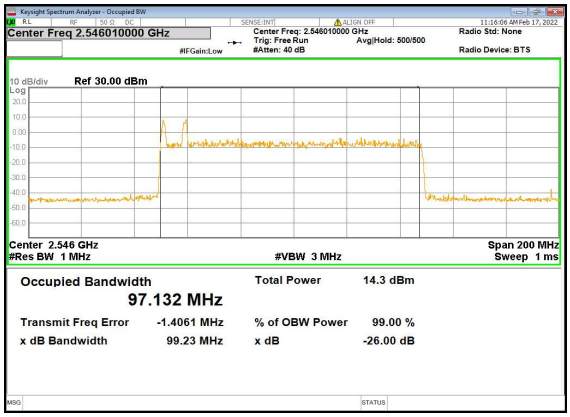
B2_n41(90M)_CP-OFDM_QPSK_Outer_Full_High_CH



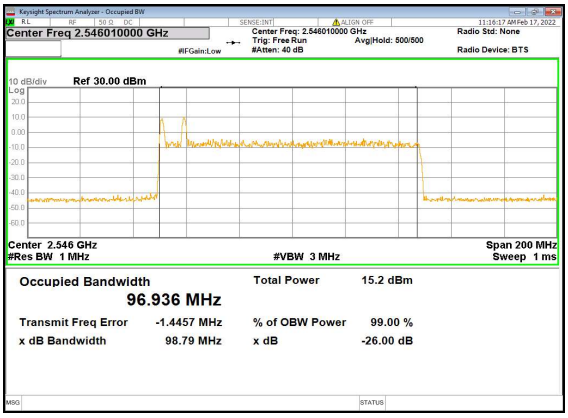
B2_n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



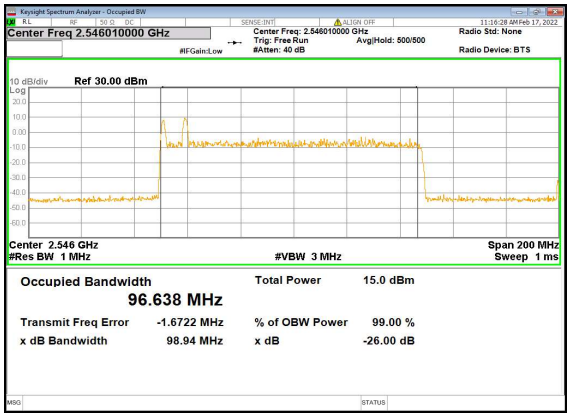
B2_n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



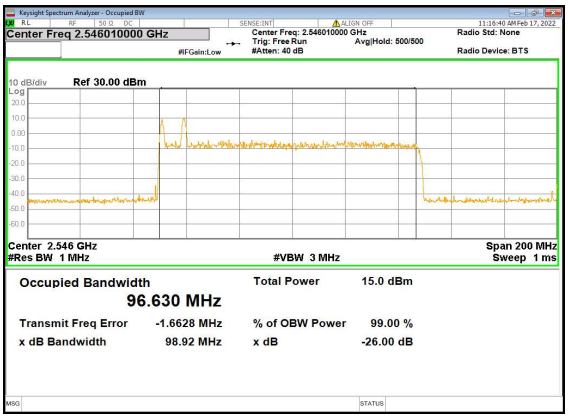
B2_n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_Low_CH



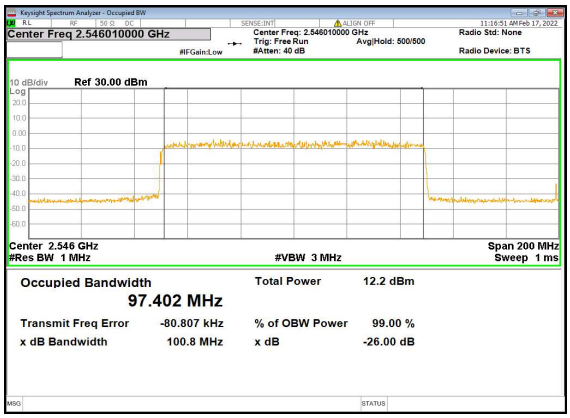
B2_n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_Low_CH



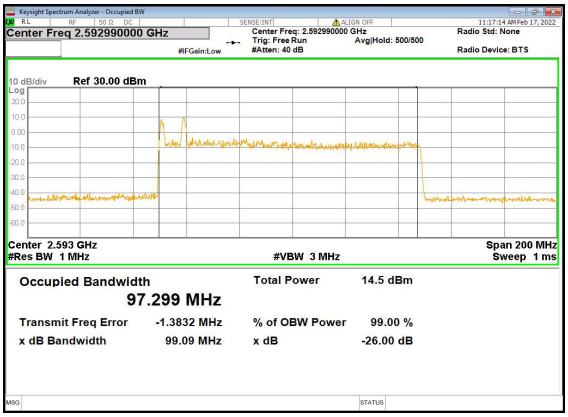
B2_n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_Low_CH



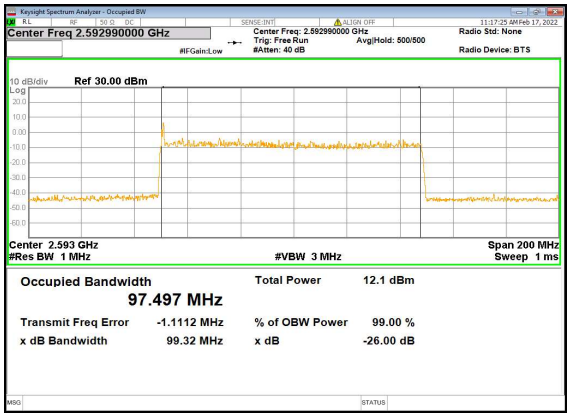
B2_n41(100M)_CP-OFDM_QPSK_Outer_Full_Low_CH



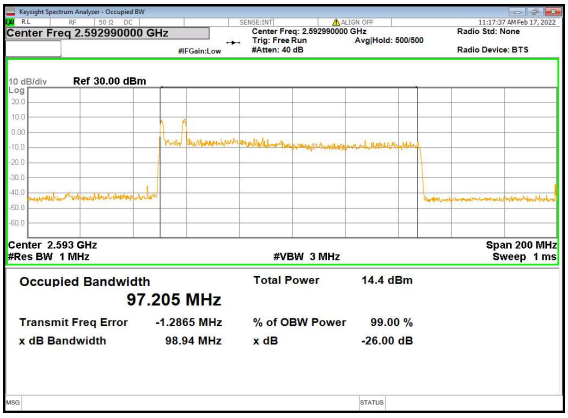
B2_n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



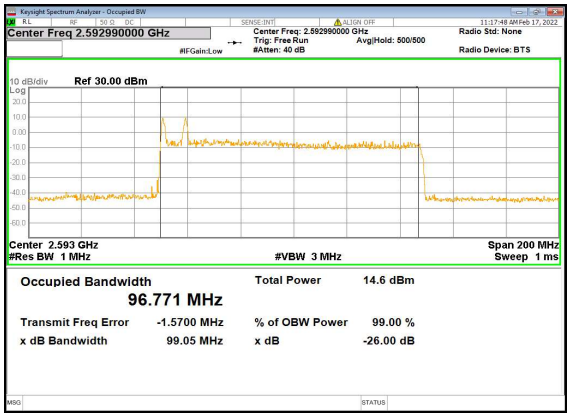
B2_n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



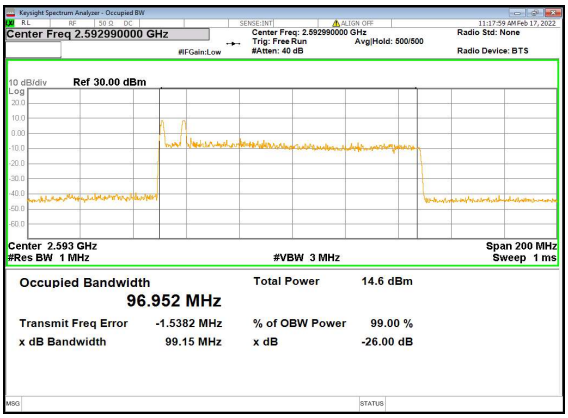
B2_n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_Mid_CH



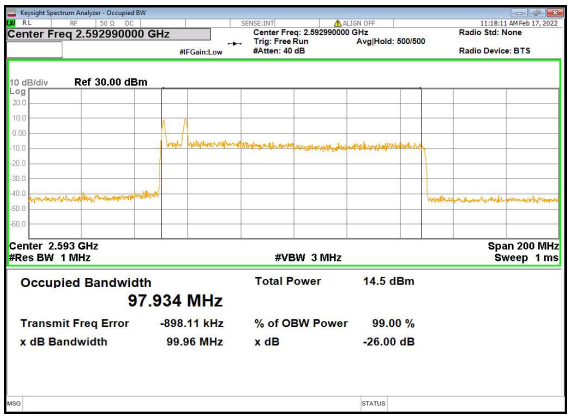
B2_n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_Mid_CH



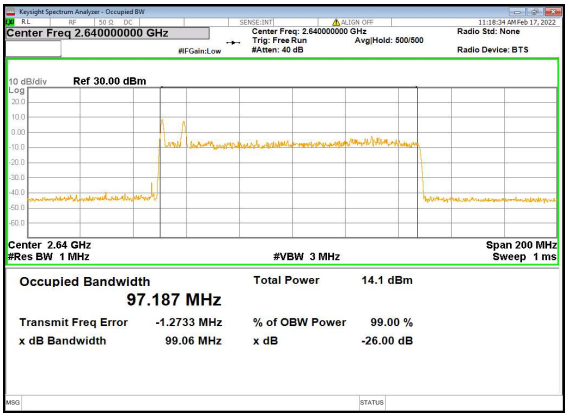
B2_n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_Mid_CH



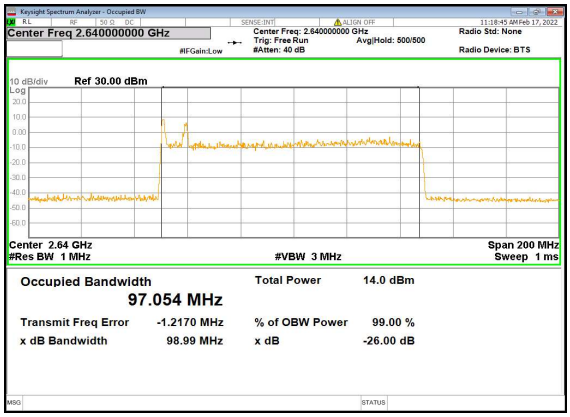
B2_n41(100M)_CP-OFDM_QPSK_Outer_Full_Mid_CH



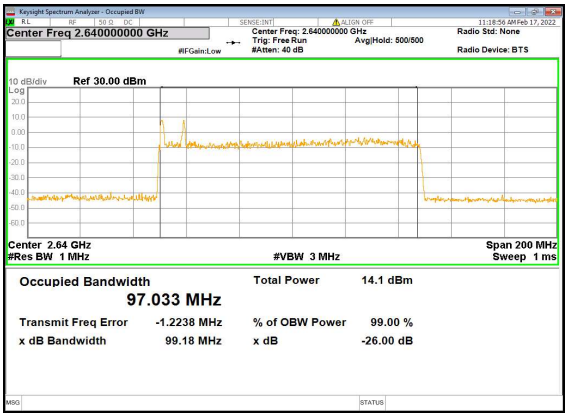
B2_n41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



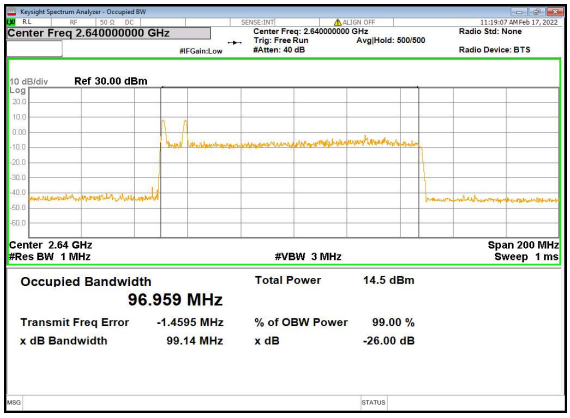
B2_n41(100M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



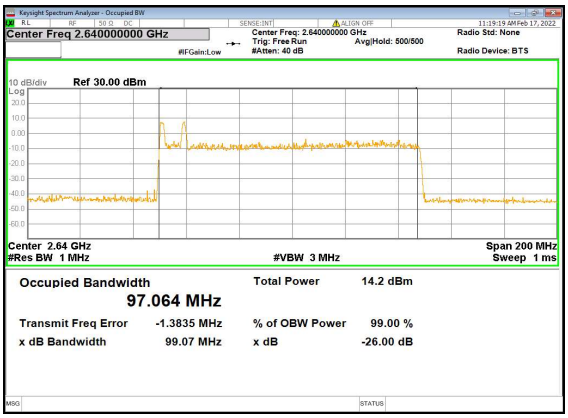
B2_n41(100M)_DFT-s-OFDM_16QAM_Outer_Full_High_CH



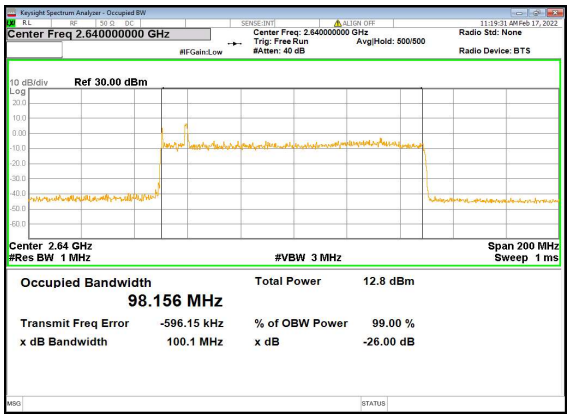
B2_n41(100M)_DFT-s-OFDM_64QAM_Outer_Full_High_CH



B2_n41(100M)_DFT-s-OFDM_256QAM_Outer_Full_High_CH



B2_n41(100M)_CP-OFDM_QPSK_Outer_Full_High_CH



2.3. Frequency Stability

2.3.1. Requirement

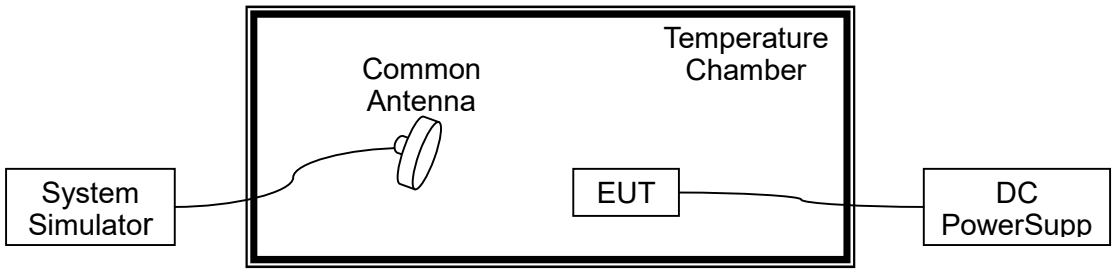
According to FCC section 2.1055, the frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. According to FCC section 2.1055, the test conditions are:

(a) The temperature is varied from -30°C to +50°C at intervals of not more than 10°C.

(b) For hand carried battery powered equipment, the primary supply voltage is reduced to the battery operating end point which shall be specified by the manufacture. The supply voltage shall be measured at the input to the cable normally provided with the equipment, or at the power supply terminals if cables are not normally provided.

Note: The operating temperature of EUT is from 0°C to 40°C, which are specified by the applicant.

2.3.2. Test Description



The EUT which is powered by the DC Power Supply directly, is located in the Temperature Chamber. The EUT is commanded by the System Simulator (SS) to operate at the maximum output power. A call is established between the EUT and the SS via a Common Antenna.

2.3.3. Test procedure

KDB 971168 D01v03 Section 9.0 and ANSI/TIA-603-E-2016.

2.3.4. Test Result

The nominal, highest and lowest extreme voltages are separately 7.78VDC, 8.96VDC and 6.3VDC, which are specified by the applicant; the normal temperature here used is 20°C.

NR n41, QPSK, Channel 518598, SCS 30kHz, Frequency 2593MHz Limit =±2.5ppm					
Voltage (%)	**Power (VDC)**	**Temp (°C)**	**Fre. Dev. (Hz)**	**Deviation (ppm)**	**Result**
Normal	7.78	+20(Ref)	18	0.007	PASS
Normal		0	-22	-0.008	
Normal		+10	23	0.009	
Normal		+20	14	0.005	
Normal		+30	40	0.015	
Normal		+40	-13	-0.005	
High	8.96	+20	-24	-0.009	
BATT.ENDPOINT	6.30	+20	26	0.010	

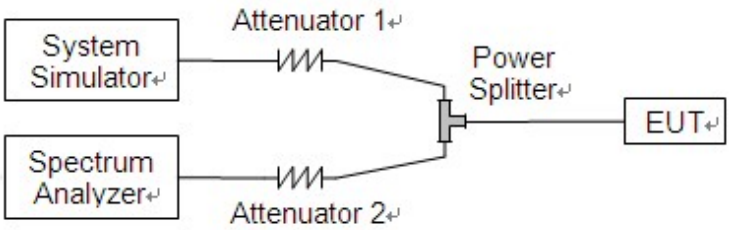
2.4. Conducted Spurious Emissions

2.4.1. Requirement

According to FCC section 2.1051,the power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least 43+10*log(P)dB. This calculated to be -13dBm.

According to FCC section 27.53(m)(4) for n41,The power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least 55 + 10 log(P) dB. This calculated to be -25dBm.

2.4.2. Test Description



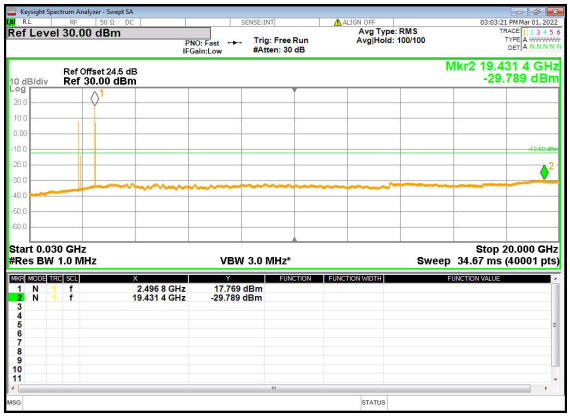
The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

2.4.3. Test procedure

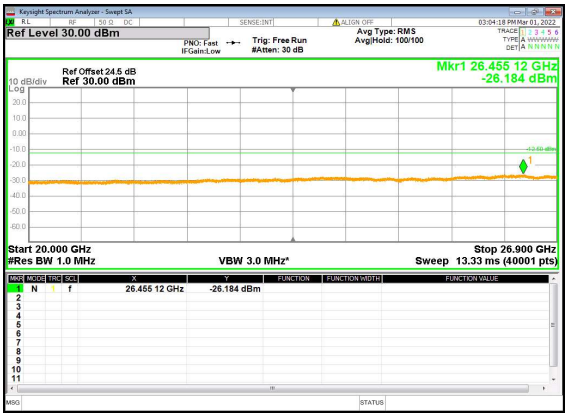
KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.

2.4.4. Test Result

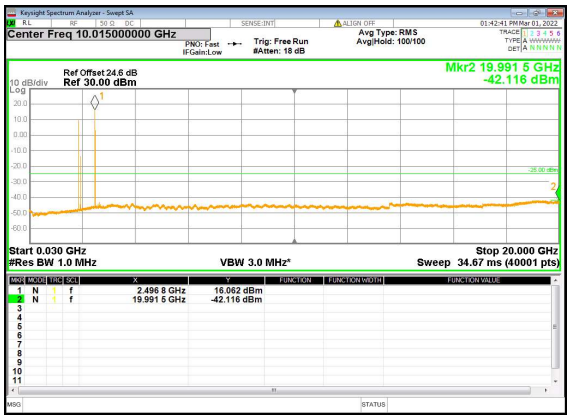
B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



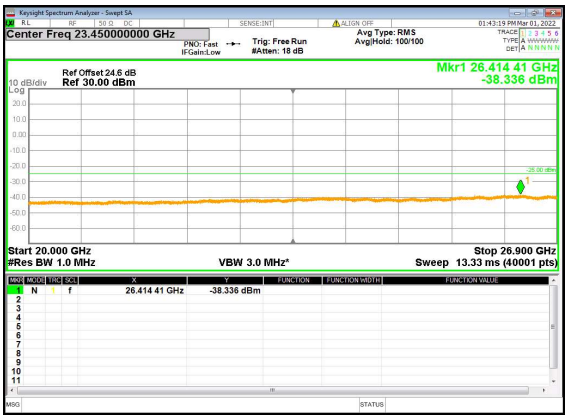
B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Low_CH



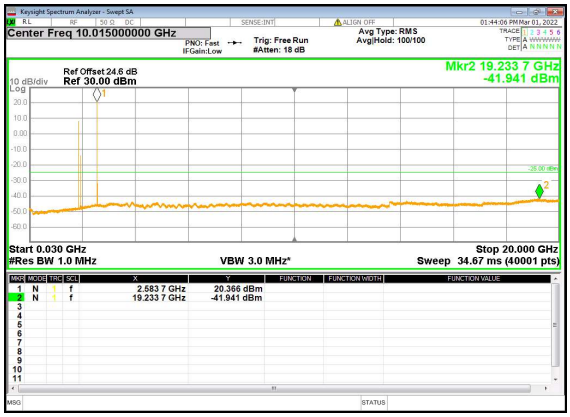
B2_n41(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



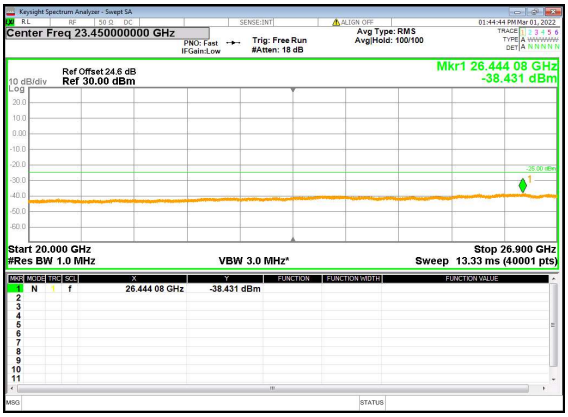
B2_n41(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Low_CH



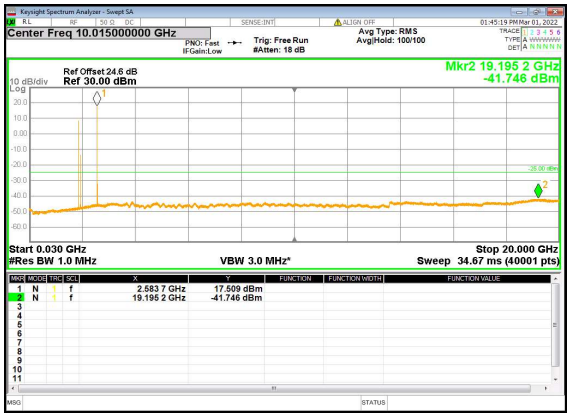
B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



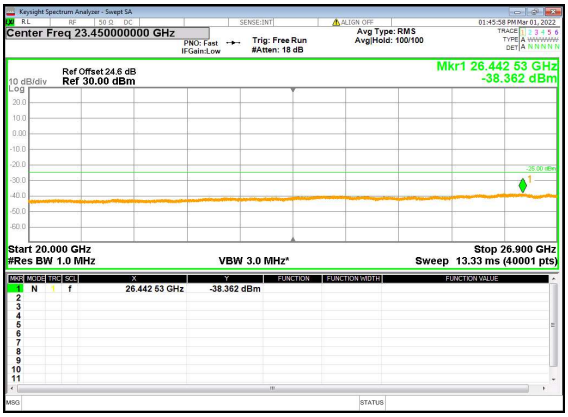
B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_Mid_CH



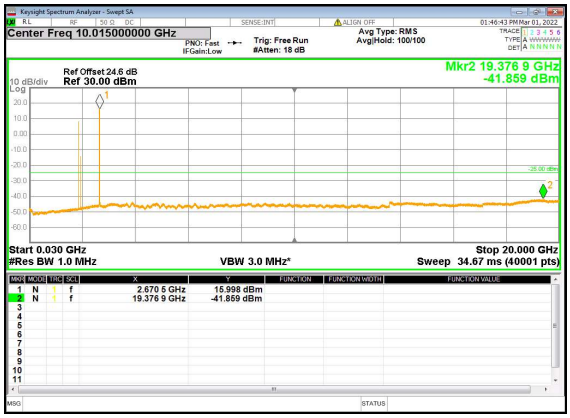
B2_n41(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



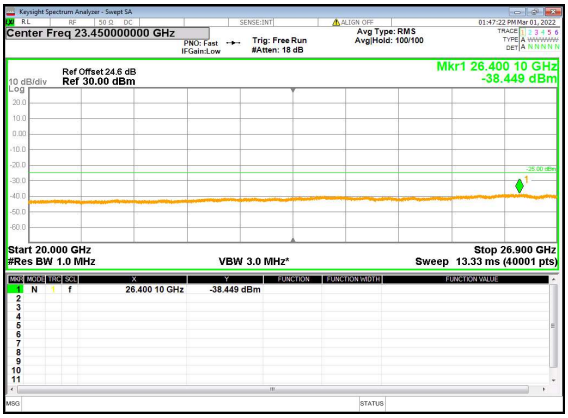
B2_n41(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_Mid_CH



B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



B2_n41(20M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_High_CH



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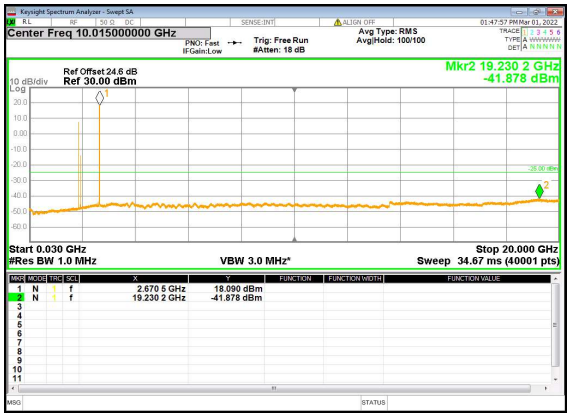
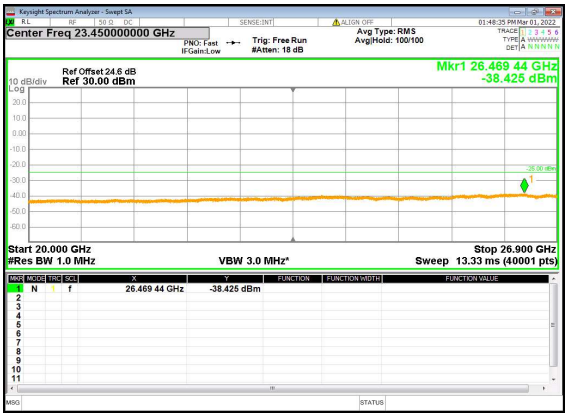
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