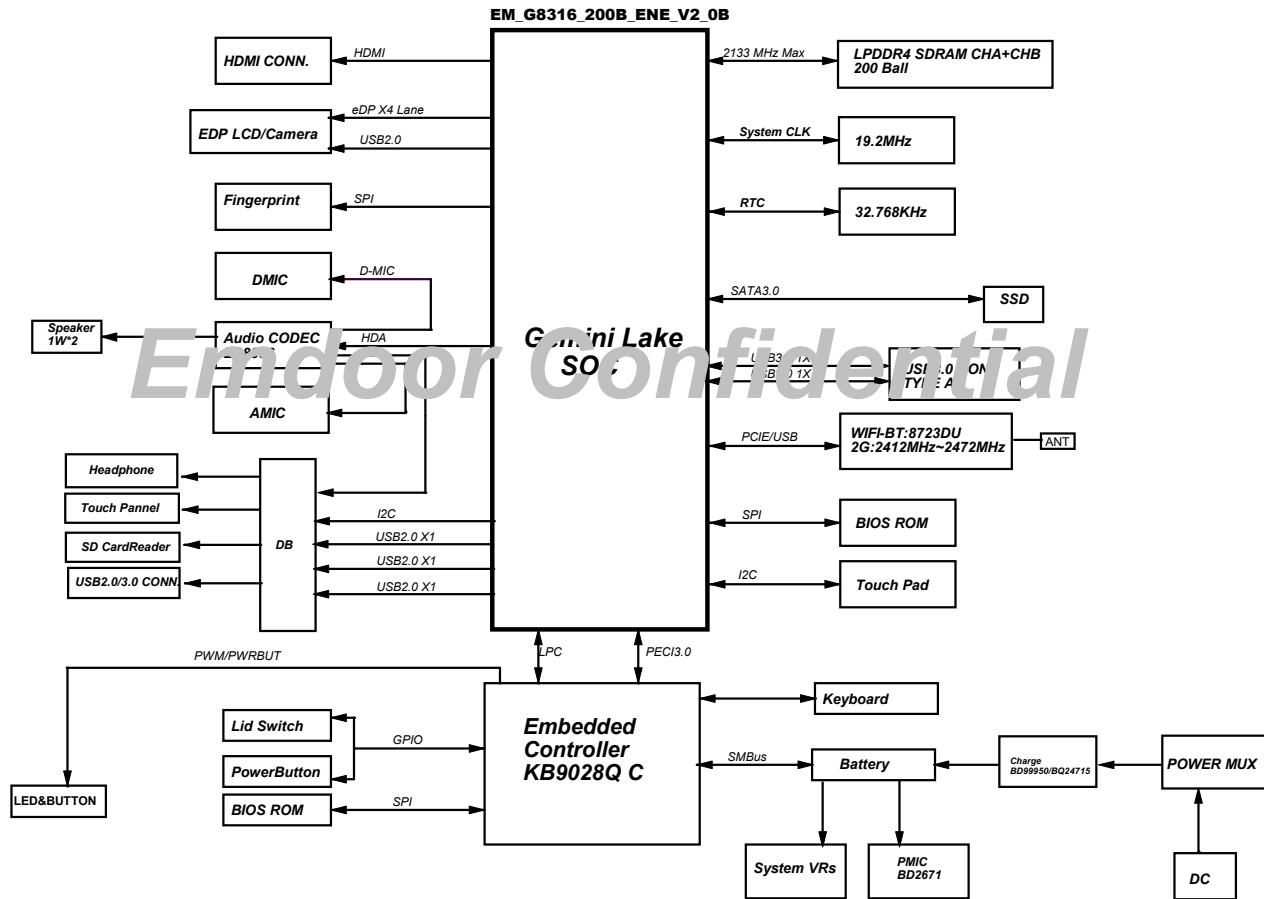


Block Diagram



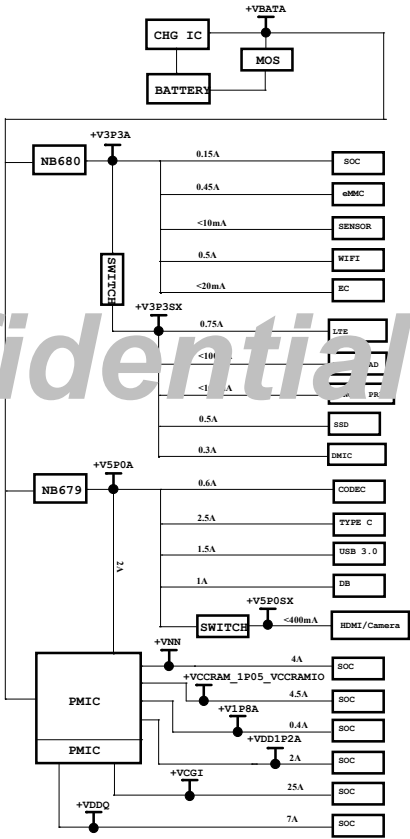
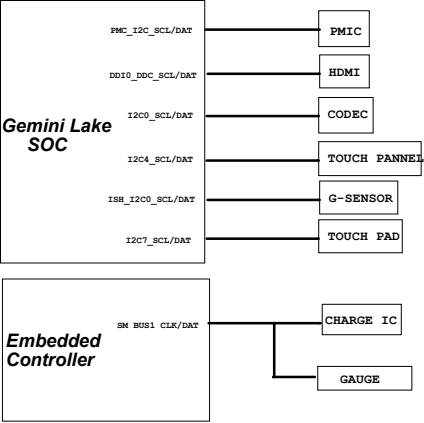
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Designer				
Reviewed				
Approved	Title:			
DO NOT REPRODUCE		Size A	SCHMATIC	Drawing No.
		Ver. 1.0		

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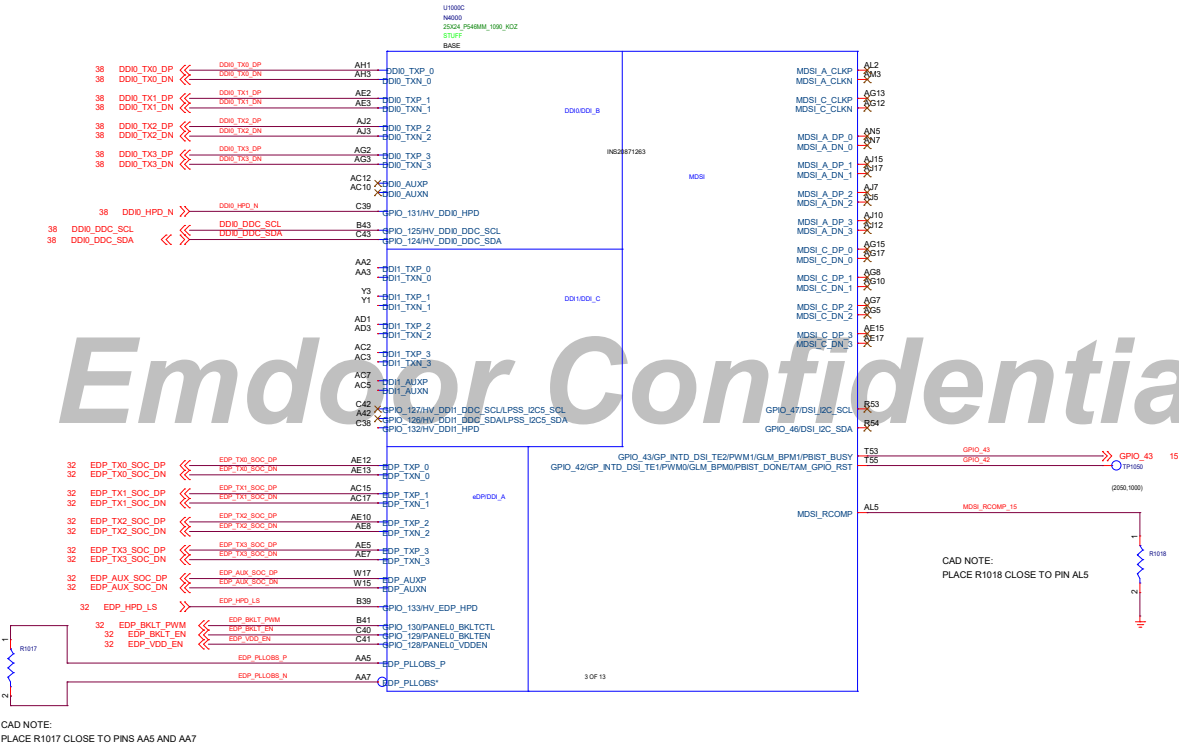
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Approved		
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	DATE: Wednesday, November 24, 2021	Sheet: 2 of 56

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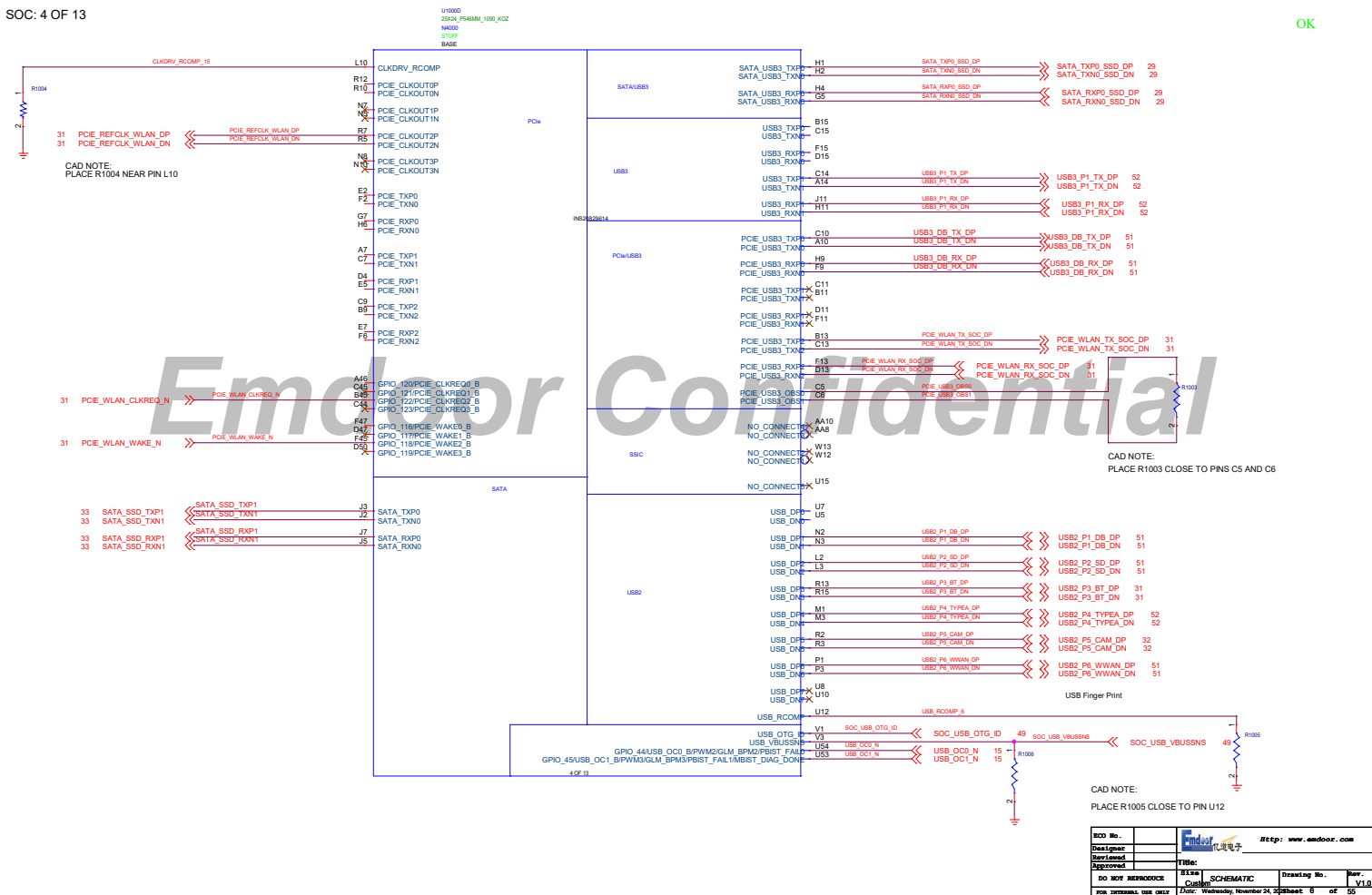
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17	M1_B_D0B182	AD17	DDR1CHDQD174_D0A174_D0B182_D0B182
17	M1_A_D0B182	BD17	DDR1CHDQD175_D0A175_D0B183_D0B183
17	M1_B_D0B184	AD17	DDR1CHDQD176_D0A176_D0B184_D0B184
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17	M1_B_D0B186	AD17	DDR1CHDQD178_D0A178_D0B186_D0B186
17	M1_A_D0B186	BD17	DDR1CHDQD179_D0A179_D0B187_D0B187
17	M1_B_D0B188	AD17	DDR1CHDQD180_D0A180_D0B188_D0B188
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17	M1_A_D0B190	BD17	DDR1CHDQD183_D0A183_D0B191_D0B191
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17	M1_B_D0B196	AD17	DDR1CHDQD188_D0A188_D0B196_D0B196
17	M1_A_D0B196	BD17	DDR1CHDQD189_D0A189_D0B197_D0B197
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17	M1_B_D0B200	AD17	DDR1CHDQD192_D0A192_D0B200_D0B200
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17	M1_B_D0B202	AD17	DDR1CHDQD194_D0A194_D0B202_D0B202
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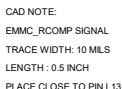


CAD NOTE:
PLACE R1017 CLOSE TO PINS AA5 AND AA7

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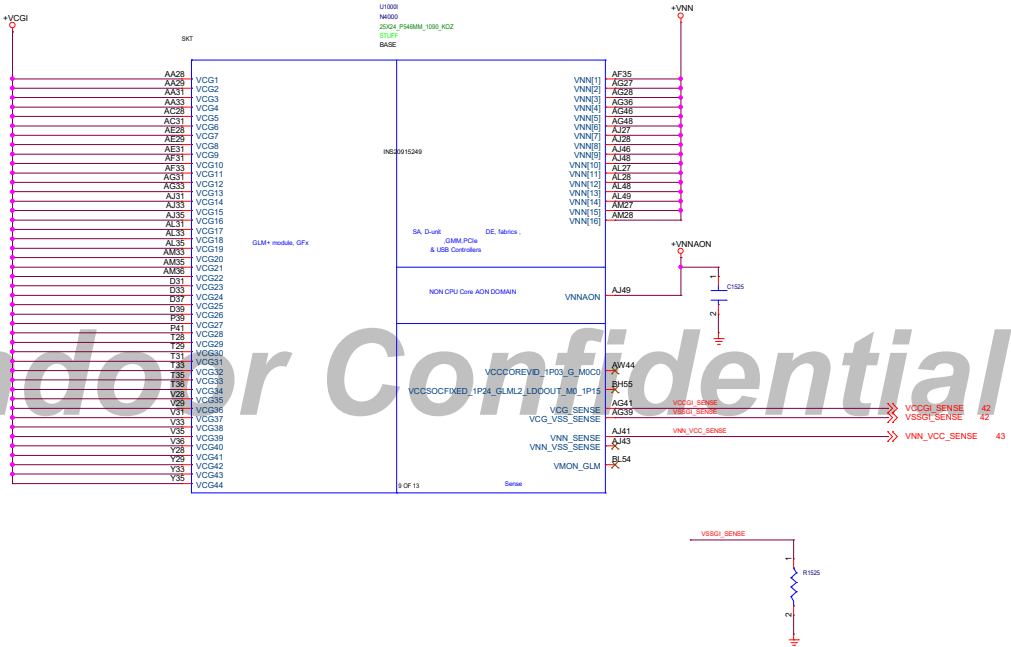


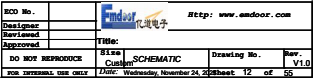


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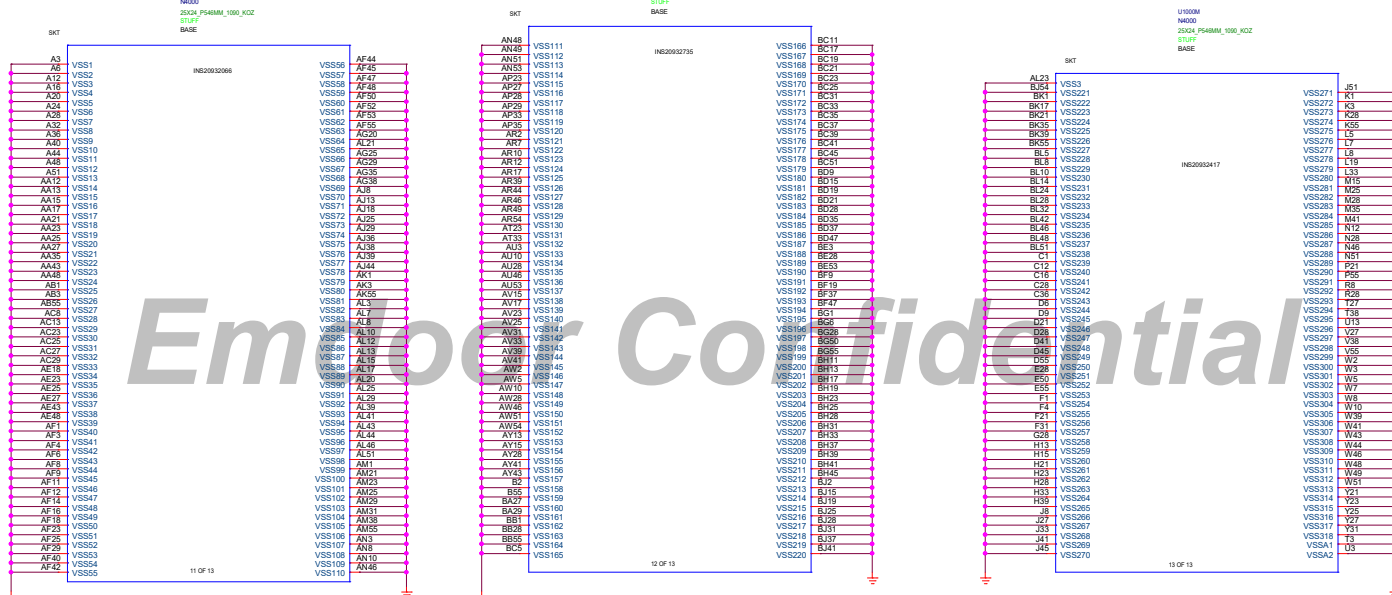


SOC: GND

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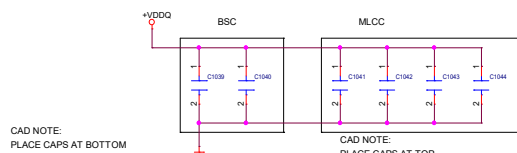
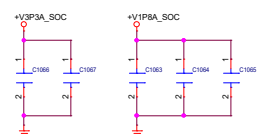
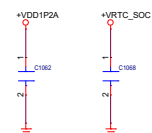
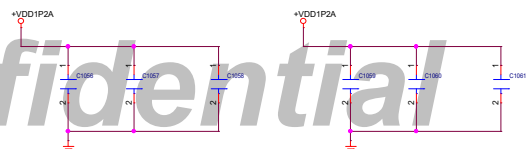
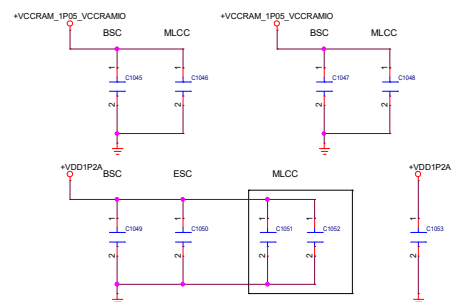
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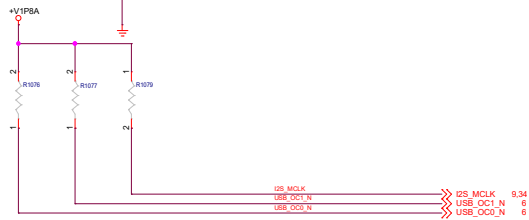
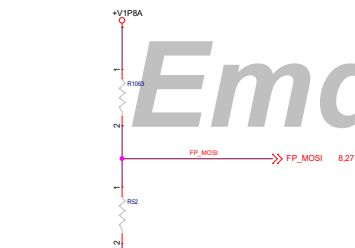
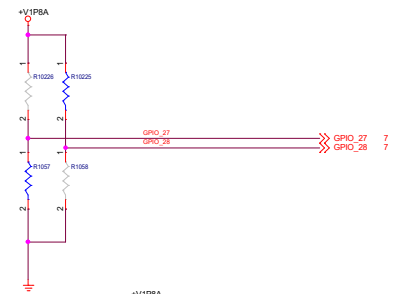
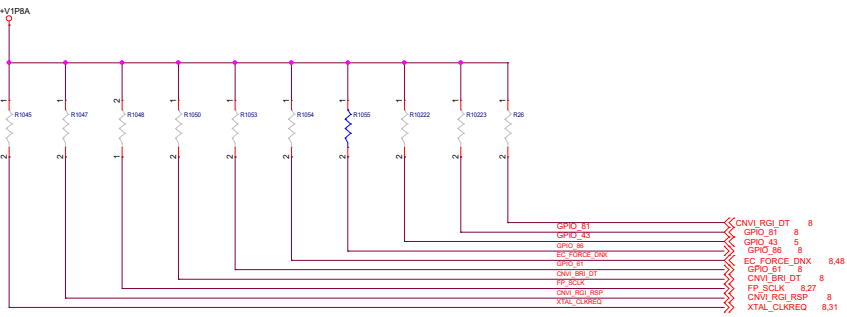


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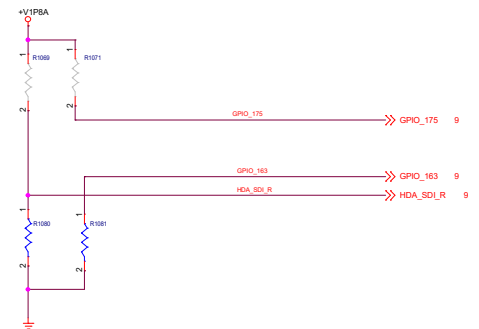
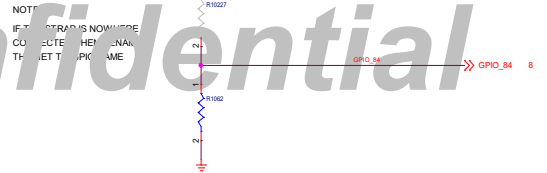
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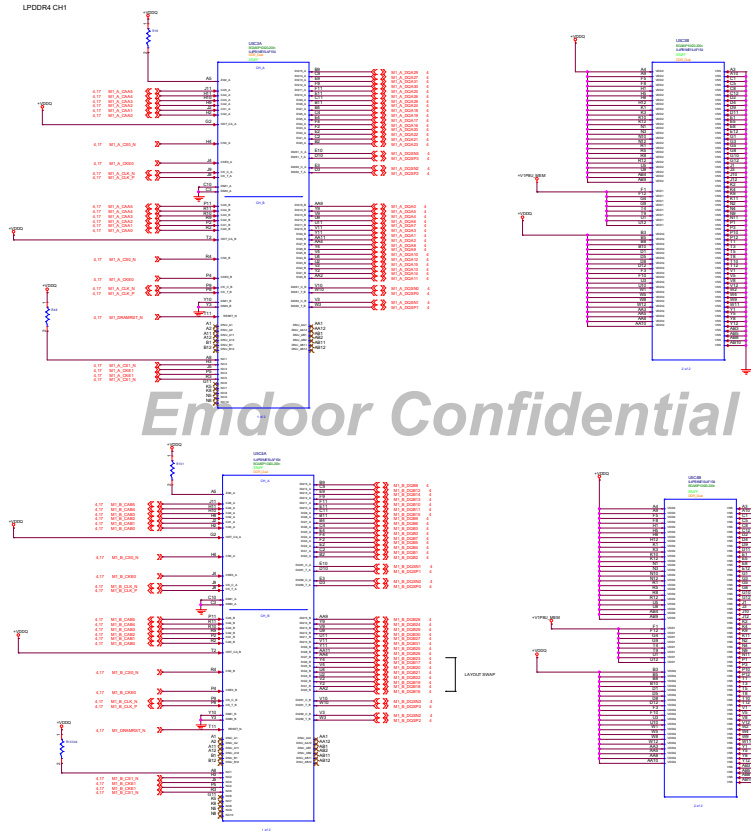
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NOTE:
AVS_I2S1_WS_SYNC, HDA_SDI HAS INTERNAL 20K PD AS PER EDS
BUT IN RVP IT IS STUFFED 1K PD. NEED CLARIFICATION



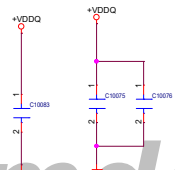
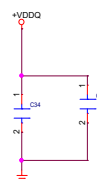
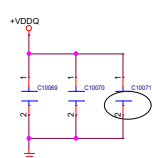
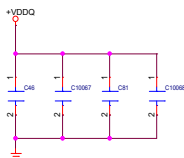
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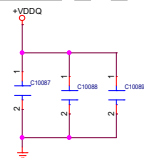
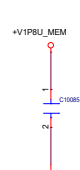
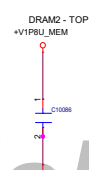
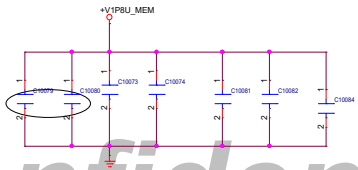
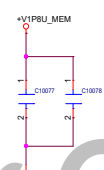
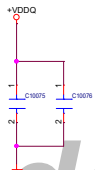
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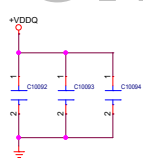
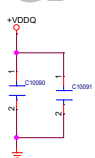
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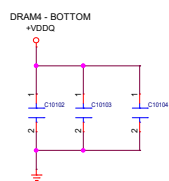
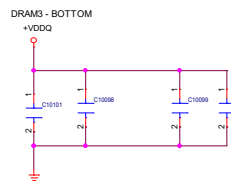
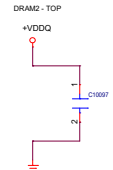
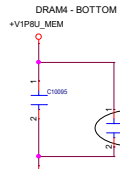
FOUR 0402 IN 1/8" HANH



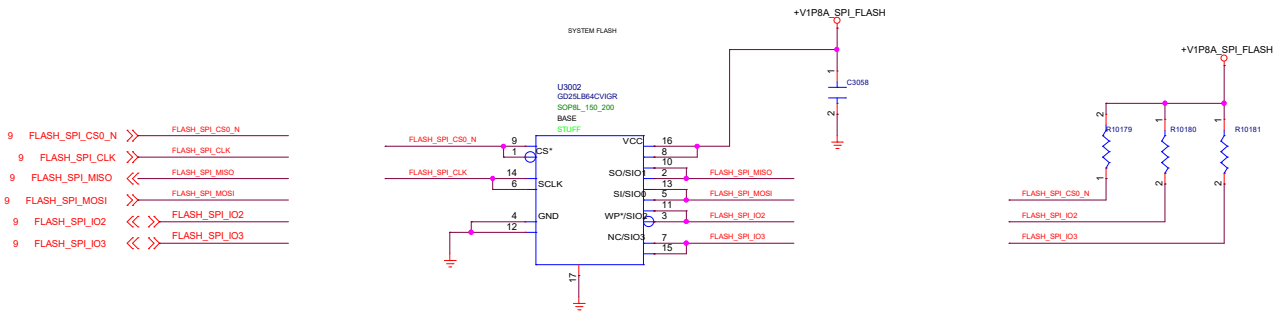
NOTE: PLACE ON TOP - 4 PER DEVICE



NOTE: PLACE ON TOP - 4 PER DEVICE

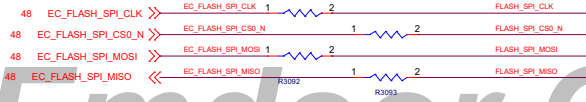


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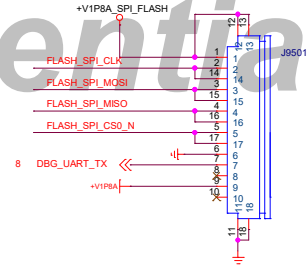
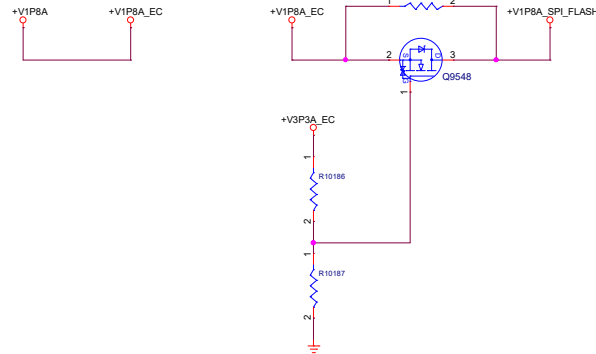


R3090

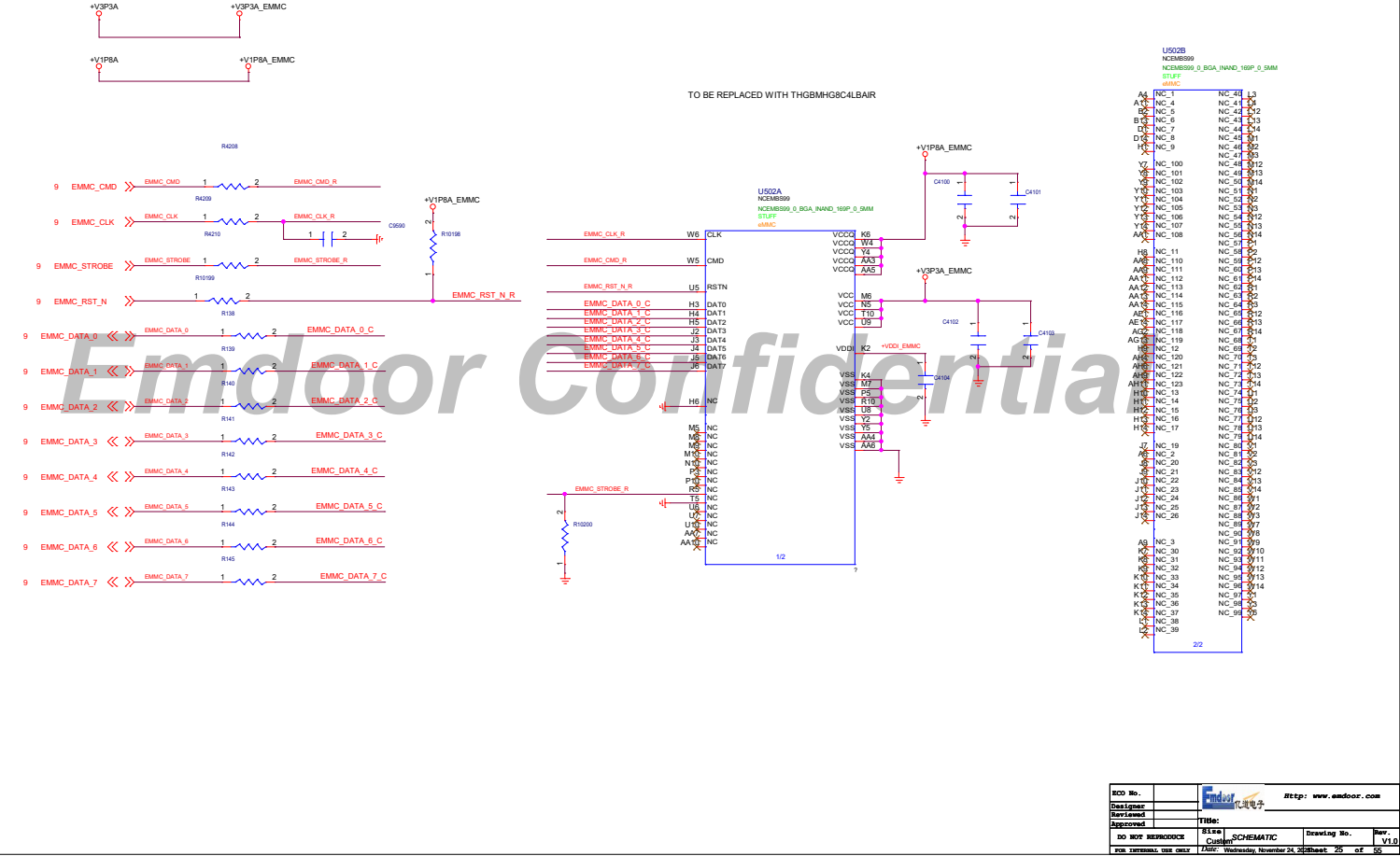
R3091



R47

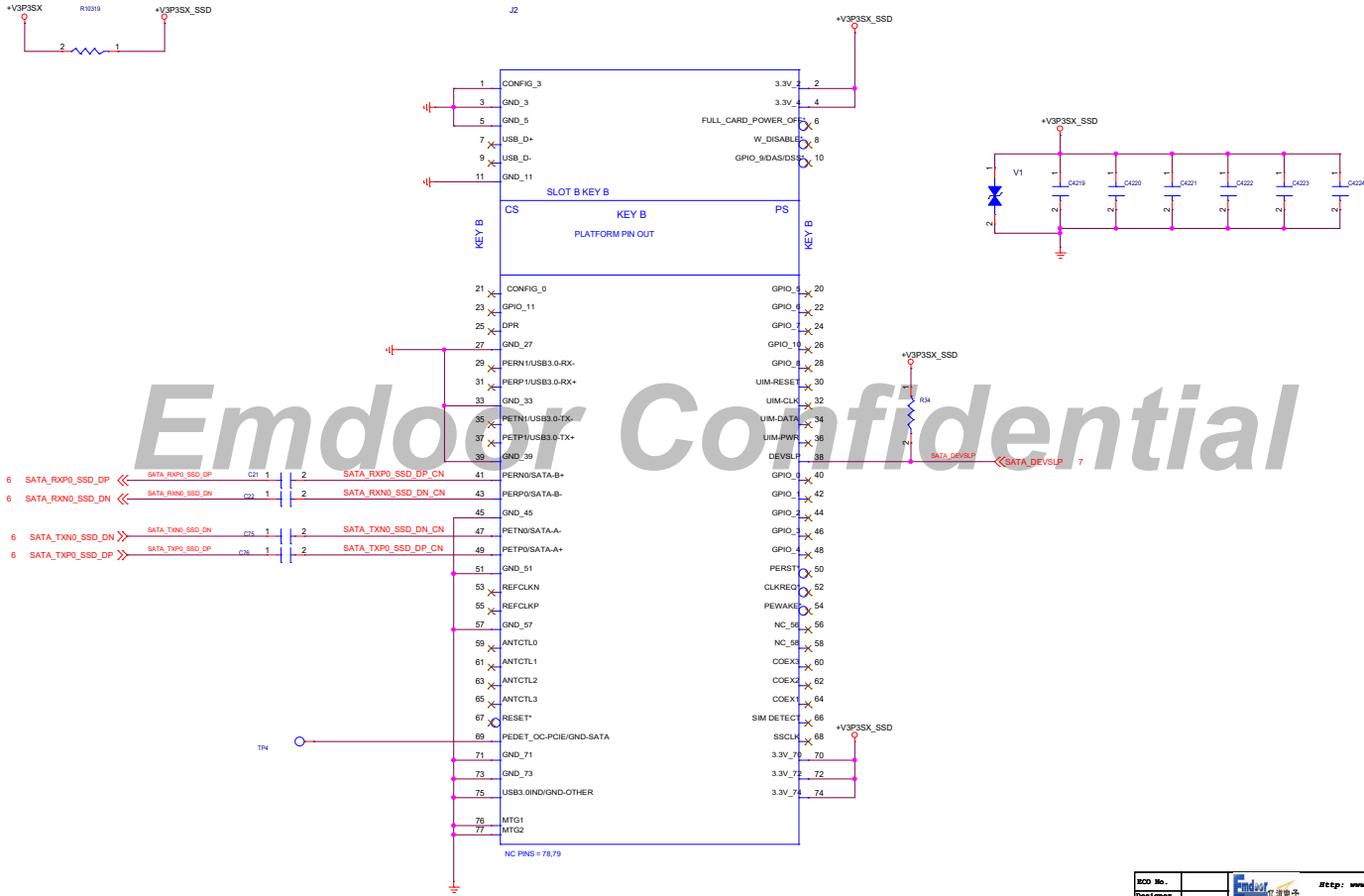


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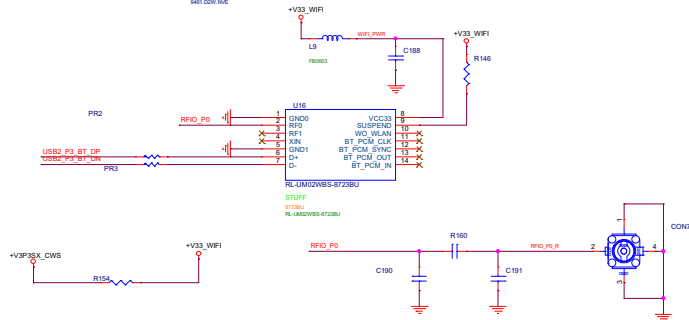
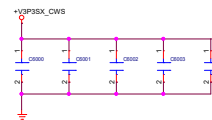
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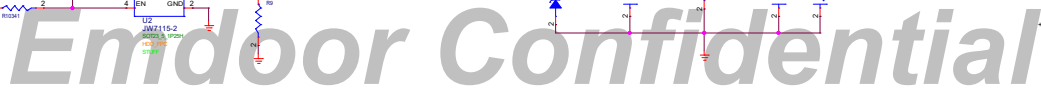


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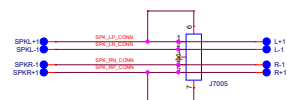
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	DATE	Wednesday, October 13, 2010	Sheet 30 of 55

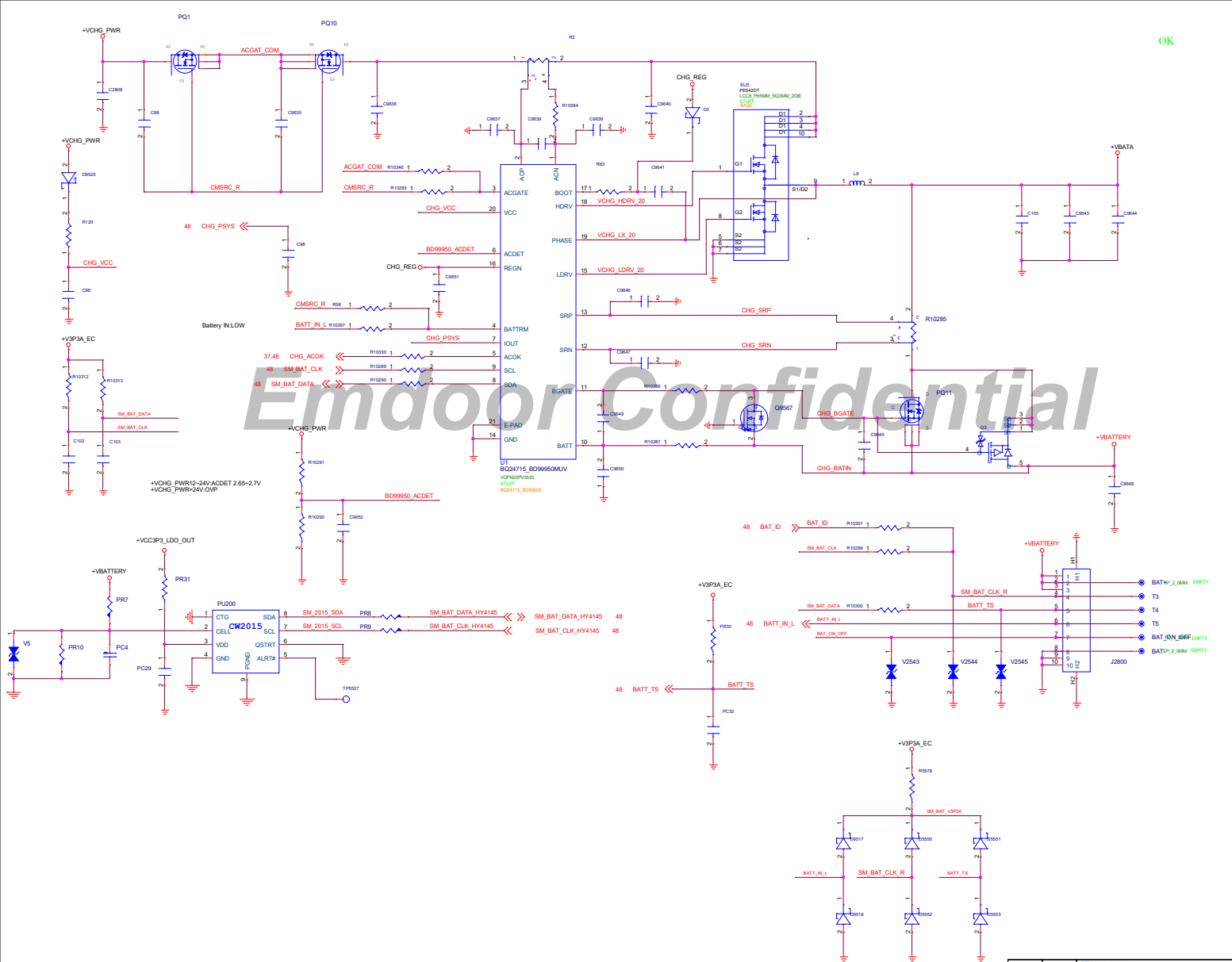


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Date: Wednesday, November 24, 2010		Sheet: 31 of 64		

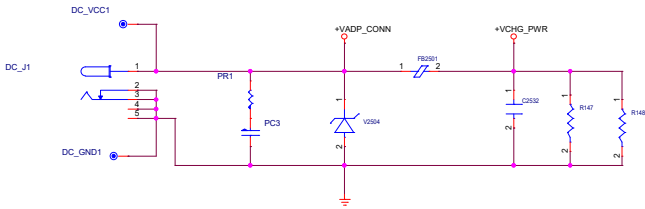


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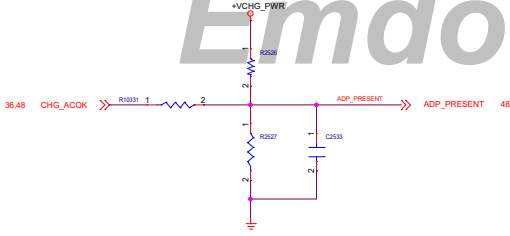


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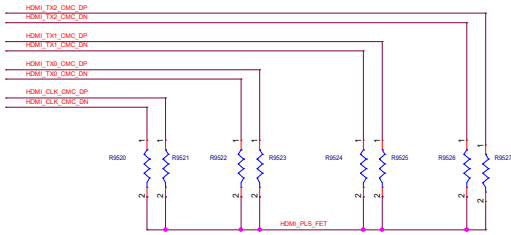
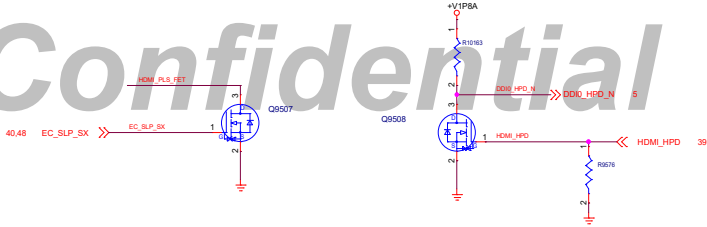
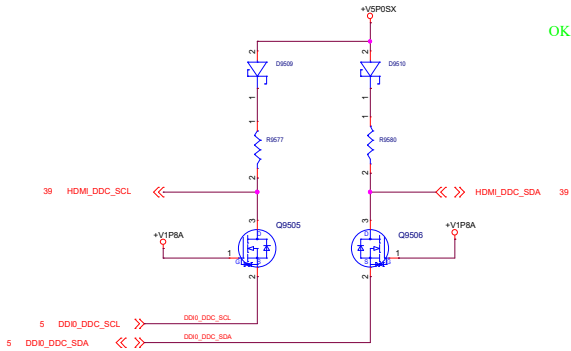
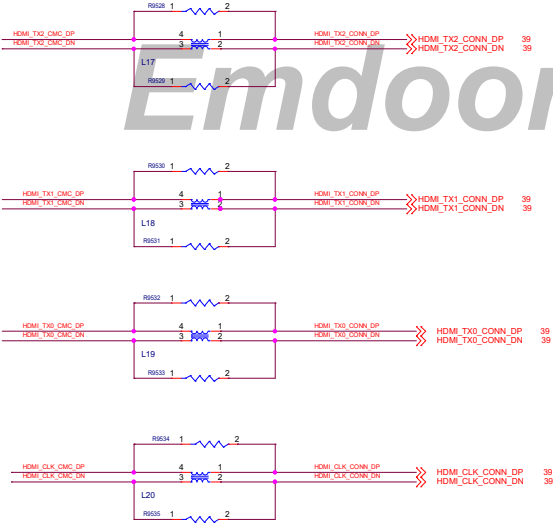
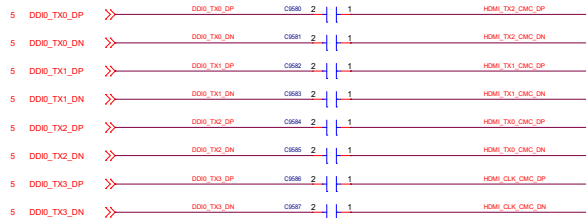


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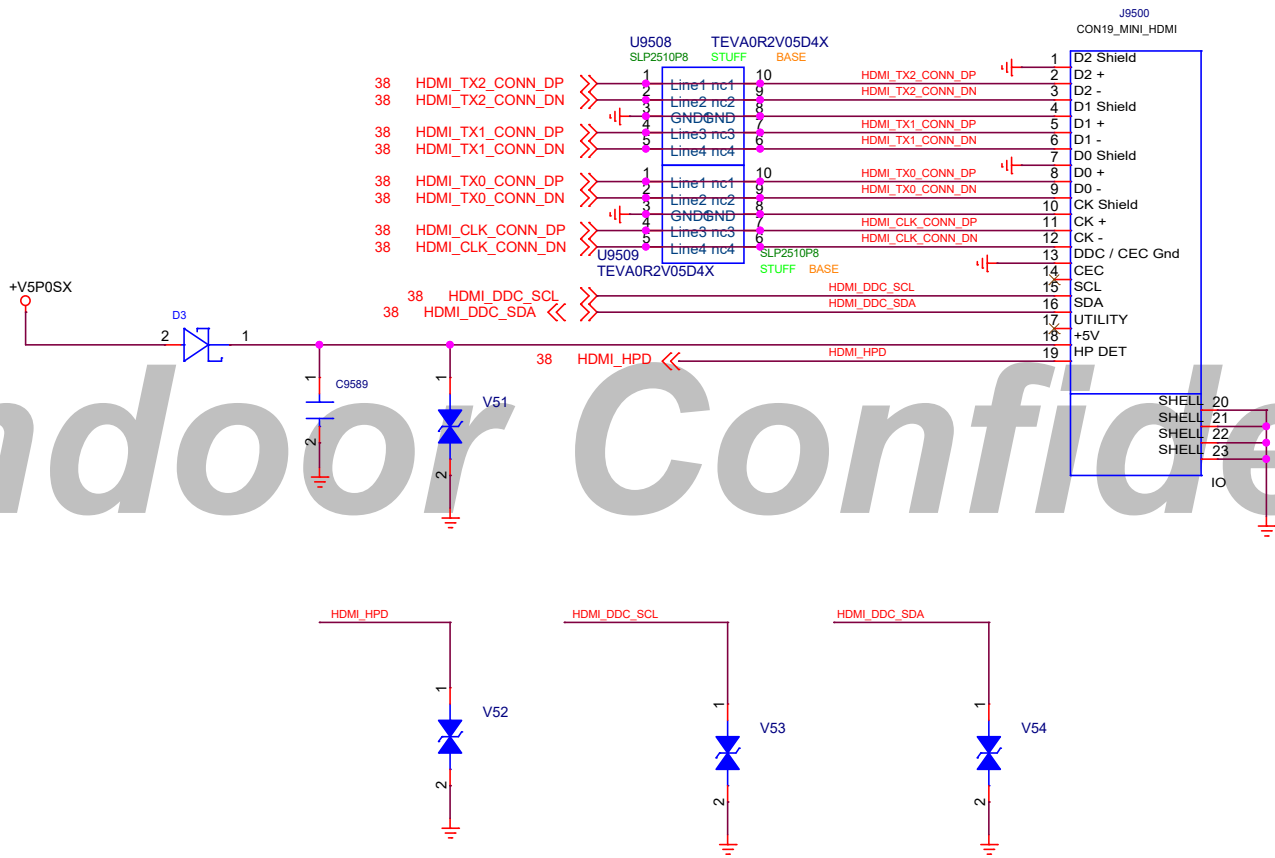
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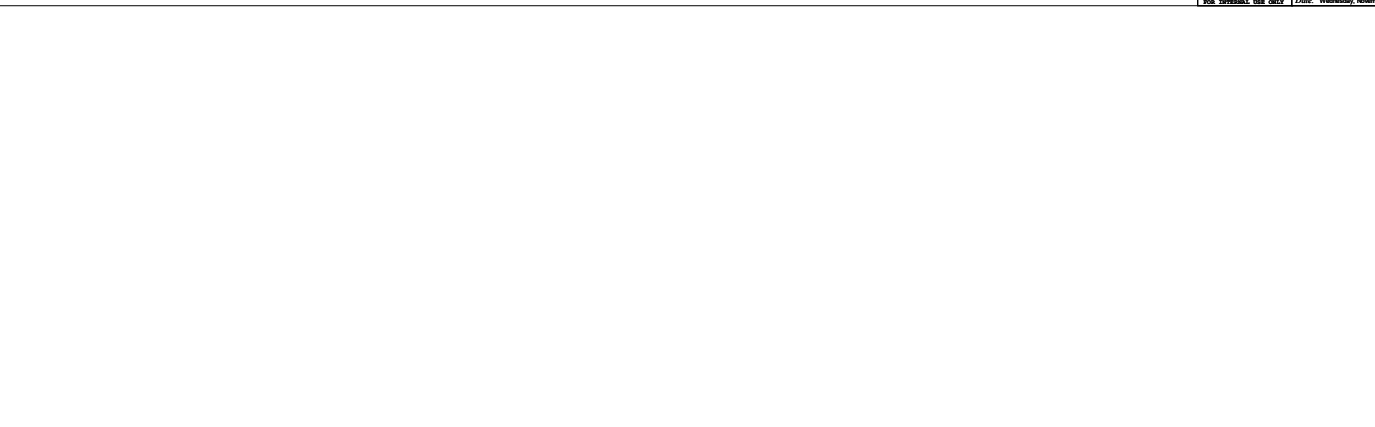
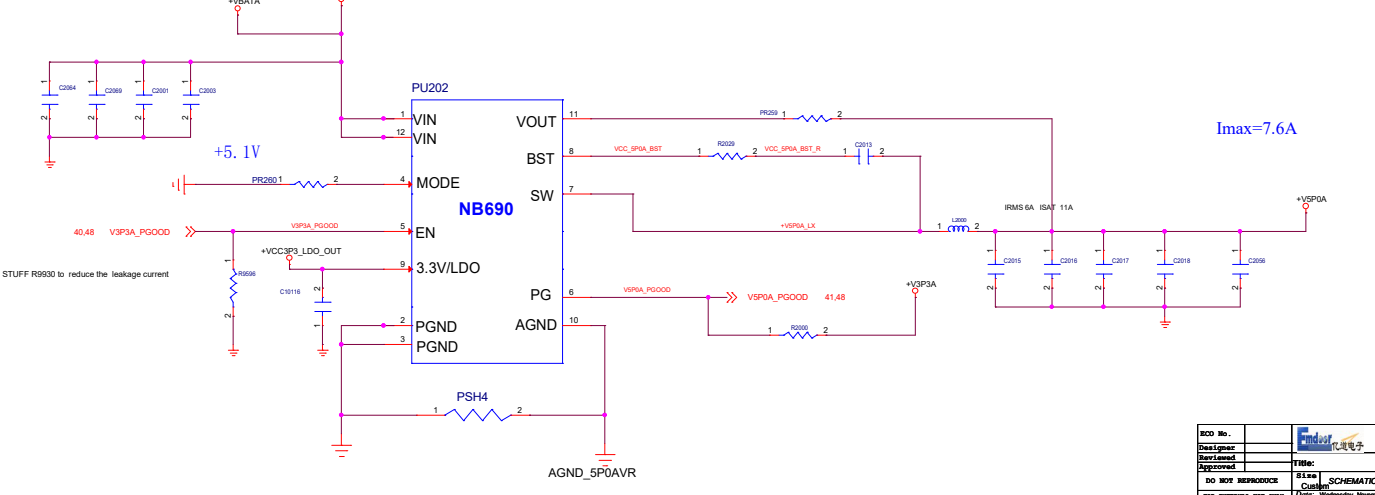
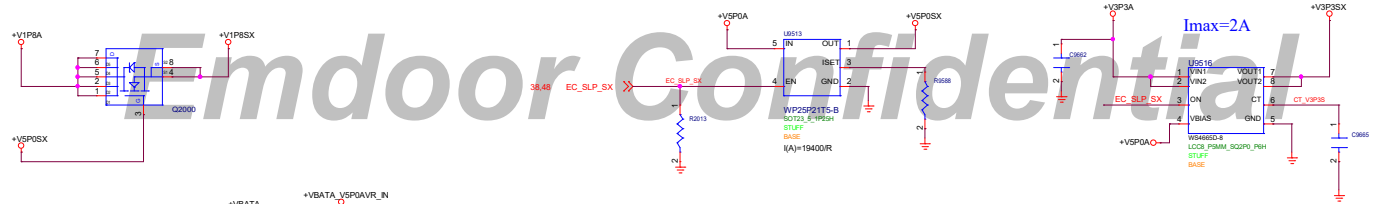
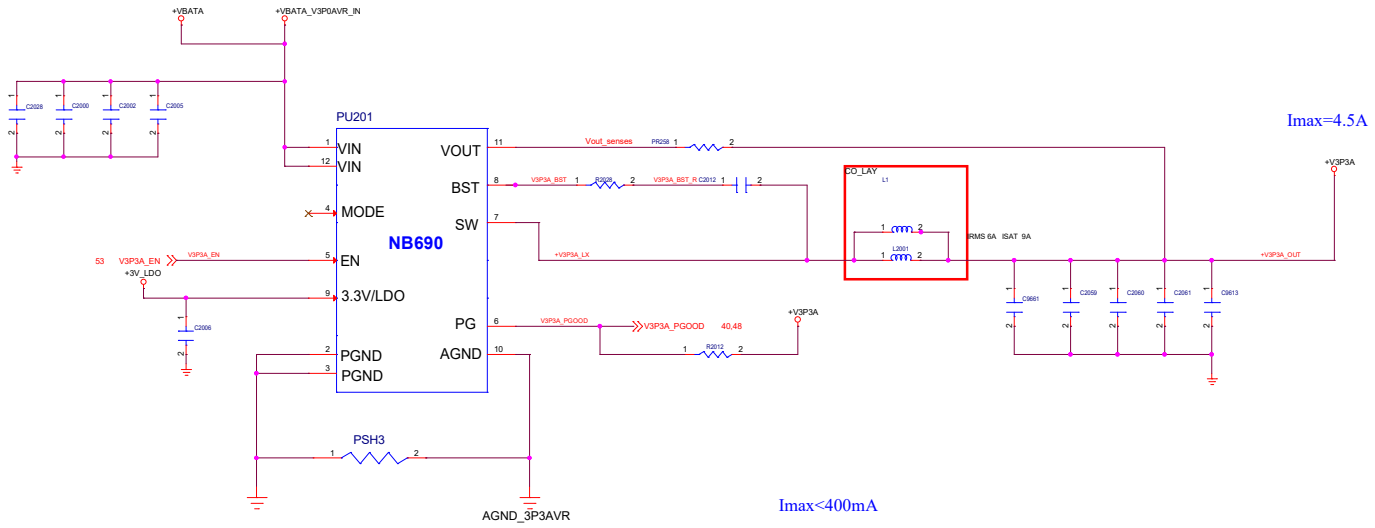
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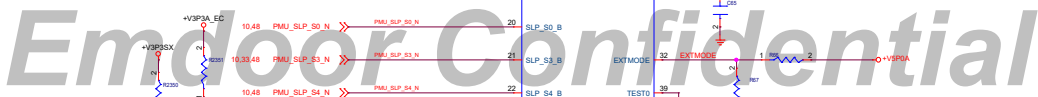
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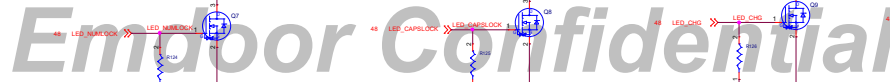

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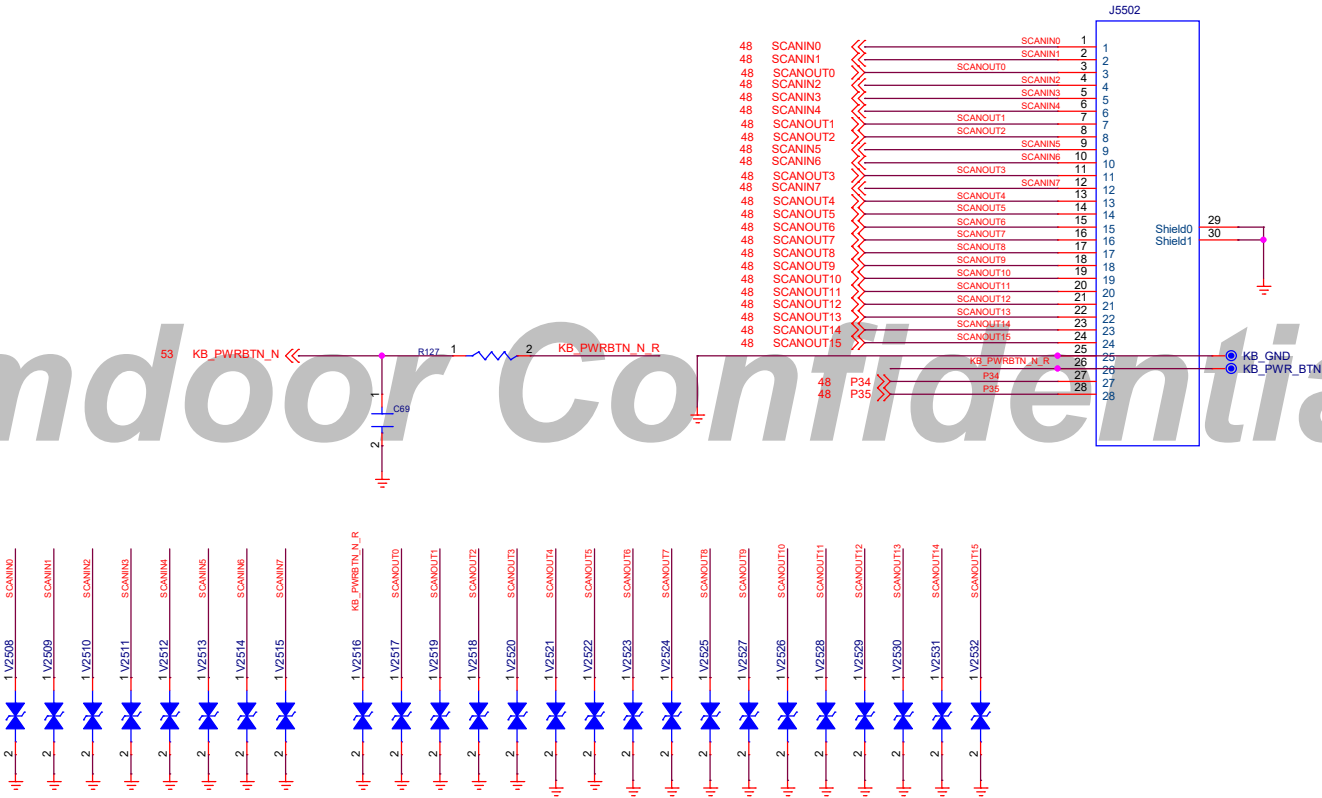
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Approved		Title:	
DO NOT REPRODUCE		REUSE	Drawing No.
FOR INTERNAL USE ONLY		Content	Rev
		Issue: Wednesday, October 13, 2010	Sheet: 47 of 56

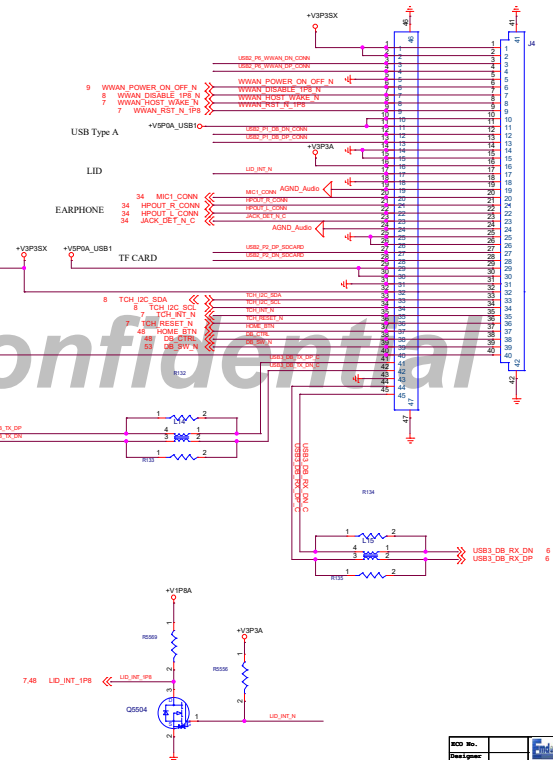
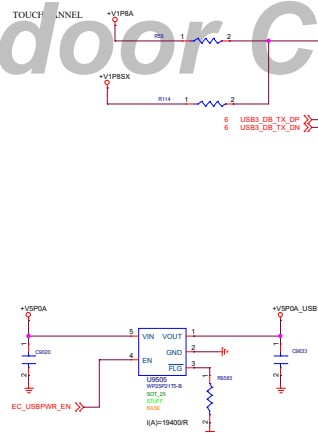




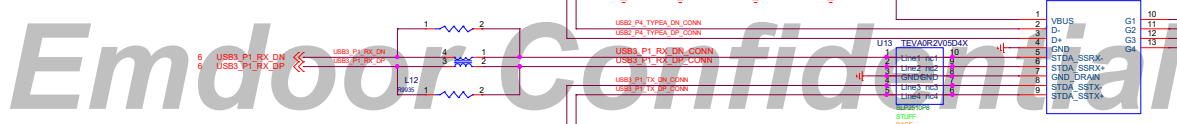
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Designer			
Reviewed			
Approved		Title:	
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		SCHEMATIC	
		Drawing No.	
		Rev.	V1.0
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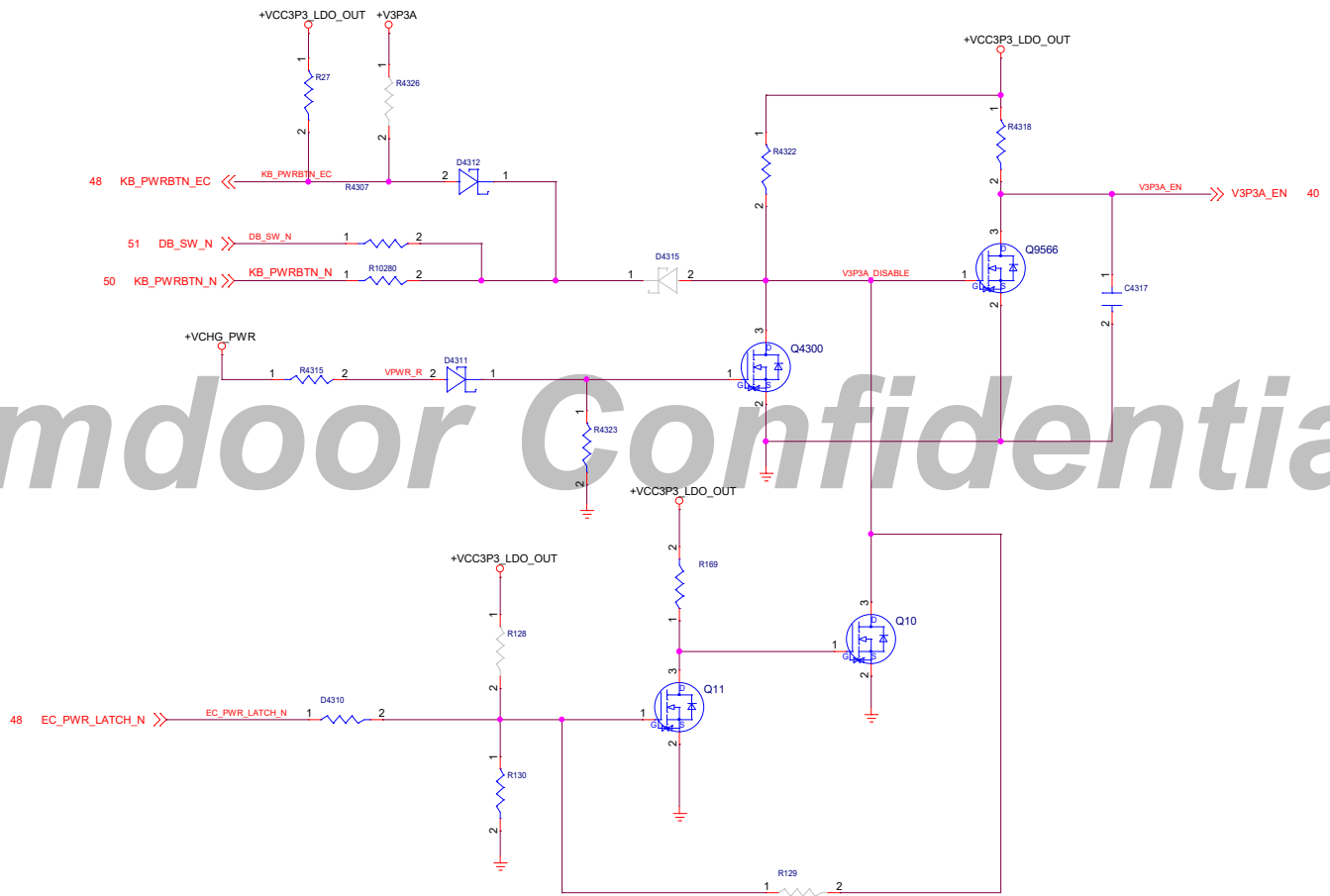
ECO No.		 代通电子	Http: www.endoor.com	
Designer:				
Reviewed:				
Approved				
		Title:		
DO NOT REPRODUCE		Size	CHEMATIC	Drawing No.
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Date: Wednesday, November 24, 2010		Sheet 51 of 55		V1.0



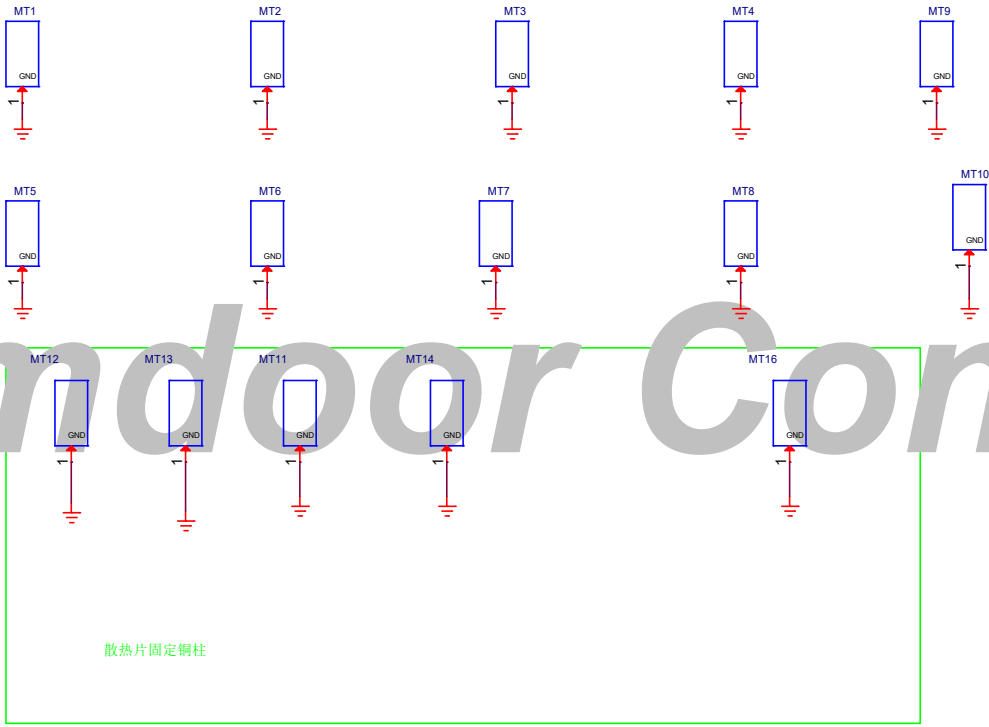
NOTE:material is not the same as different pattern

ECO No.	 代通电子		Http: www.emdoor.com	
Designer				
Reviewed				
Approved				
Title:				
DO NOT REPRODUCE	Size Custom	SCHEMATIC	Drawing No.	Rev.
FOR INTERNAL USE ONLY	Date: Wednesday, November 24, 2010	Sheet 52	of 55	V1.0

OK

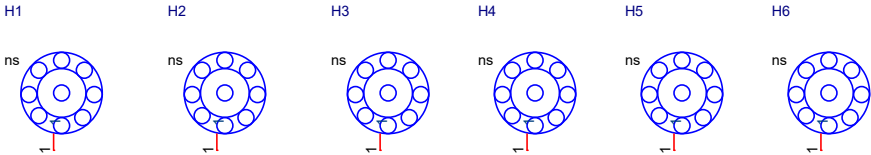


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Approved		SCHEMATIC	Drawing No.
DO NOT REPRODUCE		Date: Wednesday, November 24, 2010	Rev. V1.0
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散热片固定铜柱

Mark Point



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Designer				
Reviewed				
Approved		Title:		
DO NOT REPRODUCE	Size A	SCHEMATIC	Drawing No.	Rev. V1.0
FOR INTERNAL USE ONLY	Date: Wednesday, November 24, 2023	Sheet 54 of 55		

ECO No.		 http://www.endoor.com	
Designer			
Reviewed			
Approved		Title:	
DO NOT REPRODUCE		Size	Drawing No.
		Unit	Rev.
FOR INTERNAL USE ONLY		Date: Wednesday, October 13, 2010	Sheet 55 of 56