

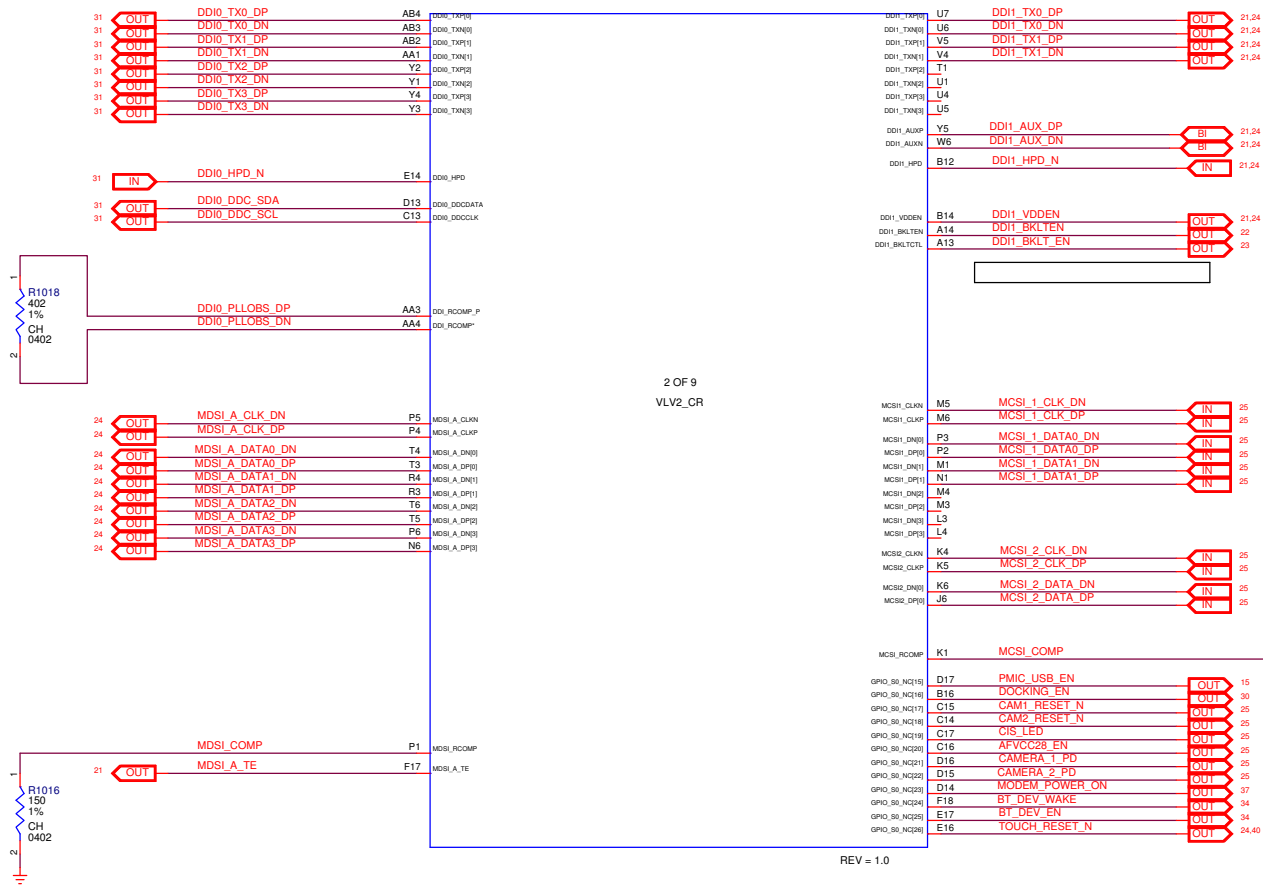
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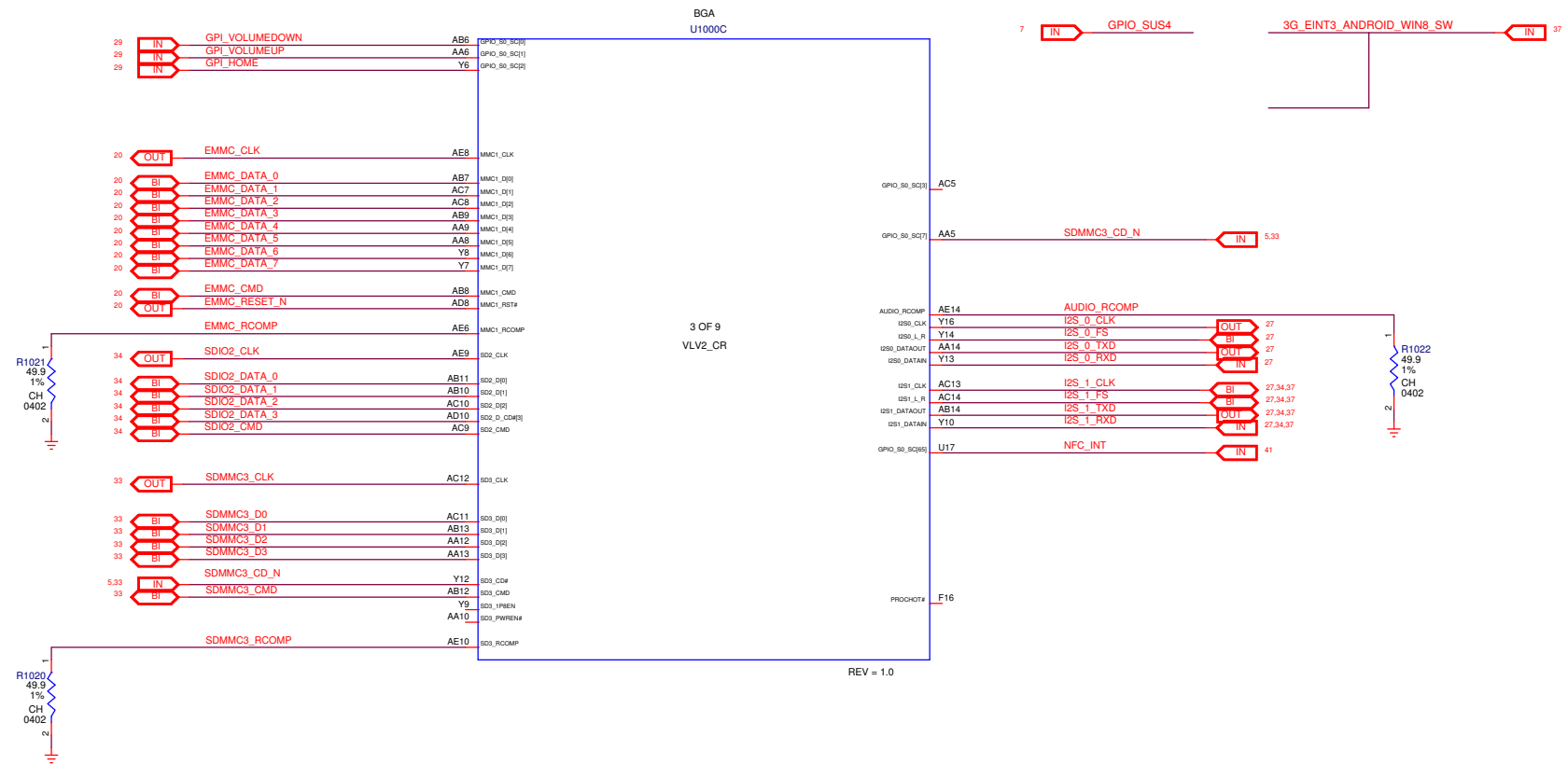


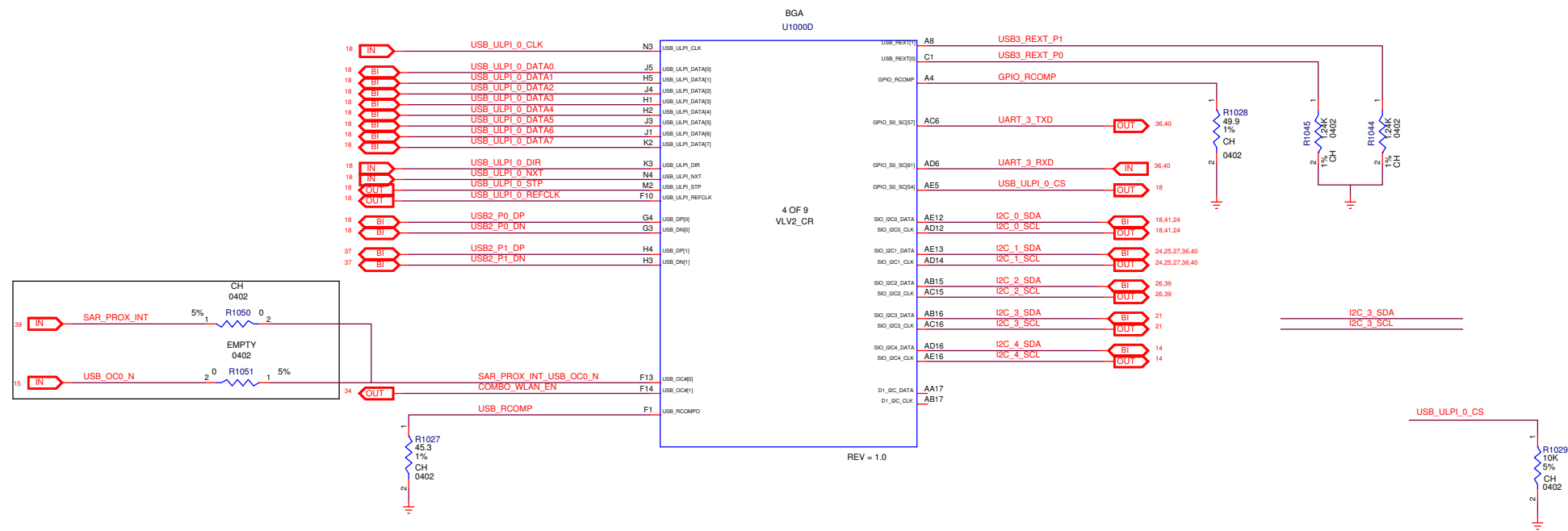
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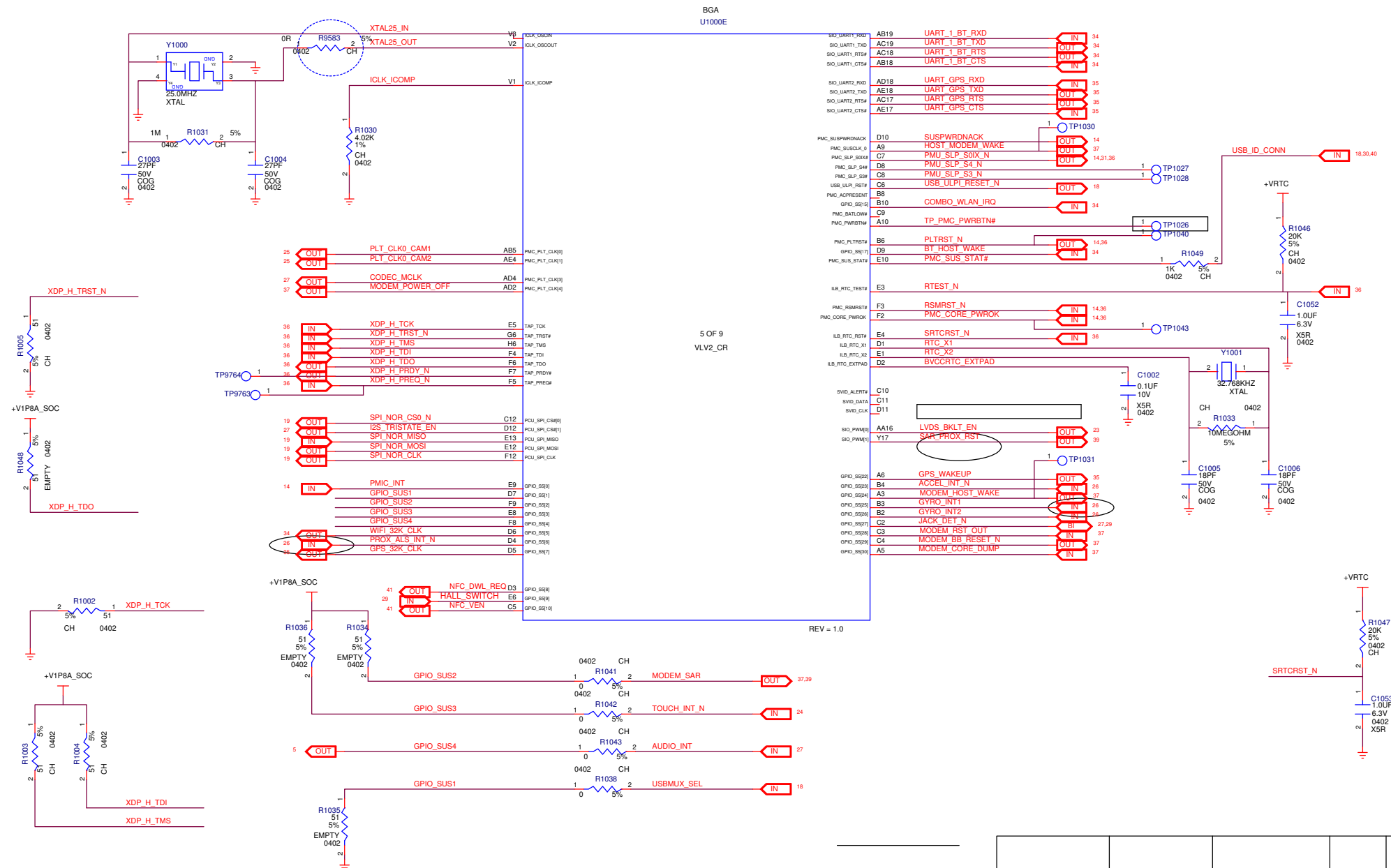
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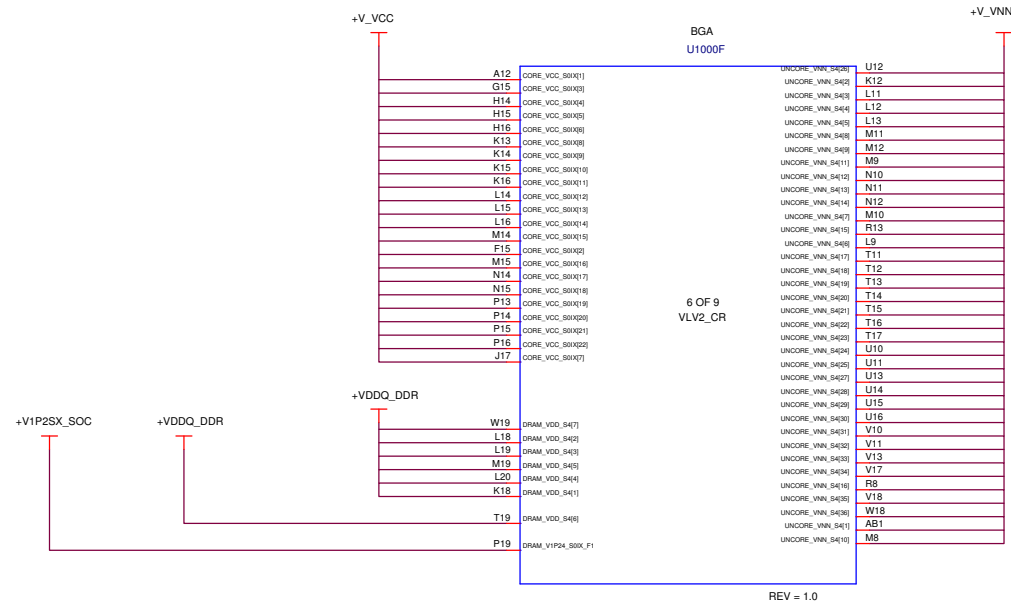
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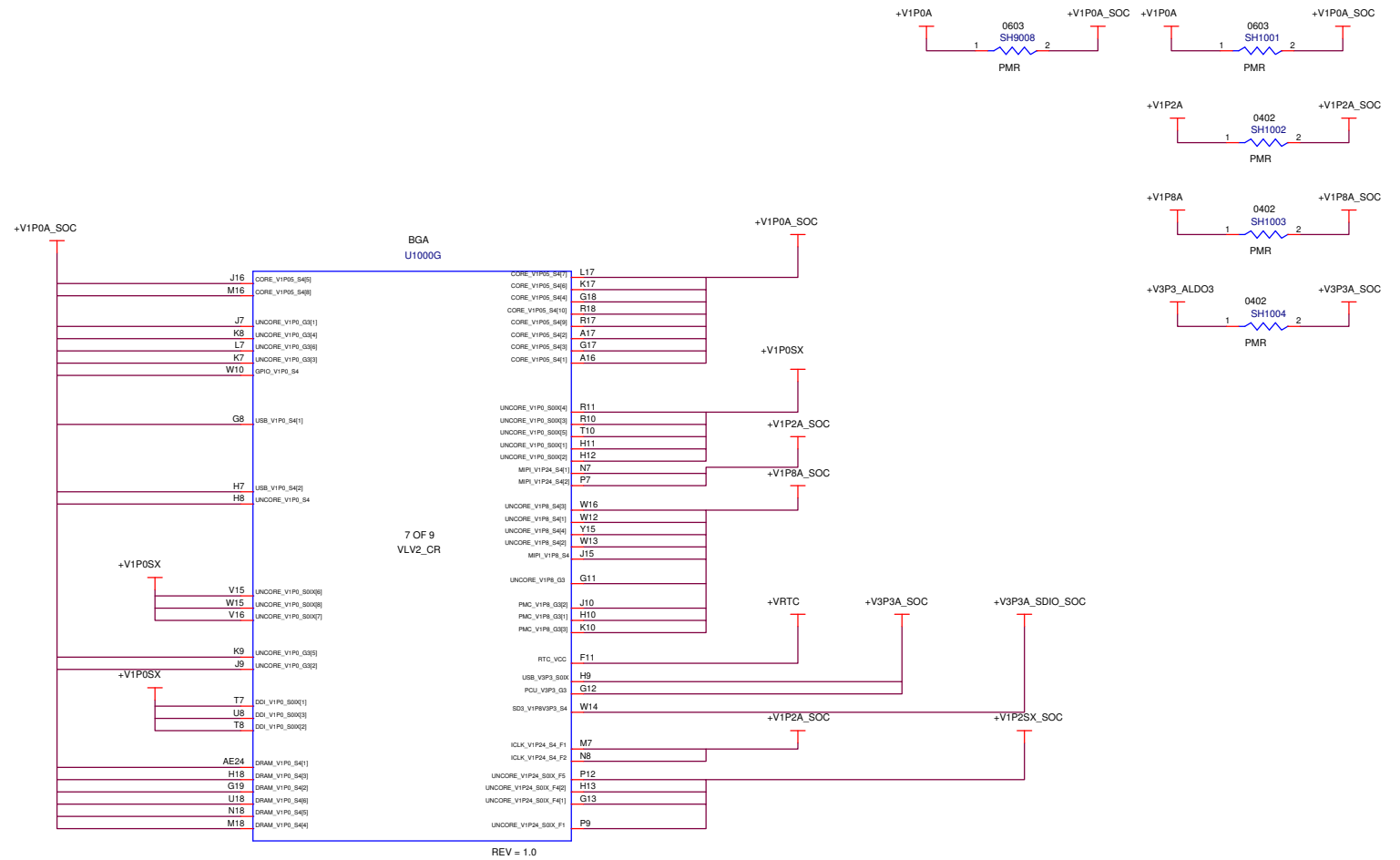


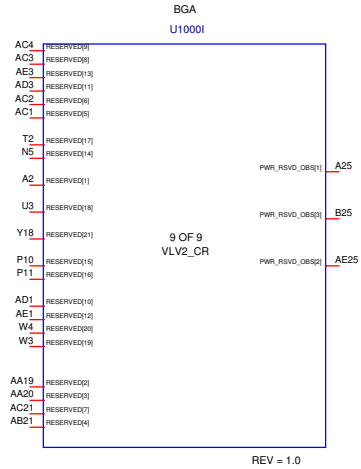
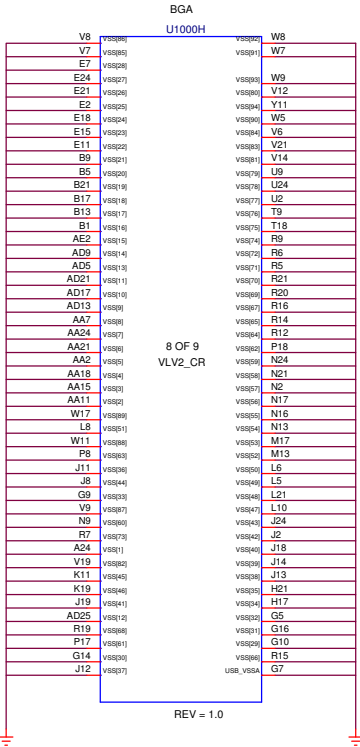


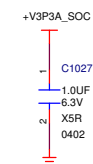
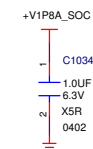
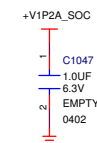
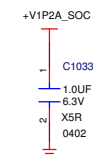
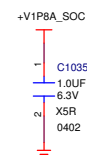
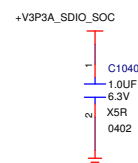
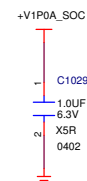
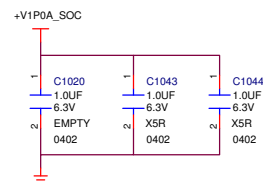
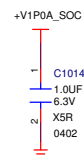
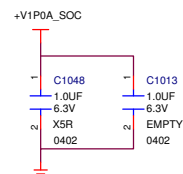
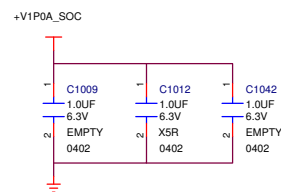
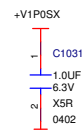
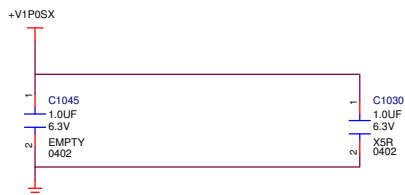
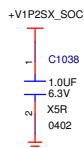
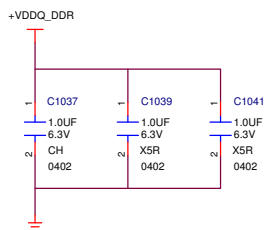
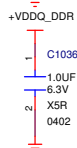
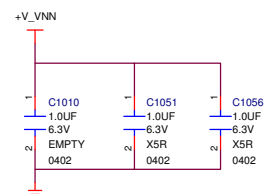
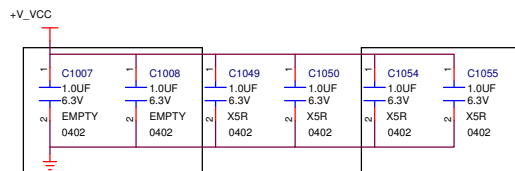


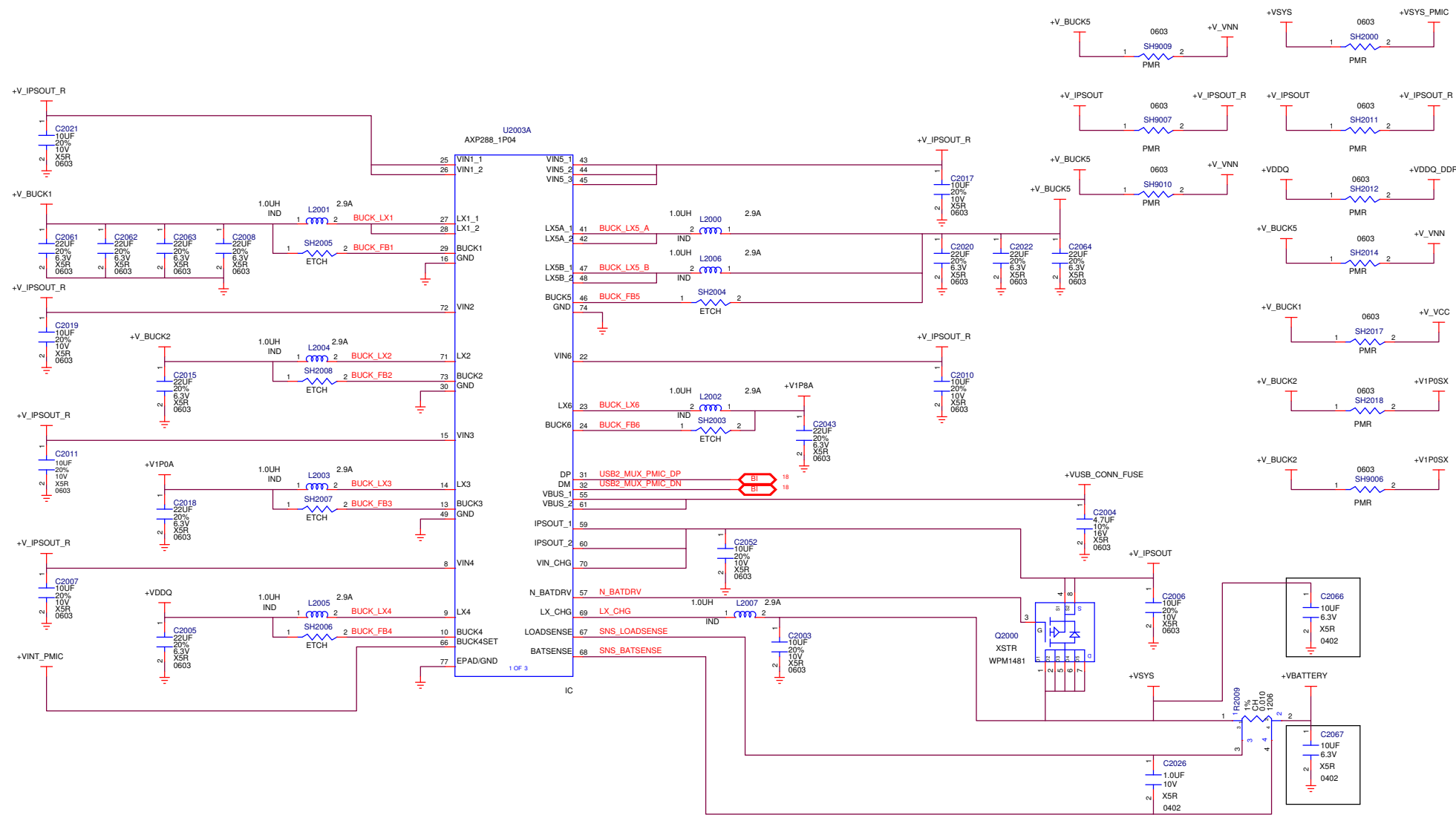


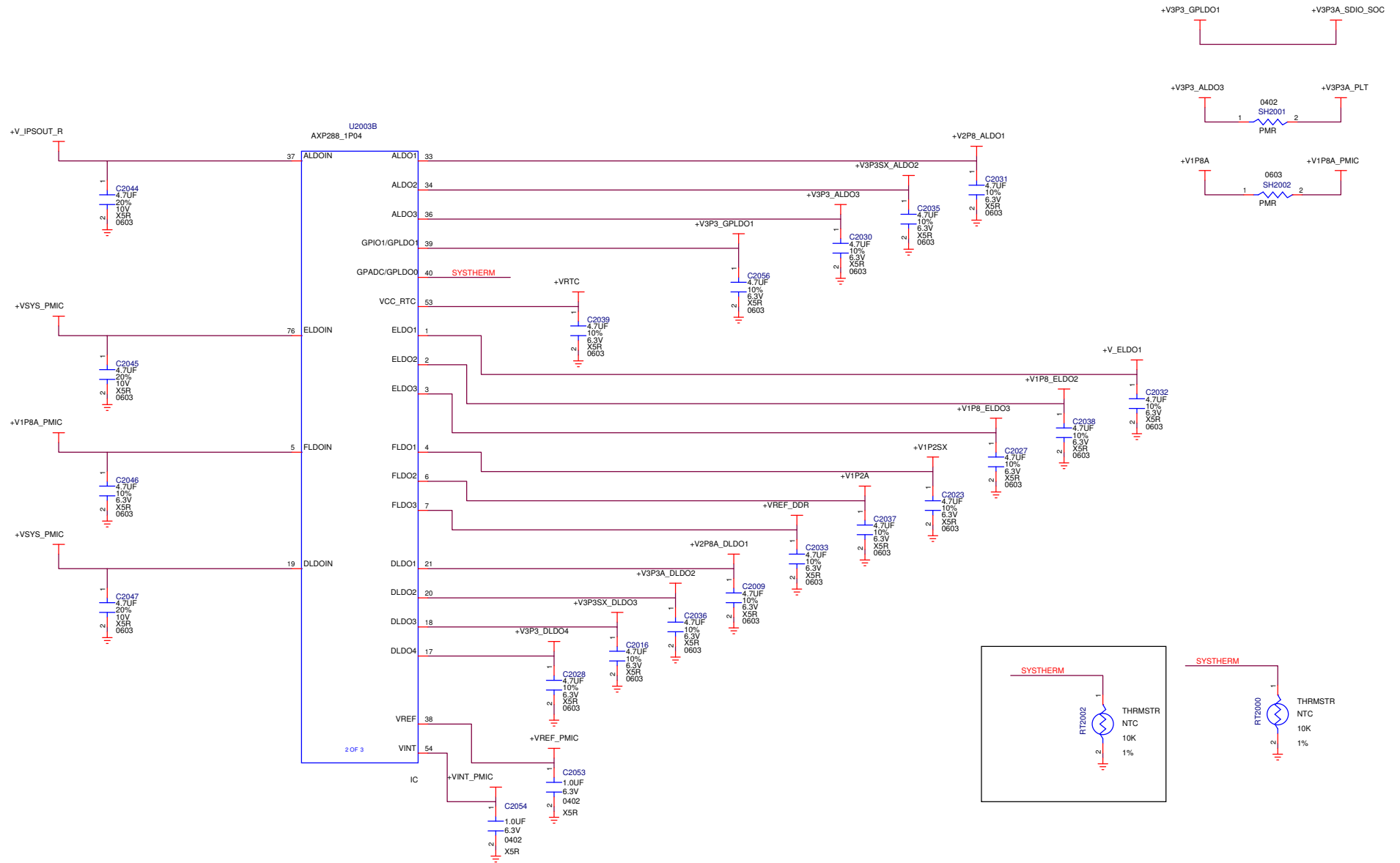


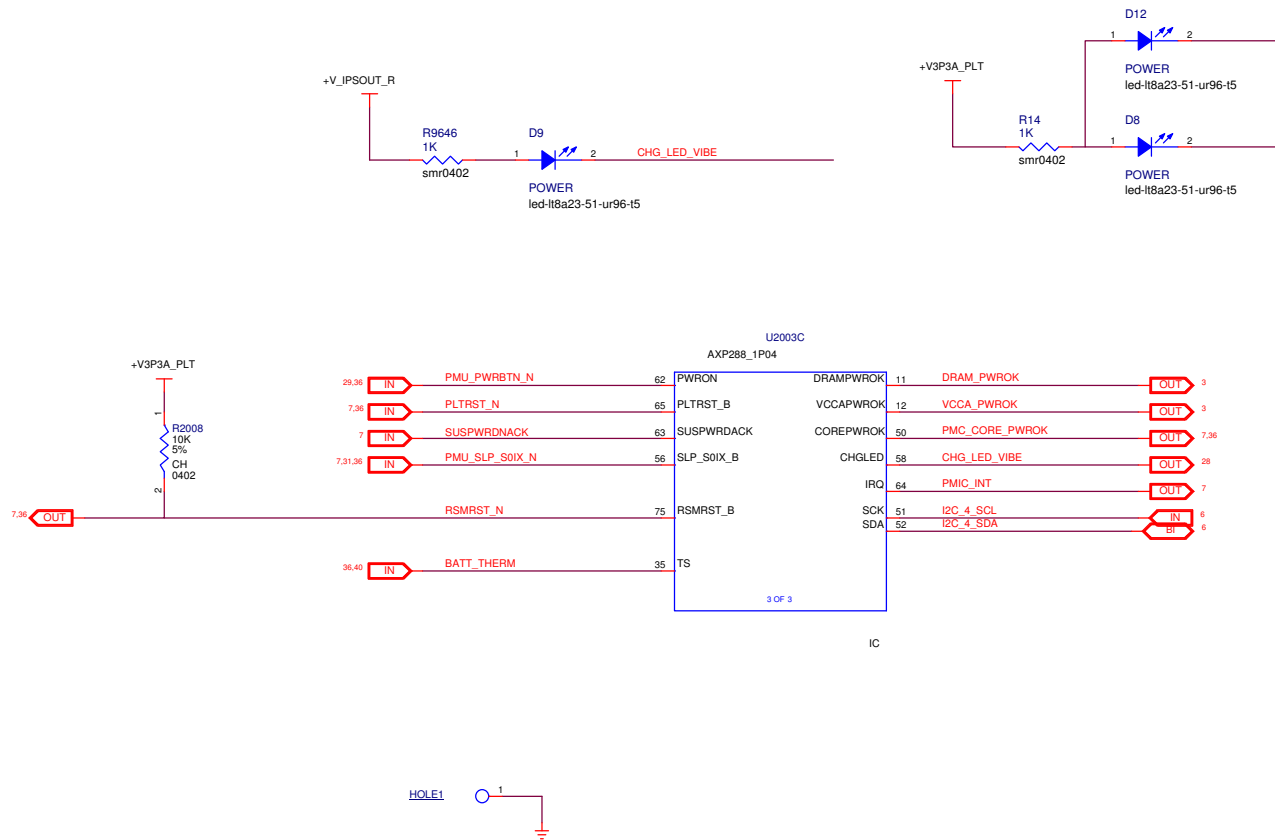


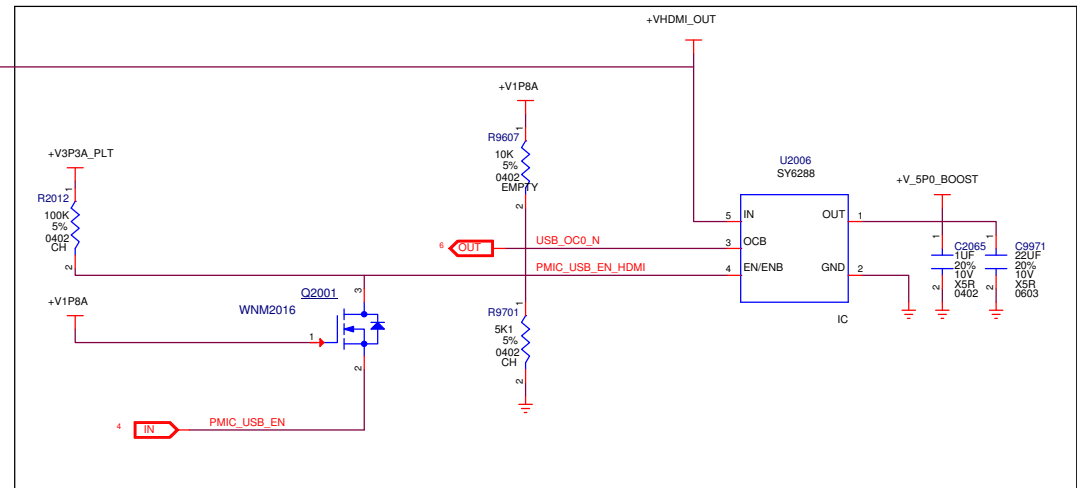
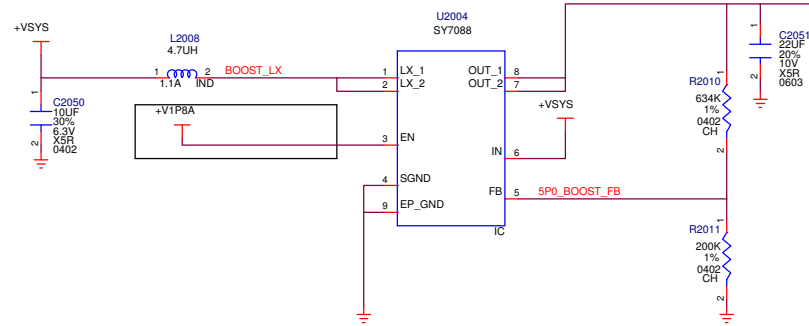


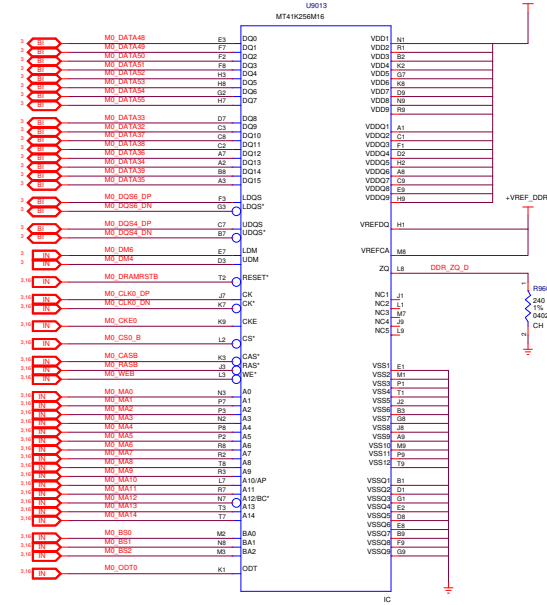
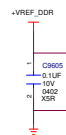
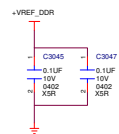
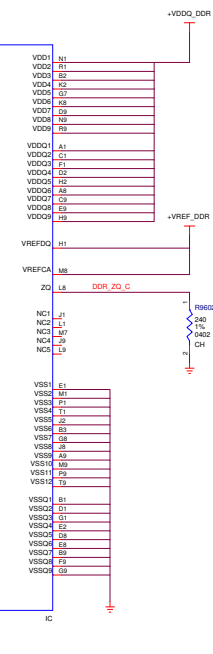
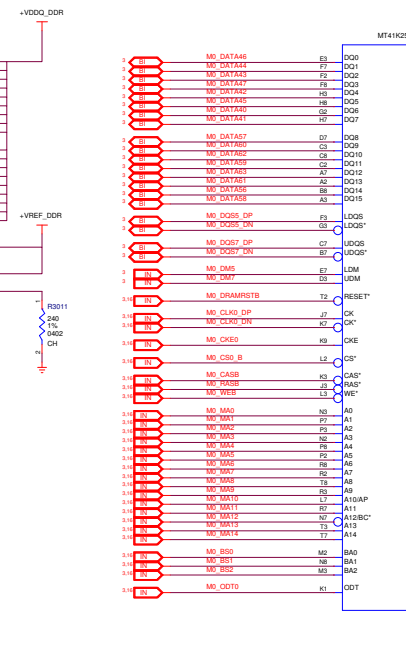


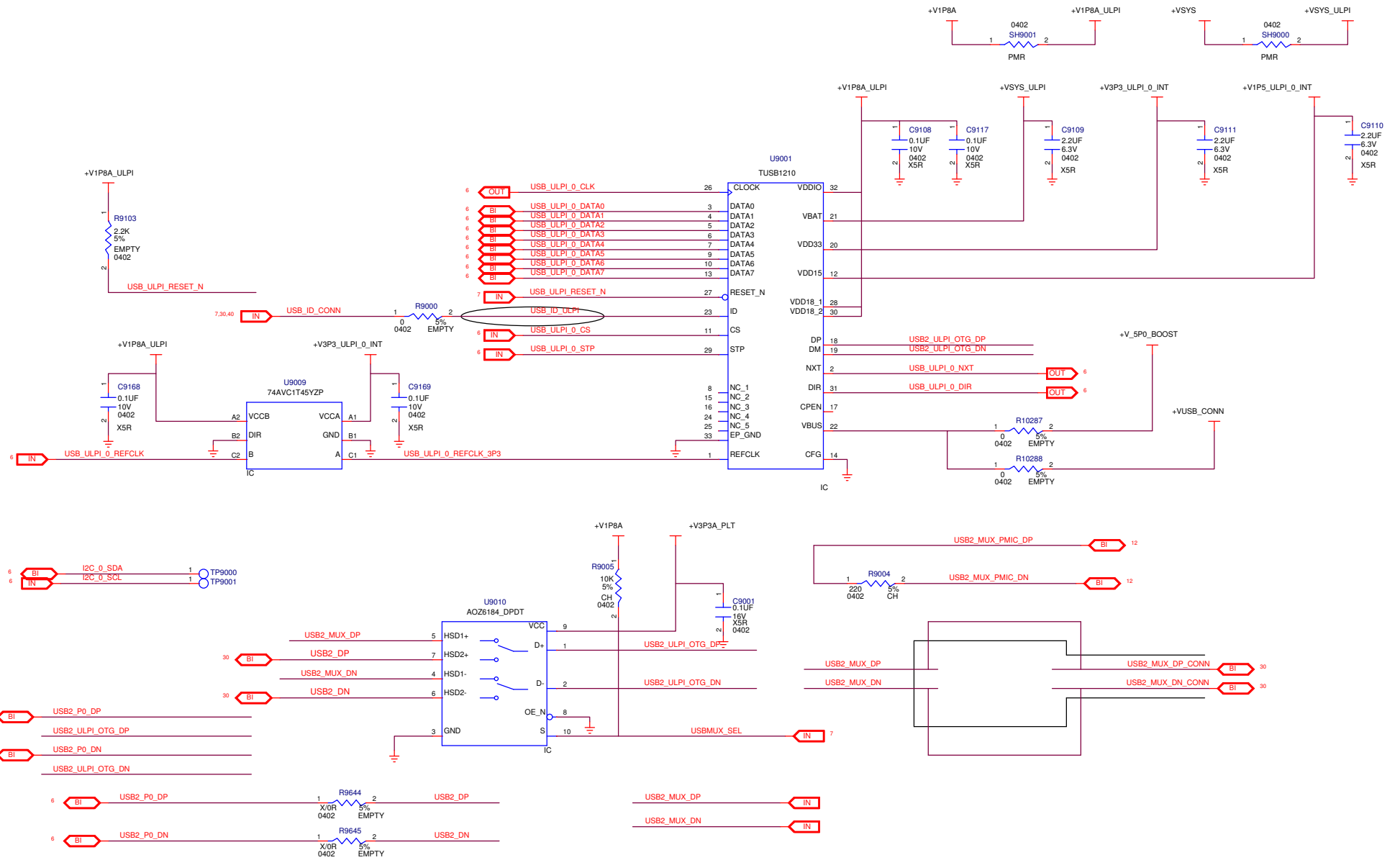


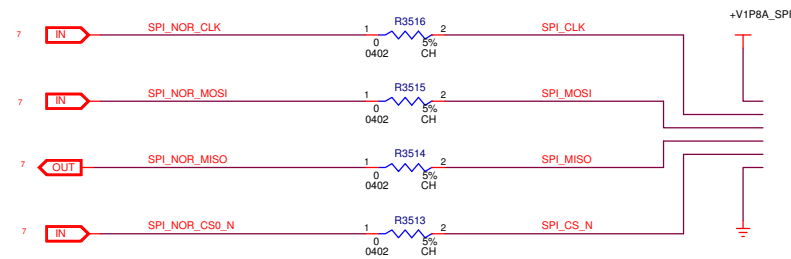
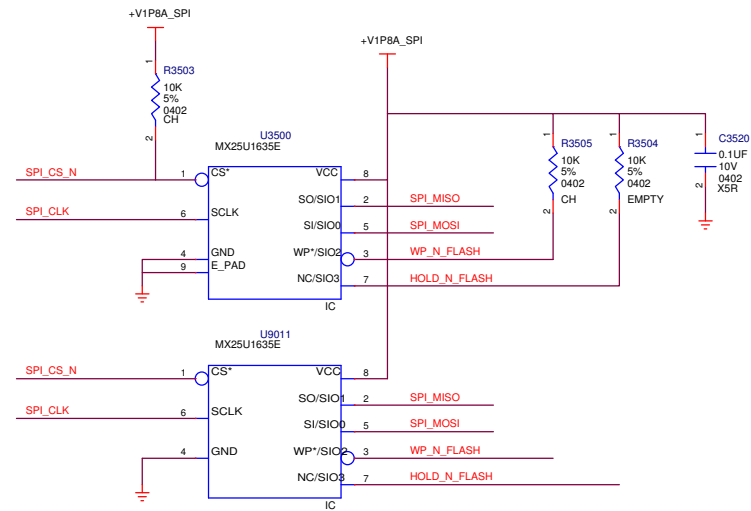
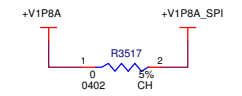


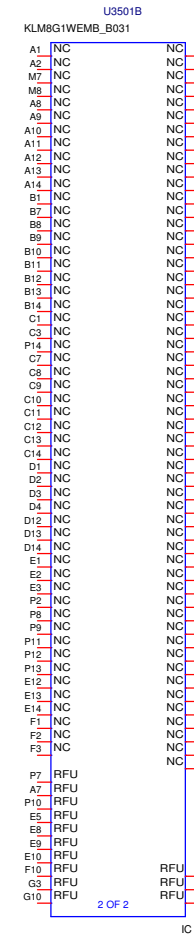
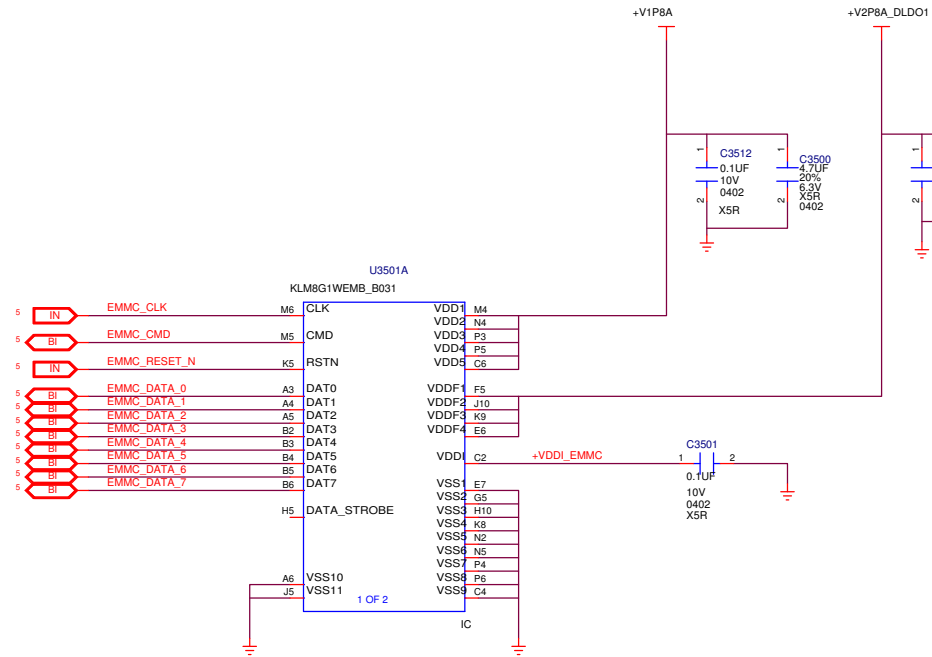


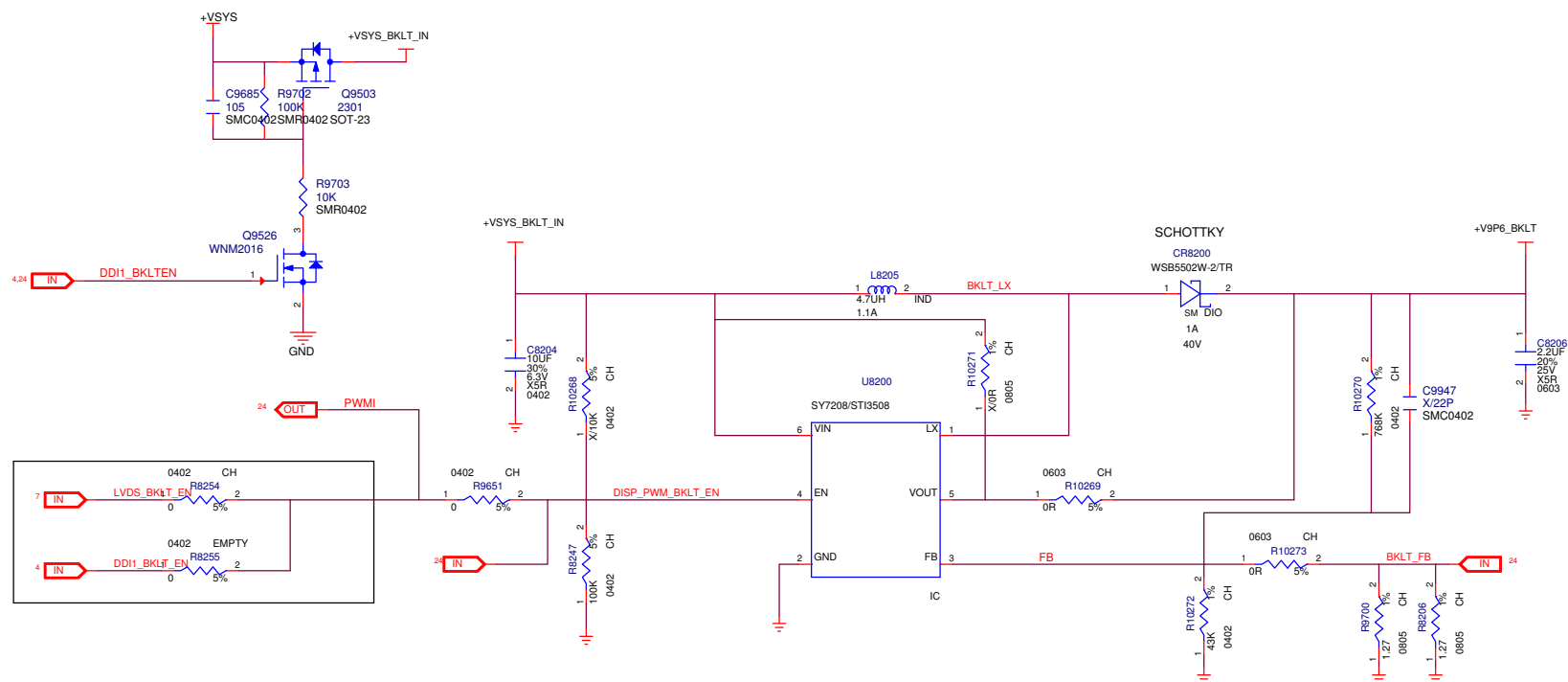




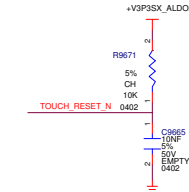
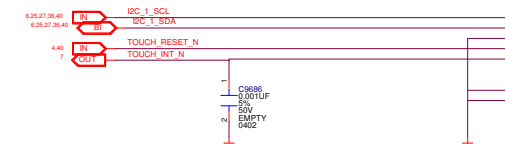
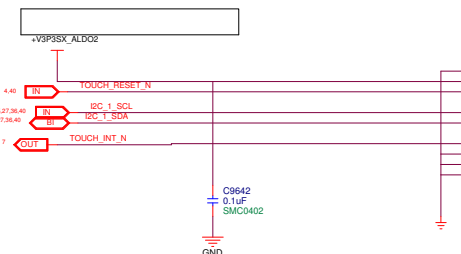
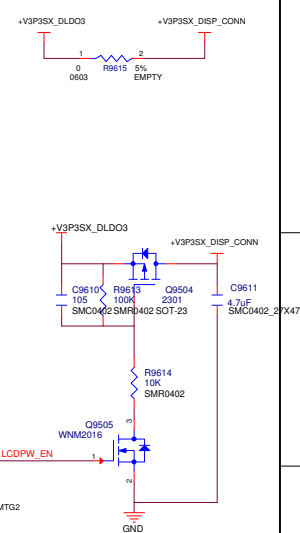
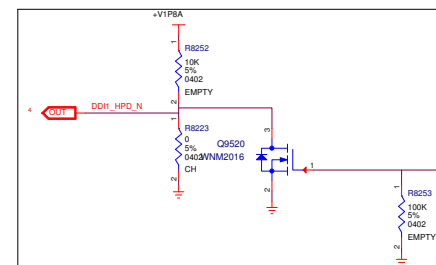
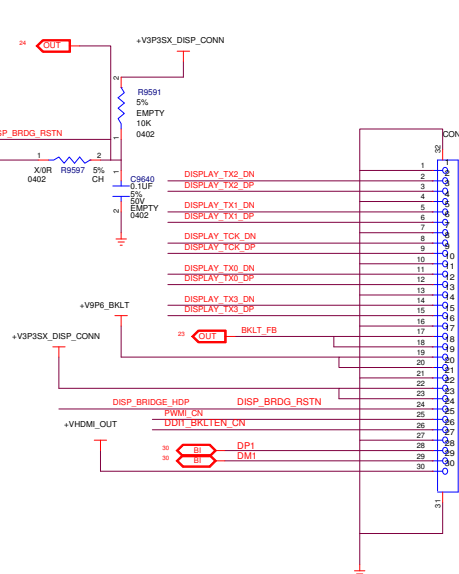
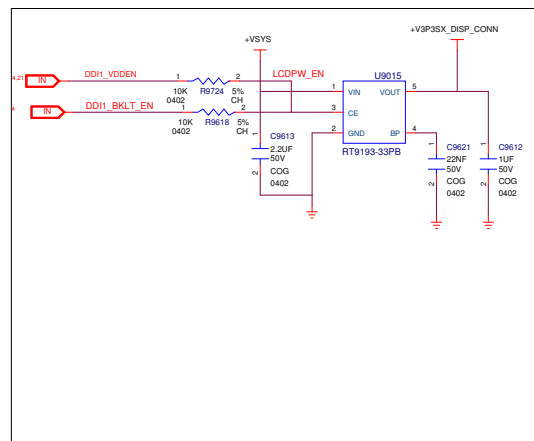
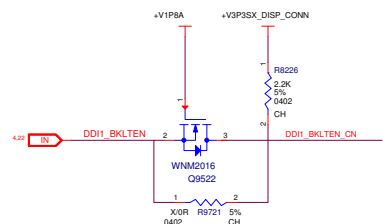
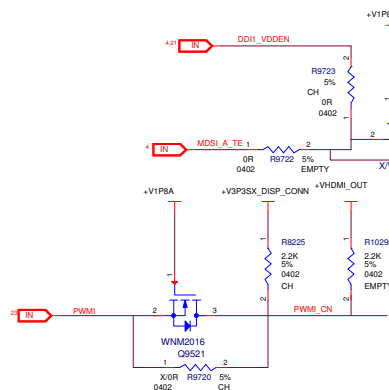
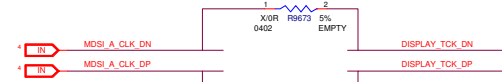
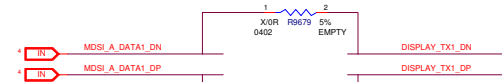
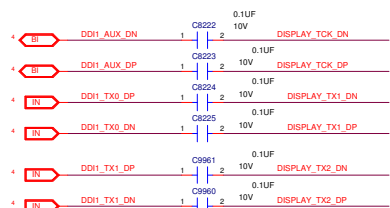


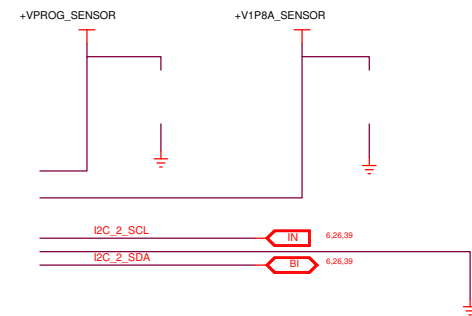
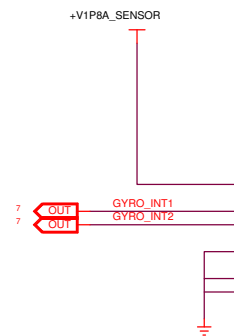
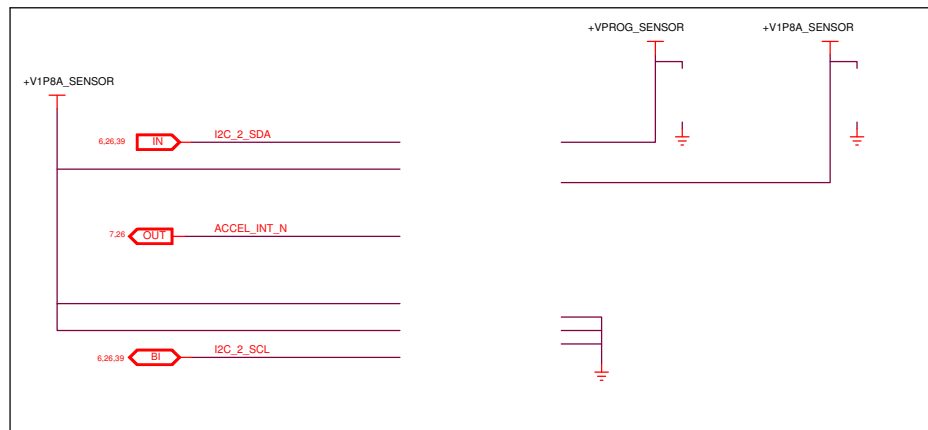
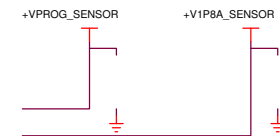
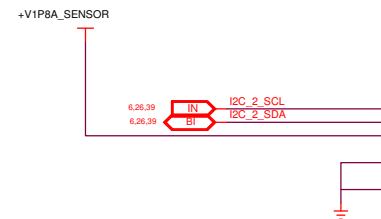
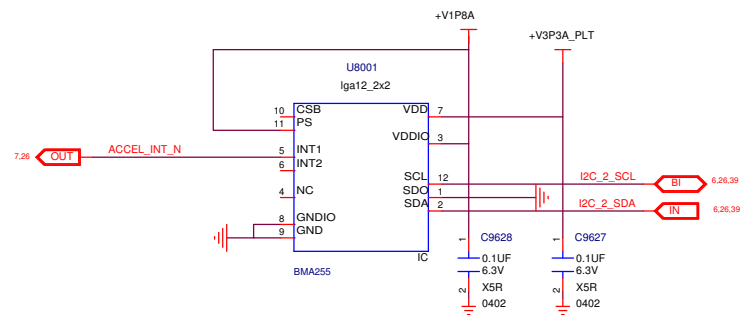
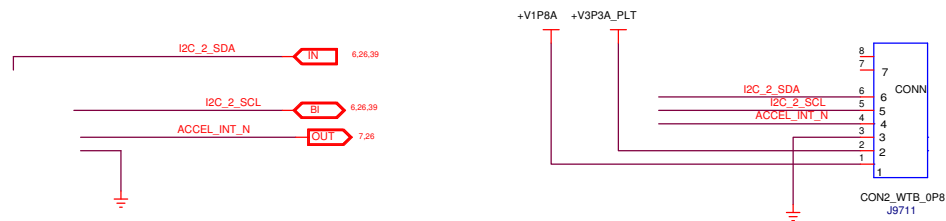


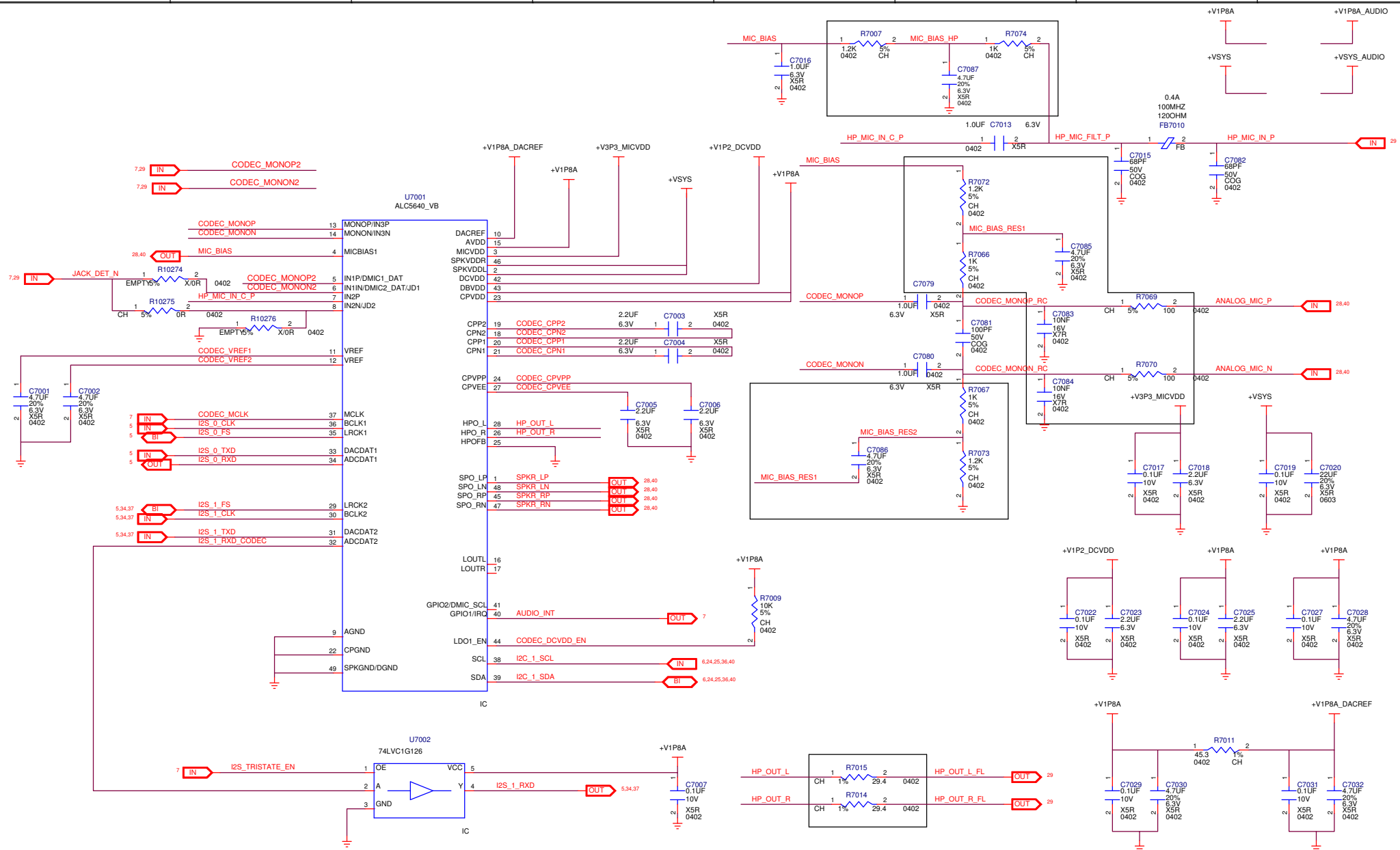


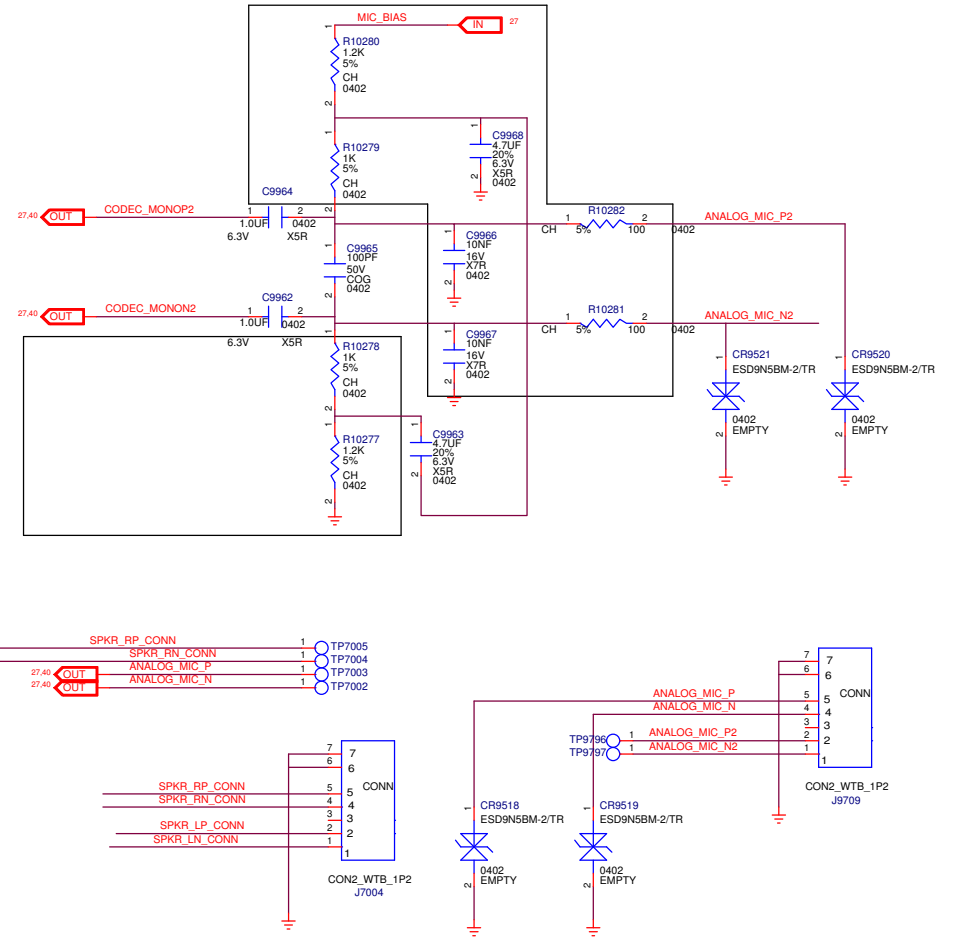
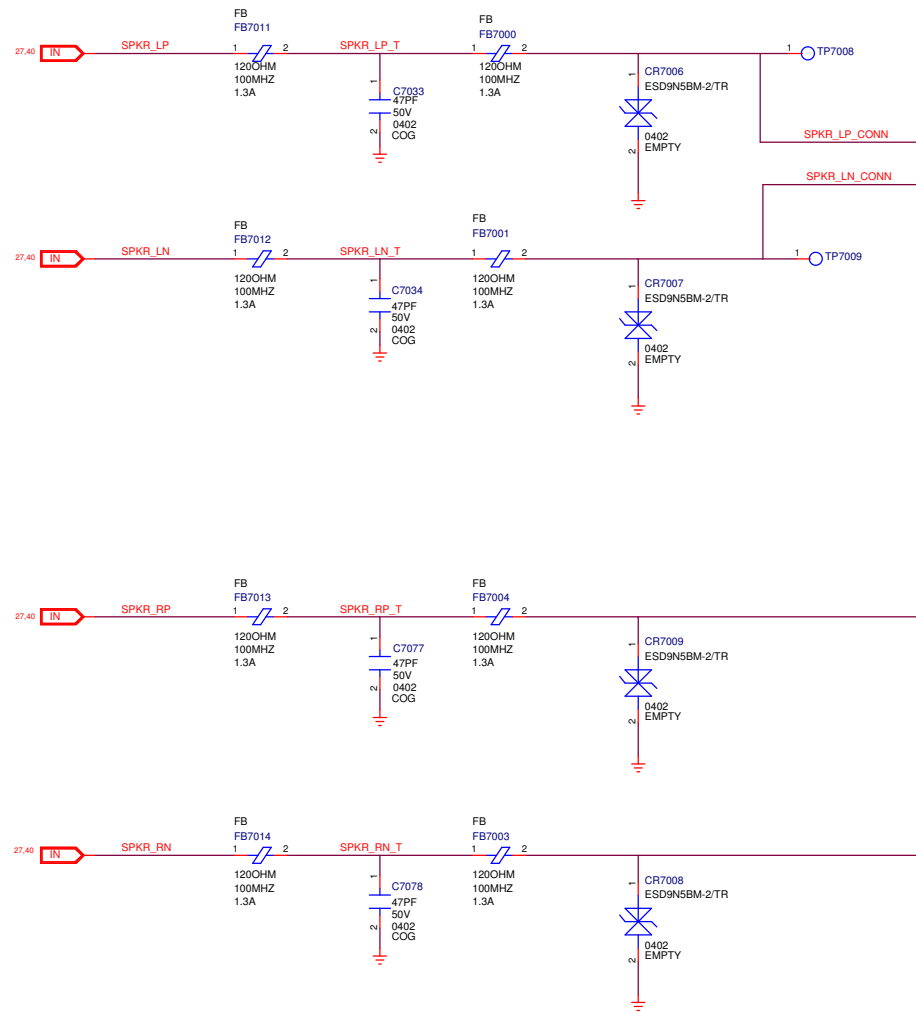


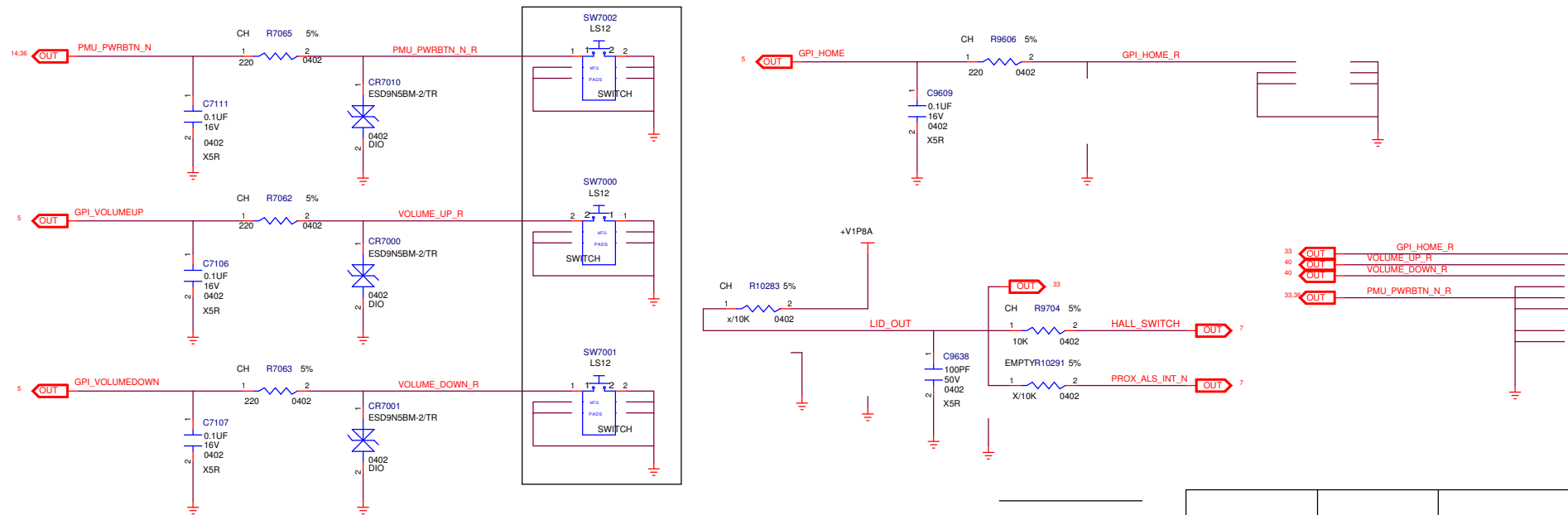
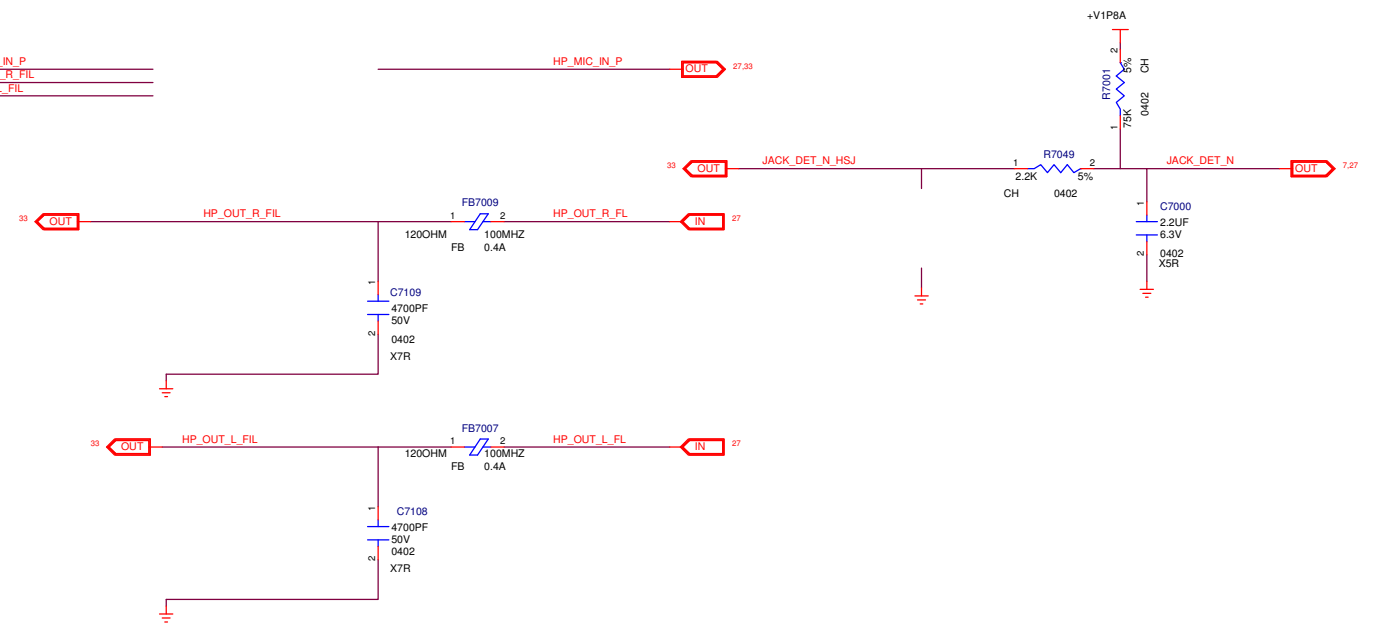
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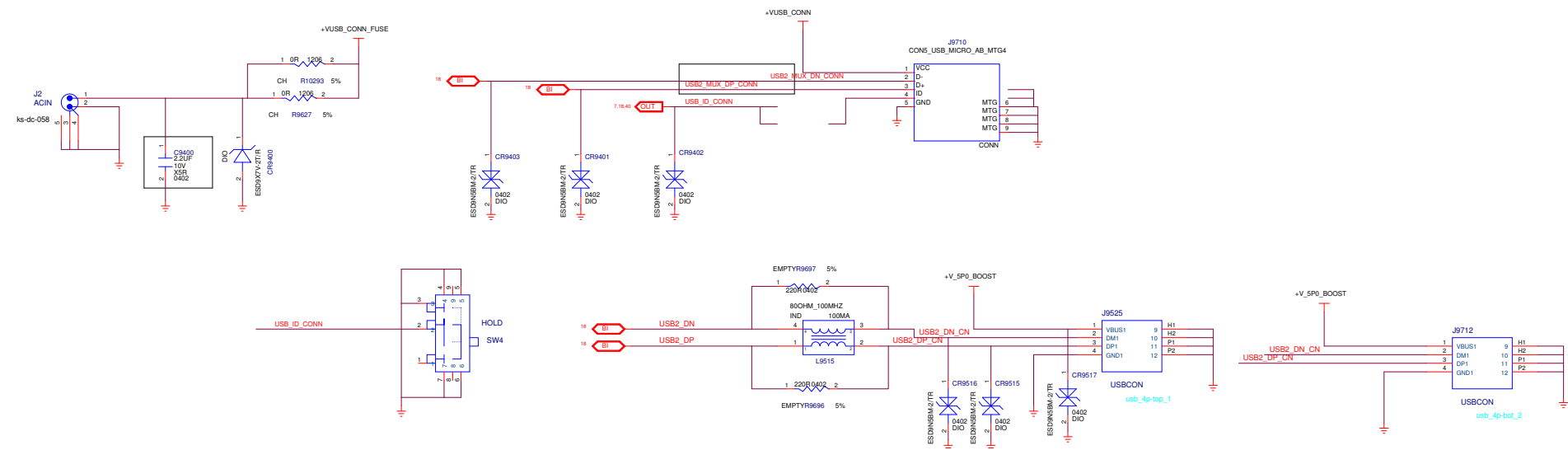


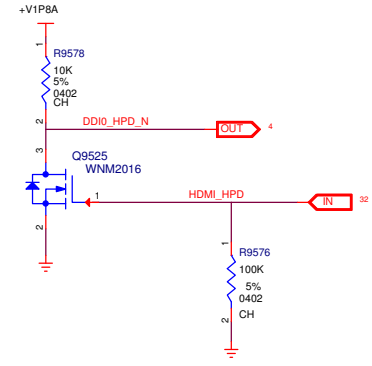
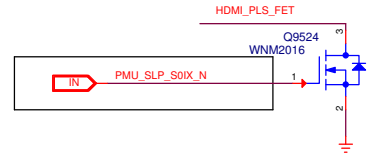
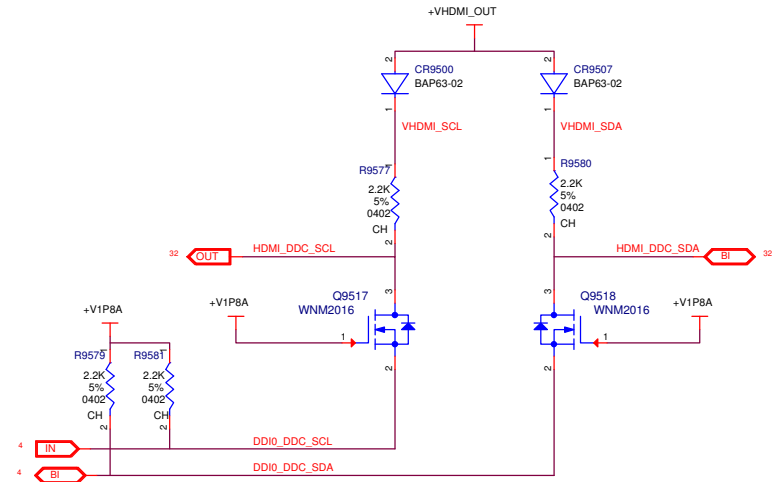
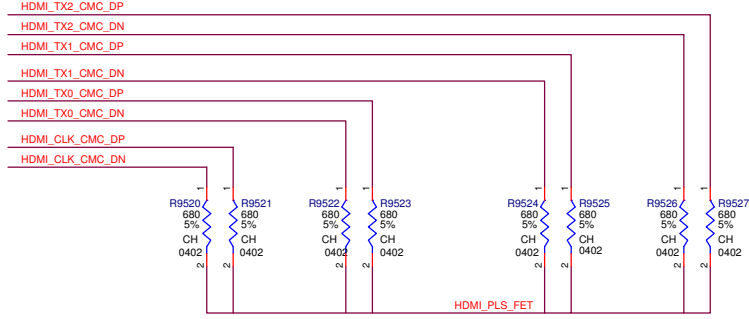
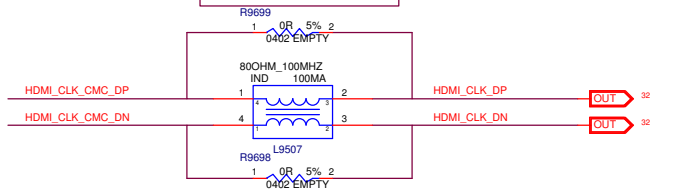
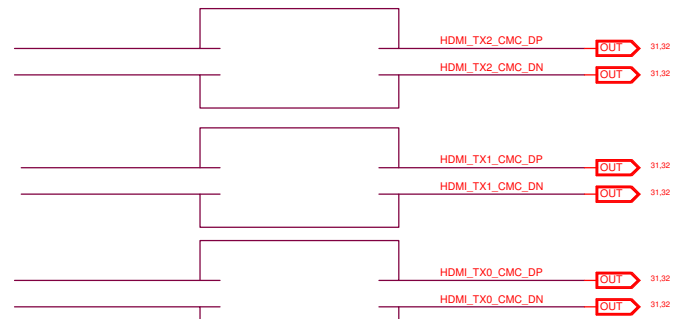
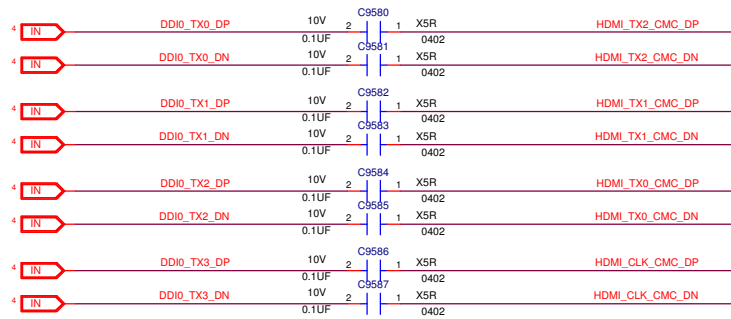


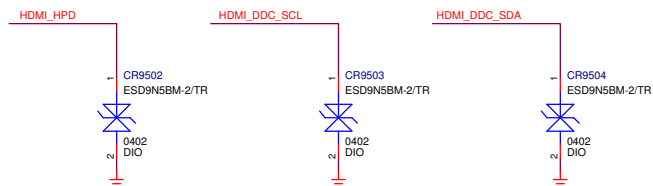
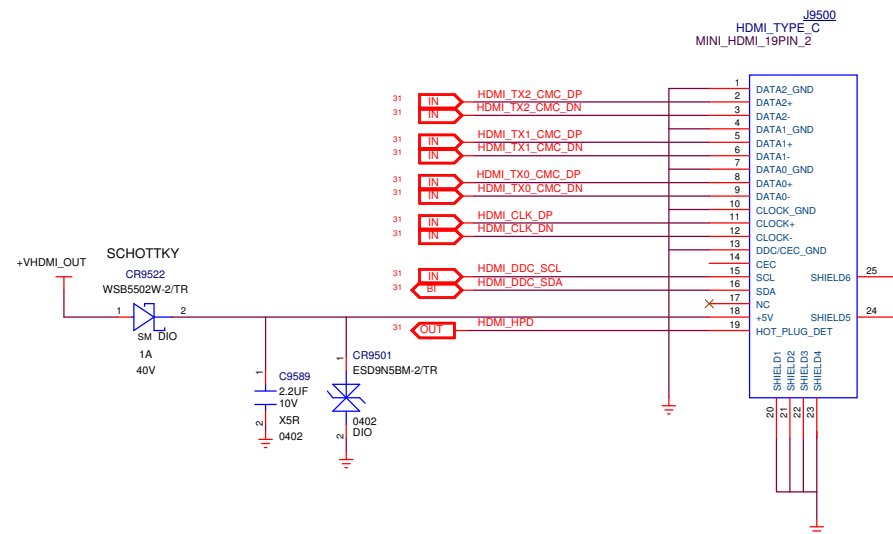
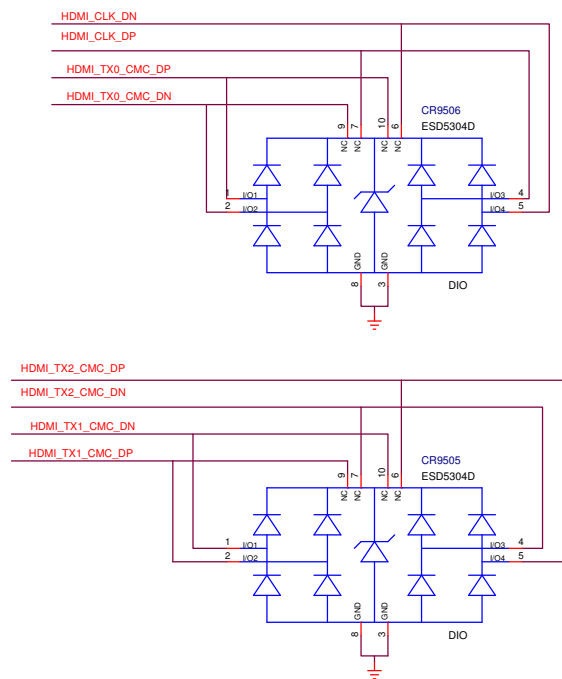


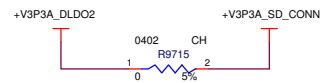




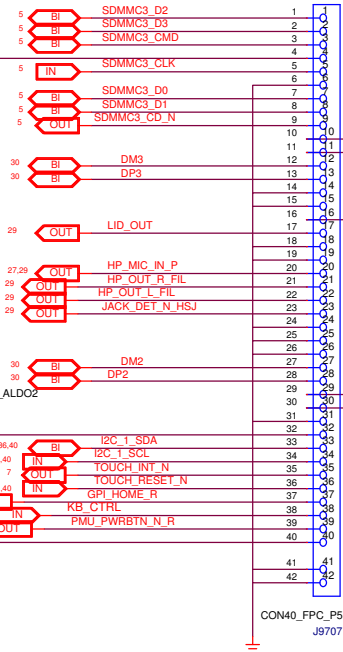
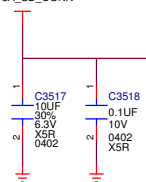








+V3P3A_SD_CONN



+V_5P0_BOOST

+V3P3A_PLT

+V_5P0_BOOST

+V3P3A_SD_CONN



