

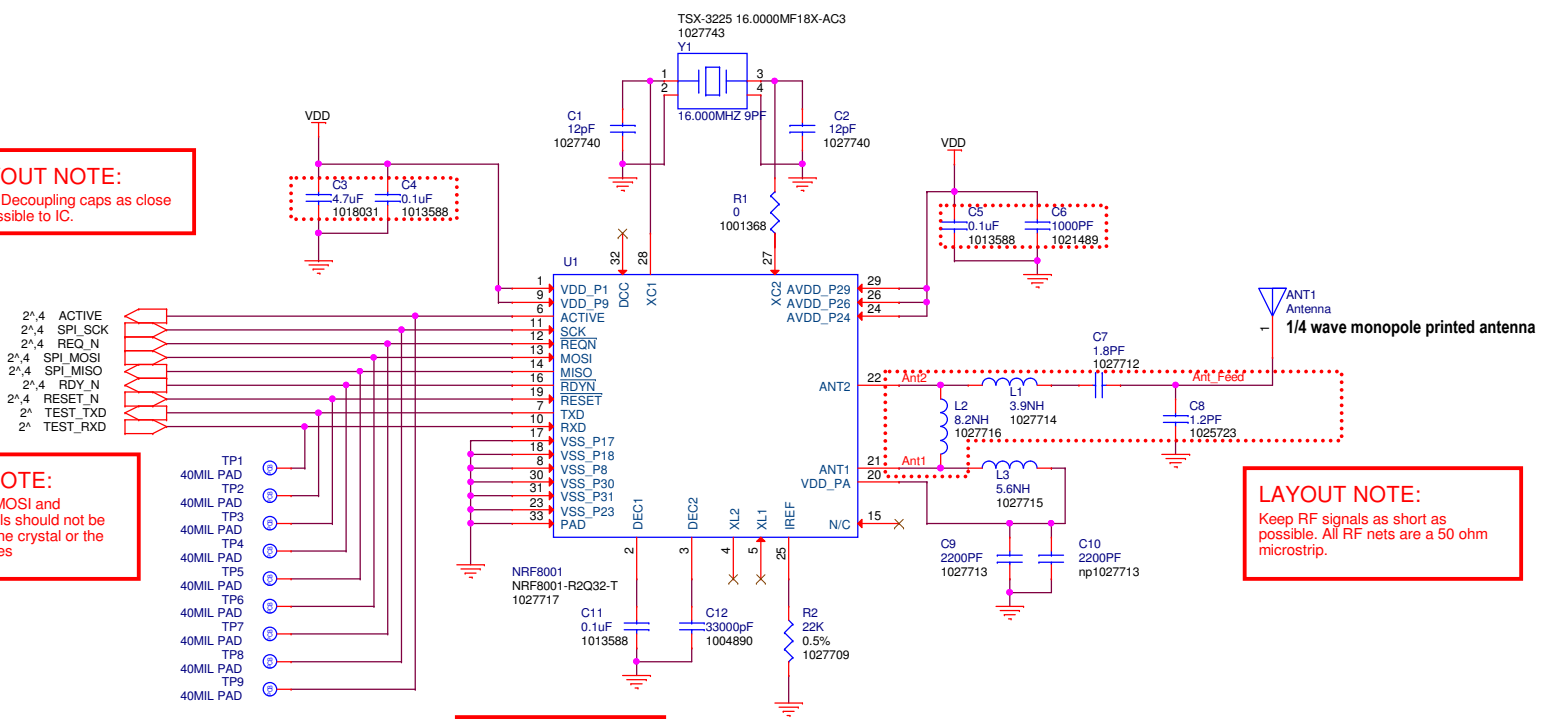
03 - BLE RADIO

LAYOUT NOTE:
Y1 should be physically located as close as possible to the input pin and with as direct a signal trace as possible on the top layer. Do not route signals under oscillator components - on same layer or layer below.

LAYOUT NOTE:
Place Decoupling caps as close as possible to IC.

LAYOUT NOTE:
SPI_SCK, SPI_MOSI and SPI_MISO signals should not be routed close to the crystal or the power supply lines

LAYOUT NOTE:
VSS pins should be connected directly to the ground plane. If ground plane is on different layer place via holes as close as possible to the VSS pad.



LAYOUT NOTE:
Keep RF signals as short as possible. All RF nets are a 50 ohm microstrip.