

EXHIBIT B - EUT INTERNAL PHOTOGRAPHS

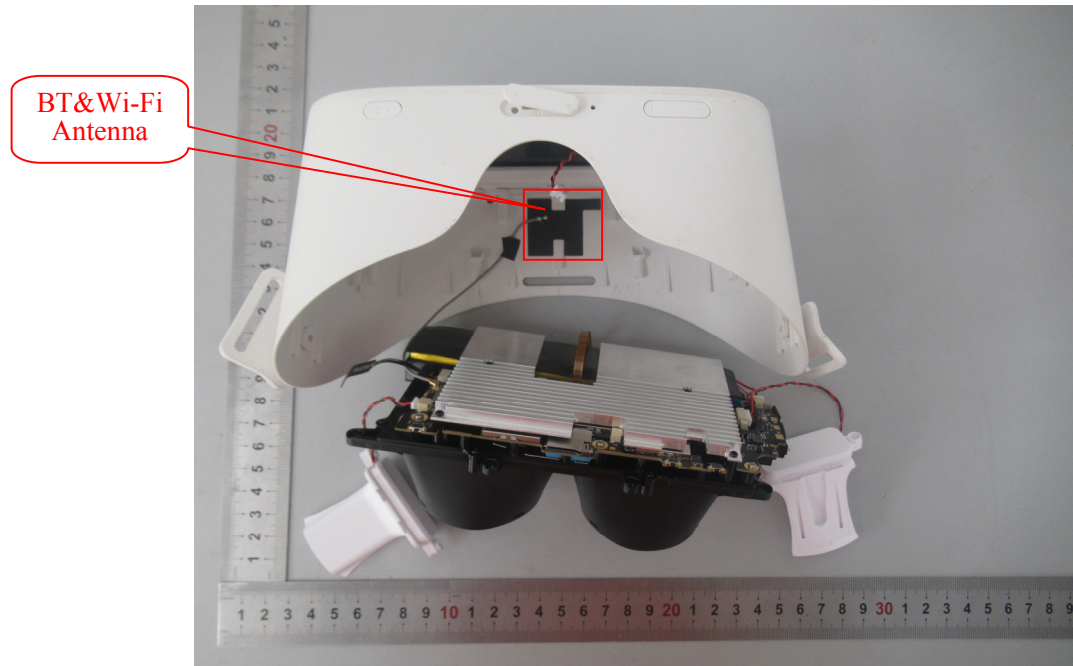
EUT – Cover off View-1



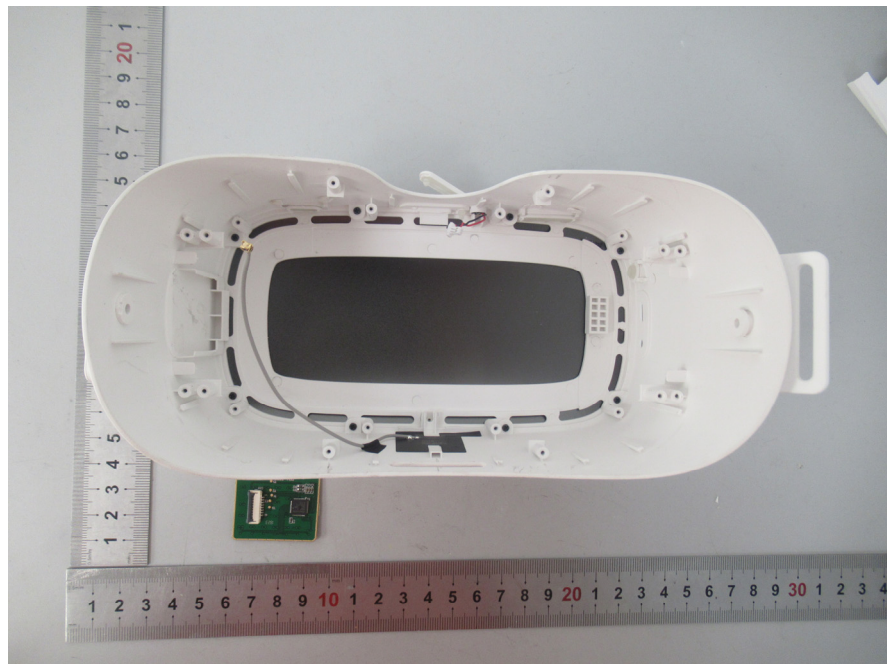
EUT – Cover off View-2



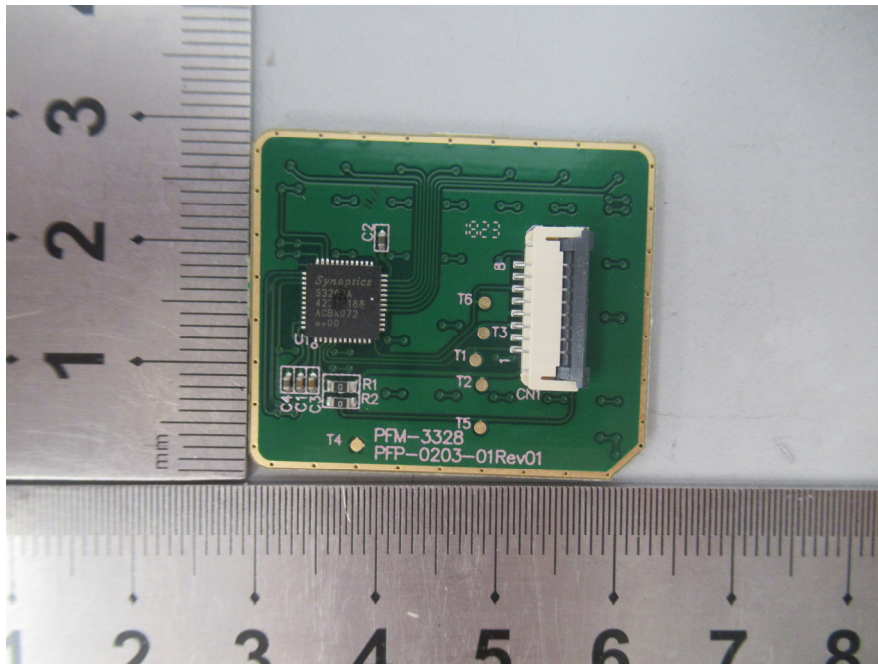
EUT – Cover off View-3



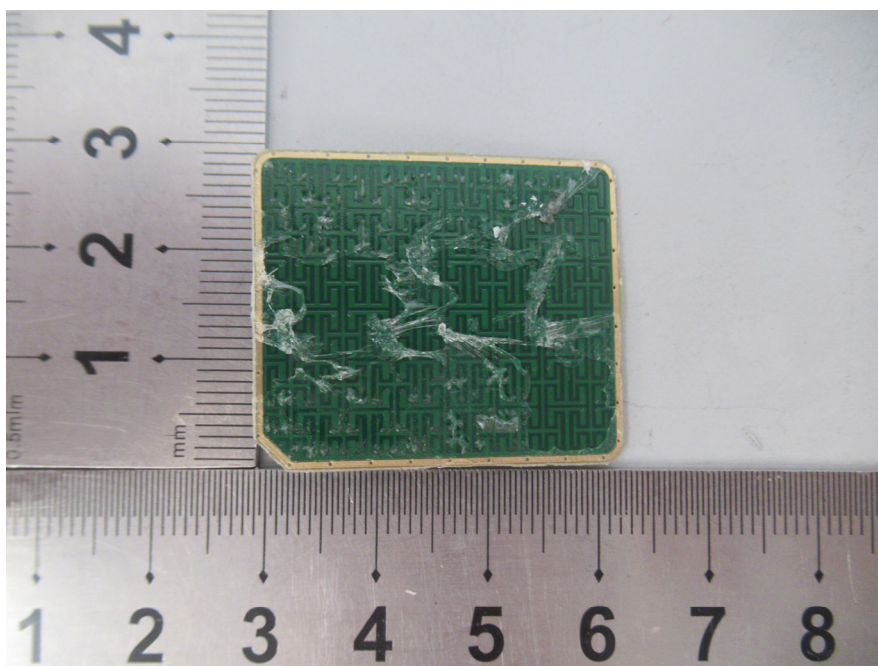
EUT – Cover off View-4



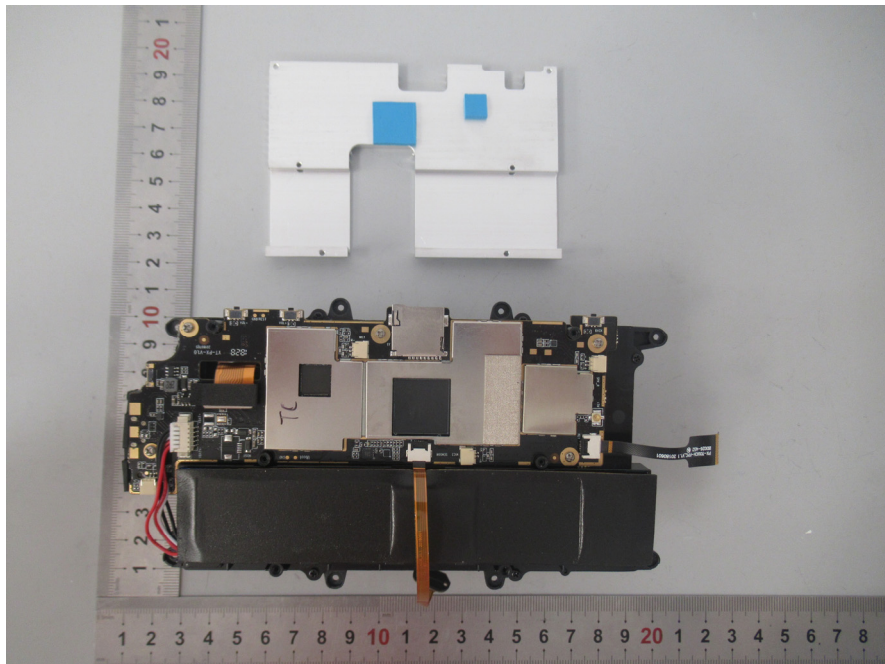
EUT – PCB-1 Top View



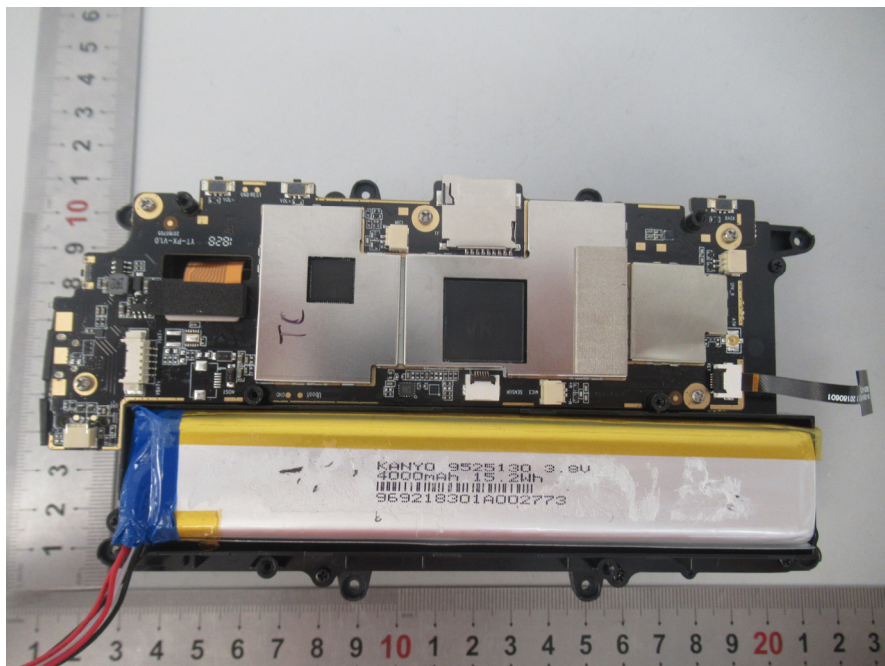
EUT – PCB-1 Bottom View



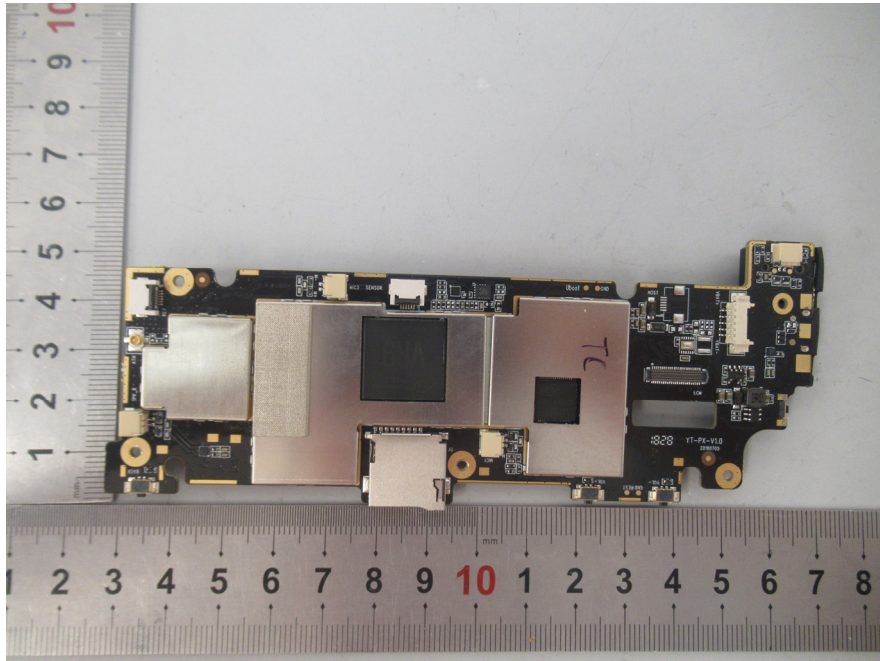
EUT – Cover off View-5



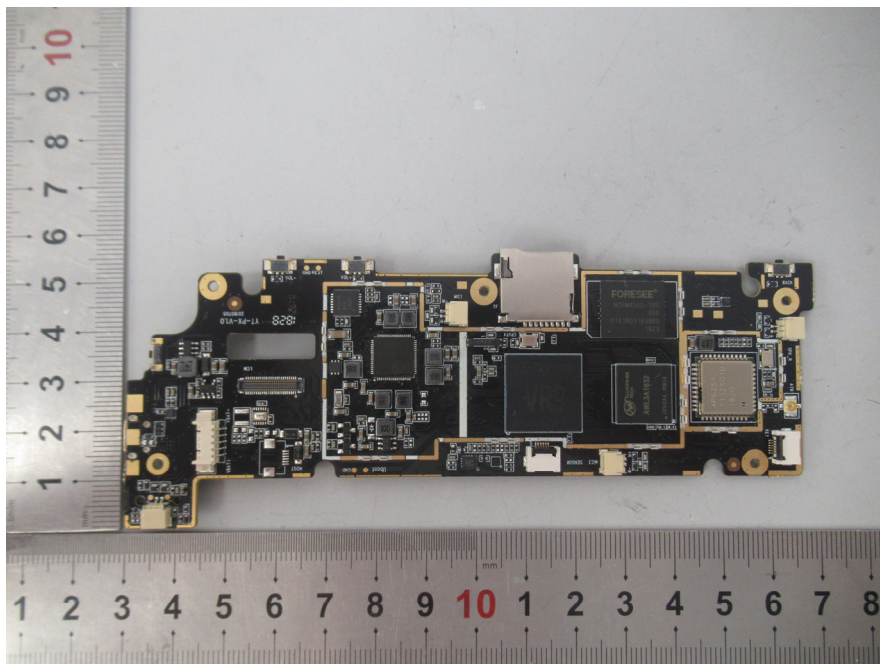
EUT – Cover off View-6



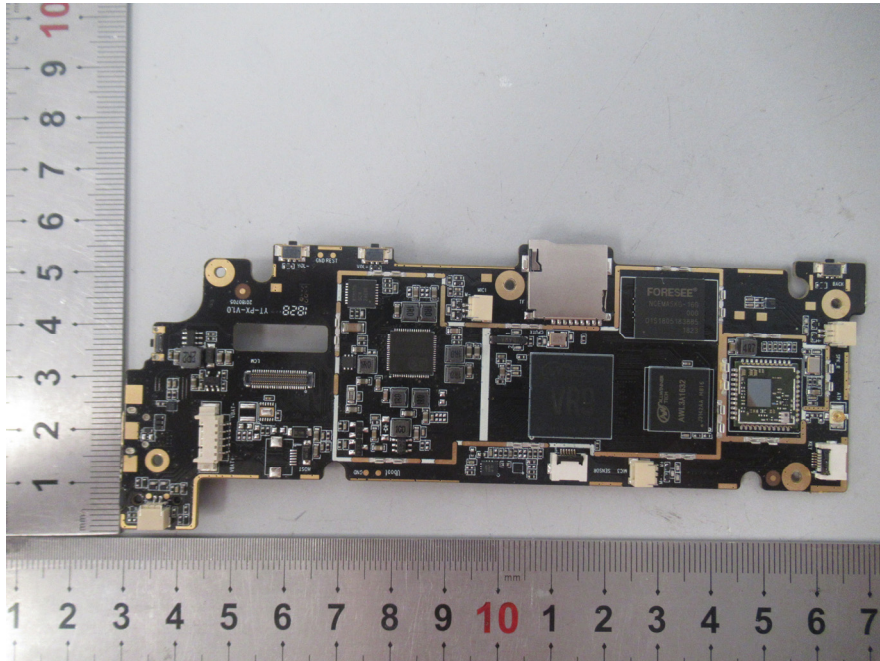
EUT – PCB-2 Top View



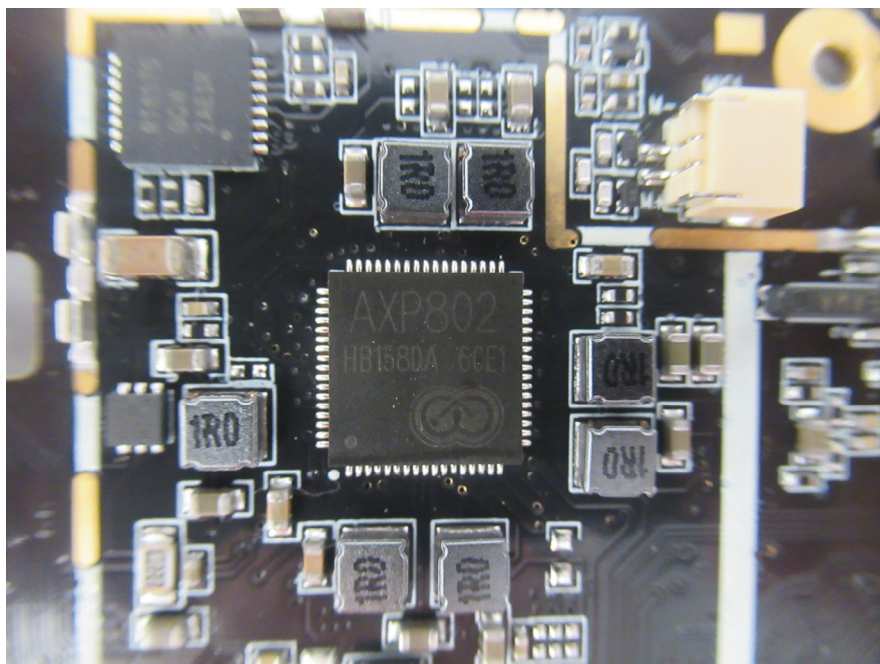
EUT –PCB-2 Top Shielding off View-1



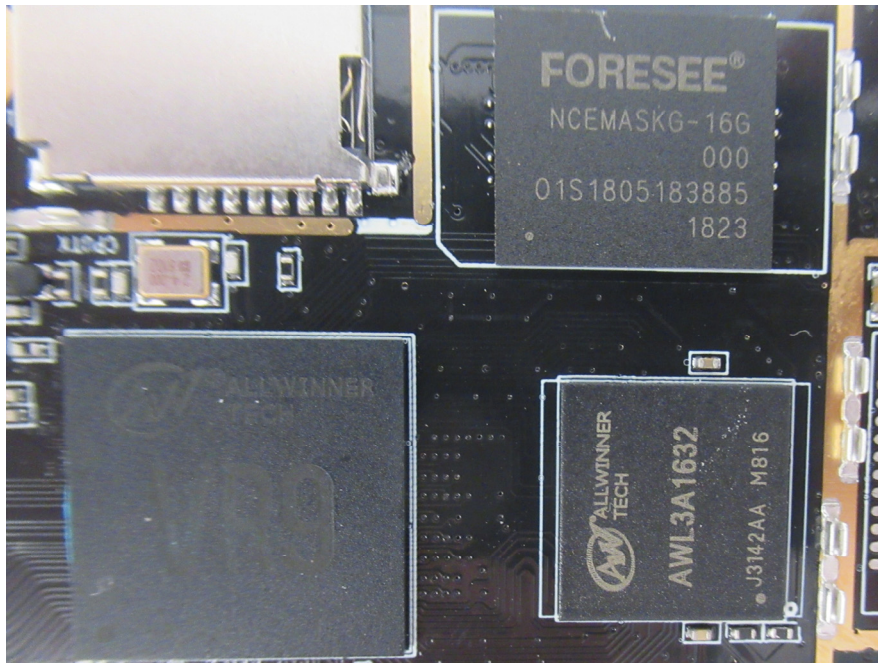
EUT –PCB-2 Top Shielding off View-2



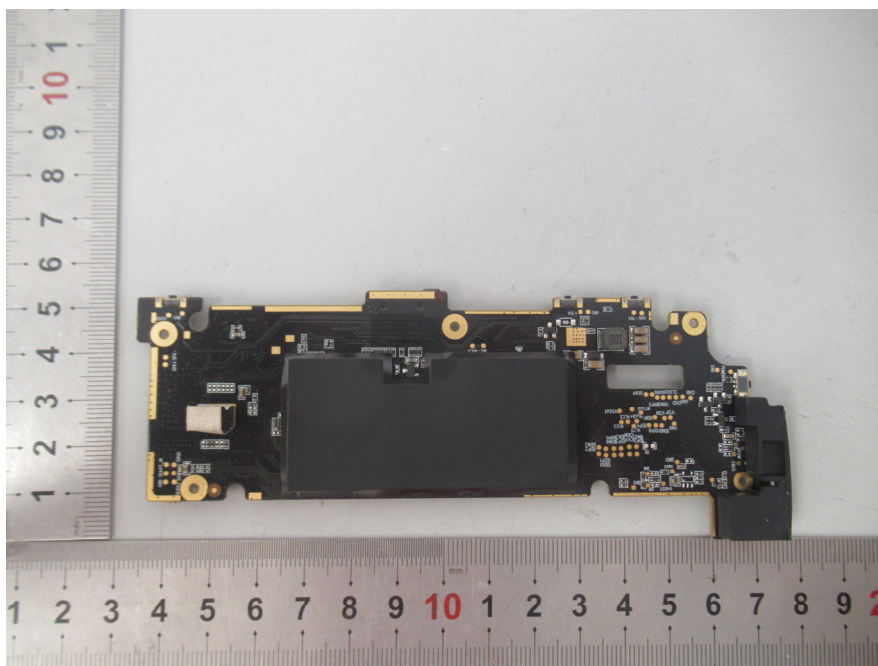
EUT –PCB-2 Top Chip View-1



EUT –PCB-2 Top Chip View-2



EUT – PCB-2 Bottom View



EUT –PCB-2 Bottom Shielding off View

