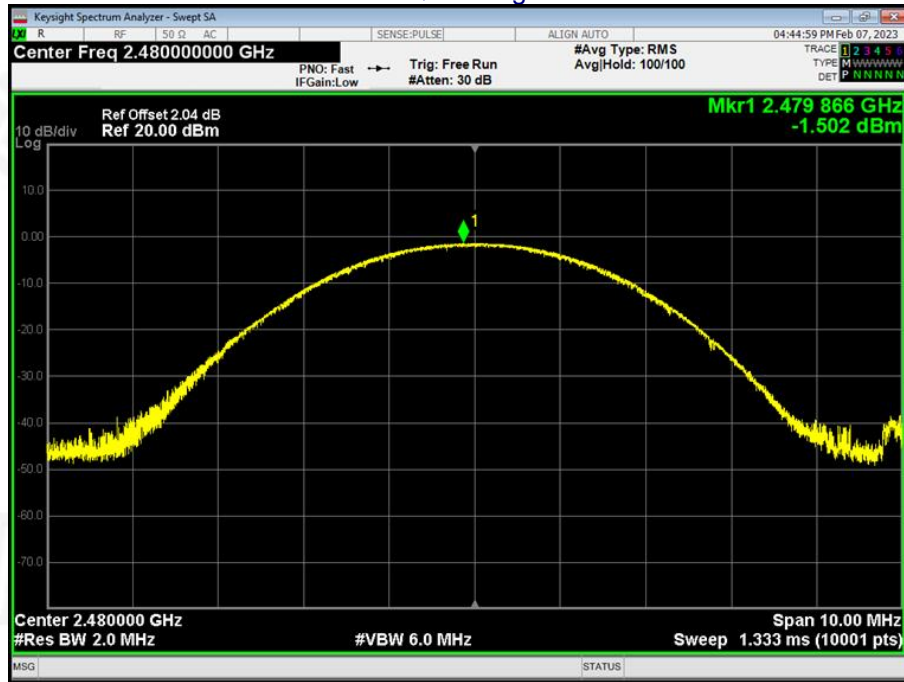
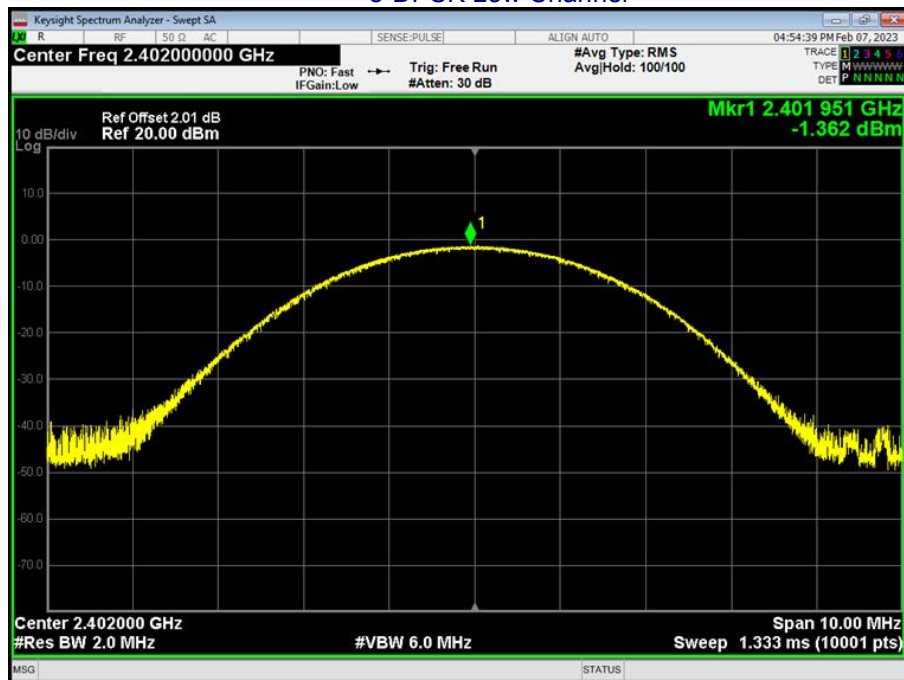




$\pi/4$ -DQPSK High Channel

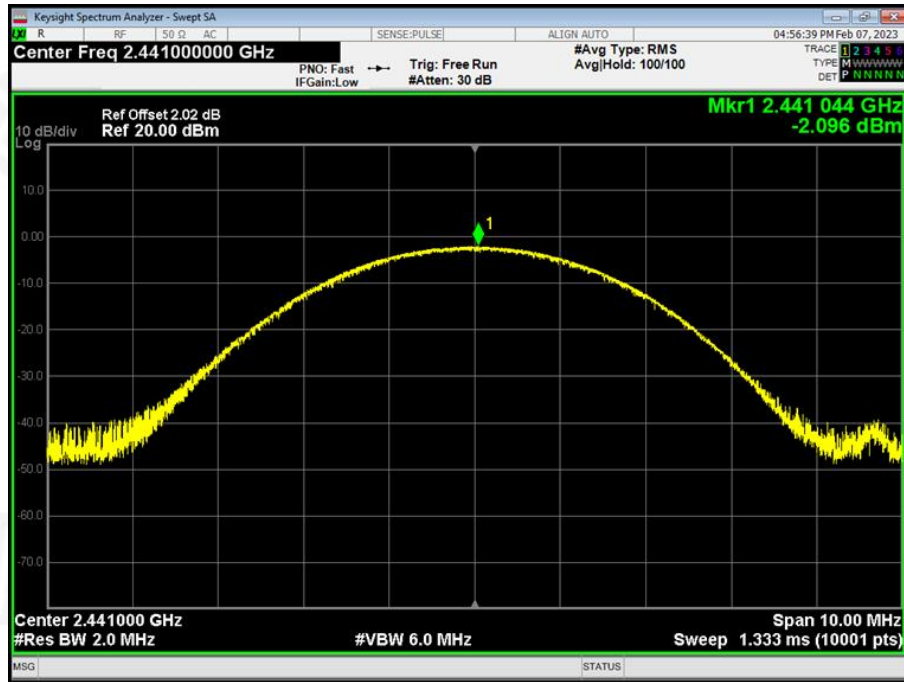


8-DPSK Low Channel

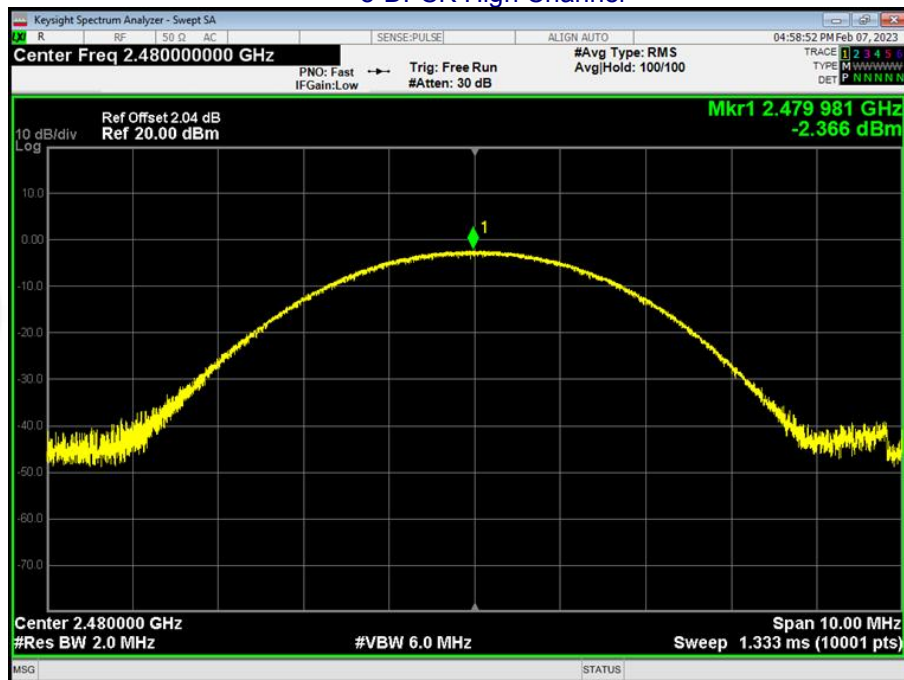




8-DPSK Middle Channel



8-DPSK High Channel





9. HOPPING CHANNEL SEPARATION

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)
Test Method:	ANSI C63.10:2013
Receiver setup:	RBW=30KHz, VBW=100KHz, detector=Peak
Limit:	GFSK: 20dB bandwidth $\pi/4$ -DQPSK & 8DSK: 0.025MHz or 2/3 of the 20dB bandwidth (whichever is greater)

9.1 Test Setup



9.2 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 30kHz. VBW = 100kHz , Span = 3.0MHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. Use the marker-delta function to determine the separation between the peaks of the adjacent channels. The limit is specified in one of the subparagraphs of this Section Submit this plot.

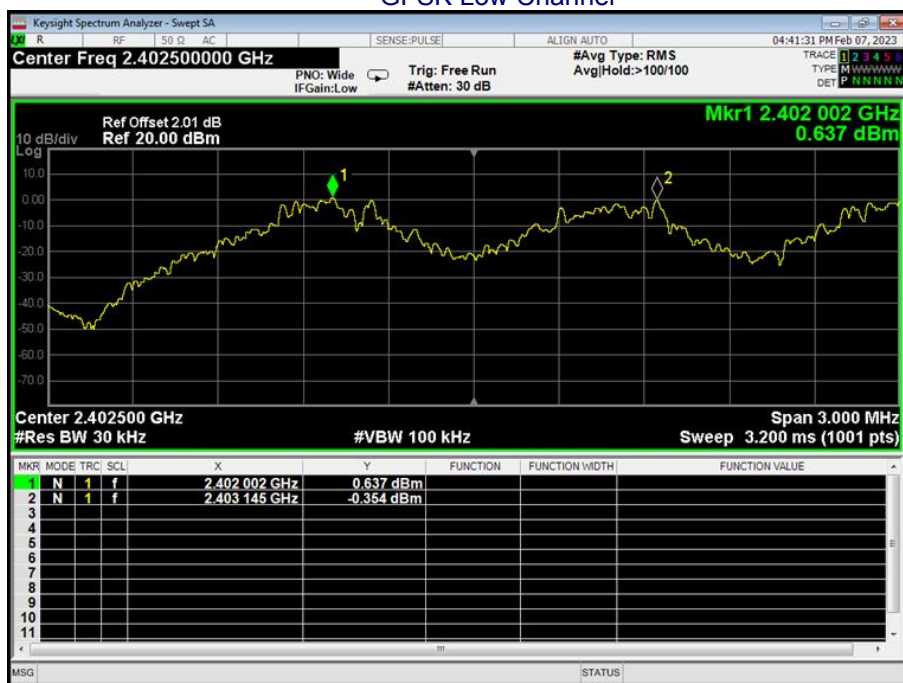
9.3 DEVIATION FROM STANDARD

No deviation.



9.4 Test Result

Modulation	Test Channel	Separation (MHz)	Limit(MHz)	Result
GFSK	Low	1.1430	0.8524	PASS
GFSK	Middle	0.8940	0.8747	PASS
GFSK	High	1.0050	0.8572	PASS
$\pi/4$ -DQPSK	Low	0.9810	0.9573	PASS
$\pi/4$ -DQPSK	Middle	0.9840	0.9500	PASS
$\pi/4$ -DQPSK	High	1.0140	0.9480	PASS
8-DPSK	Low	1.0020	0.9467	PASS
8-DPSK	Middle	1.1370	0.9340	PASS
8-DPSK	High	0.9840	0.9627	PASS

Test plots
GFSK Low Channel



GFSK Middle Channel

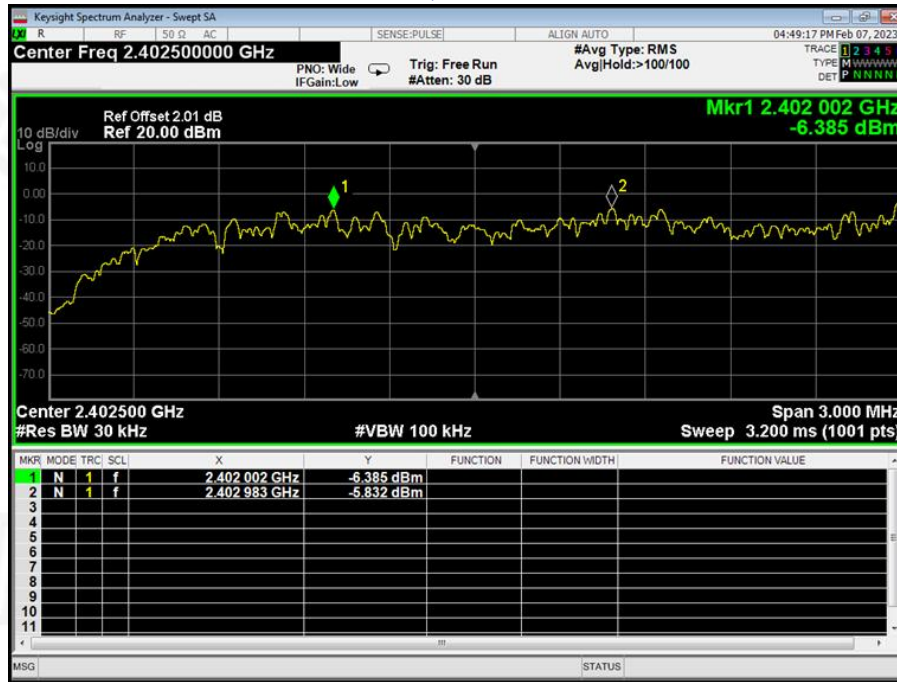


GFSK High Channel

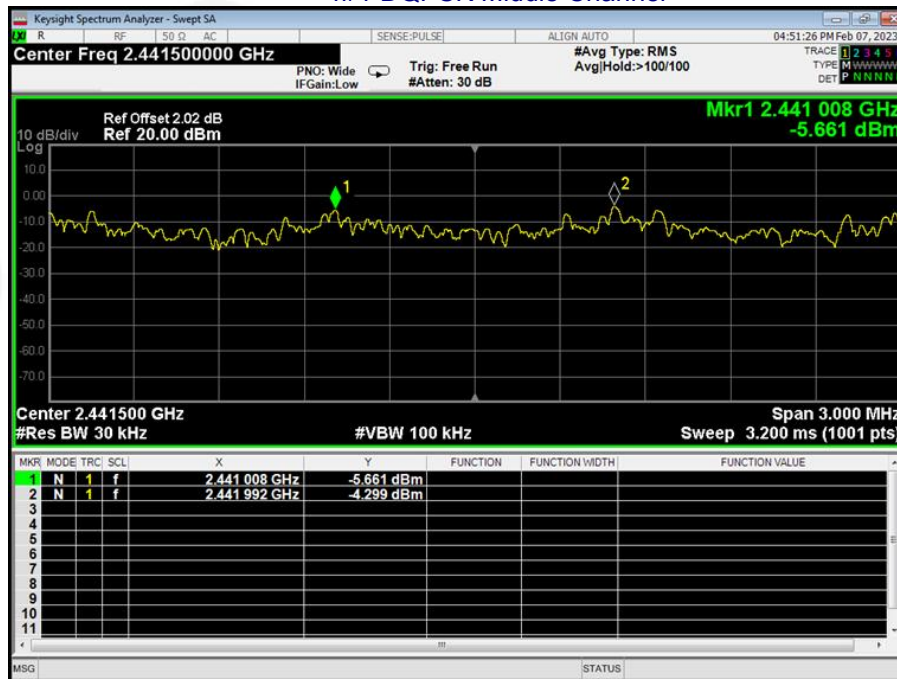




$\pi/4$ -DQPSK Low Channel

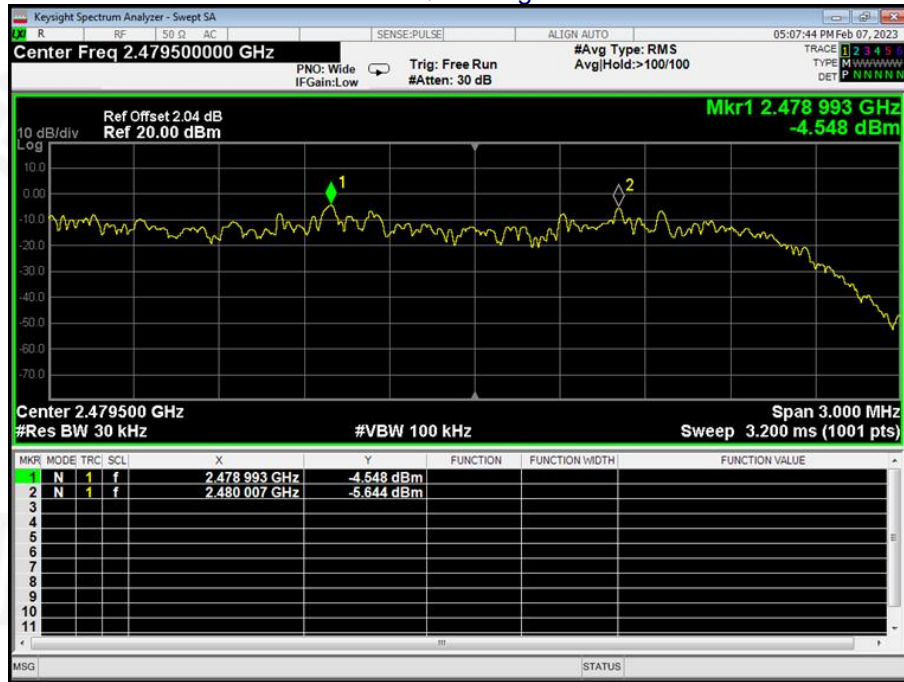


$\pi/4$ -DQPSK Middle Channel

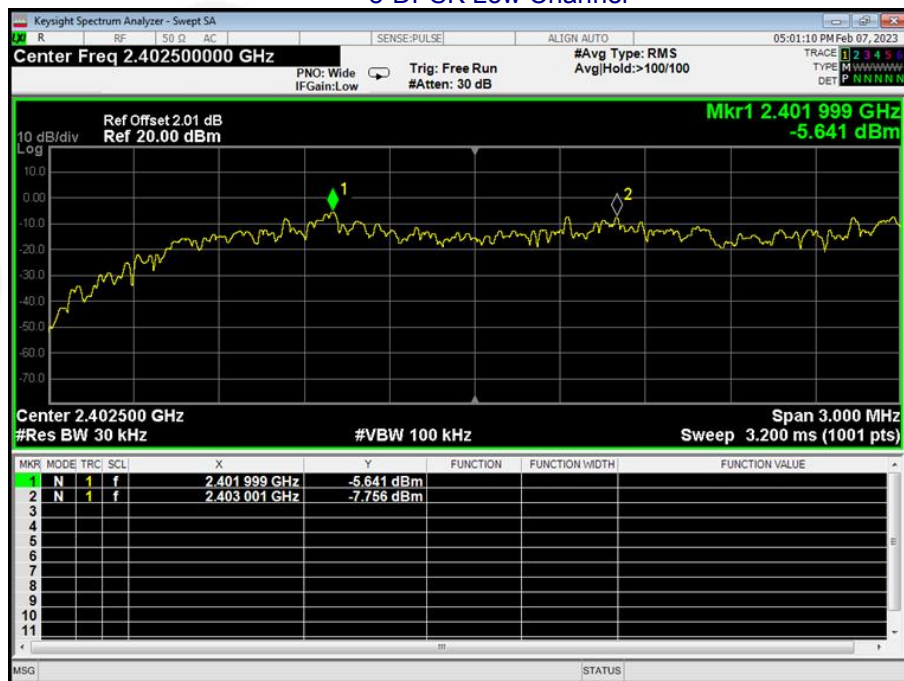




$\pi/4$ -DQPSK High Channel

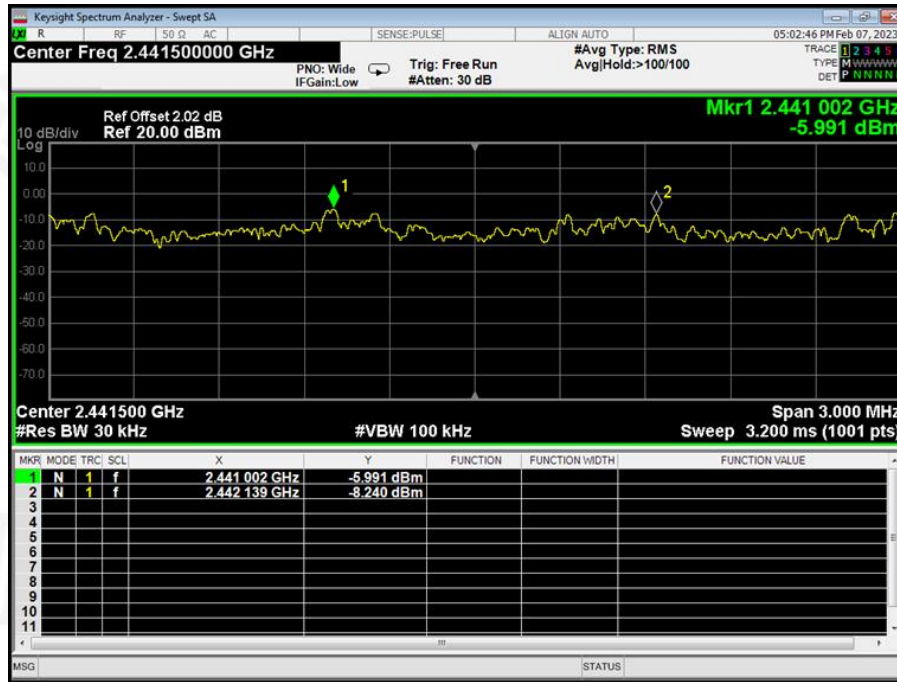


8-DPSK Low Channel

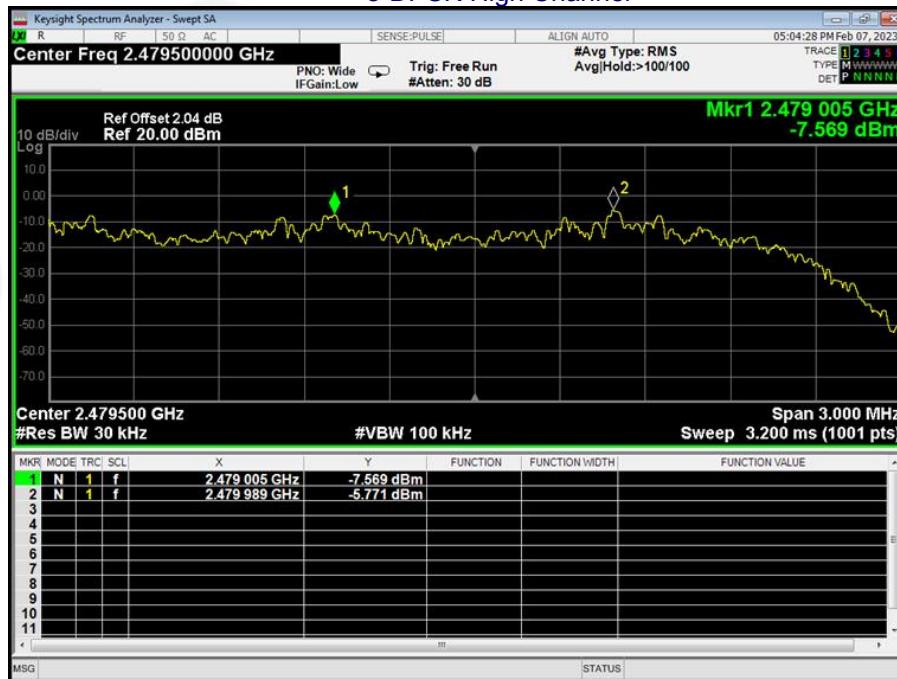




8-DPSK Middle Channel



8-DPSK High Channel





10.NUMBER OF HOPPING FREQUENCY

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2013
Receiver setup:	RBW=100kHz, VBW=300kHz, Frequency range=2400MHz-2483.5MHz, Detector=Peak
Limit:	15 channels

10.1 Test Setup



10.2 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 100kHz. VBW = 300kHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. It may prove necessary to break the span up to sections. in order to clearly show all of the hopping frequencies. The limit is specified in one of the subparagraphs of this Section.
4. Set the spectrum analyzer: Start Frequency = 2.4GHz, Stop Frequency = 2.4835GHz. Sweep=auto;

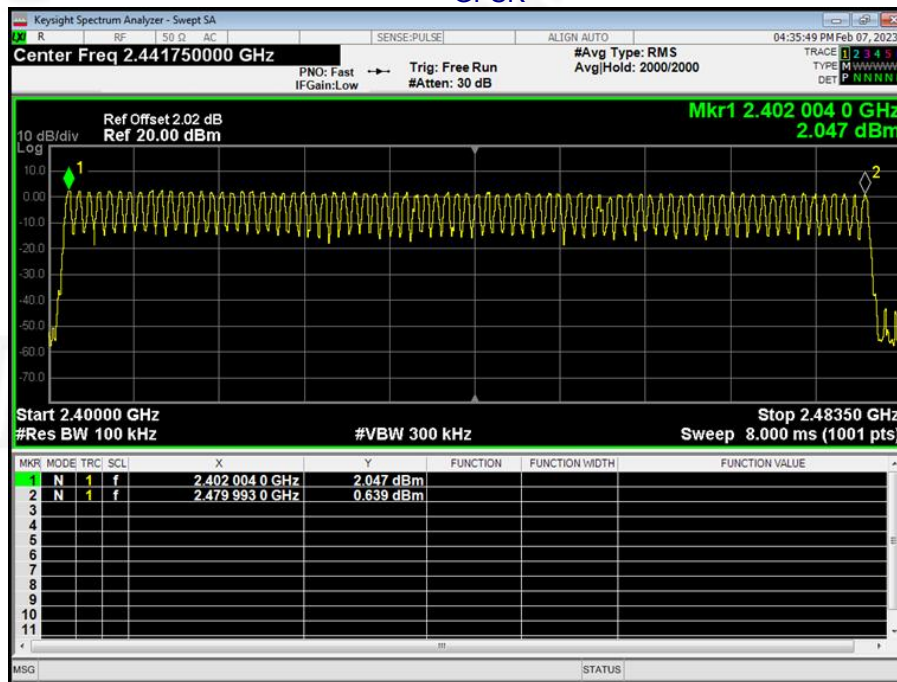
10.3 DEVIATION FROM STANDARD

No deviation.



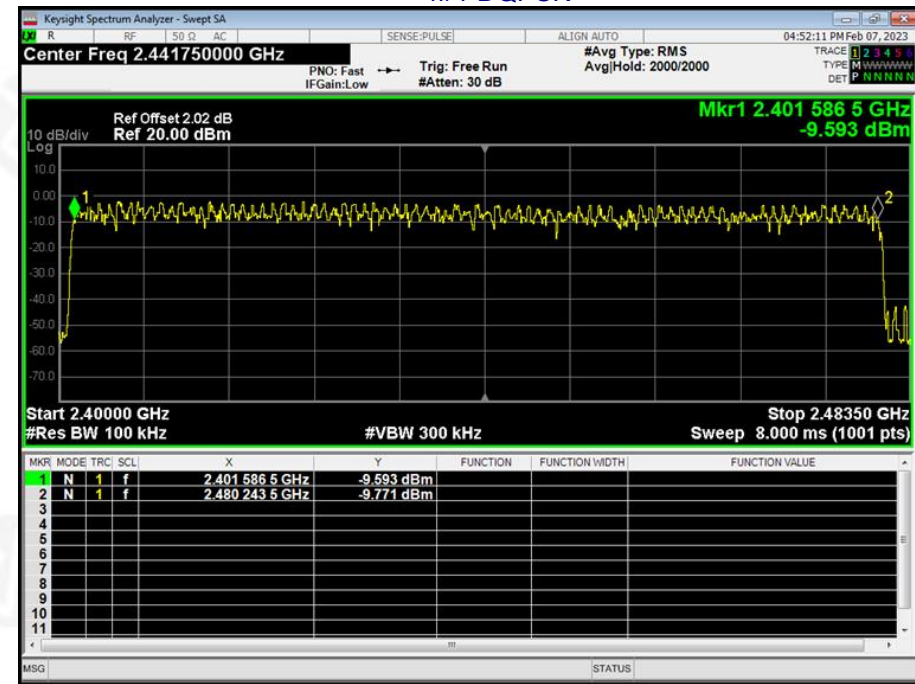
10.4 Test Result

Test Plots:
79 Channels in total
GFSK

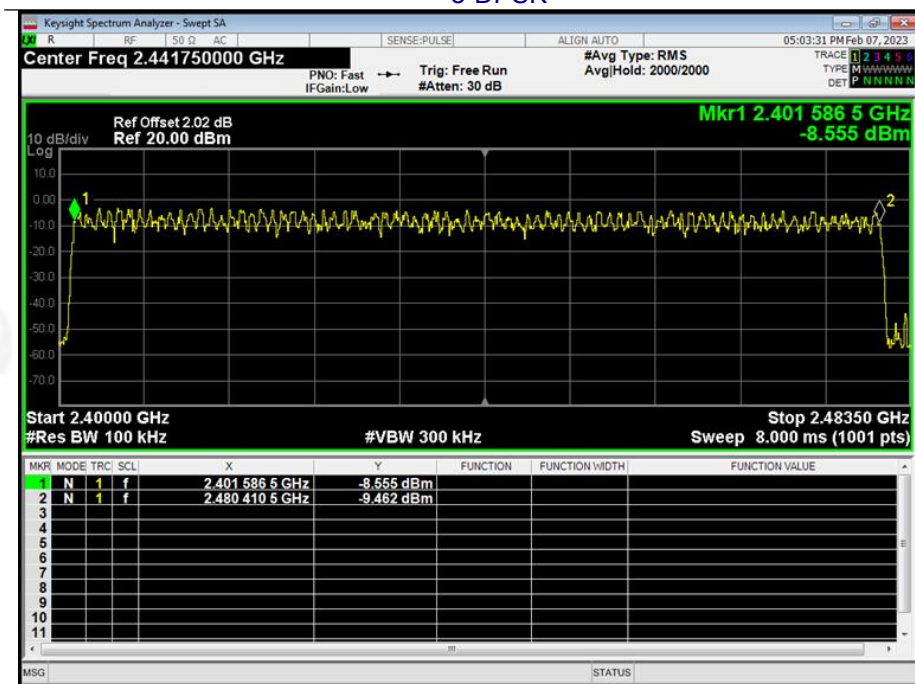




$\pi/4$ -DQPSK



8-DPSK

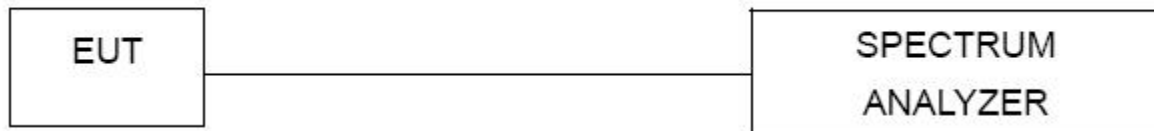




11. DWELL TIME

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10:2013
Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak
Limit:	0.4 Second

11.1 Test Setup



11.2 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0Hz;
3. Set RBW = 1MHz and VBW = 3MHz. Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g.. data rate. modulation format. etc.). repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

11.3 DEVIATION FROM STANDARD

No deviation.



11.4 Test Result

GFSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	DH1	121.600	400	Pass
2441MHz	DH3	262.760	400	Pass
2441MHz	DH5	307.627	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

Test channel: as blow

CH:2441MHz time slot= $0.380(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 121.600 \text{ms}$

CH:2441MHz time slot= $1.636(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 262.760 \text{ms}$

CH:2441MHz time slot= $2.884(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 307.627 \text{ms}$

$\pi/4$ -DQPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	2DH1	124.480	400	Pass
2441MHz	2DH3	262.560	400	Pass
2441MHz	2DH5	308.160	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

Test channel: as blow

CH:2441MHz time slot= $0.389(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 124.480 \text{ms}$

CH:2441MHz time slot= $1.641(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 262.560 \text{ms}$

CH:2441MHz time slot= $2.889(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 308.160 \text{ms}$

8-DPSK mode:

Frequency	Packet	Dwell time(ms)	Limit(ms)	Result
2441MHz	3DH1	124.480	400	Pass
2441MHz	3DH3	262.400	400	Pass
2441MHz	3DH5	308.373	400	Pass

Remarks:

The test period: $T = 0.4 \text{ Second/Channel} \times 79 \text{ Channel} = 31.6 \text{ s}$

Test channel: as blow

CH:2441MHz time slot= $0.389(\text{ms}) \times (1600 / (2 \times 79)) \times 31.6 = 124.480 \text{ms}$

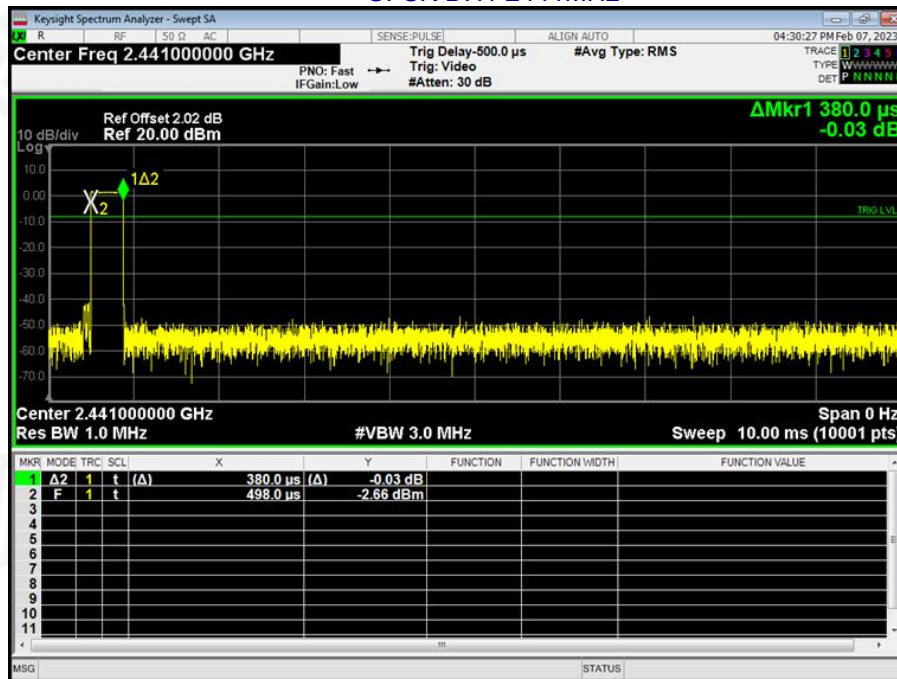
CH:2441MHz time slot= $1.640(\text{ms}) \times (1600 / (4 \times 79)) \times 31.6 = 262.400 \text{ms}$

CH:2441MHz time slot= $2.891(\text{ms}) \times (1600 / (6 \times 79)) \times 31.6 = 308.373 \text{ms}$

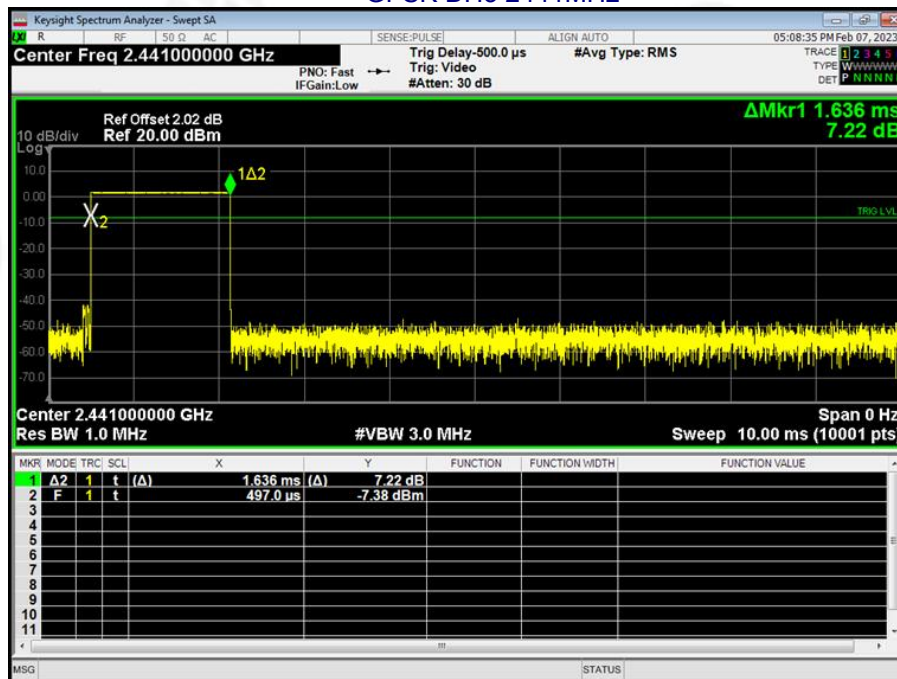


Test Plots

GFSK DH1 2441MHz

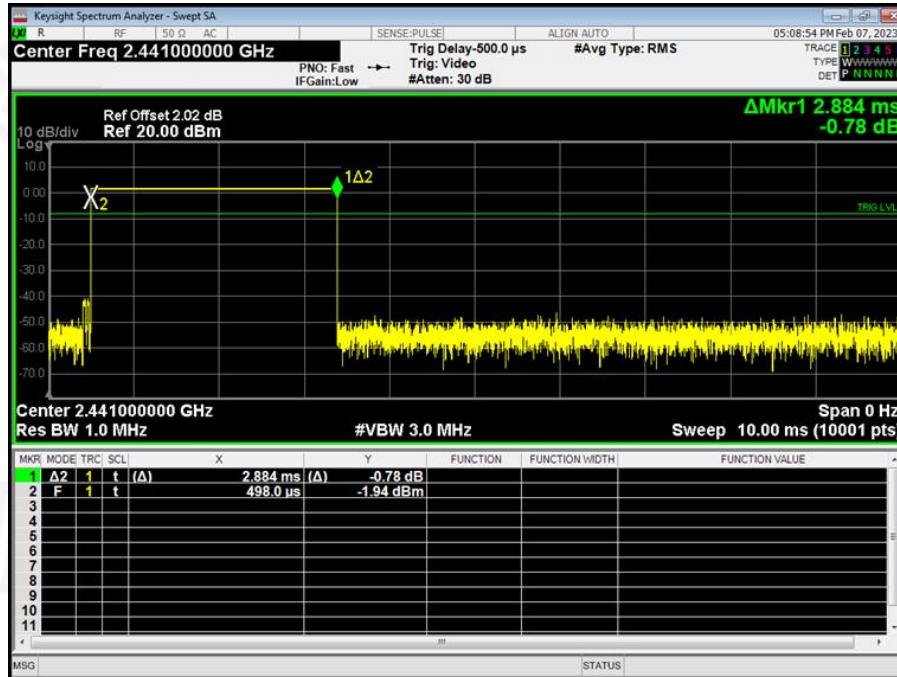


GFSK DH3 2441MHz

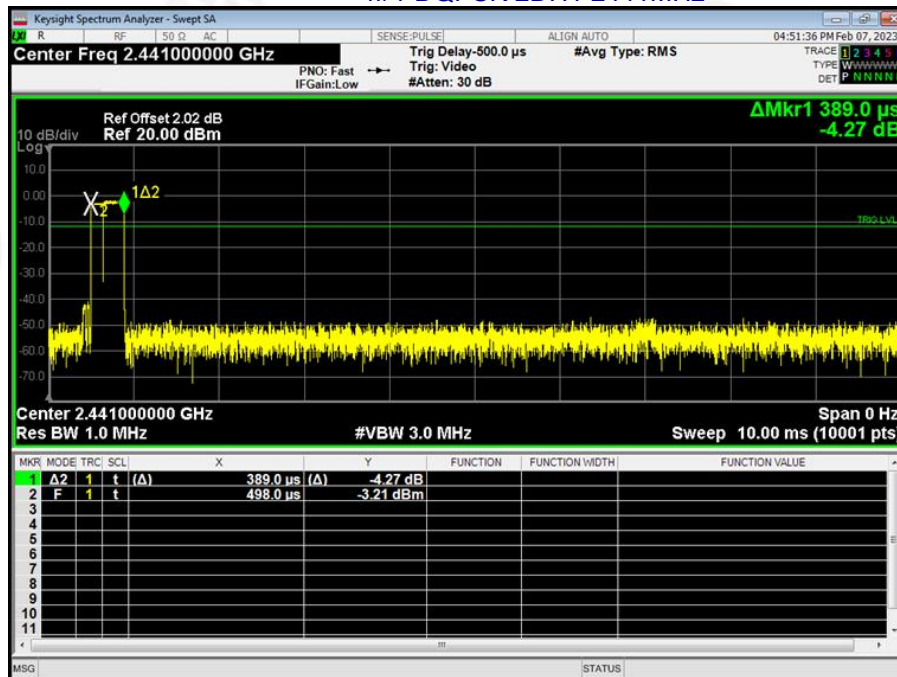




GFSK DH5 2441MHz

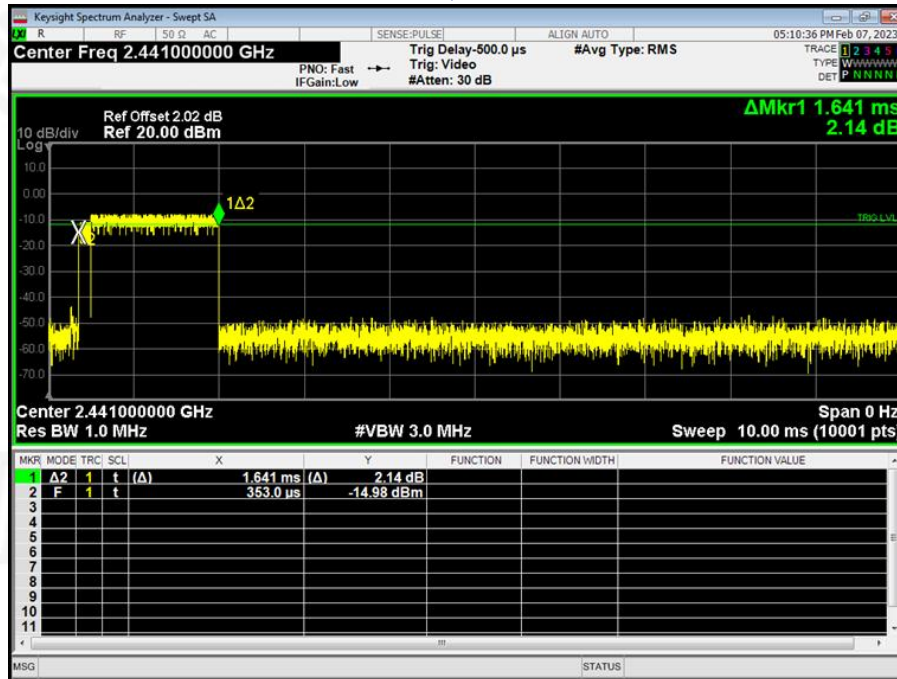


$\pi/4$ -DQPSK 2DH1 2441MHz

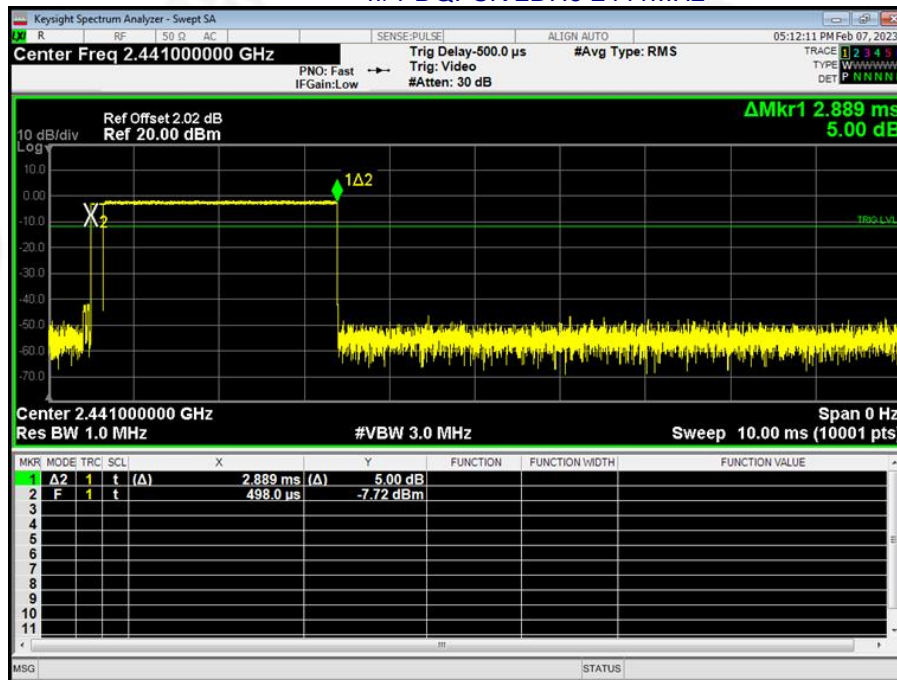




$\pi/4$ -DQPSK 2DH3 2441MHz



$\pi/4$ -DQPSK 2DH5 2441MHz



[illegible]

Center Freq 2.441000000 GHz

Ref Offset 2.02 dB
Ref 20.00 dBm

PNO: Fast IFGain: Low

Trig Delay-500.0 μs
Trig: Video
#Atten: 30 dB

#Avg Type: RMS

05:13:38 PM Feb 07, 2023

TRACE 1 2 3 4 5 6
TYPE W W W W W W W W
DET P N N N N N N

ΔMkr1 1.640 ms
-1.44 dB

TRIG-LVL

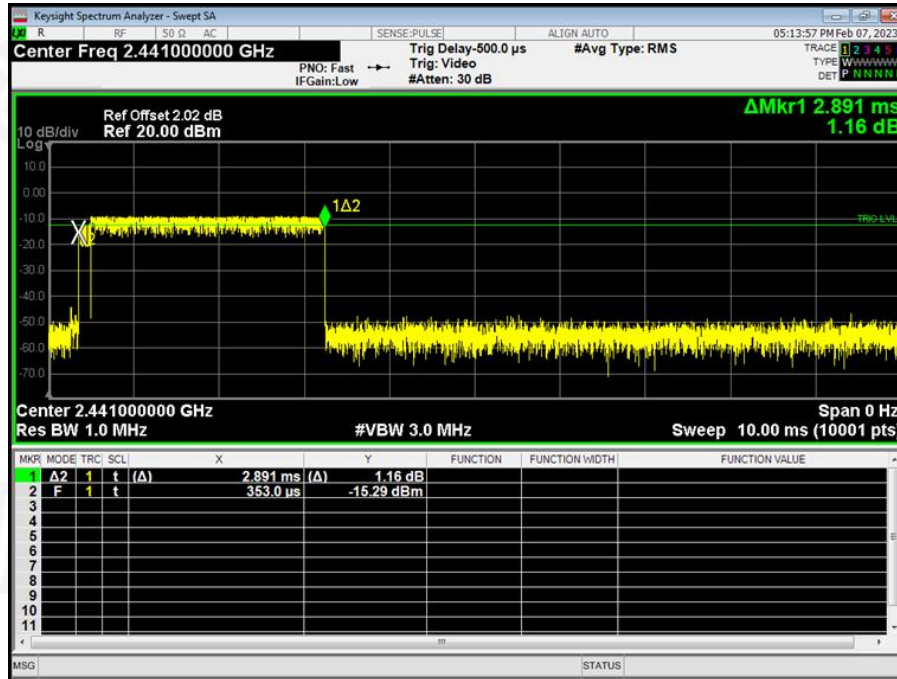
Center 2.441000000 GHz
Res BW 1.0 MHz
#VBW 3.0 MHz
Span 0 Hz
Sweep 10.00 ms (10001 pts)

MKR	MODE	TRC	SCL	X	Y	FUNCTION	FUNCTION WIDTH	FUNCTION VALUE
1	A2	1	t	(Δ)	1.640 ms (Δ)	-1.44 dB		
2	F	1	t		355.0 μs	-16.23 dBm		
3								
4								
5								
6								
7								
8								
9								
10								
11								

MSG STATUS



8-DPSK 3DH5 2441MHz





12. Antenna Requirement

Standard requirement:	FCC Part15 C Section 15.203 /247(c)
15.203 requirement: An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited. 15.247(c) (1)(i) requirement: (i) Systems operating in the 2400-2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.	
EUT Antenna:	
The antenna is PCB Antenna, the best case gain of the antennas is -0.58dBi, reference to the appendix II for details	



13. Test Setup Photo

Reference to the appendix I for details.

14. EUT Constructional Details

Reference to the appendix II for details.

***** END OF REPORT *****