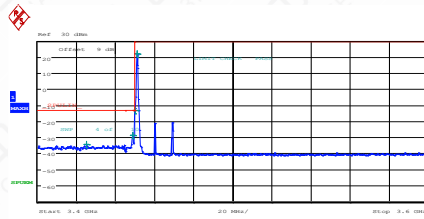
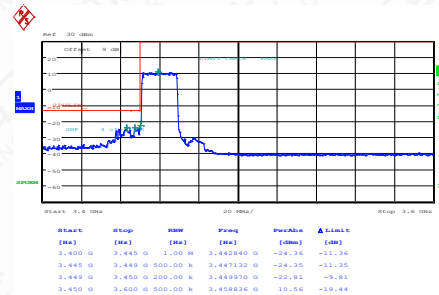


Test Graphs



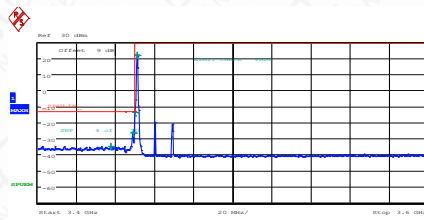
Date: 22.FEB.2023 21:33:54



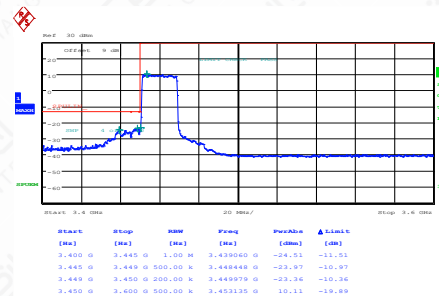
Date: 22.FEB.2023 21:34:08

NTNV_N77-3450-3550_PC2_30_20_L_TID1_RID1#1

NTNV_N77-3450-3550_PC2_30_20_L_TID3_RID1#1



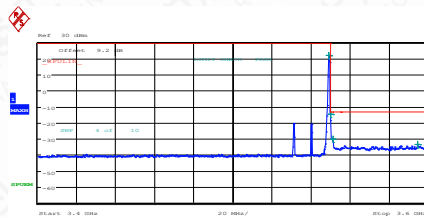
Date: 22.FEB.2023 21:34:26



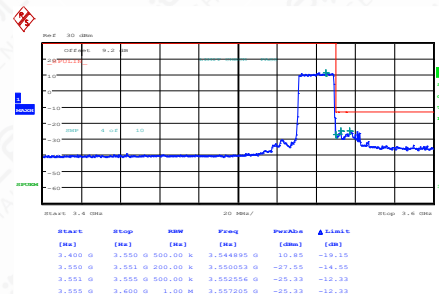
Date: 22.FEB.2023 21:34:39

NTNV_N77-3450-3550_PC2_30_20_L_TID4_RID1#1

NTNV_N77-3450-3550_PC2_30_20_L_TID6_RID1#1

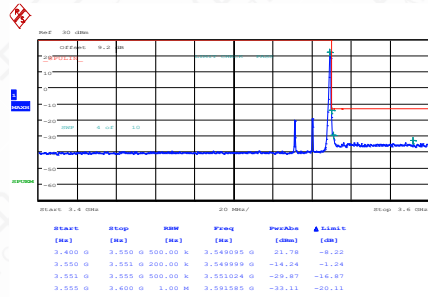


Date: 22.FEB.2023 21:35:00



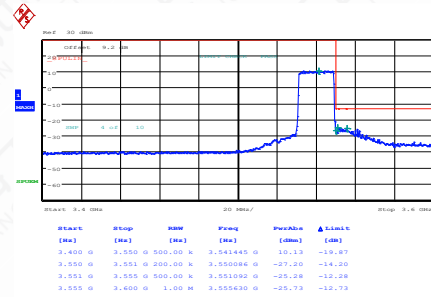
Date: 22.FEB.2023 21:35:11

NTNV_N77-3450-3550_PC2_30_20_H_TID2_RID1#1



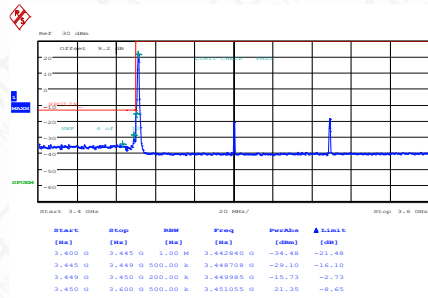
Date: 22.FEB.2023 21:35:31

NTNV_N77-3450-3550_PC2_30_20_H_TID3_RID1#1



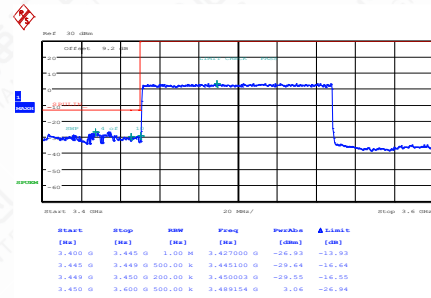
Date: 22.FEB.2023 21:35:43

NTNV_N77-3450-3550_PC2_30_20_H_TID5_RID1#1



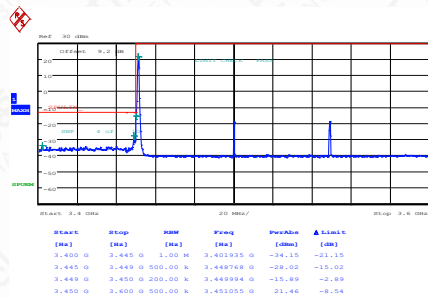
Date: 22.FEB.2023 21:36:03

NTNV_N77-3450-3550_PC2_30_20_H_TID6_RID1#1



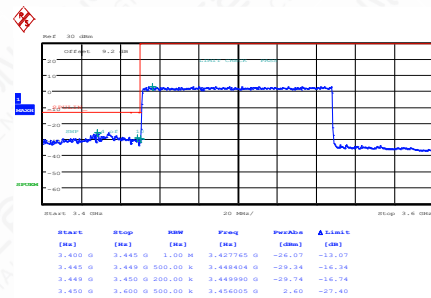
Date: 22.FEB.2023 21:36:15

NTNV_N77-3450-3550_PC2_30_100_L_TID1_RID1#1



Date: 22.FEB.2023 21:36:33

NTNV_N77-3450-3550_PC2_30_100_L_TID3_RID1#1



Date: 22.FEB.2023 21:36:47

NTNV_N77-3450-3550_PC2_30_100_L_TID4_RID1#1

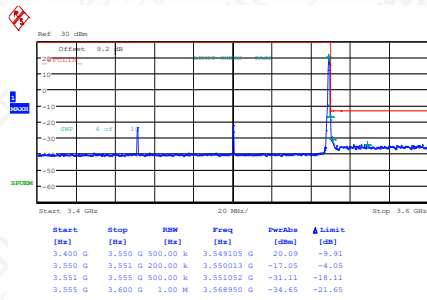


Date: 22.FEB.2023 21:36:47

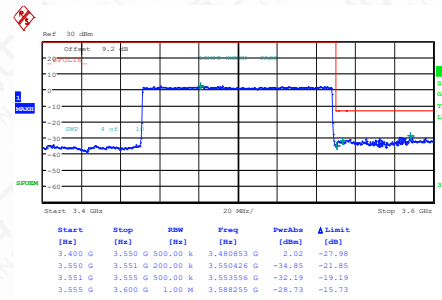
NTNV_N77-3450-3550_PC2_30_100_L_TID6_RID1#1



Date: 22.FEB.2023 21:36:47



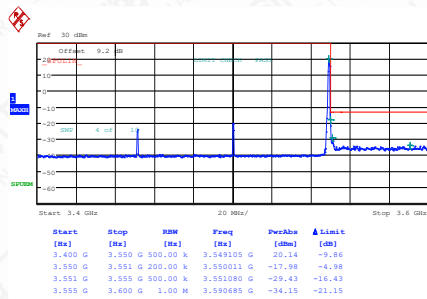
Date: 23.FEB.2023 19:42:07



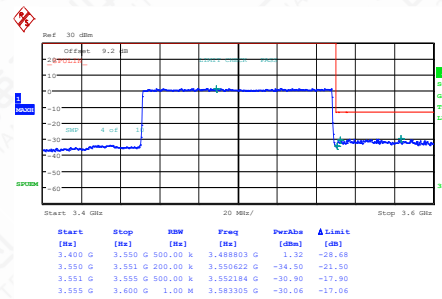
Date: 23.FEB.2023 19:42:20

NTNV_N77-3450-3550_PC2_30_100_H_TID2_RID1#1

NTNV_N77-3450-3550_PC2_30_100_H_TID3_RID1#1



Date: 23.FEB.2023 19:42:42



Date: 23.FEB.2023 19:42:55

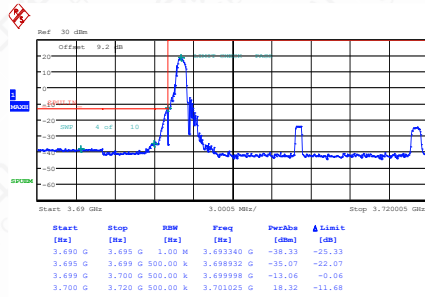
NTNV_N77-3450-3550_PC2_30_100_H_TID5_RID1#1

NTNV_N77-3450-3550_PC2_30_100_H_TID6_RID1#1

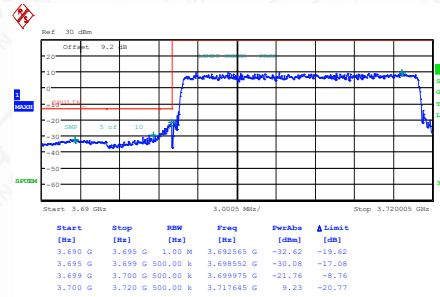
N77(3700-3980)Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N77-3700-3980	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	20	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	20	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	20	DFT-QPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N77-3700-3980	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS

Test Graphs



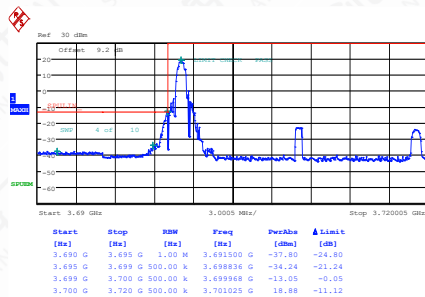
Date: 23.FEB.2023 20:05:25



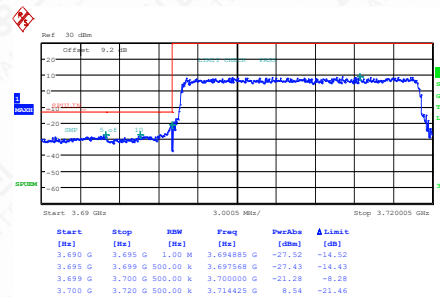
Date: 23.FEB.2023 19:23:50

NTNV_N77-3700-3980_PC2_30_20_L_TID1_RID1#1

NTNV_N77-3700-3980_PC2_30_20_L_TID3_RID1#1



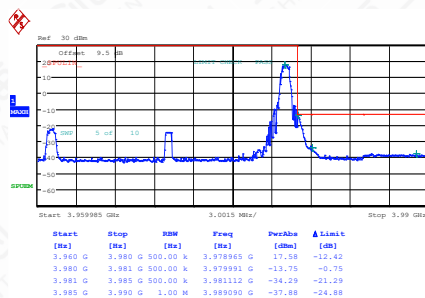
Date: 23.FEB.2023 19:52:18



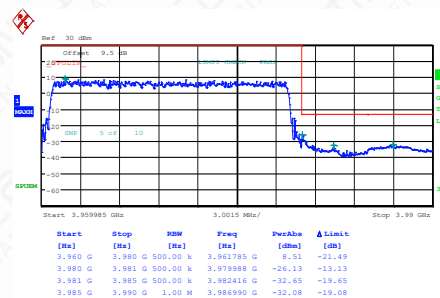
Date: 23.FEB.2023 19:24:38

NTNV_N77-3700-3980_PC2_30_20_L_TID4_RID1#1

NTNV_N77-3700-3980_PC2_30_20_L_TID6_RID1#1

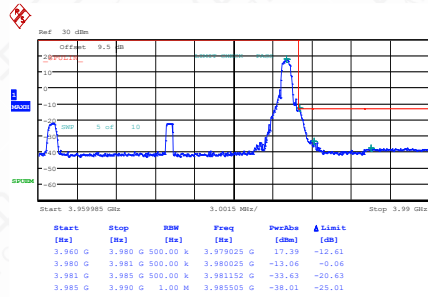


Date: 23.FEB.2023 19:25:06



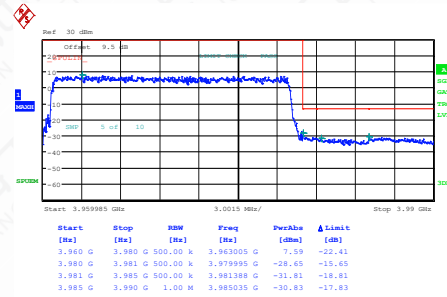
Date: 23.FEB.2023 19:25:19

NTNV_N77-3700-3980_PC2_30_20_H_TID2_RID1#1



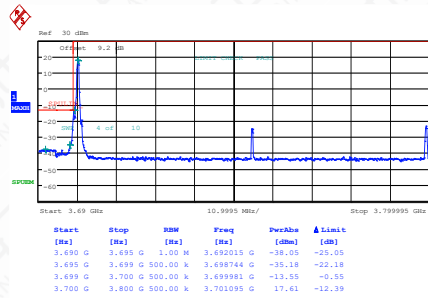
Date: 23.FEB.2023 19:25:40

NTNV_N77-3700-3980_PC2_30_20_H_TID3_RID1#1



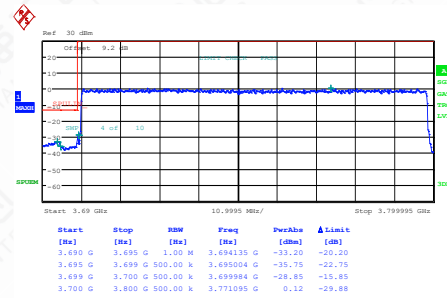
Date: 23.FEB.2023 19:25:54

NTNV_N77-3700-3980_PC2_30_20_H_TID5_RID1#1



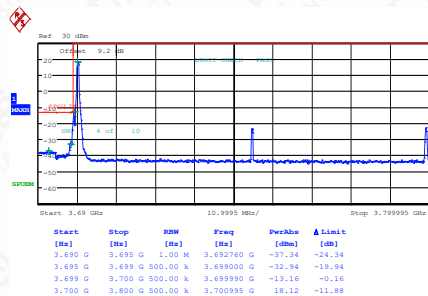
Date: 23.FEB.2023 19:52:47

NTNV_N77-3700-3980_PC2_30_20_H_TID6_RID1#1



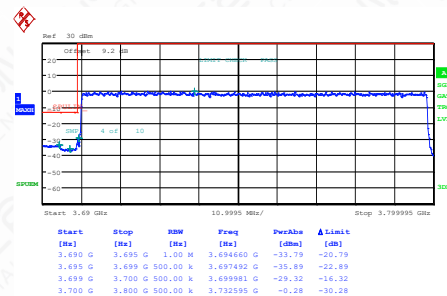
Date: 23.FEB.2023 19:26:36

NTNV_N77-3700-3980_PC2_30_100_L_TID1_RID1#1



Date: 23.FEB.2023 19:26:56

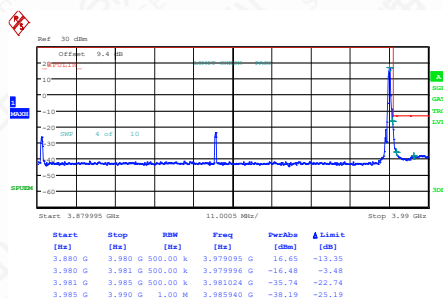
NTNV_N77-3700-3980_PC2_30_100_L_TID3_RID1#1



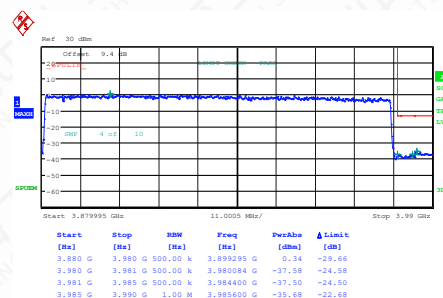
Date: 23.FEB.2023 19:27:11

NTNV_N77-3700-3980_PC2_30_100_L_TID4_RID1#1

NTNV_N77-3700-3980_PC2_30_100_L_TID6_RID1#1



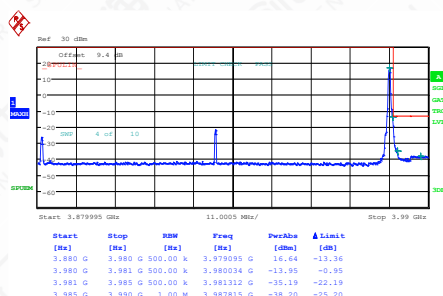
Date: 23.FEB.2023 19:27:39



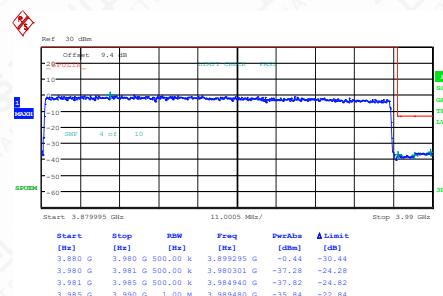
Date: 23.FEB.2023 19:27:52

NTNV_N77-3700-3980_PC2_30_100_H_TID2_RID1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID3_RID1#1



Date: 23.FEB.2023 19:28:14



Date: 23.FEB.2023 19:28:26

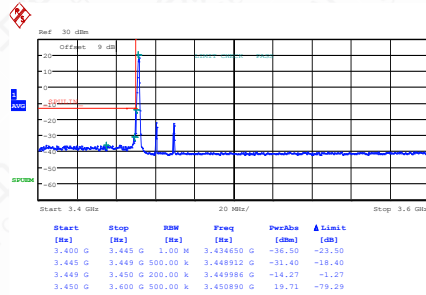
NTNV_N77-3700-3980_PC2_30_100_H_TID5_RID1#1

NTNV_N77-3700-3980_PC2_30_100_H_TID6_RID1#1

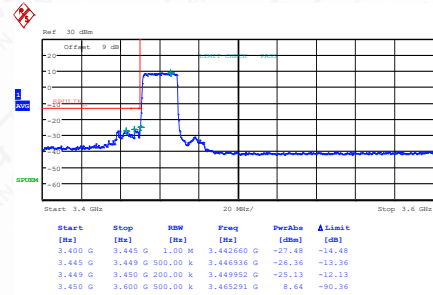
N78(3450-3550)Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
N78-3450-3550	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	20	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	20	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	20	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	20	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	20	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	20	DFT-QPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N78-3450-3550	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS

Test Graphs



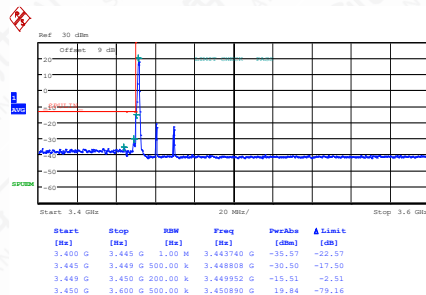
Date: 23.FEB.2023 19:30:53



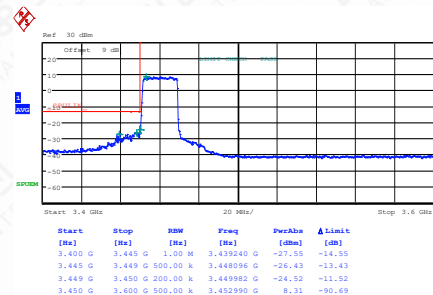
Date: 23.FEB.2023 19:31:07

NTNV_N78-3450-3550_PC2_30_20_L_TID1_RID1#1

NTNV_N78-3450-3550_PC2_30_20_L_TID3_RID1#1



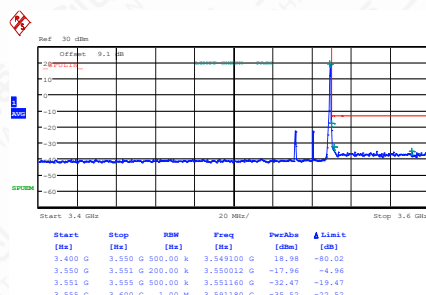
Date: 23.FEB.2023 19:31:25



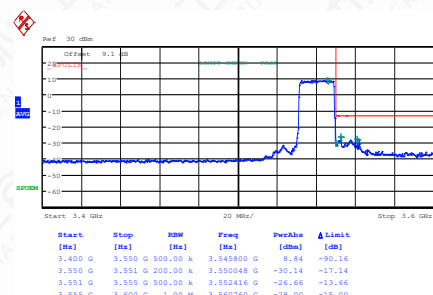
Date: 23.FEB.2023 19:31:38

NTNV_N78-3450-3550_PC2_30_20_L_TID4_RID1#1

NTNV_N78-3450-3550_PC2_30_20_L_TID6_RID1#1



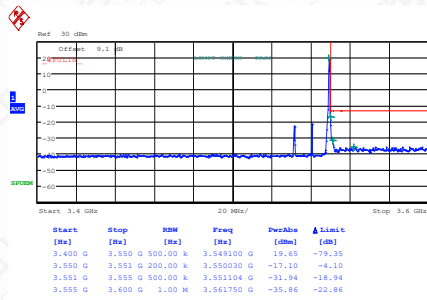
Date: 23.FEB.2023 19:32:05



Date: 23.FEB.2023 19:32:17

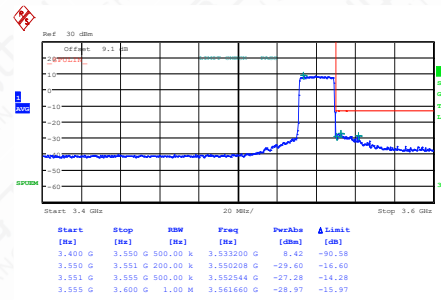
NTNV_N78-3450-3550_PC2_30_20_H_TID2_RID1#1

NTNV_N78-3450-3550_PC2_30_20_H_TID3_RID1#1



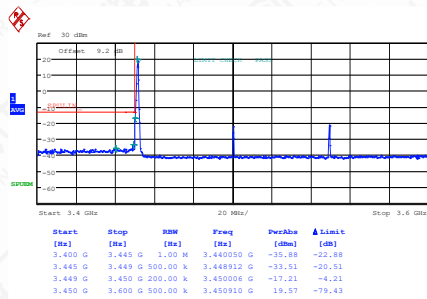
Date: 23.FEB.2023 19:32:37

NTNV_N78-3450-3550_PC2_30_20_H_TID5_RID1#1



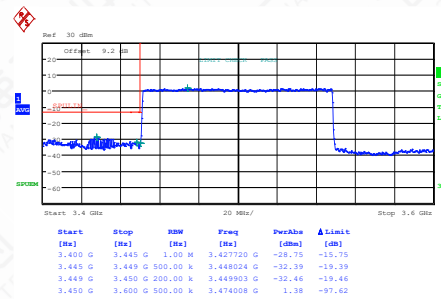
Date: 23.FEB.2023 19:32:48

NTNV_N78-3450-3550_PC2_30_20_H_TID6_RID1#1



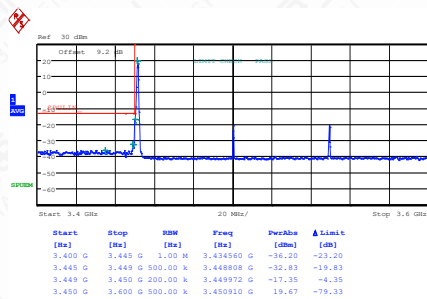
Date: 23.FEB.2023 19:33:14

NTNV_N78-3450-3550_PC2_30_100_L_TID1_RID1#1



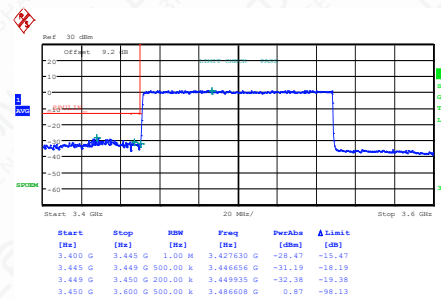
Date: 23.FEB.2023 19:33:26

NTNV_N78-3450-3550_PC2_30_100_L_TID3_RID1#1



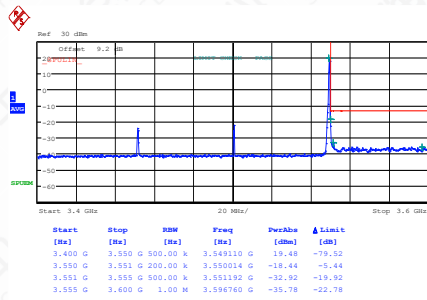
Date: 23.FEB.2023 19:33:45

NTNV_N78-3450-3550_PC2_30_100_L_TID4_RID1#1

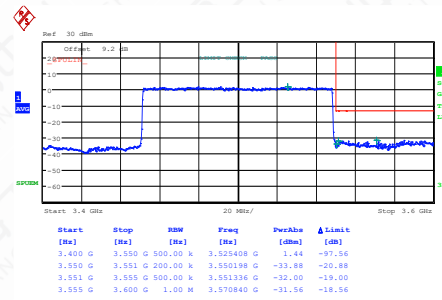


Date: 23.FEB.2023 19:33:57

NTNV_N78-3450-3550_PC2_30_100_L_TID6_RID1#1



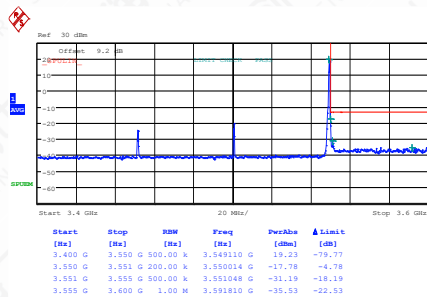
Date: 23.FEB.2023 19:34:18



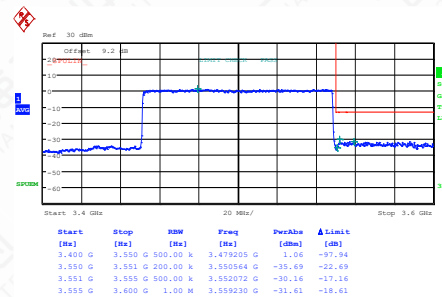
Date: 23.FEB.2023 19:34:27

NTNV_N78-3450-3550_PC2_30_100_H_TID2_RID1#1

NTNV_N78-3450-3550_PC2_30_100_H_TID3_RID1#1



Date: 23.FEB.2023 19:34:49



Date: 23.FEB.2023 19:35:00

NTNV_N78-3450-3550_PC2_30_100_H_TID5_RID1#1

NTNV_N78-3450-3550_PC2_30_100_H_TID6_RID1#1

END OF REPORT