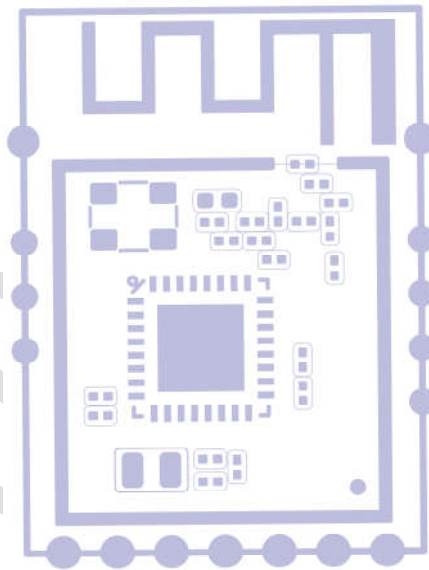


# EW N - 8 2 5 8 F A T 1 C C

Datasheet V1.0

ZigBee Soc Module



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## 1 General Specifications

EWN-8258FAT1CC is ZigBee SoC solution with internal Flash and audio support, which combines the features and functions needed for all 2.4GHz IoT standards into a single SoC.

## 2 Features

### 2.1 ZigBee Features

- ❖ Based on IEEE 802.15.4 Standard, certified ZigBee Pro and ZigBee 3.0 platform, with ZHA/ZLL profile and ZigBee 3.0 device support.
- ❖ Uses multi-hop mesh networking to eliminate single points of failure and expand the reach of networks.
- ❖ Allow low power operation, even support the Green Power feature.
- ❖ Supports networks of thousands of nodes, providing a networking for the smart home or the smart city.
- ❖ Uses a variety of security mechanisms, such as AES-128 encryption, device and network keys and frame counters.
- ❖ Include all application level functionality of ZigBee Smart Energy.
- ❖ Support seamless interoperability with a wide variety of smart devices.
- ❖ Over the air (OTA) firmware upgrade with hardware support.

### 2.2 General features

- ❖ 14bit 10-channel SAR ADC
- ❖ I/O : I2C、UART、PWM
- ❖ Program memory: internal 1MB Flash.
- ❖ Data memory: 64kB on-chip SRAM, including up to 32kB SRAM with retention in deep sleep, and one 32kB SRAM without retention in deep sleep.
- ❖ Embedded hardware AES and AES-CCM.
- ❖ Embedded low power comparator.
- ❖ Swire debug Interface.

### 3 System Block Diagram

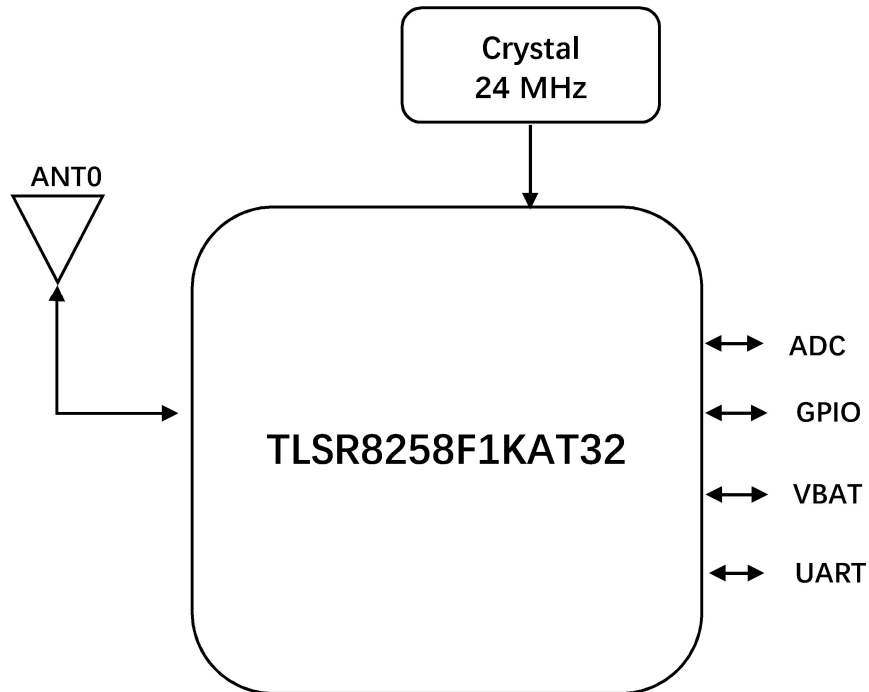


Fig 1 EWN-8258FAT1CC System Block Diagram

### 4 PHY Specification

Table 1 EWN-8258FAT1CC ZigBee RF Parameters

|                  |                                                 |
|------------------|-------------------------------------------------|
| <b>Protocol</b>  | ZigBee                                          |
| <b>Interface</b> | UART                                            |
| <b>Frequency</b> | 2405 MHz ~ 2480MHz                              |
| <b>Data rate</b> | 802.15.4 250Kbps, $\pm 500\text{Khz}$ deviation |

Table 2 RX Performance

| Item                                                                                      | Sym.          | min  | typ   | Max  | Unit | Condition               |
|-------------------------------------------------------------------------------------------|---------------|------|-------|------|------|-------------------------|
| <b>IEEE802.15.4(250Kbps) RF_Rx performance (<math>\pm 500\text{KHz}</math> deviation)</b> |               |      |       |      |      |                         |
| <b>Sensitivity</b>                                                                        | 250Kbps       |      | -99.5 |      | dBm  |                         |
| <b>Frequency Offset Tolerance</b>                                                         |               | -300 |       | +300 | KHz  |                         |
| <b>Adjacent channel rejection</b>                                                         | -1/+1 channel |      | 42/42 |      | dB   | Wanted signal at -82dBm |
|                                                                                           | -2/+2 channel |      | 42/42 |      | dB   | Wanted signal at -82dBm |
| <b>Error vector magnitude</b>                                                             | EVM           |      |       | 2%   |      | Max(10dBm)power output  |

Table 3 TX Performance

| Item                                           | Sym. | min | typ | Max | Unit | Condition |
|------------------------------------------------|------|-----|-----|-----|------|-----------|
| <b>IEEE802.15.4(250Kbps) RF_Tx performance</b> |      |     |     |     |      |           |
| <b>Output power,<br/>maximum setting</b>       |      |     | 10  |     | dBm  |           |
| <b>Output power,<br/>minimum setting</b>       |      |     | -45 |     | dBm  |           |
| <b>Programmable<br/>output power range</b>     |      | 55  |     |     | dB   |           |
| <b>Modulation 20dB<br/>bandwidth</b>           |      |     | 2.7 |     | MHz  |           |

## 5 Other Specifications

Table 4 other Specifications

|                              |                                              |
|------------------------------|----------------------------------------------|
| <b>Operating Temperature</b> | -40°C~+105°C                                 |
| <b>Storage Temperature</b>   | Module: -40°C~+105°C<br>Package: -20°C~+70°C |
| <b>Operating Humidity</b>    | RH 95%(Non-Condensing)                       |
| <b>Storage Humidity</b>      | RH 95%(Non-Condensing)                       |
| <b>Humidity level</b>        | Level 3                                      |

## 6 DC Specifications

Table 5 DC Specifications

| Item                   | Sym.               | Min. | Typ. | Max. | Unit | Condition              |
|------------------------|--------------------|------|------|------|------|------------------------|
| <b>VDD_3.3V</b>        | V <sub>BAT</sub>   | 1.8  | 3.3  | 3.6  | V    | 3.3V Supply Voltage    |
| <b>RX current</b>      | I <sub>RX</sub>    | -    | 5.3  | -    | mA   | whole chip             |
| <b>TX current</b>      | I <sub>TX</sub>    | -    | 4.8  | -    | mA   | whole chip @0dBm       |
| <b>TX current</b>      | I <sub>TX</sub>    | -    | 20   | -    | mA   | whole chip @10dBm      |
| <b>Deep sleep with</b> | I <sub>deep1</sub> | -    | 1    | -    | uA   | 8kB SRAM retention     |
|                        |                    | -    | 1.2  | -    | uA   | 16kB SRAM retention    |
|                        |                    | -    | 1.4  | -    | uA   | 32kB SRAM retention    |
|                        | I <sub>deep2</sub> | -    | 0.4  | -    | uA   | without SRAM retention |

## 7 Module configurations

Module Size (Unit: mm): 20.0(±0.2) \*15.0(±0.3)\*2.4 ( ±0.2 )

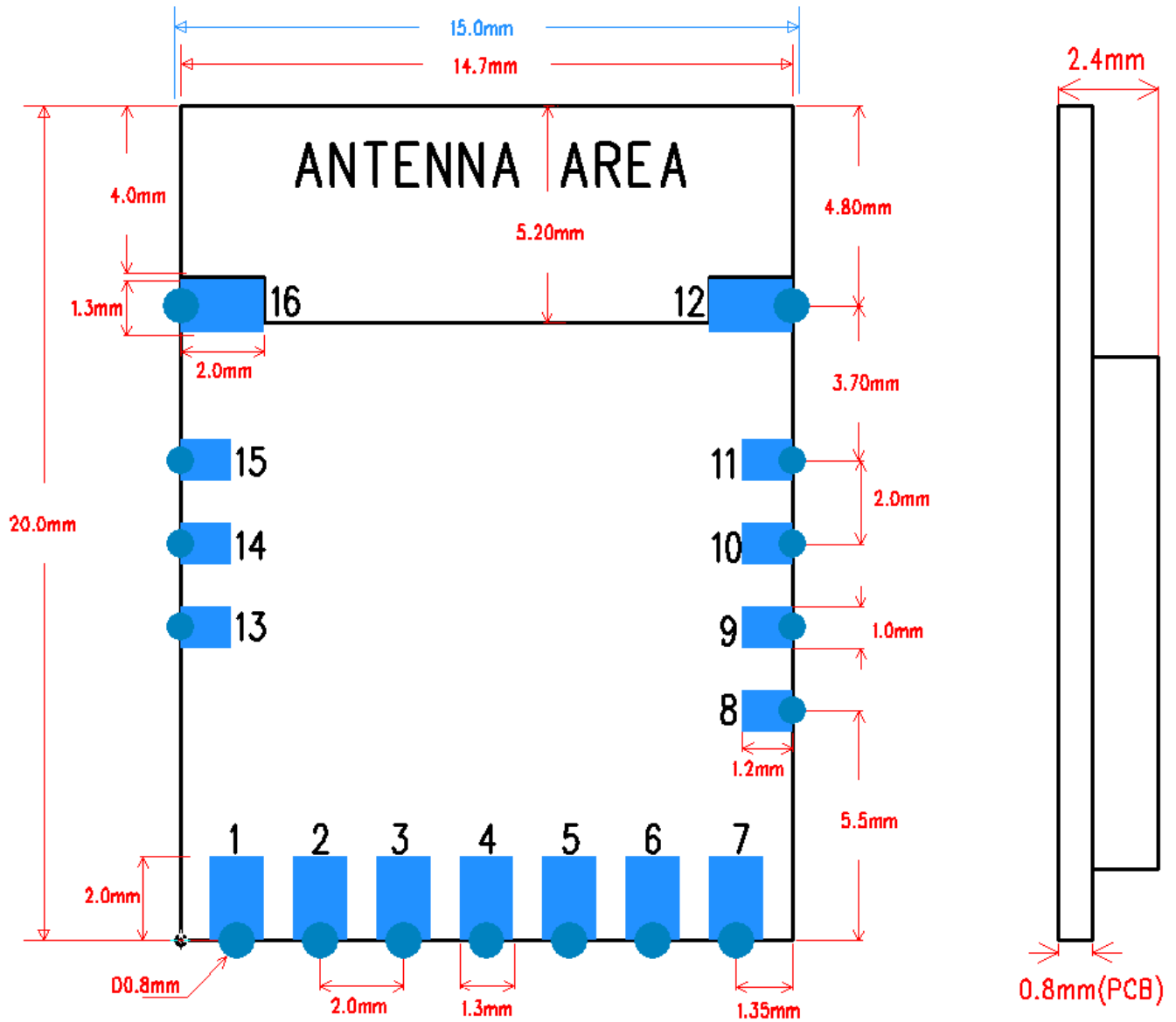


Fig 2 EWN-8258FAT1CC Module Size (TOP VIEW)

## 8 Pin Definition

Table 6 the hardware Pin definition of the module

Table 6 EWN-8258FAT1CC Pin definition

| PIN | Definition | Description                                                                                                                               |
|-----|------------|-------------------------------------------------------------------------------------------------------------------------------------------|
| 1   | VBAT       | Supply power 1.8V-3.3V ;                                                                                                                  |
| 2   | GND        | Ground                                                                                                                                    |
| 3   | PC3        | PWM1 output / UART_RX / I2C serial clock / (optional) 32kHz crystal input / PGA right channel negative input / GPIO PC[3]                 |
| 4   | PB4        | SDM positive output 0 / PWM4 output / Low power comparator input / SAR ADC input / GPIO PB[4]                                             |
| 5   | PB5        | SDM negative output 0 / PWM5 output / Low power comparator input / SAR ADC input / GPIO PB[5]                                             |
| 6   | PC4        | PWM2 output / UART_CTS / PWM0 inverting output / SAR ADC input / GPIO PC[4]                                                               |
| 7   | PC1        | I2C serial clock / PWM1 inverting output / PWM0 output / PGA left channel negative input / GPIO PC[1]                                     |
| 8   | PB7        | SDM negative output 1 / SPI data output / UART_RX / Low power comparator input / SAR ADC input / GPIO PB[7]                               |
| 9   | PC0        | I2C serial data / PWM4 inverting output / UART_RTS / PGA left channel positive input / GPIO PC[0]                                         |
| 10  | PC2        | PWM0 output / UART 7816 TRX (UART_TX) / I2C serial data / (optional) 32kHz crystal output / PGA right channel positive input / GPIO PC[2] |
| 11  | RST        | Power on reset, active low ;                                                                                                              |
| 12  | GND        | Ground                                                                                                                                    |
| 13  | SWS        | Single wire slave ;                                                                                                                       |
| 14  | PA0        | DMIC data input / PWM0 inverting output / UART_RX/ GPIO PA[0]                                                                             |
| 15  | PD7        | SPI clock (I2C_SCK) / I2S bit clock / UART 7816 TRX (UART_TX) / GPIO PD[7]                                                                |
| 16  | GND        | Ground                                                                                                                                    |

### Notes:

1. Download Interface use SWS test point.
2. Only SWS interface can be used for debugging firmware.

## 9 Module Photos

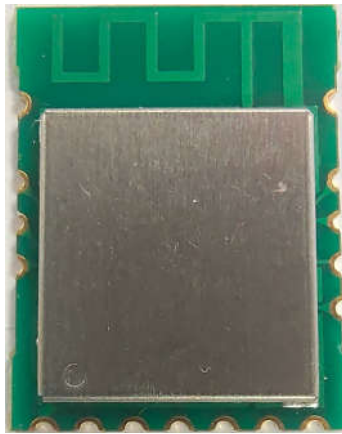


FIG 3 TOP View

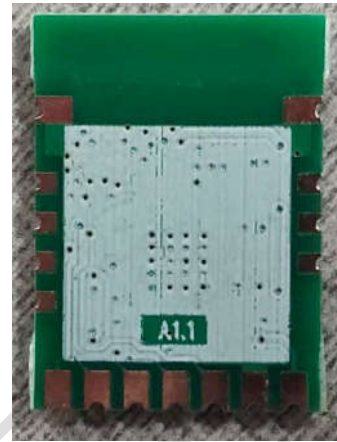


FIG 4 Bottom View

## 10Key material list

Table 7 EWN-8258FAT1CC Key material list

| Type       | Model            | Footprint | QTY. |
|------------|------------------|-----------|------|
| Crystal    | 24MHz 12pF 20ppm | 3225      | 1PCS |
| Inductance | 10uH             | 0805      | 1PCS |
| IC         | TLSR8258F1KAT32  | QFN32     | 1PCS |

## 11 Package Information

Carrier dimension: ( Unit : mm )

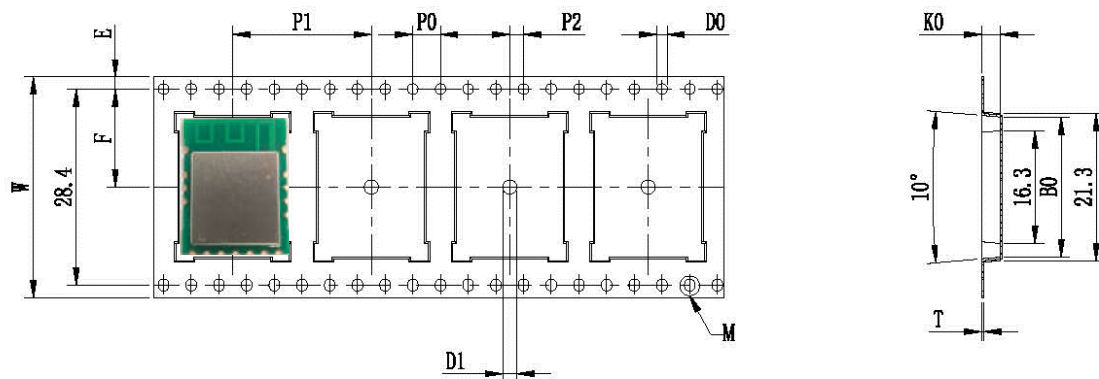


FIG 5 Carrier size

Reel dimension : D=38cm 1500PCS Modules Per Reel

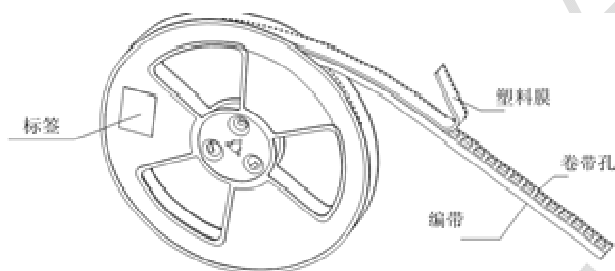


FIG 6 Reel



FIG 7 Reel figure

## 12 Reference design

### 12.1 RF

a) Under the antenna and in the two directions indicated by the arrow, avoid covering the ground, routing and placing metal components. It is better to directly hollow out the PCB in this area.

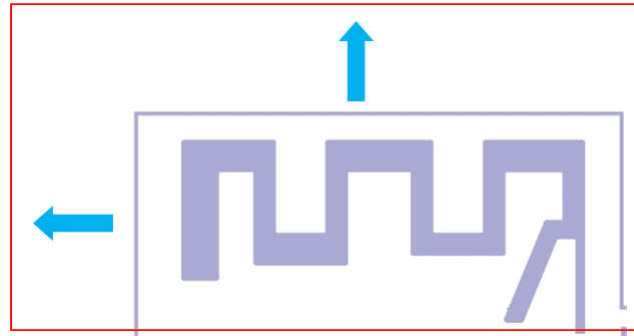


Fig 8 Antenna

b) It is recommended not to use any components within 30mm of the module antenna area, and the module baseboard should also avoid wiring and covering the ground as much as possible.

c) It is strongly recommended that the user place the antenna of the Bluetooth module close to the edge of the backplane as far as possible when laying out the PCB, as shown in the figure below, so as to ensure the good performance of the antenna.

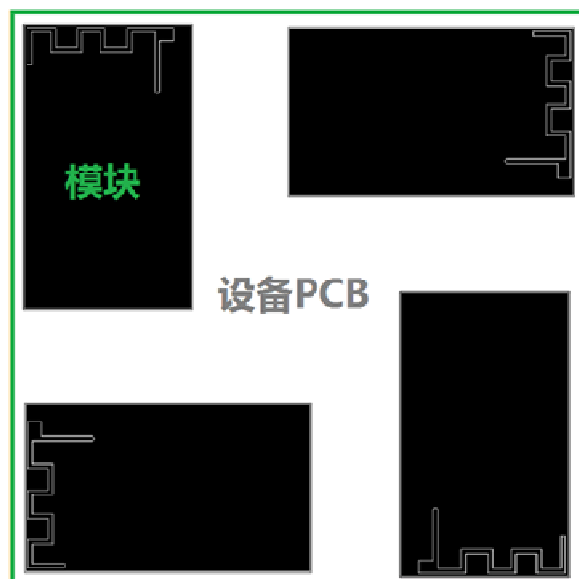
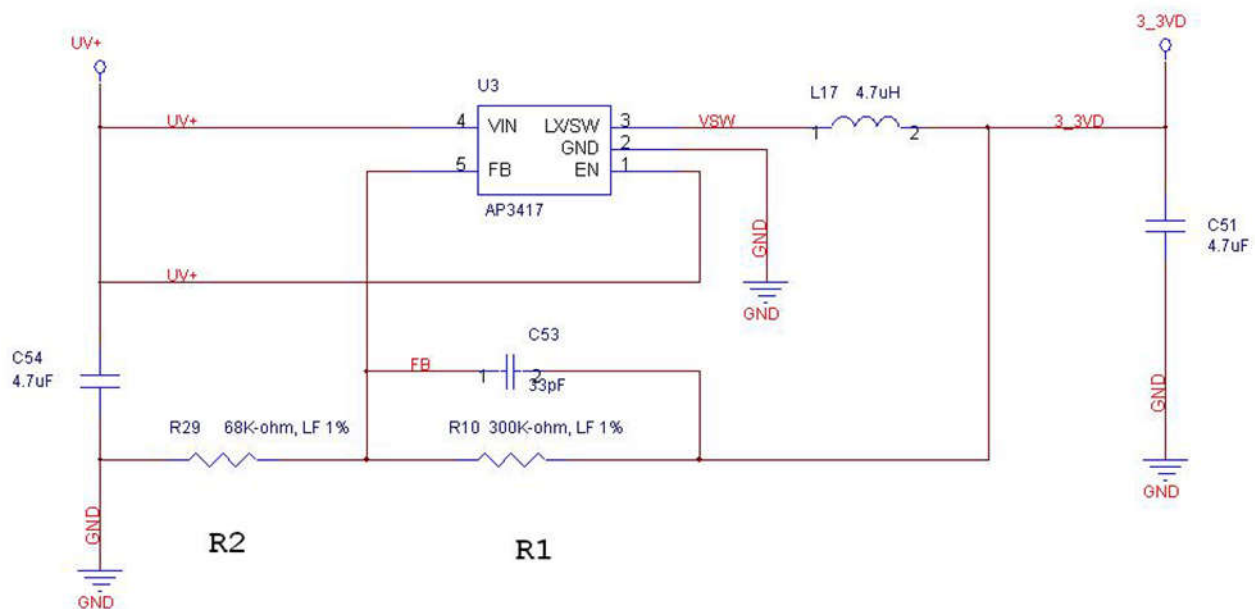


Fig 9 Recommended PCB layout

## 12.2 Power supply requirement

The module power supply voltage is DC+3.3V. The power supply design needs to consider the output current and power interference. The power supply current design needs to reserve **50mA**. To avoid the +3.3V power supply from interfering with other circuits on the motherboard, it is recommended to supply to the module using the regulator circuit alone. the recommended DC-DC circuit structure shown in the figure below. A 4.7uF~10uF capacitor is connected in parallel at 3\_3VD output to filter out the interference. A bead is connected in series at 3\_3VD output. The bead and capacitor must be placed as close to the module as possible. If you need to share +3.3V with other circuits, consider whether the current of the shared power supply is sufficient.



$$V_o = 0.6 * ( 1 + R1/R2 ) = 0.6 * ( 1+300K/68K ) = 3.3V$$

Step-Down Regulator, Vfb=0.6V, 1A, 1.8MHz, ADJ, LF

Fig 10 Power supply Circuit schematic

## 12.3 Motherboard interference avoidance

Motherboard interference comes from: high-speed data interface (HDMI), the Operating frequency of main chip, DDR, DC-DC power supply. The method of avoiding interference according to the characteristics of various signals is also different. The main methods of interference avoidance include:

1. keeping away from the source of interference;
2. Adding shields to avoid interference leakage;
3. Reasonable layout to eliminate interference.

### 12.3.1 Interface interference

When HDMI uses the 74.2MHz frequency, its 33x frequency is in the 2.4G band of BT, which will seriously interfere with the BT signal. If the HDMI frequency is 148.5MHz, although the 16x frequency is not in the BT band, the isolation of the frequency is not good, and the BT signal will be interfered to some extent. If the distance between the HDMI interface and the BT module on the PCB is less than 5cm, the HDMI output display will interfere with the BT signal, resulting in problems such as BT connection failure and throughput drop. Therefore, keep the location of the BT module away from the HDMI port on the hardware layout to avoid interference.

At the same time, if the BT antenna is built-in the motherboard, its placement must also be carefully considered to be far from the interface interference. If the antenna is placed in an incorrect position, even if the module is shielded, the interference signal is coupled through the antenna, which will eventually result in a lower BT throughput. (Note: In addition to interference, the placement of the internal antenna should also evaluate the effect of the metal interface, motherboard, and housing material on the antenna impedance.)

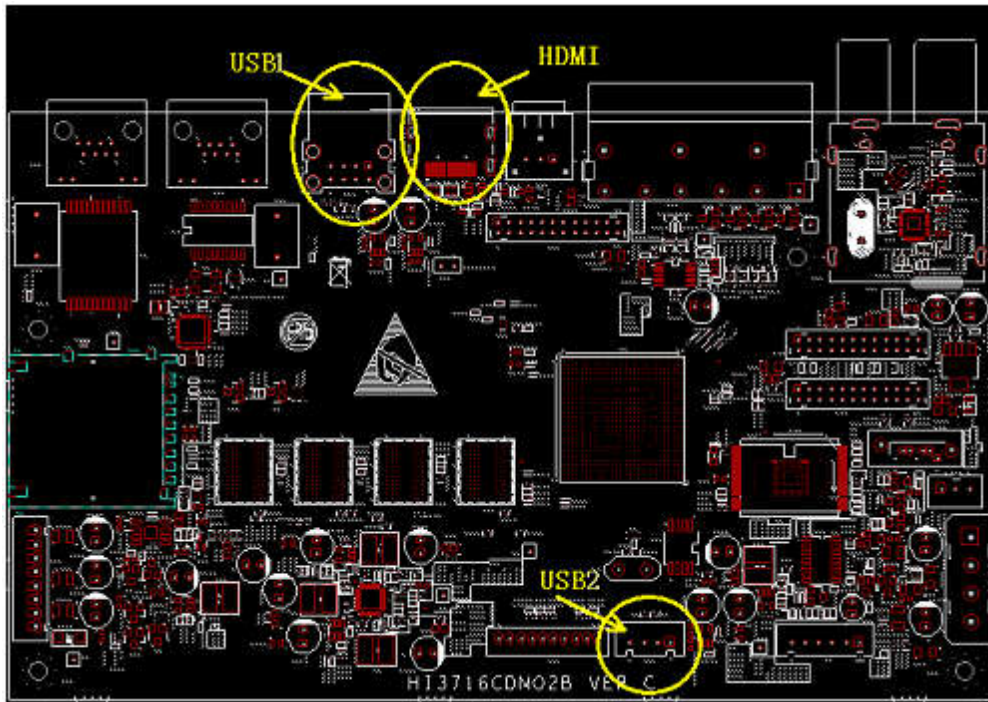


Fig 11 HDIM and USB interference

### 12.3.2 The main chip interferes with DDR

Because the main chips operate at about 800MHz or DDR2 operate at 667MHz, 3x frequency of 800MHz and 4x frequency of 667MHz are near 2.4GHz band. It must to place BT modules and antennas far away from the main chip and DDR. It is strongly recommended that the main chip be isolated from the the DDR by a shield. As shown in the figure below.

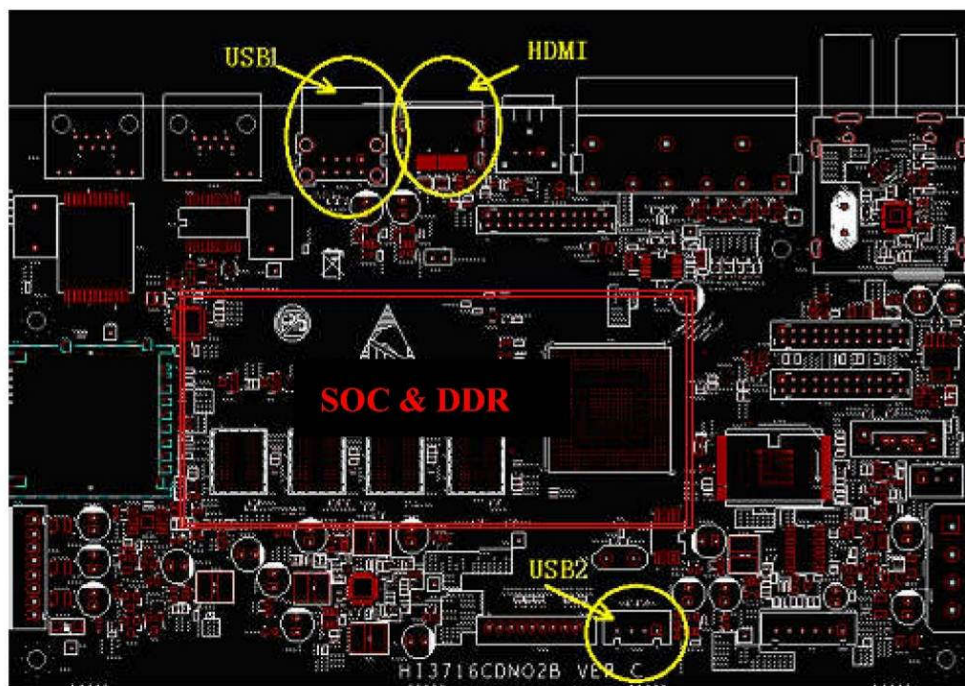


Fig 12 Main chip and DDR interference

## 12.4 Recommended secondary reflux temperature curve

The number of reflux shall not exceed 2 times, and the tin feeding height of the half hole of the module shall be no less than 1/4.

The lead-free reflux curve requirements of BT module products are shown in figure 13 :

| Stage                                                                          | Note                              | Pb-free assembly                                                                    |
|--------------------------------------------------------------------------------|-----------------------------------|-------------------------------------------------------------------------------------|
| Average ramp-up rate                                                           | $T_L$ to $T_p$                    | 3 °C/ second max.                                                                   |
| Preheat                                                                        | Temperature min ( $T_{smin}$ )    | 150°C                                                                               |
|                                                                                | Temperature max ( $T_{smax}$ )    | 200°C                                                                               |
|                                                                                | Time ( $t_{smin}$ to $t_{smax}$ ) | 60 – 120 seconds                                                                    |
| Time maintained above                                                          | Temperature( $T_L$ )              | 217°C                                                                               |
|                                                                                | Time ( $t_L$ )                    | 60 – 150 seconds                                                                    |
| Peak package body temperature ( $T_p$ )                                        |                                   | $T_p$ must not exceed the specified classification temp( $T_c=245^\circ\text{C}$ ). |
| Time( $t_p$ ) within 5°C of the specified classification temperature ( $T_c$ ) |                                   | 30 seconds                                                                          |
| Ramp-down rate ( $T_p$ to $T_L$ )                                              |                                   | 6 °C / seconds max.                                                                 |
| Time 25°C to peak temperature                                                  |                                   | 8 minutes max.                                                                      |

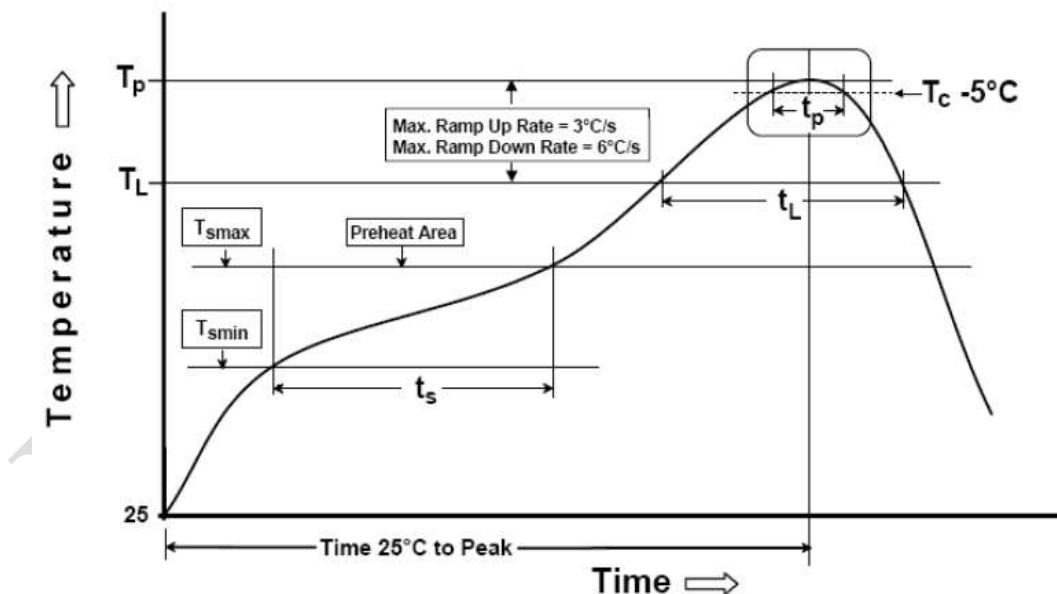


Fig 13 Furnace temperature curve

### Note:

1. The maximum furnace temperature of the module is 260°C, don't exceed this temperature.
2. The gold plating thickness of the module pad is 2u".

## 13 Revision History

| Revision | Release Date | Summary       | Revised By |
|----------|--------------|---------------|------------|
| V1.0     | 2022-9-7     | First release | Yuan       |
|          |              |               |            |
|          |              |               |            |

### FCC WARNING

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation.

If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

To maintain compliance with FCC's RF Exposure guidelines, This equipment should be installed and operated with minimum distance between 20cm the radiator your body: Use only the supplied antenna.

**IC Caution**

Radio Standards Specification RSS-Gen, issue 5

- English:

This device contains licence-exempt transmitter(s)/receiver(s) that comply with Innovation, Science and Economic Development Canada's licence-exempt RSS(s). Operation is subject to the following two conditions:

This device may not cause interference.

This device must accept any interference, including interference that may cause undesired operation of the device.

RF exposure statement:

The equipment complies with IC Radiation exposure limit set forth for uncontrolled environment.

This equipment should be installed and operated with minimum distance 20cm between the radiator and your body.

- French:

Cet appareil contient des émetteurs / récepteurs exemptés de licence conformes aux RSS (RSS) d'Innovation, Sciences et Développement économique Canada. Le fonctionnement est soumis aux deux conditions suivantes:

Cet appareil ne doit pas causer d'interférences.

Cet appareil doit accepter toutes les interférences, y compris celles susceptibles de provoquer un fonctionnement indésirable de l'appareil.

Déclaration d'exposition RF:

L'équipement est conforme à la limite d'exposition aux radiations de la IC établie pour un environnement non contrôlé. Cet équipement doit être installé et utilisé avec une distance minimale de 20 cm entre le radiateur et votre corps.

**OEM integration instructions:**

This device is intended only for OEM integrators under the following conditions:  
The transmitter module may not be co-located with any other transmitter or antenna. The module shall be only used with the external antenna(s) that has been originally tested and certified with this module.

As long as the conditions above are met, further transmitter test will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.).

**Validity of using the module certification:**

In the event that these conditions cannot be met (for example certain laptop configurations or co-location with another transmitter), then the FCC/IC authorization for this module in combination with the host equipment is no longer considered valid and the FCC ID/IC of the module cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC/IC authorization.

**End product labeling:**

The final end product must be labeled in a visible area with the following: "Contains Transmitter Module FCC ID: 2AMM6-8258FAT1CC. Contains IC: 26313-8258FAT1CC."

**Information that must be placed in the end user manual:**

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warning as show in this manual.

**Integration instructions for host product manufacturers according to KDB 996369 D03****OEM Manual v01****2.2 List of applicable FCC/IC rules**

FCC Part 15 Subpart C 15.247 & 15.207 & 15.209& RSS GEN&RSS 247

**2.3 Specific operational use conditions**

The module is a ZigBee module with 2.4G function.

Operation Frequency: 2405-2480MHz

Number of Channel: 16 Modulation: OQPSK

Type: PCB Antenna

Gain: -1.99 dBi Max.

The module can be used for mobile or applications with a maximum 0.5dBi antenna. The host manufacturer installing this module into their product must ensure that the final composite product complies with the FCC/IC requirements by a technical assessment or evaluation to the FCC/IC rules, including the transmitter operation. The host manufacturer has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warning as show in this manual.

**2.4 Limited module procedures**

Not applicable. The module is a Single module and complies with the requirement of FCC Part 15.212.

**2.5 Trace antenna designs**

Not applicable. The module has its own antenna, and doesn't need a host' s printed board microstrip trace antenna etc.

**2.6 RF exposure considerations**

The module must be installed in the host equipment such that at least 20cm is maintained between the antenna and users' body; and if RF exposure statement or module layout is changed, then the host product manufacturer required to take responsibility of the module through a change in FCC ID/IC or new application. The FCC ID/IC of the module cannot be used on the final product. In these circumstances, the host manufacturer will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC/IC authorization

## **2.7 Antennas**

PCB Antenna

Type: OQPSK

Gain: -1.99dBi

This device is intended only for host manufacturers under the following conditions: The transmitter module may not be co-located with any other transmitter or antenna; The module shall be only used with the internal antenna(s) that has been originally tested and certified with this module. The antenna must be either permanently attached or employ a 'unique' antenna coupler.

As long as the conditions above are met, further transmitter test will not be required. However, the host manufacturer is still responsible for testing their end-product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.).

## **2.8 Label and compliance information**

Host product manufacturers need to provide a physical or e-label stating "Contains FCC ID: 2AMM6-8258FAT1CC. Contains IC: 26313-8258FAT1CC." with their finished product.

## **2.9 Information on test modes and additional testing requirements**

Operation Frequency: 2405~2480MHz Number of Channel: 16

Modulation: OQPSK

Host manufacturer must perform test of radiated & conducted emission and spurious emission, etc. according to the actual test modes for a stand-alone modular transmitter in a host, as well as for multiple simultaneously transmitting modules or other transmitters in a host product. Only when all the test results of test modes comply with FCC requirements, then the end product can be sold legally.

### **2.10 Additional testing, Part 15 Subpart B disclaimer**

The modular transmitter is only FCC/IC authorized for FCC Part 15 Subpart C 15.247 & 15.207 & 15.209 & RSS GEN&RSS 247 and that the host product manufacturer is responsible for compliance to any other FCC/IC rules that apply to the host not covered by the modular transmitter grant of certification. If the grantee markets their product as being Part 15 Subpart B/RSS GEN compliant (when it also contains unintentional-radiator digital circuitry), then the grantee shall provide a notice stating that the final host product still requires Part 15 Subpart B/ RSS GEN compliance testing with the modular transmitter installed.