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Appendix for DC40A-n78

**Appendix A: Effective (Isotropic) Radiated Power Output Data****for NSA****Test Result**

Band	SCS (kHz)	Bandwidth (MHz)	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_40A-n78A	30	5+10	DFT-BPSK	M+L	Edge_1RB_Left	22.17	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+L	Edge_1RB_Right	22.04	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+L	Inner_Full	22.58	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+L	Outer_Full	22.12	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	21.57	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	21.4	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+L	Inner_Full	22.58	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+L	Outer_Full	21.52	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	20.24	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	20.25	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+L	Inner_Full	21.58	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+L	Outer_Full	20.37	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Left	20.17	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+L	Edge_1RB_Right	20.15	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+L	Inner_Full	19.96	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+L	Outer_Full	20.02	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Left	18.03	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+L	Edge_1RB_Right	17.98	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+L	Inner_Full	18.13	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+L	Outer_Full	18.06	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+L	Edge_1RB_Left	19.64	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+L	Edge_1RB_Right	19.35	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+L	Inner_Full	21.01	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+L	Outer_Full	19.48	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+L	Edge_1RB_Left	19.22	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+L	Edge_1RB_Right	19.14	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+L	Inner_Full	20.77	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+L	Outer_Full	19.56	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+L	Edge_1RB_Left	19.29	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+L	Edge_1RB_Right	19.21	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+L	Inner_Full	19.05	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+L	Outer_Full	19.1	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+L	Edge_1RB_Left	16.09	PC3	PASS



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DC_40A-n78A	30	5+10	CP-256QAM	M+L	Edge_1RB_Right	15.97	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+L	Inner_Full	16.08	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+L	Outer_Full	16.14	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+M	Edge_1RB_Left	22.31	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+M	Edge_1RB_Right	22.27	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+M	Inner_Full	22.76	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+M	Outer_Full	22.33	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.8	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	21.77	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+M	Inner_Full	22.93	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+M	Outer_Full	21.84	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	20.47	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	20.6	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+M	Inner_Full	21.8	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+M	Outer_Full	20.88	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Left	20.73	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+M	Edge_1RB_Right	20.58	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+M	Inner_Full	20.26	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+M	Outer_Full	20.35	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Left	18.31	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+M	Edge_1RB_Right	18.26	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+M	Inner_Full	18.33	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+M	Outer_Full	18.27	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+M	Edge_1RB_Left	19.74	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+M	Edge_1RB_Right	19.77	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+M	Inner_Full	21.17	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+M	Outer_Full	19.79	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+M	Edge_1RB_Left	19.58	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+M	Edge_1RB_Right	19.53	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+M	Inner_Full	21.01	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+M	Outer_Full	19.65	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+M	Edge_1RB_Left	19.6	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+M	Edge_1RB_Right	19.55	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+M	Inner_Full	19.43	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+M	Outer_Full	19.28	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+M	Edge_1RB_Left	16.43	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+M	Edge_1RB_Right	16.46	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+M	Inner_Full	16.66	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+M	Outer_Full	16.43	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+H	Edge_1RB_Left	22.16	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+H	Edge_1RB_Right	21.81	PC3	PASS



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DC_40A-n78A	30	5+10	DFT-BPSK	M+H	Inner_Full	22.43	PC3	PASS
DC_40A-n78A	30	5+10	DFT-BPSK	M+H	Outer_Full	22.01	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	21.63	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	21.3	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+H	Inner_Full	22.35	PC3	PASS
DC_40A-n78A	30	5+10	DFT-QPSK	M+H	Outer_Full	21.44	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	20.51	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	20.1	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+H	Inner_Full	21.48	PC3	PASS
DC_40A-n78A	30	5+10	DFT-16QAM	M+H	Outer_Full	20.41	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Left	20.41	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+H	Edge_1RB_Right	20.07	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+H	Inner_Full	20.06	PC3	PASS
DC_40A-n78A	30	5+10	DFT-64QAM	M+H	Outer_Full	19.97	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Left	18.17	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+H	Edge_1RB_Right	17.8	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+H	Inner_Full	18.1	PC3	PASS
DC_40A-n78A	30	5+10	DFT-256QAM	M+H	Outer_Full	17.96	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+H	Edge_1RB_Left	19.65	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+H	Edge_1RB_Right	19.4	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+H	Inner_Full	20.88	PC3	PASS
DC_40A-n78A	30	5+10	CP-QPSK	M+H	Outer_Full	19.48	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+H	Edge_1RB_Left	19.28	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+H	Edge_1RB_Right	19.08	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+H	Inner_Full	20.61	PC3	PASS
DC_40A-n78A	30	5+10	CP-16QAM	M+H	Outer_Full	19.38	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+H	Edge_1RB_Left	19.37	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+H	Edge_1RB_Right	19.07	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+H	Inner_Full	19.15	PC3	PASS
DC_40A-n78A	30	5+10	CP-64QAM	M+H	Outer_Full	19	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+H	Edge_1RB_Left	16.19	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+H	Edge_1RB_Right	15.83	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+H	Inner_Full	16.16	PC3	PASS
DC_40A-n78A	30	5+10	CP-256QAM	M+H	Outer_Full	15.93	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+L	Edge_1RB_Left	22.14	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+L	Edge_1RB_Right	21.75	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+L	Inner_Full	22.52	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+L	Outer_Full	21.97	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	21.63	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.29	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+L	Inner_Full	22.38	PC3	PASS



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DC_40A-n78A	30	5+15	DFT-QPSK	M+L	Outer_Full	21.49	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	20.29	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	20	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+L	Inner_Full	21.52	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+L	Outer_Full	20.44	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Left	20.28	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+L	Edge_1RB_Right	20.08	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+L	Inner_Full	20.16	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+L	Outer_Full	19.97	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Left	18.1	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+L	Edge_1RB_Right	17.81	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+L	Inner_Full	17.97	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+L	Outer_Full	18.16	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+L	Edge_1RB_Left	19.7	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+L	Edge_1RB_Right	19.2	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+L	Inner_Full	21.02	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+L	Outer_Full	19.43	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+L	Edge_1RB_Left	19.34	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+L	Edge_1RB_Right	18.93	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+L	Inner_Full	20.48	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+L	Outer_Full	19.55	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+L	Edge_1RB_Left	19.27	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+L	Edge_1RB_Right	18.92	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+L	Inner_Full	18.97	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+L	Outer_Full	19.05	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+L	Edge_1RB_Left	16.16	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+L	Edge_1RB_Right	15.8	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+L	Inner_Full	16.16	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+L	Outer_Full	16.05	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+M	Edge_1RB_Left	22.43	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+M	Edge_1RB_Right	22.43	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+M	Inner_Full	22.78	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+M	Outer_Full	22.28	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.83	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	21.84	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+M	Inner_Full	22.85	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+M	Outer_Full	21.76	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	20.58	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	20.62	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+M	Inner_Full	21.68	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+M	Outer_Full	20.86	PC3	PASS



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DC_40A-n78A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Left	20.67	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+M	Edge_1RB_Right	20.6	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+M	Inner_Full	20.45	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+M	Outer_Full	20.2	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Left	18.37	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+M	Edge_1RB_Right	18.45	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+M	Inner_Full	18.27	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+M	Outer_Full	18.32	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+M	Edge_1RB_Left	19.74	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+M	Edge_1RB_Right	19.97	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+M	Inner_Full	21.36	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+M	Outer_Full	19.8	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+M	Edge_1RB_Left	19.68	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+M	Edge_1RB_Right	19.59	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+M	Inner_Full	20.77	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+M	Outer_Full	19.81	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+M	Edge_1RB_Left	19.61	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+M	Edge_1RB_Right	19.62	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+M	Inner_Full	19.19	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+M	Outer_Full	19.3	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+M	Edge_1RB_Left	16.34	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+M	Edge_1RB_Right	16.4	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+M	Inner_Full	16.48	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+M	Outer_Full	16.41	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+H	Edge_1RB_Left	22.25	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+H	Edge_1RB_Right	21.81	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+H	Inner_Full	22.67	PC3	PASS
DC_40A-n78A	30	5+15	DFT-BPSK	M+H	Outer_Full	22.04	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	21.71	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	21.31	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+H	Inner_Full	22.56	PC3	PASS
DC_40A-n78A	30	5+15	DFT-QPSK	M+H	Outer_Full	21.6	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	20.51	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	20.04	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+H	Inner_Full	21.55	PC3	PASS
DC_40A-n78A	30	5+15	DFT-16QAM	M+H	Outer_Full	20.56	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Left	20.49	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+H	Edge_1RB_Right	20.01	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+H	Inner_Full	20.26	PC3	PASS
DC_40A-n78A	30	5+15	DFT-64QAM	M+H	Outer_Full	20.09	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Left	18.19	PC3	PASS



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DC_40A-n78A	30	5+15	DFT-256QAM	M+H	Edge_1RB_Right	17.88	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+H	Inner_Full	17.97	PC3	PASS
DC_40A-n78A	30	5+15	DFT-256QAM	M+H	Outer_Full	18.06	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+H	Edge_1RB_Left	19.71	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+H	Edge_1RB_Right	19.42	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+H	Inner_Full	21.07	PC3	PASS
DC_40A-n78A	30	5+15	CP-QPSK	M+H	Outer_Full	19.56	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+H	Edge_1RB_Left	19.56	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+H	Edge_1RB_Right	19.09	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+H	Inner_Full	20.51	PC3	PASS
DC_40A-n78A	30	5+15	CP-16QAM	M+H	Outer_Full	19.63	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+H	Edge_1RB_Left	19.42	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+H	Edge_1RB_Right	19.03	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+H	Inner_Full	19.14	PC3	PASS
DC_40A-n78A	30	5+15	CP-64QAM	M+H	Outer_Full	19.07	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+H	Edge_1RB_Left	16.22	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+H	Edge_1RB_Right	15.86	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+H	Inner_Full	16.08	PC3	PASS
DC_40A-n78A	30	5+15	CP-256QAM	M+H	Outer_Full	16.11	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+L	Edge_1RB_Left	22.16	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+L	Edge_1RB_Right	21.83	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+L	Inner_Full	22.41	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+L	Outer_Full	22	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	21.6	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.28	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+L	Inner_Full	22.42	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+L	Outer_Full	21.49	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	20.41	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	20.14	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+L	Inner_Full	21.45	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+L	Outer_Full	20.47	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Left	20.32	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+L	Edge_1RB_Right	20.1	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+L	Inner_Full	19.95	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+L	Outer_Full	19.95	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Left	18.2	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+L	Edge_1RB_Right	17.75	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+L	Inner_Full	17.98	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+L	Outer_Full	17.98	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+L	Edge_1RB_Left	19.63	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+L	Edge_1RB_Right	19.33	PC3	PASS



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DC_40A-n78A	30	5+20	CP-QPSK	M+L	Inner_Full	20.94	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+L	Outer_Full	19.53	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+L	Edge_1RB_Left	19.53	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+L	Edge_1RB_Right	18.94	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+L	Inner_Full	20.46	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+L	Outer_Full	19.49	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+L	Edge_1RB_Left	19.4	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+L	Edge_1RB_Right	19.13	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+L	Inner_Full	19.06	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+L	Outer_Full	19.08	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+L	Edge_1RB_Left	16.16	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+L	Edge_1RB_Right	15.91	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+L	Inner_Full	16.04	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+L	Outer_Full	16.11	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+M	Edge_1RB_Left	22.41	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+M	Edge_1RB_Right	22.45	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+M	Inner_Full	22.83	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+M	Outer_Full	22.34	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.85	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	21.86	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+M	Inner_Full	22.83	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+M	Outer_Full	21.9	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	20.71	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	20.72	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+M	Inner_Full	21.85	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+M	Outer_Full	20.9	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Left	20.74	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+M	Edge_1RB_Right	20.76	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+M	Inner_Full	20.35	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+M	Outer_Full	20.31	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Left	18.42	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+M	Edge_1RB_Right	18.45	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+M	Inner_Full	18.28	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+M	Outer_Full	18.34	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+M	Edge_1RB_Left	19.89	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.02	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+M	Inner_Full	21.4	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+M	Outer_Full	19.87	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+M	Edge_1RB_Left	19.78	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+M	Edge_1RB_Right	19.67	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+M	Inner_Full	20.79	PC3	PASS



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DC_40A-n78A	30	5+20	CP-16QAM	M+M	Outer_Full	19.87	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+M	Edge_1RB_Left	19.68	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+M	Edge_1RB_Right	19.79	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+M	Inner_Full	19.43	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+M	Outer_Full	19.48	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+M	Edge_1RB_Left	16.5	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+M	Edge_1RB_Right	16.53	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+M	Inner_Full	16.38	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+M	Outer_Full	16.39	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+H	Edge_1RB_Left	22.3	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+H	Edge_1RB_Right	21.87	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+H	Inner_Full	22.66	PC3	PASS
DC_40A-n78A	30	5+20	DFT-BPSK	M+H	Outer_Full	22.15	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	21.72	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	21.24	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+H	Inner_Full	22.63	PC3	PASS
DC_40A-n78A	30	5+20	DFT-QPSK	M+H	Outer_Full	21.62	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	20.55	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	20.11	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+H	Inner_Full	21.71	PC3	PASS
DC_40A-n78A	30	5+20	DFT-16QAM	M+H	Outer_Full	20.65	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Left	20.6	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+H	Edge_1RB_Right	20.14	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+H	Inner_Full	20.15	PC3	PASS
DC_40A-n78A	30	5+20	DFT-64QAM	M+H	Outer_Full	20.12	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Left	18.29	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+H	Edge_1RB_Right	17.85	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+H	Inner_Full	18.07	PC3	PASS
DC_40A-n78A	30	5+20	DFT-256QAM	M+H	Outer_Full	18.17	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+H	Edge_1RB_Left	19.89	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+H	Edge_1RB_Right	19.41	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+H	Inner_Full	21.16	PC3	PASS
DC_40A-n78A	30	5+20	CP-QPSK	M+H	Outer_Full	19.66	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+H	Edge_1RB_Left	19.58	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+H	Edge_1RB_Right	19.12	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+H	Inner_Full	20.61	PC3	PASS
DC_40A-n78A	30	5+20	CP-16QAM	M+H	Outer_Full	19.62	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+H	Edge_1RB_Left	19.53	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+H	Edge_1RB_Right	19.18	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+H	Inner_Full	19.21	PC3	PASS
DC_40A-n78A	30	5+20	CP-64QAM	M+H	Outer_Full	19.21	PC3	PASS



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DC_40A-n78A	30	5+20	CP-256QAM	M+H	Edge_1RB_Left	16.32	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+H	Edge_1RB_Right	15.92	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+H	Inner_Full	16.21	PC3	PASS
DC_40A-n78A	30	5+20	CP-256QAM	M+H	Outer_Full	16.16	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+L	Edge_1RB_Left	22.08	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+L	Edge_1RB_Right	21.84	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+L	Inner_Full	22.31	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+L	Outer_Full	21.95	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+L	Edge_1RB_Left	21.58	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+L	Edge_1RB_Right	21.24	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+L	Inner_Full	22.37	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+L	Outer_Full	21.39	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+L	Edge_1RB_Left	20.39	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+L	Edge_1RB_Right	20.17	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+L	Inner_Full	21.33	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+L	Outer_Full	20.34	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+L	Edge_1RB_Left	20.28	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+L	Edge_1RB_Right	20.18	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+L	Inner_Full	19.8	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+L	Outer_Full	19.87	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+L	Edge_1RB_Left	18.08	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+L	Edge_1RB_Right	17.86	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+L	Inner_Full	17.82	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+L	Outer_Full	17.93	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+L	Edge_1RB_Left	19.72	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+L	Edge_1RB_Right	19.46	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+L	Inner_Full	20.79	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+L	Outer_Full	19.41	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+L	Edge_1RB_Left	19.37	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+L	Edge_1RB_Right	19.08	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+L	Inner_Full	20.32	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+L	Outer_Full	19.47	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+L	Edge_1RB_Left	19.26	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+L	Edge_1RB_Right	19.1	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+L	Inner_Full	18.86	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+L	Outer_Full	18.86	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+L	Edge_1RB_Left	16.16	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+L	Edge_1RB_Right	15.99	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+L	Inner_Full	15.94	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+L	Outer_Full	16.04	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+M	Edge_1RB_Left	22.34	PC3	PASS



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DC_40A-n78A	30	5+25	DFT-BPSK	M+M	Edge_1RB_Right	22.38	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+M	Inner_Full	22.74	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+M	Outer_Full	22.27	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+M	Edge_1RB_Left	21.79	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+M	Edge_1RB_Right	21.71	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+M	Inner_Full	22.81	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+M	Outer_Full	21.86	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+M	Edge_1RB_Left	20.64	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+M	Edge_1RB_Right	20.6	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+M	Inner_Full	21.74	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+M	Outer_Full	20.87	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+M	Edge_1RB_Left	20.65	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+M	Edge_1RB_Right	20.67	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+M	Inner_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+M	Outer_Full	20.35	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+M	Edge_1RB_Left	18.37	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+M	Edge_1RB_Right	18.38	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+M	Inner_Full	18.19	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+M	Outer_Full	18.33	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+M	Edge_1RB_Left	19.92	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+M	Edge_1RB_Right	19.95	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+M	Inner_Full	21.23	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+M	Outer_Full	19.85	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+M	Edge_1RB_Left	19.64	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+M	Edge_1RB_Right	19.51	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+M	Inner_Full	20.73	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+M	Outer_Full	19.87	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+M	Edge_1RB_Left	19.56	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+M	Edge_1RB_Right	19.56	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+M	Inner_Full	19.24	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+M	Outer_Full	19.29	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+M	Edge_1RB_Left	16.45	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+M	Edge_1RB_Right	16.38	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+M	Inner_Full	16.4	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+M	Outer_Full	16.5	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+H	Edge_1RB_Left	22.21	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+H	Edge_1RB_Right	21.75	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+H	Inner_Full	22.57	PC3	PASS
DC_40A-n78A	30	5+25	DFT-BPSK	M+H	Outer_Full	22.12	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+H	Edge_1RB_Left	21.73	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+H	Edge_1RB_Right	21.16	PC3	PASS



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DC_40A-n78A	30	5+25	DFT-QPSK	M+H	Inner_Full	22.59	PC3	PASS
DC_40A-n78A	30	5+25	DFT-QPSK	M+H	Outer_Full	21.62	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+H	Edge_1RB_Left	20.57	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+H	Edge_1RB_Right	19.97	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+H	Inner_Full	21.62	PC3	PASS
DC_40A-n78A	30	5+25	DFT-16QAM	M+H	Outer_Full	20.52	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+H	Edge_1RB_Left	20.5	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+H	Edge_1RB_Right	19.98	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+H	Inner_Full	20.17	PC3	PASS
DC_40A-n78A	30	5+25	DFT-64QAM	M+H	Outer_Full	20.16	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+H	Edge_1RB_Left	18.29	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+H	Edge_1RB_Right	17.77	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+H	Inner_Full	18.07	PC3	PASS
DC_40A-n78A	30	5+25	DFT-256QAM	M+H	Outer_Full	18.14	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+H	Edge_1RB_Left	19.69	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+H	Edge_1RB_Right	19.22	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+H	Inner_Full	21	PC3	PASS
DC_40A-n78A	30	5+25	CP-QPSK	M+H	Outer_Full	19.59	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+H	Edge_1RB_Left	19.36	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+H	Edge_1RB_Right	18.91	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+H	Inner_Full	20.59	PC3	PASS
DC_40A-n78A	30	5+25	CP-16QAM	M+H	Outer_Full	19.54	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+H	Edge_1RB_Left	19.49	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+H	Edge_1RB_Right	18.94	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+H	Inner_Full	19.16	PC3	PASS
DC_40A-n78A	30	5+25	CP-64QAM	M+H	Outer_Full	19.11	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+H	Edge_1RB_Left	16.27	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+H	Edge_1RB_Right	15.77	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+H	Inner_Full	16.22	PC3	PASS
DC_40A-n78A	30	5+25	CP-256QAM	M+H	Outer_Full	16.23	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+L	Edge_1RB_Left	22.1	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+L	Edge_1RB_Right	22.04	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+L	Inner_Full	22.29	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+L	Outer_Full	21.87	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	21.49	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.53	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+L	Inner_Full	22.28	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+L	Outer_Full	21.37	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	20.34	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	20.22	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+L	Inner_Full	21.32	PC3	PASS



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DC_40A-n78A	30	5+30	DFT-16QAM	M+L	Outer_Full	20.38	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Left	20.26	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+L	Edge_1RB_Right	20.25	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+L	Inner_Full	19.76	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+L	Outer_Full	19.97	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Left	18.08	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+L	Edge_1RB_Right	18.07	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+L	Inner_Full	17.79	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+L	Outer_Full	17.94	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+L	Edge_1RB_Left	19.51	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+L	Edge_1RB_Right	19.48	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+L	Inner_Full	20.8	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+L	Outer_Full	19.41	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+L	Edge_1RB_Left	19.25	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+L	Edge_1RB_Right	19.21	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+L	Inner_Full	20.33	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+L	Outer_Full	19.43	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+L	Edge_1RB_Left	19.31	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+L	Edge_1RB_Right	19.27	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+L	Inner_Full	18.74	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+L	Outer_Full	18.98	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+L	Edge_1RB_Left	16.17	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+L	Edge_1RB_Right	16.09	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+L	Inner_Full	15.95	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+L	Outer_Full	16.05	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+M	Edge_1RB_Left	22.34	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+M	Edge_1RB_Right	22.35	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+M	Inner_Full	22.83	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+M	Outer_Full	22.32	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.8	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	21.82	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+M	Inner_Full	22.76	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+M	Outer_Full	21.81	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	20.57	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	20.43	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+M	Inner_Full	21.79	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+M	Outer_Full	20.82	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Left	20.52	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+M	Edge_1RB_Right	20.59	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+M	Inner_Full	20.22	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+M	Outer_Full	20.36	PC3	PASS



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DC_40A-n78A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Left	18.31	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+M	Edge_1RB_Right	18.35	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+M	Inner_Full	18.29	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+M	Outer_Full	18.37	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+M	Edge_1RB_Left	19.77	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+M	Edge_1RB_Right	19.89	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+M	Inner_Full	21.3	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+M	Outer_Full	19.85	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+M	Edge_1RB_Left	19.69	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+M	Edge_1RB_Right	19.53	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+M	Inner_Full	20.81	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+M	Outer_Full	19.85	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+M	Edge_1RB_Left	19.52	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+M	Edge_1RB_Right	19.59	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+M	Inner_Full	19.4	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+M	Outer_Full	19.34	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+M	Edge_1RB_Left	16.41	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+M	Edge_1RB_Right	16.37	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+M	Inner_Full	16.41	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+M	Outer_Full	16.47	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+H	Edge_1RB_Left	22.33	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+H	Edge_1RB_Right	21.76	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+H	Inner_Full	22.66	PC3	PASS
DC_40A-n78A	30	5+30	DFT-BPSK	M+H	Outer_Full	22.09	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	21.76	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	21.11	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+H	Inner_Full	22.65	PC3	PASS
DC_40A-n78A	30	5+30	DFT-QPSK	M+H	Outer_Full	21.64	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	20.46	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	19.98	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+H	Inner_Full	21.69	PC3	PASS
DC_40A-n78A	30	5+30	DFT-16QAM	M+H	Outer_Full	20.61	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Left	20.52	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+H	Edge_1RB_Right	19.97	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+H	Inner_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+30	DFT-64QAM	M+H	Outer_Full	20.21	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Left	18.34	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+H	Edge_1RB_Right	17.78	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+H	Inner_Full	18.14	PC3	PASS
DC_40A-n78A	30	5+30	DFT-256QAM	M+H	Outer_Full	18.12	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+H	Edge_1RB_Left	19.91	PC3	PASS



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DC_40A-n78A	30	5+30	CP-QPSK	M+H	Edge_1RB_Right	19.21	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+H	Inner_Full	21.14	PC3	PASS
DC_40A-n78A	30	5+30	CP-QPSK	M+H	Outer_Full	19.64	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+H	Edge_1RB_Left	19.6	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+H	Edge_1RB_Right	19.02	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+H	Inner_Full	20.68	PC3	PASS
DC_40A-n78A	30	5+30	CP-16QAM	M+H	Outer_Full	19.66	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+H	Edge_1RB_Left	19.51	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+H	Edge_1RB_Right	19.03	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+H	Inner_Full	19.17	PC3	PASS
DC_40A-n78A	30	5+30	CP-64QAM	M+H	Outer_Full	19.13	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+H	Edge_1RB_Left	16.42	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+H	Edge_1RB_Right	15.67	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+H	Inner_Full	16.24	PC3	PASS
DC_40A-n78A	30	5+30	CP-256QAM	M+H	Outer_Full	16.23	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+L	Edge_1RB_Left	22.07	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+L	Edge_1RB_Right	22.22	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+L	Inner_Full	22.34	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+L	Outer_Full	21.97	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.48	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	21.72	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+L	Inner_Full	22.33	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+L	Outer_Full	21.47	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	20.3	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	20.46	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+L	Inner_Full	21.44	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+L	Outer_Full	20.45	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Left	20.35	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+L	Edge_1RB_Right	20.52	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+L	Inner_Full	19.88	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+L	Outer_Full	19.96	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Left	18.08	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+L	Edge_1RB_Right	18.26	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+L	Inner_Full	17.84	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+L	Outer_Full	18.05	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+L	Edge_1RB_Left	19.58	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+L	Edge_1RB_Right	19.77	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+L	Inner_Full	20.85	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+L	Outer_Full	19.48	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+L	Edge_1RB_Left	19.35	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+L	Edge_1RB_Right	19.49	PC3	PASS



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DC_40A-n78A	30	5+40	CP-16QAM	M+L	Inner_Full	20.32	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+L	Outer_Full	19.43	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+L	Edge_1RB_Left	19.28	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+L	Edge_1RB_Right	19.47	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+L	Inner_Full	18.86	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+L	Outer_Full	19.06	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+L	Edge_1RB_Left	16.06	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+L	Edge_1RB_Right	16.33	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+L	Inner_Full	16.03	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+L	Outer_Full	16.14	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+M	Edge_1RB_Left	22.2	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+M	Edge_1RB_Right	22.24	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+M	Inner_Full	22.8	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+M	Outer_Full	22.37	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.63	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.72	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+M	Inner_Full	22.79	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+M	Outer_Full	21.85	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	20.45	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	20.45	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+M	Inner_Full	21.84	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+M	Outer_Full	20.83	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Left	20.44	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+M	Edge_1RB_Right	20.51	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+M	Inner_Full	20.31	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+M	Outer_Full	20.33	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Left	18.26	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+M	Edge_1RB_Right	18.31	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+M	Inner_Full	18.28	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+M	Outer_Full	18.37	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+M	Edge_1RB_Left	19.66	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+M	Edge_1RB_Right	19.66	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+M	Inner_Full	21.27	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+M	Outer_Full	19.85	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+M	Edge_1RB_Left	19.44	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+M	Edge_1RB_Right	19.6	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+M	Inner_Full	20.82	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+M	Outer_Full	19.84	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+M	Edge_1RB_Left	19.4	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+M	Edge_1RB_Right	19.41	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+M	Inner_Full	19.3	PC3	PASS



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DC_40A-n78A	30	5+40	CP-64QAM	M+M	Outer_Full	19.34	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+M	Edge_1RB_Left	16.31	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+M	Edge_1RB_Right	16.35	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+M	Inner_Full	16.34	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+M	Outer_Full	16.41	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+H	Edge_1RB_Left	22.39	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+H	Edge_1RB_Right	21.64	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+H	Inner_Full	22.7	PC3	PASS
DC_40A-n78A	30	5+40	DFT-BPSK	M+H	Outer_Full	22.2	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.89	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	21.13	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+H	Inner_Full	22.68	PC3	PASS
DC_40A-n78A	30	5+40	DFT-QPSK	M+H	Outer_Full	21.7	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	20.63	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	19.85	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+H	Inner_Full	21.76	PC3	PASS
DC_40A-n78A	30	5+40	DFT-16QAM	M+H	Outer_Full	20.69	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Left	20.63	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+H	Edge_1RB_Right	20.03	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+H	Inner_Full	20.21	PC3	PASS
DC_40A-n78A	30	5+40	DFT-64QAM	M+H	Outer_Full	20.2	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Left	18.4	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+H	Edge_1RB_Right	17.7	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+H	Inner_Full	18.22	PC3	PASS
DC_40A-n78A	30	5+40	DFT-256QAM	M+H	Outer_Full	18.23	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+H	Edge_1RB_Left	19.99	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+H	Edge_1RB_Right	19.15	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+H	Inner_Full	21.13	PC3	PASS
DC_40A-n78A	30	5+40	CP-QPSK	M+H	Outer_Full	19.64	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+H	Edge_1RB_Left	19.66	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+H	Edge_1RB_Right	18.88	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+H	Inner_Full	20.65	PC3	PASS
DC_40A-n78A	30	5+40	CP-16QAM	M+H	Outer_Full	19.71	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+H	Edge_1RB_Left	19.59	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+H	Edge_1RB_Right	18.91	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+H	Inner_Full	19.22	PC3	PASS
DC_40A-n78A	30	5+40	CP-64QAM	M+H	Outer_Full	19.22	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+H	Edge_1RB_Left	16.48	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+H	Edge_1RB_Right	15.76	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+H	Inner_Full	16.25	PC3	PASS
DC_40A-n78A	30	5+40	CP-256QAM	M+H	Outer_Full	16.26	PC3	PASS



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DC_40A-n78A	30	5+50	DFT-BPSK	M+L	Edge_1RB_Left	22.04	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+L	Edge_1RB_Right	22.18	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+L	Inner_Full	22.51	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+L	Outer_Full	22.07	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	21.52	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	21.57	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+L	Inner_Full	22.48	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+L	Outer_Full	21.64	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	20.35	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	20.45	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+L	Inner_Full	21.5	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+L	Outer_Full	20.61	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+L	Edge_1RB_Left	20.34	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+L	Edge_1RB_Right	20.4	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+L	Inner_Full	20.01	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+L	Outer_Full	20.09	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Left	18.12	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+L	Edge_1RB_Right	18.16	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+L	Inner_Full	18.03	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+L	Outer_Full	18.2	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+L	Edge_1RB_Left	19.73	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+L	Edge_1RB_Right	19.63	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+L	Inner_Full	20.95	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+L	Outer_Full	19.6	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+L	Edge_1RB_Left	19.28	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+L	Edge_1RB_Right	19.44	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+L	Inner_Full	20.51	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+L	Outer_Full	19.64	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+L	Edge_1RB_Left	19.3	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+L	Edge_1RB_Right	19.37	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+L	Inner_Full	19.03	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+L	Outer_Full	19.05	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+L	Edge_1RB_Left	16.24	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+L	Edge_1RB_Right	16.2	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+L	Inner_Full	16.14	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+L	Outer_Full	16.26	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+M	Edge_1RB_Left	22	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+M	Edge_1RB_Right	22.15	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+M	Inner_Full	22.85	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+M	Outer_Full	22.37	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	21.44	PC3	PASS



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DC_40A-n78A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	21.55	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+M	Inner_Full	22.84	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+M	Outer_Full	21.79	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	20.23	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	20.37	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+M	Inner_Full	21.76	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+M	Outer_Full	20.7	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Left	20.24	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+M	Edge_1RB_Right	20.4	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+M	Inner_Full	20.43	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+M	Outer_Full	20.29	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Left	18.07	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+M	Edge_1RB_Right	18.14	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+M	Inner_Full	18.36	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+M	Outer_Full	18.4	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+M	Edge_1RB_Left	19.48	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+M	Edge_1RB_Right	19.75	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+M	Inner_Full	21.27	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+M	Outer_Full	19.81	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+M	Edge_1RB_Left	19.26	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+M	Edge_1RB_Right	19.4	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+M	Inner_Full	20.88	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+M	Outer_Full	19.84	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+M	Edge_1RB_Left	19.2	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+M	Edge_1RB_Right	19.38	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+M	Inner_Full	19.38	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+M	Outer_Full	19.32	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+M	Edge_1RB_Left	16	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+M	Edge_1RB_Right	16.19	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+M	Inner_Full	16.46	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+M	Outer_Full	16.45	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+H	Edge_1RB_Left	22.24	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+H	Edge_1RB_Right	21.65	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+H	Inner_Full	22.74	PC3	PASS
DC_40A-n78A	30	5+50	DFT-BPSK	M+H	Outer_Full	22.19	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	21.73	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	21.11	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+H	Inner_Full	22.73	PC3	PASS
DC_40A-n78A	30	5+50	DFT-QPSK	M+H	Outer_Full	21.69	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	20.49	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	19.9	PC3	PASS



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DC_40A-n78A	30	5+50	DFT-16QAM	M+H	Inner_Full	21.72	PC3	PASS
DC_40A-n78A	30	5+50	DFT-16QAM	M+H	Outer_Full	20.76	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Left	20.5	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+H	Edge_1RB_Right	19.84	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+H	Inner_Full	20.3	PC3	PASS
DC_40A-n78A	30	5+50	DFT-64QAM	M+H	Outer_Full	20.22	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Left	18.3	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+H	Edge_1RB_Right	17.66	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+H	Inner_Full	18.34	PC3	PASS
DC_40A-n78A	30	5+50	DFT-256QAM	M+H	Outer_Full	18.26	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+H	Edge_1RB_Left	19.74	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+H	Edge_1RB_Right	19.09	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+H	Inner_Full	21.2	PC3	PASS
DC_40A-n78A	30	5+50	CP-QPSK	M+H	Outer_Full	19.76	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+H	Edge_1RB_Left	19.49	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+H	Edge_1RB_Right	19.01	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+H	Inner_Full	20.73	PC3	PASS
DC_40A-n78A	30	5+50	CP-16QAM	M+H	Outer_Full	19.72	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+H	Edge_1RB_Left	19.5	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+H	Edge_1RB_Right	18.9	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+H	Inner_Full	19.31	PC3	PASS
DC_40A-n78A	30	5+50	CP-64QAM	M+H	Outer_Full	19.19	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+H	Edge_1RB_Left	16.34	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+H	Edge_1RB_Right	15.67	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+H	Inner_Full	16.32	PC3	PASS
DC_40A-n78A	30	5+50	CP-256QAM	M+H	Outer_Full	16.29	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+L	Edge_1RB_Left	21.91	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+L	Edge_1RB_Right	22.2	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+L	Inner_Full	22.64	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+L	Outer_Full	22.09	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	21.55	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.69	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+L	Inner_Full	22.62	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+L	Outer_Full	21.62	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	20.22	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	20.36	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+L	Inner_Full	21.63	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+L	Outer_Full	20.64	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Left	20.24	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+L	Edge_1RB_Right	20.47	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+L	Inner_Full	20.12	PC3	PASS



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DC_40A-n78A	30	5+60	DFT-64QAM	M+L	Outer_Full	20.14	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Left	18.09	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+L	Edge_1RB_Right	18.25	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+L	Inner_Full	18.19	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+L	Outer_Full	18.16	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+L	Edge_1RB_Left	19.59	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+L	Edge_1RB_Right	19.73	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+L	Inner_Full	21.1	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+L	Outer_Full	19.63	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+L	Edge_1RB_Left	19.33	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+L	Edge_1RB_Right	19.52	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+L	Inner_Full	20.61	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+L	Outer_Full	19.51	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+L	Edge_1RB_Left	19.28	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+L	Edge_1RB_Right	19.53	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+L	Inner_Full	19.08	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+L	Outer_Full	19.13	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+L	Edge_1RB_Left	16.15	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+L	Edge_1RB_Right	16.31	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+L	Inner_Full	16.25	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+L	Outer_Full	16.22	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+M	Edge_1RB_Left	21.79	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+M	Edge_1RB_Right	22.03	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+M	Inner_Full	22.82	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+M	Outer_Full	22.25	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.22	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.48	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+M	Inner_Full	22.71	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+M	Outer_Full	21.75	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	20.05	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	20.29	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+M	Inner_Full	21.87	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+M	Outer_Full	20.73	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Left	20.03	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+M	Edge_1RB_Right	20.3	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+M	Inner_Full	20.46	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+M	Outer_Full	20.24	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Left	17.83	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+M	Edge_1RB_Right	18.09	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+M	Inner_Full	18.44	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+M	Outer_Full	18.32	PC3	PASS



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DC_40A-n78A	30	5+60	CP-QPSK	M+M	Edge_1RB_Left	19.22	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+M	Edge_1RB_Right	19.52	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+M	Inner_Full	21.28	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+M	Outer_Full	19.74	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+M	Edge_1RB_Left	19	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+M	Edge_1RB_Right	19.25	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+M	Inner_Full	20.82	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+M	Outer_Full	19.76	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+M	Edge_1RB_Left	18.98	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+M	Edge_1RB_Right	19.25	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+M	Inner_Full	19.37	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+M	Outer_Full	19.21	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+M	Edge_1RB_Left	15.9	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+M	Edge_1RB_Right	16.1	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+M	Inner_Full	16.43	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+M	Outer_Full	16.33	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+H	Edge_1RB_Left	22.33	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+H	Edge_1RB_Right	21.66	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+H	Inner_Full	22.8	PC3	PASS
DC_40A-n78A	30	5+60	DFT-BPSK	M+H	Outer_Full	22.2	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	21.84	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	21.11	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+H	Inner_Full	22.79	PC3	PASS
DC_40A-n78A	30	5+60	DFT-QPSK	M+H	Outer_Full	21.74	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	20.63	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	19.92	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+H	Inner_Full	21.75	PC3	PASS
DC_40A-n78A	30	5+60	DFT-16QAM	M+H	Outer_Full	20.72	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Left	20.53	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+H	Edge_1RB_Right	19.85	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+H	Inner_Full	20.29	PC3	PASS
DC_40A-n78A	30	5+60	DFT-64QAM	M+H	Outer_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Left	18.38	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+H	Edge_1RB_Right	17.71	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+H	Inner_Full	18.36	PC3	PASS
DC_40A-n78A	30	5+60	DFT-256QAM	M+H	Outer_Full	18.28	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+H	Edge_1RB_Left	19.87	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+H	Edge_1RB_Right	19.13	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+H	Inner_Full	21.28	PC3	PASS
DC_40A-n78A	30	5+60	CP-QPSK	M+H	Outer_Full	19.7	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+H	Edge_1RB_Left	19.54	PC3	PASS



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DC_40A-n78A	30	5+60	CP-16QAM	M+H	Edge_1RB_Right	18.85	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+H	Inner_Full	20.76	PC3	PASS
DC_40A-n78A	30	5+60	CP-16QAM	M+H	Outer_Full	19.74	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+H	Edge_1RB_Left	19.51	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+H	Edge_1RB_Right	18.89	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+H	Inner_Full	19.26	PC3	PASS
DC_40A-n78A	30	5+60	CP-64QAM	M+H	Outer_Full	19.26	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+H	Edge_1RB_Left	16.44	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+H	Edge_1RB_Right	15.72	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+H	Inner_Full	16.37	PC3	PASS
DC_40A-n78A	30	5+60	CP-256QAM	M+H	Outer_Full	16.3	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+L	Edge_1RB_Left	22.19	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+L	Edge_1RB_Right	22.13	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+L	Inner_Full	22.66	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+L	Outer_Full	22.17	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	21.63	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	21.6	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+L	Inner_Full	22.71	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+L	Outer_Full	21.69	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	20.36	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	20.27	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+L	Inner_Full	21.71	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+L	Outer_Full	20.7	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Left	20.18	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+L	Edge_1RB_Right	20.37	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+L	Inner_Full	20.24	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+L	Outer_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Left	18.2	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+L	Edge_1RB_Right	18.25	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+L	Inner_Full	18.27	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+L	Outer_Full	18.18	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+L	Edge_1RB_Left	19.76	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+L	Edge_1RB_Right	19.64	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+L	Inner_Full	21.18	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+L	Outer_Full	19.73	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+L	Edge_1RB_Left	19.39	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+L	Edge_1RB_Right	19.32	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+L	Inner_Full	20.78	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+L	Outer_Full	19.73	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+L	Edge_1RB_Left	19.33	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+L	Edge_1RB_Right	19.41	PC3	PASS



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DC_40A-n78A	30	5+70	CP-64QAM	M+L	Inner_Full	19.25	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+L	Outer_Full	19.19	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+L	Edge_1RB_Left	16.17	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+L	Edge_1RB_Right	16.22	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+L	Inner_Full	16.32	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+L	Outer_Full	16.32	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+M	Edge_1RB_Left	21.87	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+M	Edge_1RB_Right	22.03	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+M	Inner_Full	22.86	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+M	Outer_Full	22.26	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	21.36	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	21.49	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+M	Inner_Full	22.9	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+M	Outer_Full	21.78	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	19.97	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	20.29	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+M	Inner_Full	21.91	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+M	Outer_Full	20.84	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Left	20.11	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+M	Edge_1RB_Right	20.15	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+M	Inner_Full	20.42	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+M	Outer_Full	20.38	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Left	17.9	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+M	Edge_1RB_Right	18.06	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+M	Inner_Full	18.33	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+M	Outer_Full	18.27	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+M	Edge_1RB_Left	19.37	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+M	Edge_1RB_Right	19.51	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+M	Inner_Full	21.35	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+M	Outer_Full	19.8	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+M	Edge_1RB_Left	19.06	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+M	Edge_1RB_Right	19.33	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+M	Inner_Full	20.92	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+M	Outer_Full	19.79	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+M	Edge_1RB_Left	19.09	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+M	Edge_1RB_Right	19.25	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+M	Inner_Full	19.39	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+M	Outer_Full	19.28	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+M	Edge_1RB_Left	15.97	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+M	Edge_1RB_Right	16.09	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+M	Inner_Full	16.48	PC3	PASS



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DC_40A-n78A	30	5+70	CP-256QAM	M+M	Outer_Full	16.39	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+H	Edge_1RB_Left	22.29	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+H	Edge_1RB_Right	21.63	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+H	Inner_Full	22.8	PC3	PASS
DC_40A-n78A	30	5+70	DFT-BPSK	M+H	Outer_Full	22.31	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	21.77	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	21.11	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+H	Inner_Full	22.78	PC3	PASS
DC_40A-n78A	30	5+70	DFT-QPSK	M+H	Outer_Full	21.67	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	20.61	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	19.91	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+H	Inner_Full	21.84	PC3	PASS
DC_40A-n78A	30	5+70	DFT-16QAM	M+H	Outer_Full	20.76	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Left	20.56	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+H	Edge_1RB_Right	19.86	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+H	Inner_Full	20.32	PC3	PASS
DC_40A-n78A	30	5+70	DFT-64QAM	M+H	Outer_Full	20.3	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Left	18.27	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+H	Edge_1RB_Right	17.76	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+H	Inner_Full	18.25	PC3	PASS
DC_40A-n78A	30	5+70	DFT-256QAM	M+H	Outer_Full	18.33	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+H	Edge_1RB_Left	19.78	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+H	Edge_1RB_Right	19.09	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+H	Inner_Full	21.33	PC3	PASS
DC_40A-n78A	30	5+70	CP-QPSK	M+H	Outer_Full	19.79	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+H	Edge_1RB_Left	19.54	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+H	Edge_1RB_Right	19.02	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+H	Inner_Full	20.85	PC3	PASS
DC_40A-n78A	30	5+70	CP-16QAM	M+H	Outer_Full	19.81	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+H	Edge_1RB_Left	19.55	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+H	Edge_1RB_Right	18.87	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+H	Inner_Full	19.37	PC3	PASS
DC_40A-n78A	30	5+70	CP-64QAM	M+H	Outer_Full	19.28	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+H	Edge_1RB_Left	16.44	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+H	Edge_1RB_Right	15.73	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+H	Inner_Full	16.39	PC3	PASS
DC_40A-n78A	30	5+70	CP-256QAM	M+H	Outer_Full	16.41	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+L	Edge_1RB_Left	22.2	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+L	Edge_1RB_Right	22.07	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+L	Inner_Full	22.81	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+L	Outer_Full	22.25	PC3	PASS



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DC_40A-n78A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	21.69	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	21.52	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+L	Inner_Full	22.87	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+L	Outer_Full	21.7	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	20.42	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	20.34	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+L	Inner_Full	21.83	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+L	Outer_Full	20.73	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Left	20.38	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+L	Edge_1RB_Right	20.32	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+L	Inner_Full	20.37	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+L	Outer_Full	20.23	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Left	18.33	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+L	Edge_1RB_Right	18.12	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+L	Inner_Full	18.32	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+L	Outer_Full	18.31	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+L	Edge_1RB_Left	19.66	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+L	Edge_1RB_Right	19.51	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+L	Inner_Full	21.32	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+L	Outer_Full	19.7	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+L	Edge_1RB_Left	19.5	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+L	Edge_1RB_Right	19.27	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+L	Inner_Full	20.84	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+L	Outer_Full	19.72	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+L	Edge_1RB_Left	19.48	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+L	Edge_1RB_Right	19.32	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+L	Inner_Full	19.35	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+L	Outer_Full	19.25	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+L	Edge_1RB_Left	16.2	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+L	Edge_1RB_Right	16.15	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+L	Inner_Full	16.43	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+L	Outer_Full	16.31	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+M	Edge_1RB_Left	22.02	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+M	Edge_1RB_Right	22.01	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+M	Inner_Full	22.88	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+M	Outer_Full	22.24	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.49	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	21.5	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+M	Inner_Full	22.94	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+M	Outer_Full	21.78	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	20.14	PC3	PASS



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DC_40A-n78A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	20.27	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+M	Inner_Full	21.9	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+M	Outer_Full	20.75	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Left	20.26	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+M	Edge_1RB_Right	20.28	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+M	Inner_Full	20.49	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+M	Outer_Full	20.24	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Left	18.08	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+M	Edge_1RB_Right	18.05	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+M	Inner_Full	18.37	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+M	Outer_Full	18.27	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+M	Edge_1RB_Left	19.49	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+M	Edge_1RB_Right	19.56	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+M	Inner_Full	21.33	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+M	Outer_Full	19.72	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+M	Edge_1RB_Left	19.38	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+M	Edge_1RB_Right	19.31	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+M	Inner_Full	20.9	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+M	Outer_Full	19.67	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+M	Edge_1RB_Left	19.25	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+M	Edge_1RB_Right	19.25	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+M	Inner_Full	19.42	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+M	Outer_Full	19.22	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+M	Edge_1RB_Left	16.11	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+M	Edge_1RB_Right	16.07	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+M	Inner_Full	16.47	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+M	Outer_Full	16.34	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+H	Edge_1RB_Left	21.93	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+H	Edge_1RB_Right	21.7	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+H	Inner_Full	22.85	PC3	PASS
DC_40A-n78A	30	5+80	DFT-BPSK	M+H	Outer_Full	22.24	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	21.4	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	21.14	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+H	Inner_Full	22.86	PC3	PASS
DC_40A-n78A	30	5+80	DFT-QPSK	M+H	Outer_Full	21.75	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	20.2	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	19.94	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+H	Inner_Full	21.85	PC3	PASS
DC_40A-n78A	30	5+80	DFT-16QAM	M+H	Outer_Full	20.73	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Left	20.21	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+H	Edge_1RB_Right	19.86	PC3	PASS



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DC_40A-n78A	30	5+80	DFT-64QAM	M+H	Inner_Full	20.43	PC3	PASS
DC_40A-n78A	30	5+80	DFT-64QAM	M+H	Outer_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Left	17.93	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+H	Edge_1RB_Right	17.71	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+H	Inner_Full	18.42	PC3	PASS
DC_40A-n78A	30	5+80	DFT-256QAM	M+H	Outer_Full	18.34	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+H	Edge_1RB_Left	19.56	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+H	Edge_1RB_Right	19.18	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+H	Inner_Full	21.39	PC3	PASS
DC_40A-n78A	30	5+80	CP-QPSK	M+H	Outer_Full	19.75	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+H	Edge_1RB_Left	19.17	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+H	Edge_1RB_Right	18.92	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+H	Inner_Full	20.83	PC3	PASS
DC_40A-n78A	30	5+80	CP-16QAM	M+H	Outer_Full	19.79	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+H	Edge_1RB_Left	19.17	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+H	Edge_1RB_Right	18.96	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+H	Inner_Full	19.34	PC3	PASS
DC_40A-n78A	30	5+80	CP-64QAM	M+H	Outer_Full	19.25	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+H	Edge_1RB_Left	16.03	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+H	Edge_1RB_Right	15.7	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+H	Inner_Full	16.4	PC3	PASS
DC_40A-n78A	30	5+80	CP-256QAM	M+H	Outer_Full	16.33	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+L	Edge_1RB_Left	22.19	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+L	Edge_1RB_Right	21.95	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+L	Inner_Full	22.84	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+L	Outer_Full	22.19	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	21.67	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.42	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+L	Inner_Full	22.87	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+L	Outer_Full	21.71	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	20.39	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	20.21	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+L	Inner_Full	21.82	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+L	Outer_Full	20.7	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Left	20.35	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+L	Edge_1RB_Right	20.17	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+L	Inner_Full	20.37	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+L	Outer_Full	20.24	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Left	18.23	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+L	Edge_1RB_Right	17.98	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+L	Inner_Full	18.41	PC3	PASS



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DC_40A-n78A	30	5+90	DFT-256QAM	M+L	Outer_Full	18.25	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+L	Edge_1RB_Left	19.64	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+L	Edge_1RB_Right	19.47	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+L	Inner_Full	21.29	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+L	Outer_Full	19.69	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+L	Edge_1RB_Left	19.38	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+L	Edge_1RB_Right	19.35	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+L	Inner_Full	20.86	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+L	Outer_Full	19.7	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+L	Edge_1RB_Left	19.44	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+L	Edge_1RB_Right	19.19	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+L	Inner_Full	19.34	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+L	Outer_Full	19.21	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+L	Edge_1RB_Left	16.14	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+L	Edge_1RB_Right	15.94	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+L	Inner_Full	16.44	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+L	Outer_Full	16.3	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+M	Edge_1RB_Left	22.16	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+M	Edge_1RB_Right	21.86	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+M	Inner_Full	22.88	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+M	Outer_Full	22.21	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.67	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	21.32	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+M	Inner_Full	22.89	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+M	Outer_Full	21.75	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	20.39	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	20.13	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+M	Inner_Full	21.95	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+M	Outer_Full	20.75	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+M	Edge_1RB_Left	20.37	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+M	Edge_1RB_Right	20.08	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+M	Inner_Full	20.42	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+M	Outer_Full	20.26	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Left	18.21	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+M	Edge_1RB_Right	17.92	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+M	Inner_Full	18.41	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+M	Outer_Full	18.25	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+M	Edge_1RB_Left	19.69	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+M	Edge_1RB_Right	19.27	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+M	Inner_Full	21.35	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+M	Outer_Full	19.75	PC3	PASS



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DC_40A-n78A	30	5+90	CP-16QAM	M+M	Edge_1RB_Left	19.4	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+M	Edge_1RB_Right	19.1	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+M	Inner_Full	20.87	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+M	Outer_Full	19.8	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+M	Edge_1RB_Left	19.36	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+M	Edge_1RB_Right	19.1	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+M	Inner_Full	19.37	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+M	Outer_Full	19.25	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+M	Edge_1RB_Left	16.22	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+M	Edge_1RB_Right	15.95	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+M	Inner_Full	16.47	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+M	Outer_Full	16.24	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+H	Edge_1RB_Left	22.02	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+H	Edge_1RB_Right	21.68	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+H	Inner_Full	22.92	PC3	PASS
DC_40A-n78A	30	5+90	DFT-BPSK	M+H	Outer_Full	22.23	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	21.5	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	21.14	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+H	Inner_Full	22.91	PC3	PASS
DC_40A-n78A	30	5+90	DFT-QPSK	M+H	Outer_Full	21.76	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	20.34	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	20.02	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+H	Inner_Full	21.96	PC3	PASS
DC_40A-n78A	30	5+90	DFT-16QAM	M+H	Outer_Full	20.76	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Left	20.37	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+H	Edge_1RB_Right	19.88	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+H	Inner_Full	20.44	PC3	PASS
DC_40A-n78A	30	5+90	DFT-64QAM	M+H	Outer_Full	20.25	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Left	18.13	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+H	Edge_1RB_Right	17.75	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+H	Inner_Full	18.41	PC3	PASS
DC_40A-n78A	30	5+90	DFT-256QAM	M+H	Outer_Full	18.26	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+H	Edge_1RB_Left	19.63	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+H	Edge_1RB_Right	19.15	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+H	Inner_Full	21.36	PC3	PASS
DC_40A-n78A	30	5+90	CP-QPSK	M+H	Outer_Full	19.77	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+H	Edge_1RB_Left	19.27	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+H	Edge_1RB_Right	18.9	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+H	Inner_Full	20.87	PC3	PASS
DC_40A-n78A	30	5+90	CP-16QAM	M+H	Outer_Full	19.73	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+H	Edge_1RB_Left	19.27	PC3	PASS



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DC_40A-n78A	30	5+90	CP-64QAM	M+H	Edge_1RB_Right	18.95	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+H	Inner_Full	19.39	PC3	PASS
DC_40A-n78A	30	5+90	CP-64QAM	M+H	Outer_Full	19.32	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+H	Edge_1RB_Left	16.19	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+H	Edge_1RB_Right	15.73	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+H	Inner_Full	16.46	PC3	PASS
DC_40A-n78A	30	5+90	CP-256QAM	M+H	Outer_Full	16.34	PC3	PASS
DC_40A-n78A	30	5+100	DFT-BPSK	M+M	Edge_1RB_Left	22.18	PC3	PASS
DC_40A-n78A	30	5+100	DFT-BPSK	M+M	Edge_1RB_Right	21.62	PC3	PASS
DC_40A-n78A	30	5+100	DFT-BPSK	M+M	Inner_Full	22.87	PC3	PASS
DC_40A-n78A	30	5+100	DFT-BPSK	M+M	Outer_Full	22.19	PC3	PASS
DC_40A-n78A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.67	PC3	PASS
DC_40A-n78A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.08	PC3	PASS
DC_40A-n78A	30	5+100	DFT-QPSK	M+M	Inner_Full	22.88	PC3	PASS
DC_40A-n78A	30	5+100	DFT-QPSK	M+M	Outer_Full	21.73	PC3	PASS
DC_40A-n78A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	20.3	PC3	PASS
DC_40A-n78A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	19.82	PC3	PASS
DC_40A-n78A	30	5+100	DFT-16QAM	M+M	Inner_Full	21.88	PC3	PASS
DC_40A-n78A	30	5+100	DFT-16QAM	M+M	Outer_Full	20.71	PC3	PASS
DC_40A-n78A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Left	20.52	PC3	PASS
DC_40A-n78A	30	5+100	DFT-64QAM	M+M	Edge_1RB_Right	19.96	PC3	PASS
DC_40A-n78A	30	5+100	DFT-64QAM	M+M	Inner_Full	20.38	PC3	PASS
DC_40A-n78A	30	5+100	DFT-64QAM	M+M	Outer_Full	20.2	PC3	PASS
DC_40A-n78A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Left	18.29	PC3	PASS
DC_40A-n78A	30	5+100	DFT-256QAM	M+M	Edge_1RB_Right	17.64	PC3	PASS
DC_40A-n78A	30	5+100	DFT-256QAM	M+M	Inner_Full	18.37	PC3	PASS
DC_40A-n78A	30	5+100	DFT-256QAM	M+M	Outer_Full	18.24	PC3	PASS
DC_40A-n78A	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	19.71	PC3	PASS
DC_40A-n78A	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	19.09	PC3	PASS
DC_40A-n78A	30	5+100	CP-QPSK	M+M	Inner_Full	21.33	PC3	PASS
DC_40A-n78A	30	5+100	CP-QPSK	M+M	Outer_Full	19.71	PC3	PASS
DC_40A-n78A	30	5+100	CP-16QAM	M+M	Edge_1RB_Left	19.41	PC3	PASS
DC_40A-n78A	30	5+100	CP-16QAM	M+M	Edge_1RB_Right	18.85	PC3	PASS
DC_40A-n78A	30	5+100	CP-16QAM	M+M	Inner_Full	20.88	PC3	PASS
DC_40A-n78A	30	5+100	CP-16QAM	M+M	Outer_Full	19.71	PC3	PASS
DC_40A-n78A	30	5+100	CP-64QAM	M+M	Edge_1RB_Left	19.39	PC3	PASS
DC_40A-n78A	30	5+100	CP-64QAM	M+M	Edge_1RB_Right	18.85	PC3	PASS
DC_40A-n78A	30	5+100	CP-64QAM	M+M	Inner_Full	19.37	PC3	PASS
DC_40A-n78A	30	5+100	CP-64QAM	M+M	Outer_Full	19.22	PC3	PASS
DC_40A-n78A	30	5+100	CP-256QAM	M+M	Edge_1RB_Left	16.24	PC3	PASS
DC_40A-n78A	30	5+100	CP-256QAM	M+M	Edge_1RB_Right	15.7	PC3	PASS



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DC_40A-n78A	30	5+100	CP-256QAM	M+M	Inner_Full	16.46	PC3	PASS
DC_40A-n78A	30	5+100	CP-256QAM	M+M	Outer_Full	16.28	PC3	PASS

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