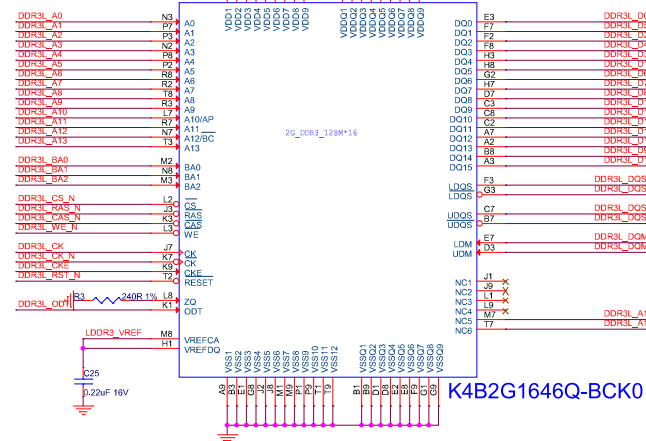
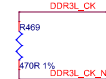
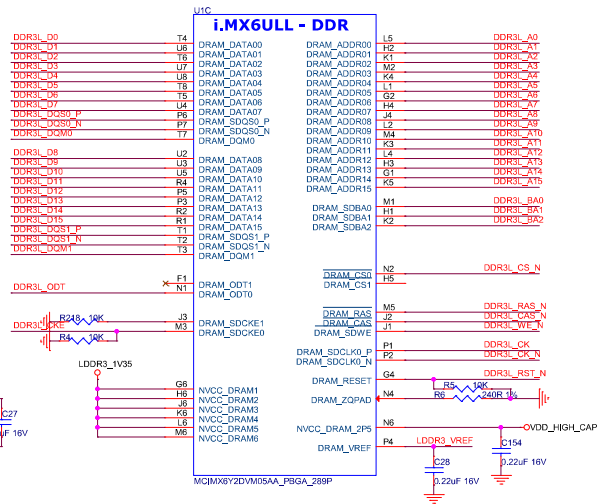
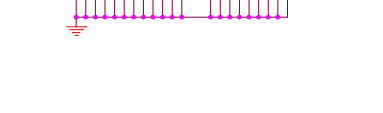
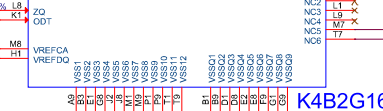
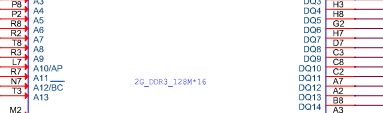
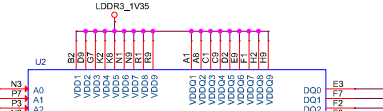
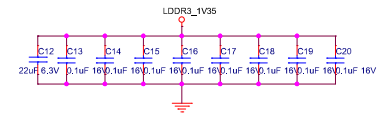
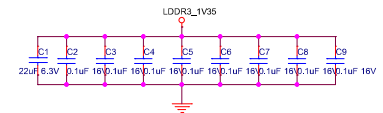
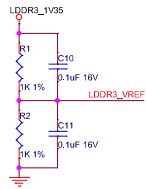
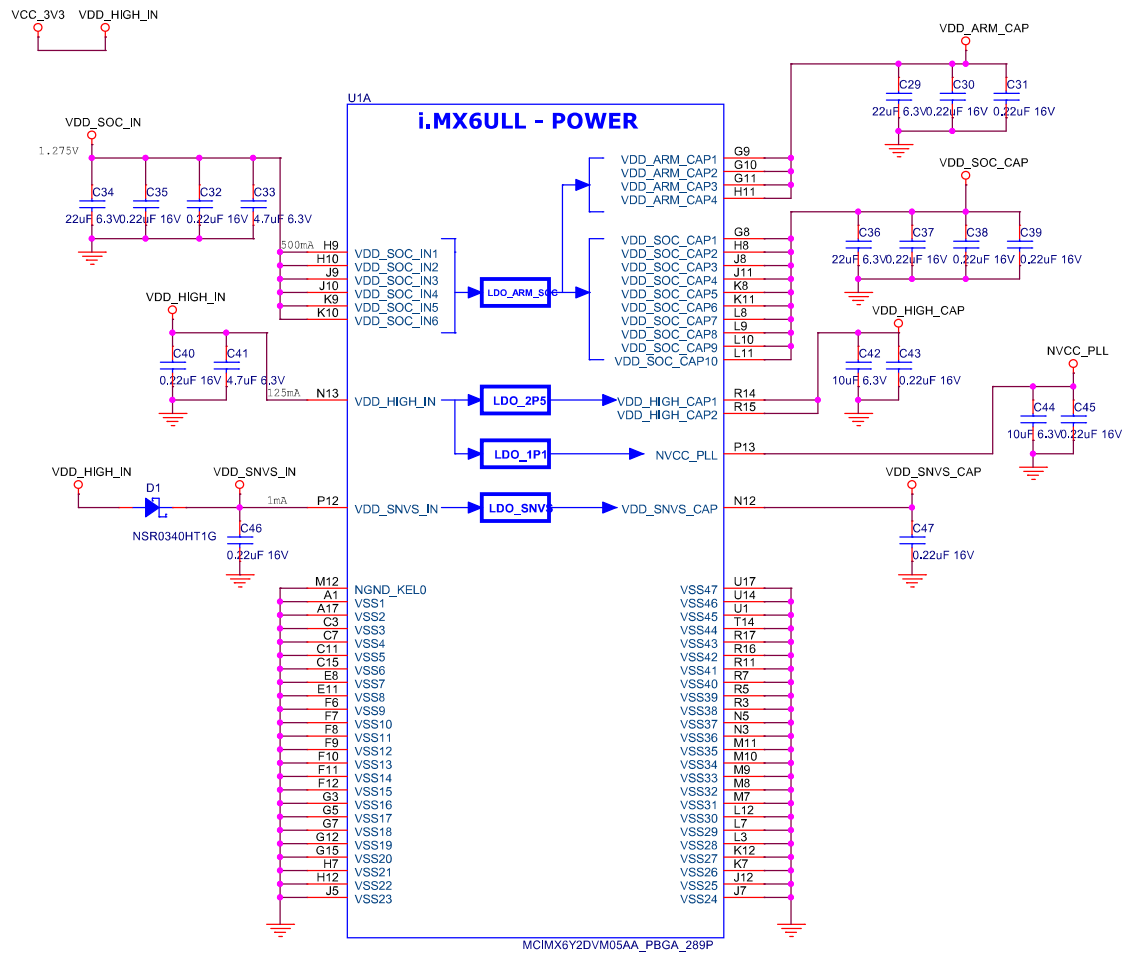
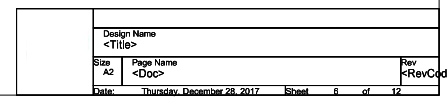
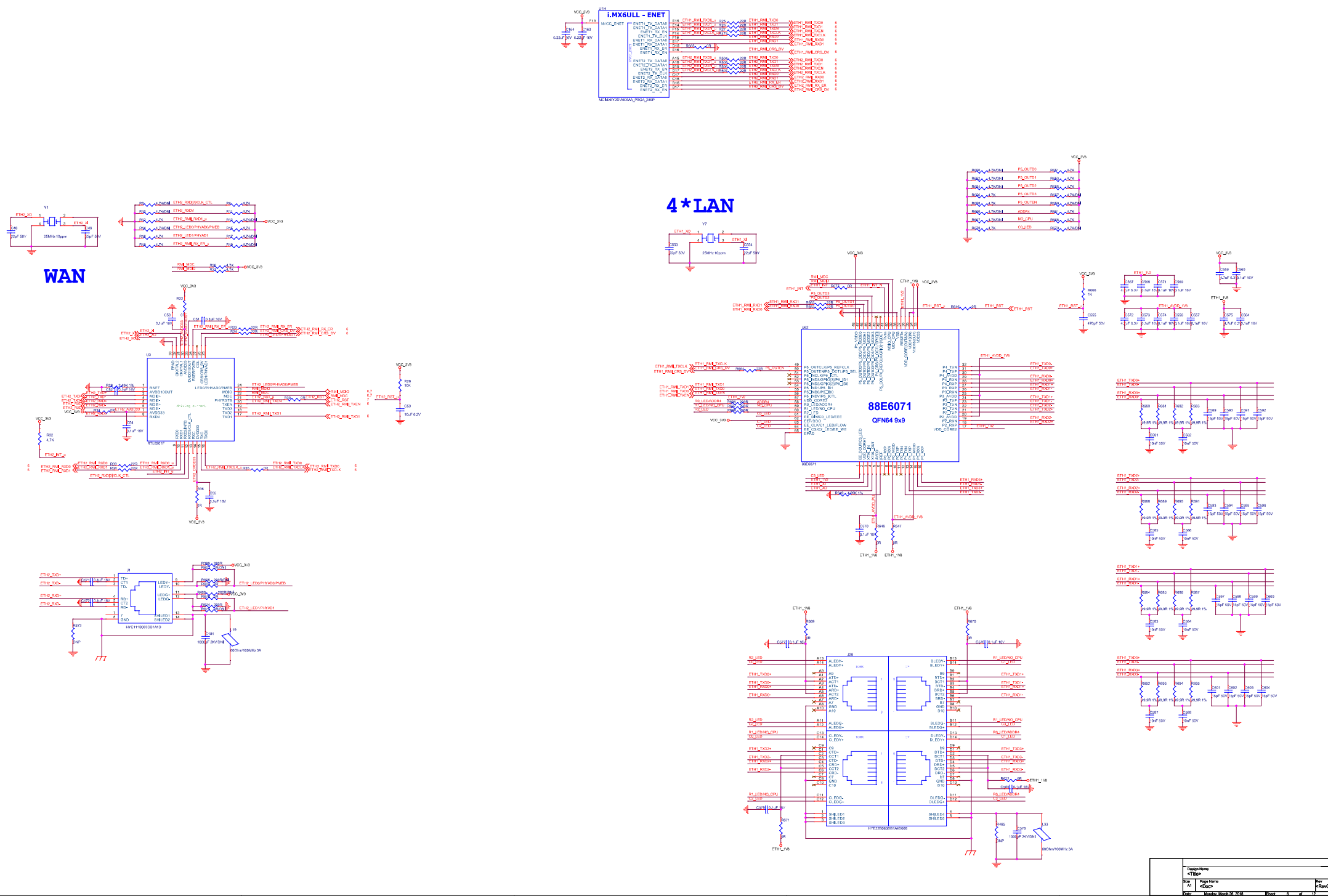




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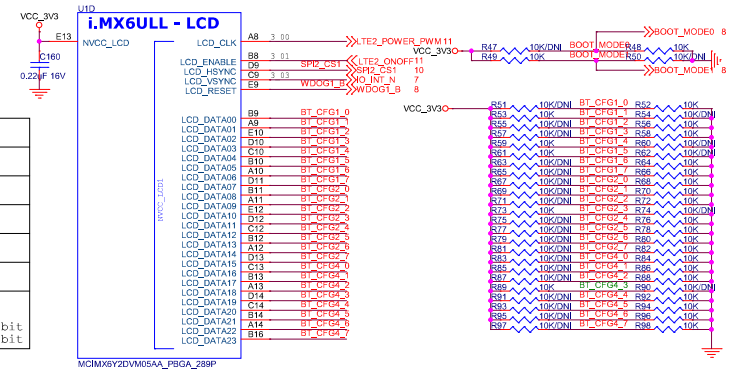




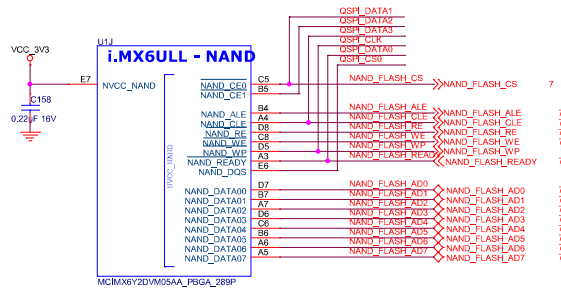
BOOT

BOOT MODE

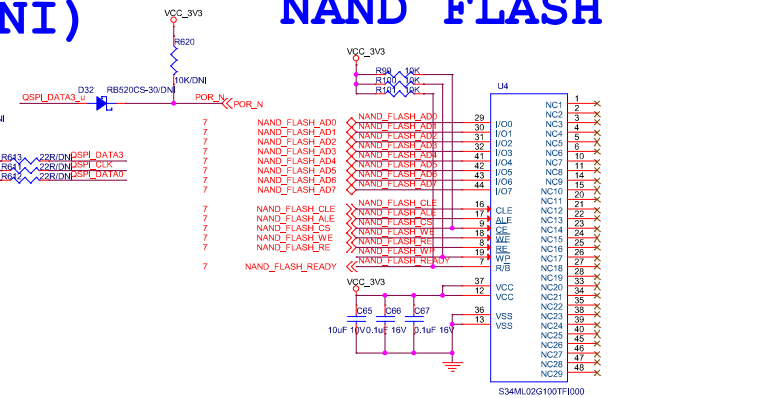
CFG_1[0]	CFG_1[5]	CFG_1[6]	CFG_1[7]	CFG_2[3]	
0	0	0	0	X	QSPI
1	0	0	1	X	NAND
0	1	1	0	X	EMMC
0	0	1	0	1	Micro SD2
0	0	1	0	0	Micro SD1
X	1	0	0	X	SP11 CFG4[3]:0--16bi 1--24bi



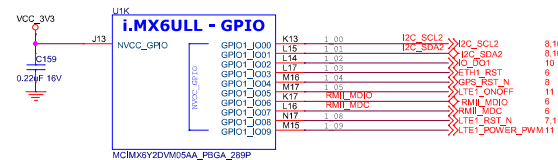
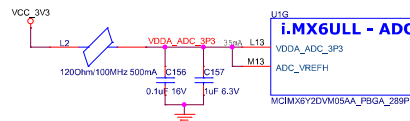
QSPI FLASH (DNI)



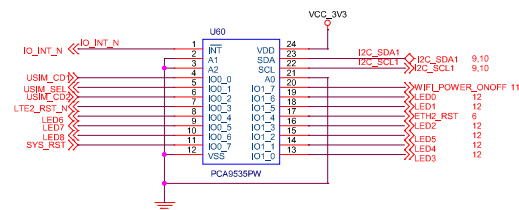
NAND FLASH

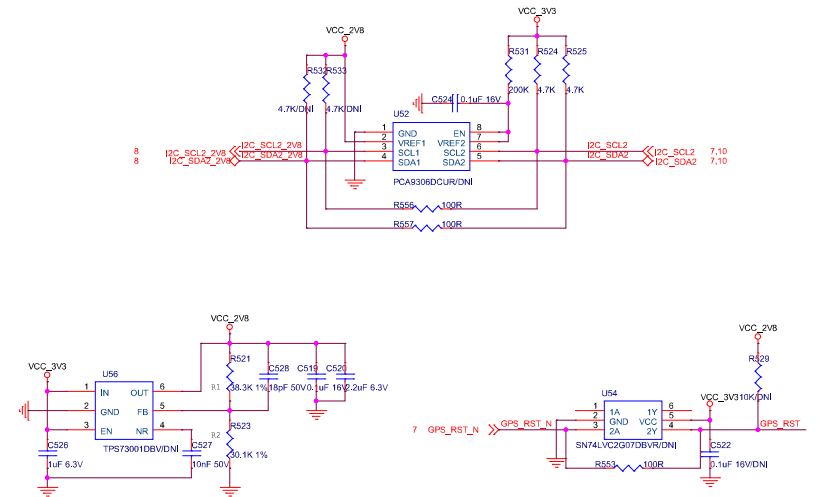
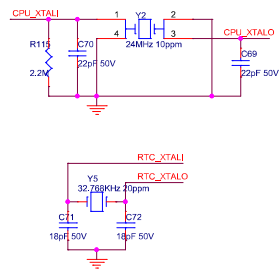


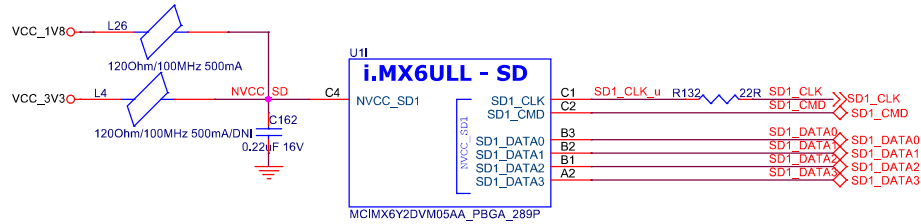
GPIO



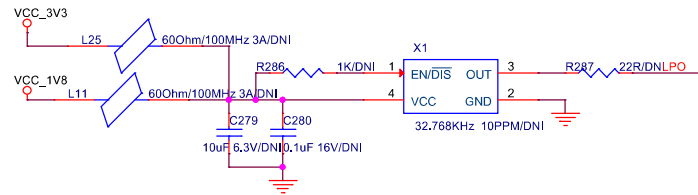
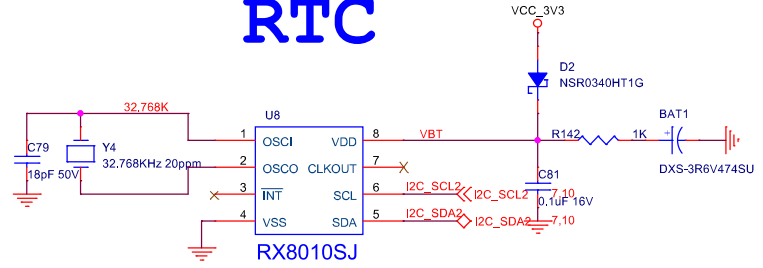
I2C->IO



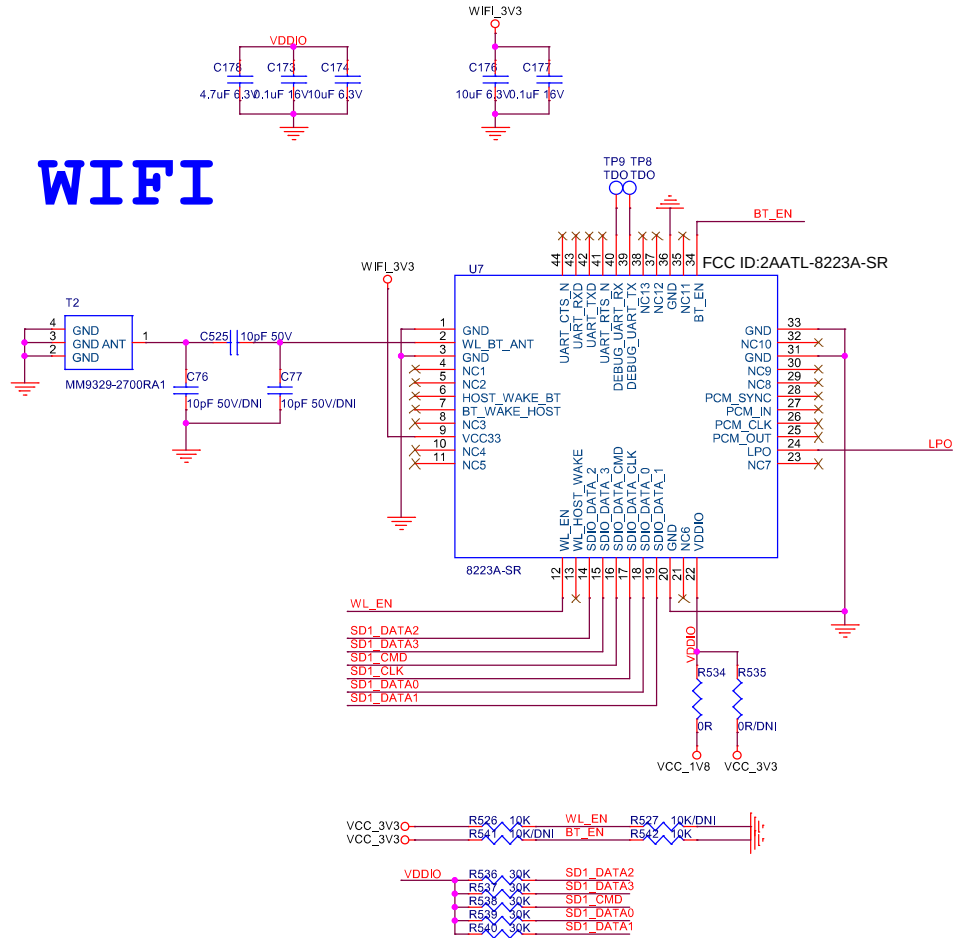
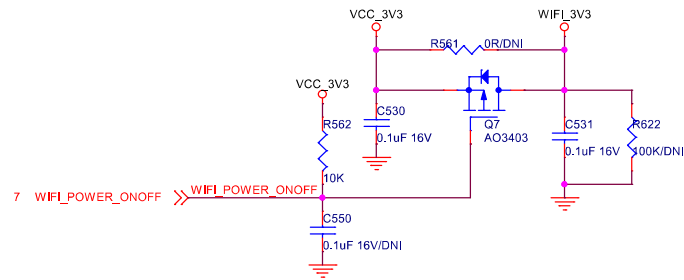
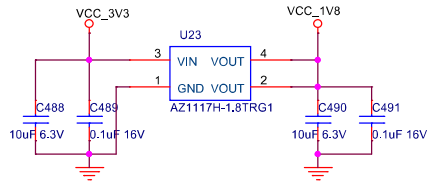




RTC

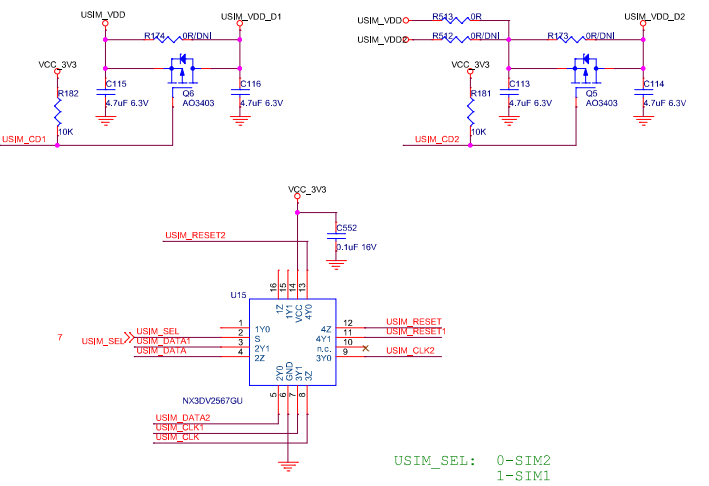
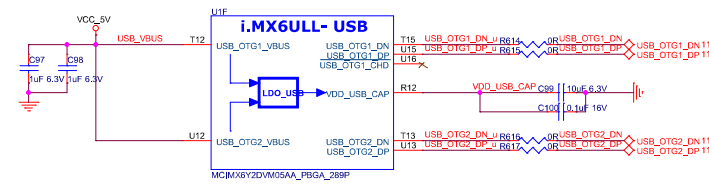


WIFI

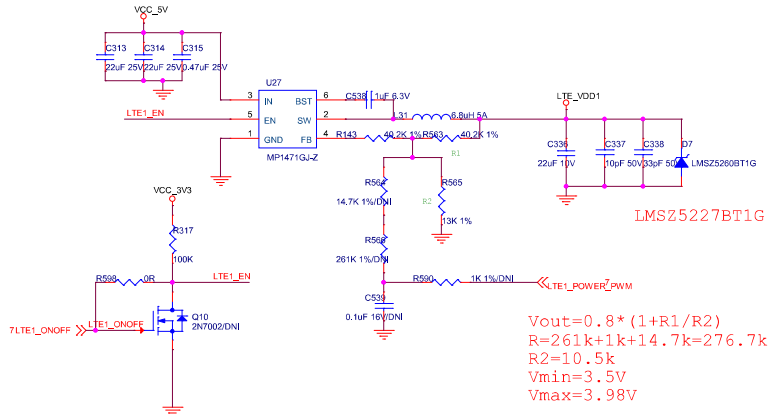


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Date: Friday, December 29, 2017		Sheet 9 of 13

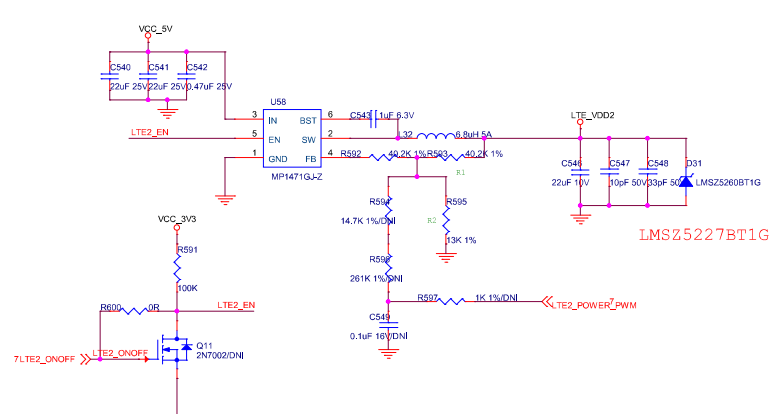
4G_MOD



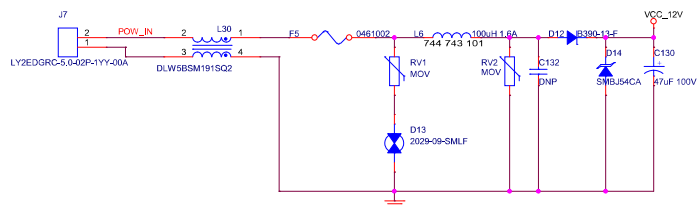
MOD1_4G_POWER/5V ->3V3 (3A)



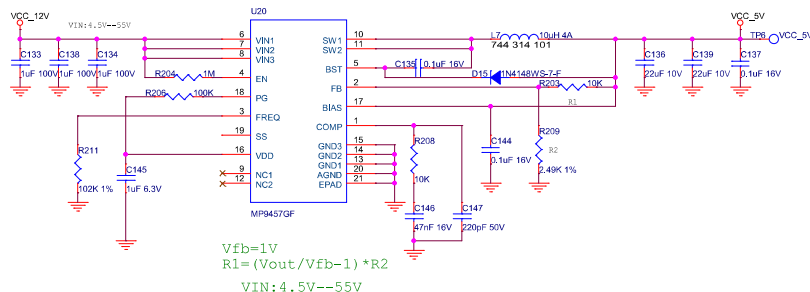
MOD2_4G_POWER/5V ->3V3 (3A)



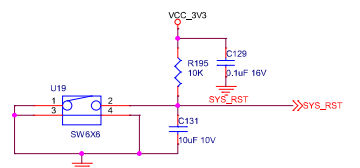
POWER IN 12V-48V (12W)



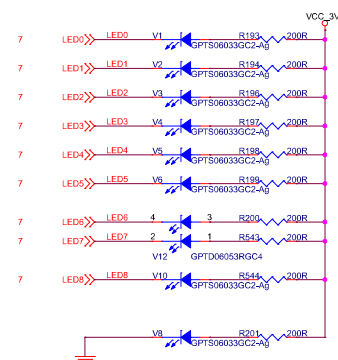
9V~48V → 5V (3A)



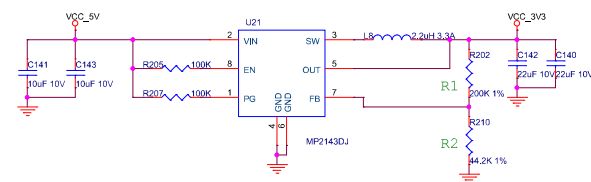
SOFT_RST



LED



5V $\rightarrow$ 3V3 (3A)



```
Vout=0.6*(1+R1/R2)
IN: 2.5V--5.5V
EN: High Threshold--1.2V
```

5V $\rightarrow$ 1.275V (2A) && 5V $\rightarrow$ 1.35V (2A)

