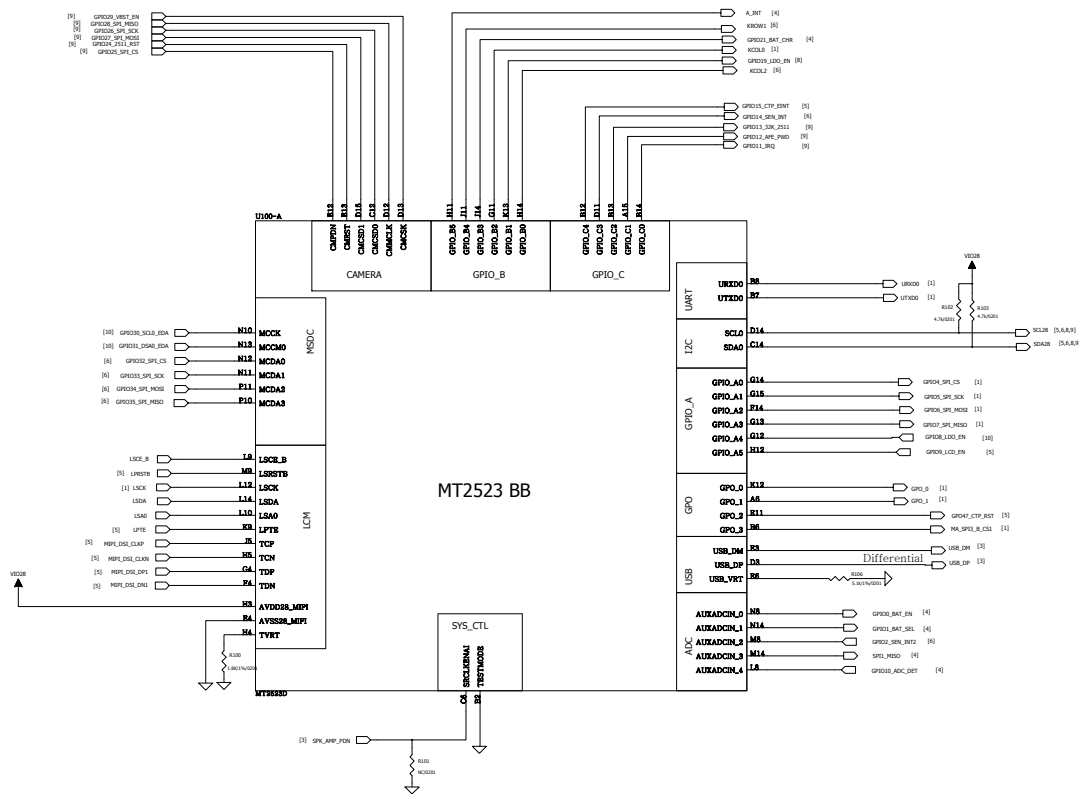
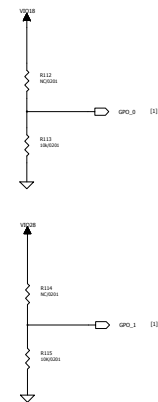
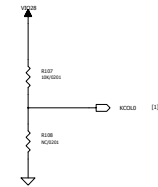




MCU JTAG Trapping



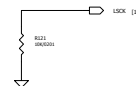
0:USBDL
1:normal



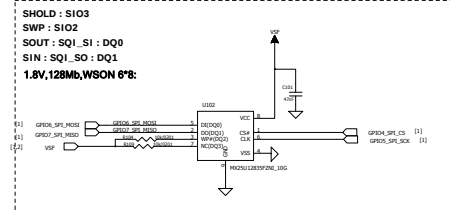
VSF(HW trapping PIN):
VSF POWER SEL
0: 1.8V
1: 3V



system reserve

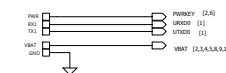


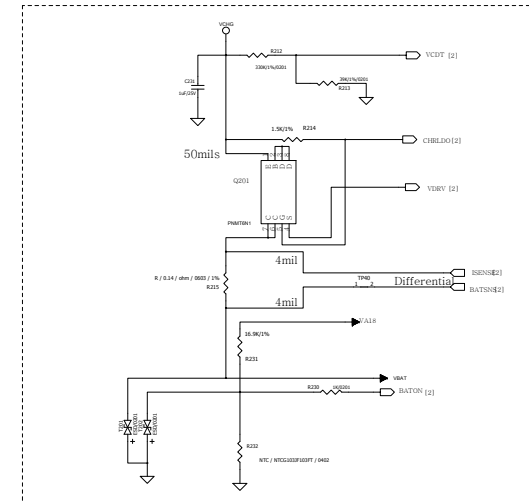
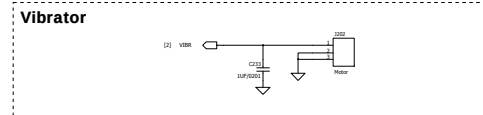
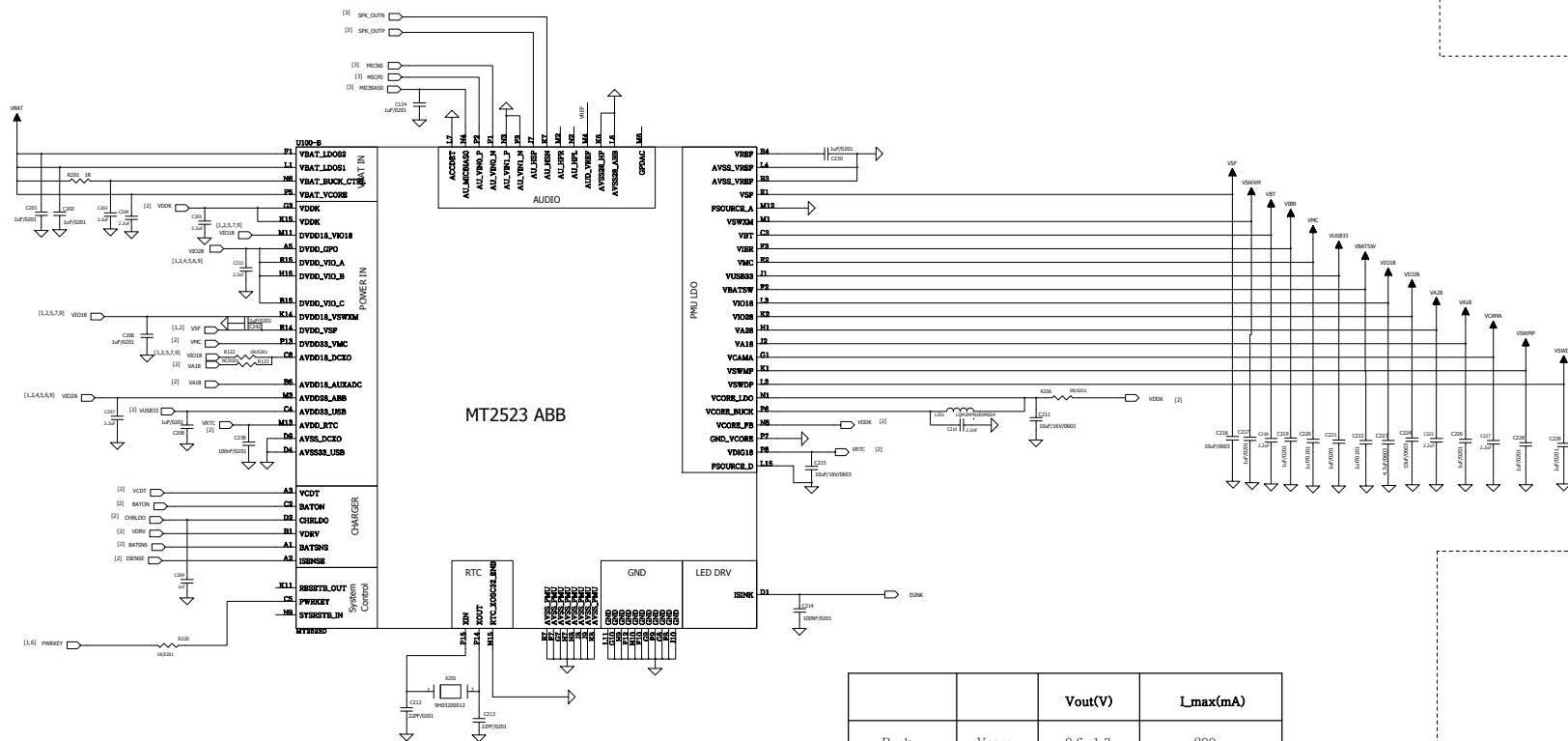
Serial Flash: Shielding



Test point

1mm





		Vout(V)	L_max(mA)
Buck	Vcore	0.6~1.3	200
Digital LDO	Vcore	0.6~1.3	20
Analog LDO	VBT	2.8/1.8	100
	VA28	2.8	150
	VCAMA	2.8	150
	VA18	1.8	2.5
Digital LDO	VSF	1.86/3.0/3.3	100
	VIO28	2.8	150
	VIO18	1.8	200
	VUSB	3.3	80
	VMC	1.8/2.8/3.3	230
	VIBR	1.3/1.5/1.8/2.5/2.8/3.3	100
	VDIG18	1.8	5
Driver	ISINK		1X(96mA:48mA/0.25V)

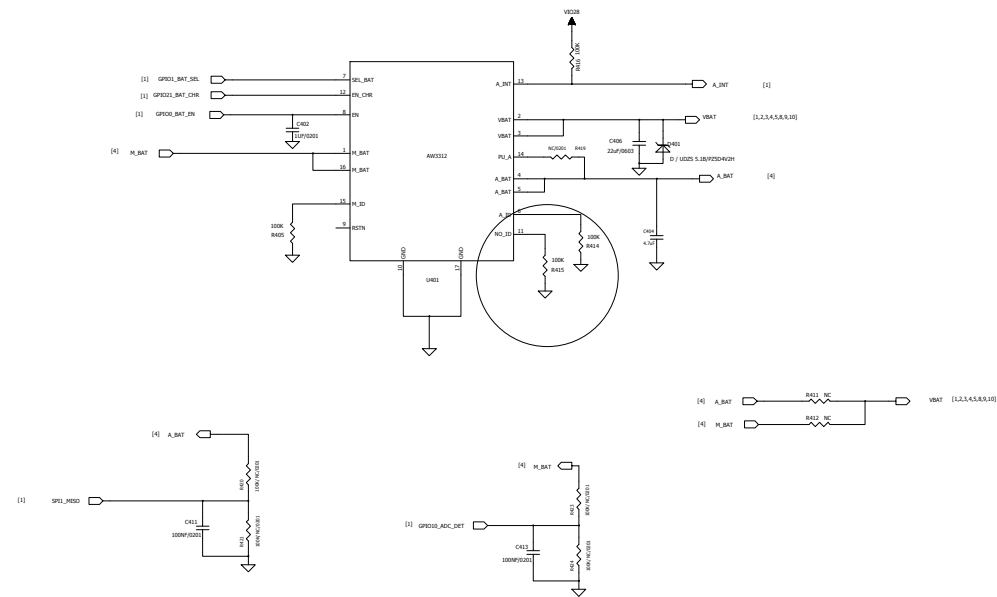
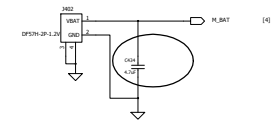
[illegible][illegible]

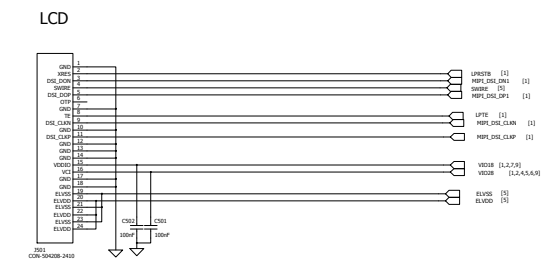
USB

The diagram shows the internal connections for a USB connector. On the left, five pins are labeled: [2] VDDG5, [1] USB_DM, [4] USB_DP, [3] VDDG5, and [2] GND. These pins are connected to a central block representing the USB controller. The block has five pins: VDDG5, DM, DP, and GND. The connections are as follows: [2] VDDG5 connects to VDDG5; [1] USB_DM connects to DM through a 0.01Ω resistor and a 100nF capacitor to GND; [4] USB_DP connects to DP through a 0.01Ω resistor and a 100nF capacitor to GND; [3] VDDG5 connects to GND; [2] GND connects to GND. The GND pin is connected to a common ground symbol.

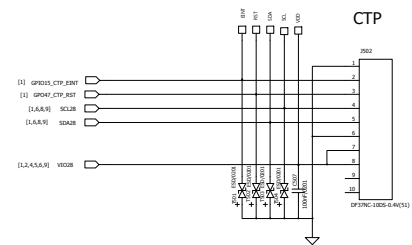
This ESD components are optional for better ESD performance
NOTE: T306, T307 C load must < 3pf and close to JACK

BATTERY

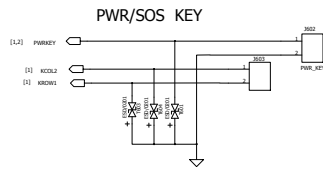




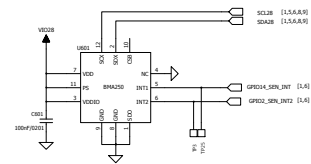
1.2 AUO屏



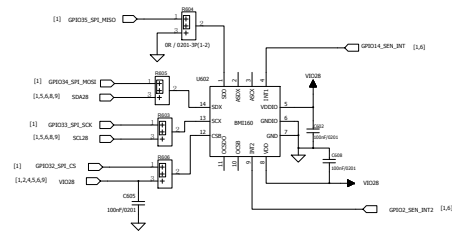
KEY



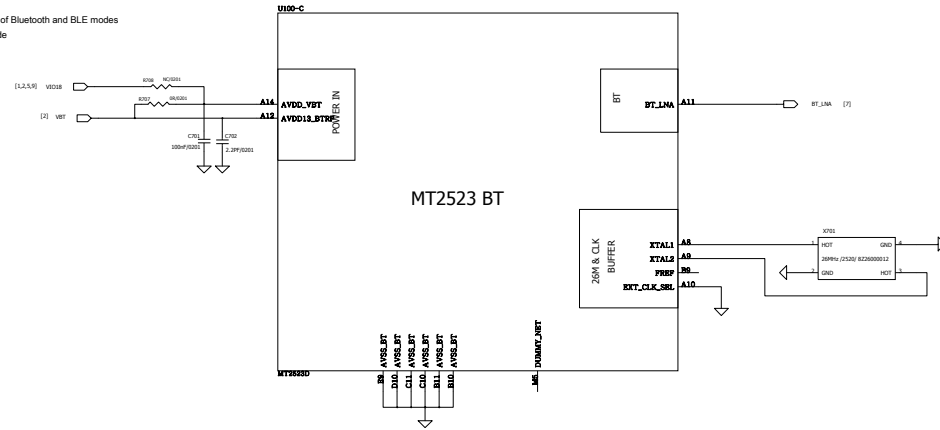
3轴 GSENSOR



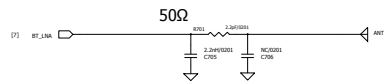
6轴 SENSOR

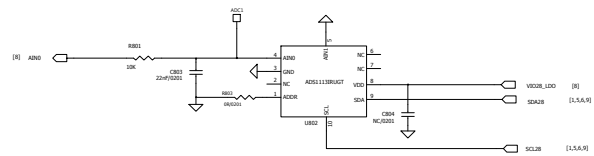


R707 connected to VBT for dual mode of Bluetooth and BLE modes
R708 connected to VIO18 for BLE mode

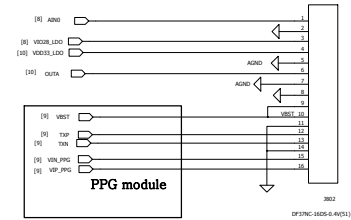
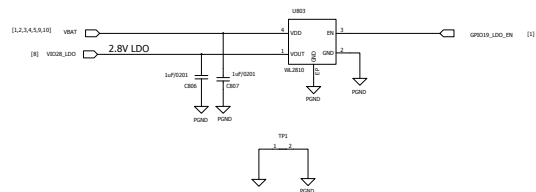


BT





LDO 2.8V



[illegible]