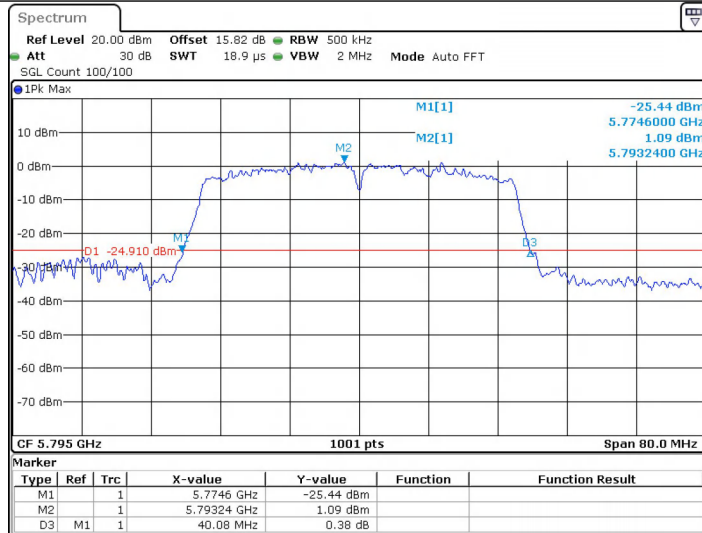
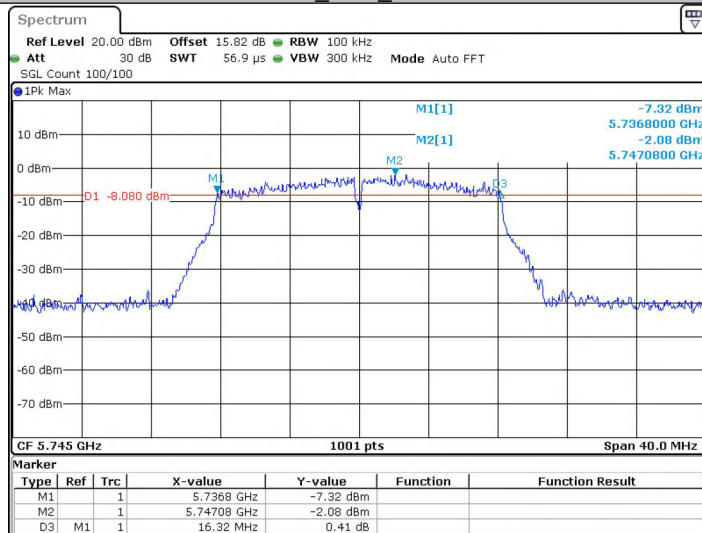


11AC40SISO_Ant1_5795



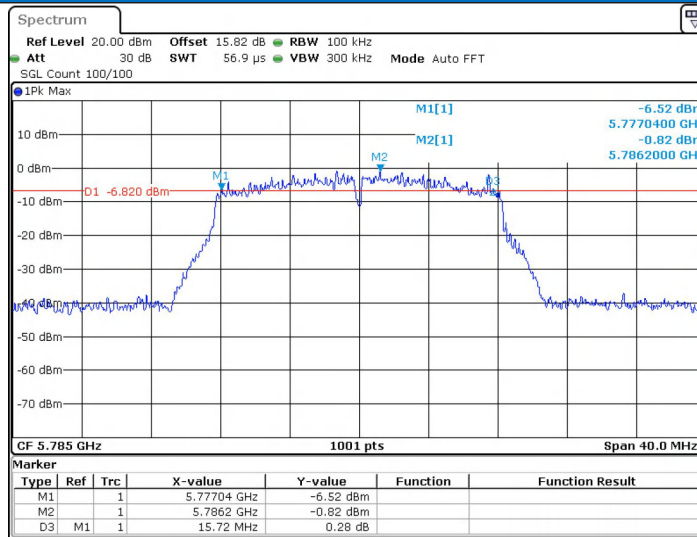
Date: 23 APR 2026 21:57:04

11A_Ant1_5745



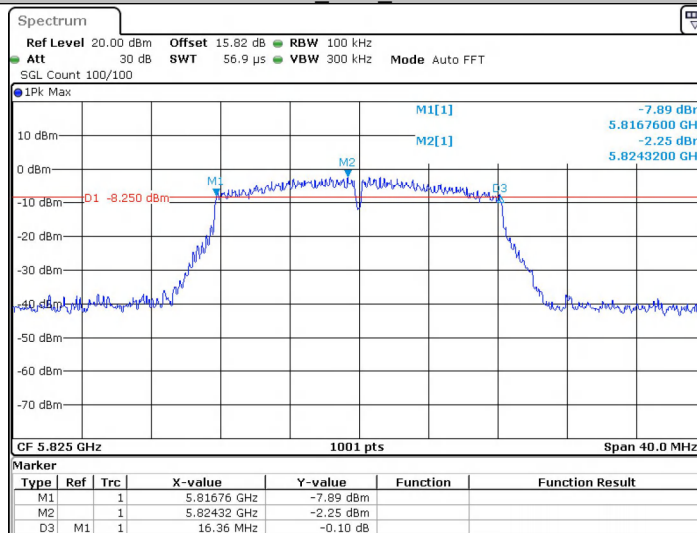
Date: 23 APR 2026 20:31:24

11A_Ant1_5785



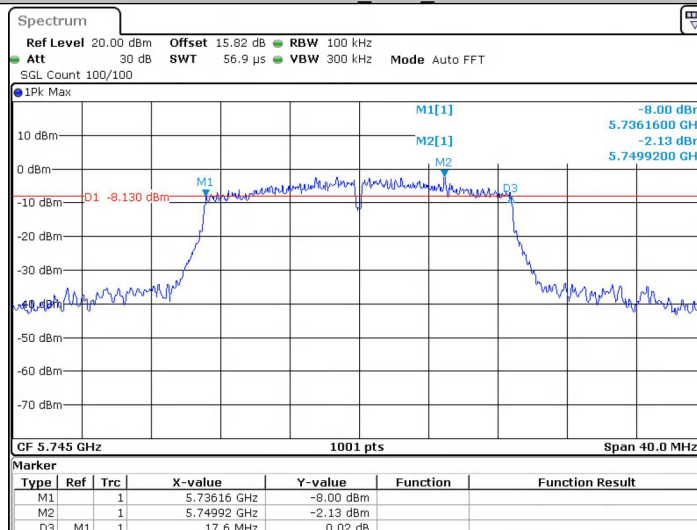
Date: 23 APR 2026 20:33:19

11A_Ant1_5825



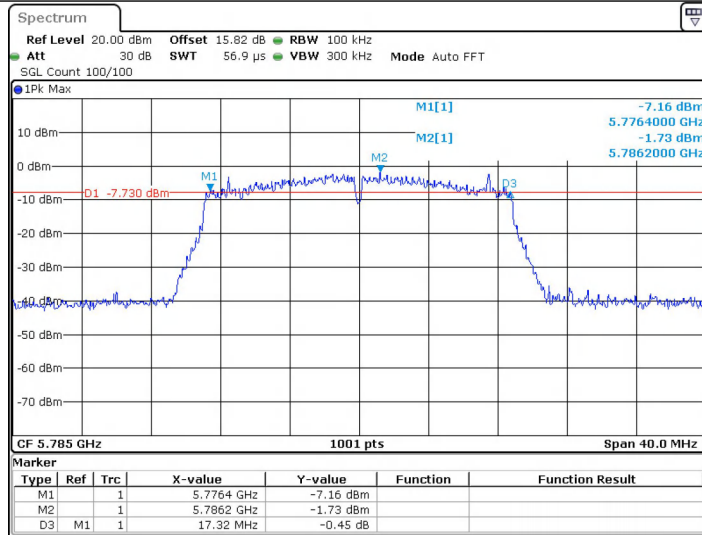
Date: 23 APR 2026 20:34:30

11N20SISO_Ant1_5745



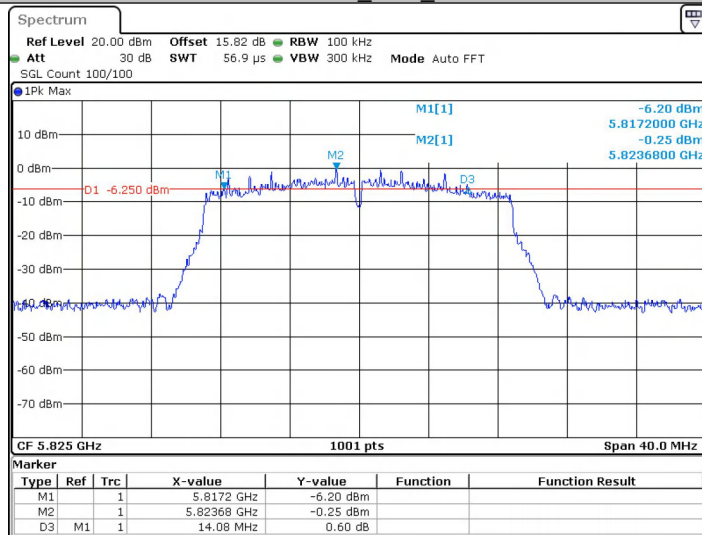
Date: 23 APR 2026 20:48:18

11N20SISO_Ant1_5785



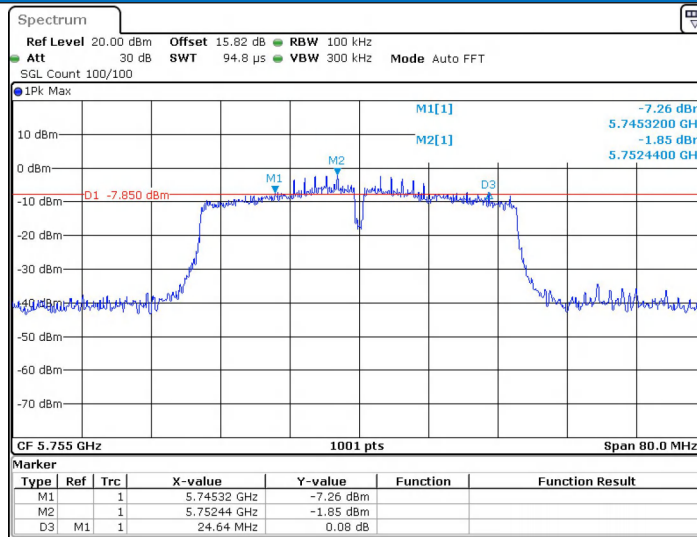
Date: 23 APR 2026 20:49:50

11N20SISO_Ant1_5825

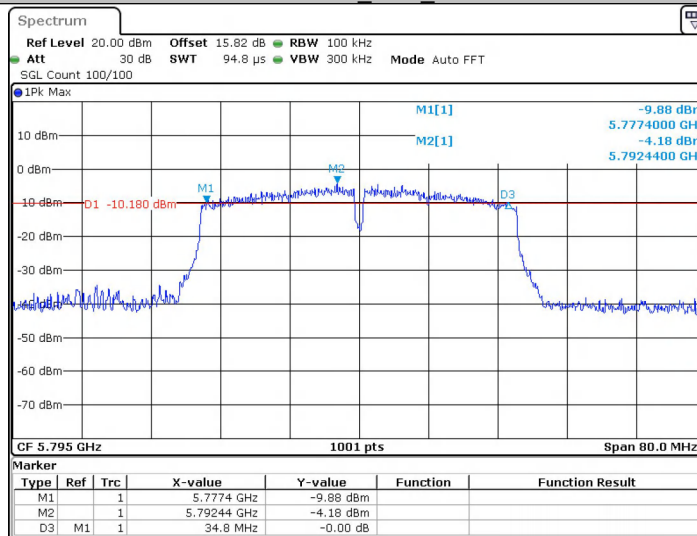


Date: 23 APR 2026 20:50:59

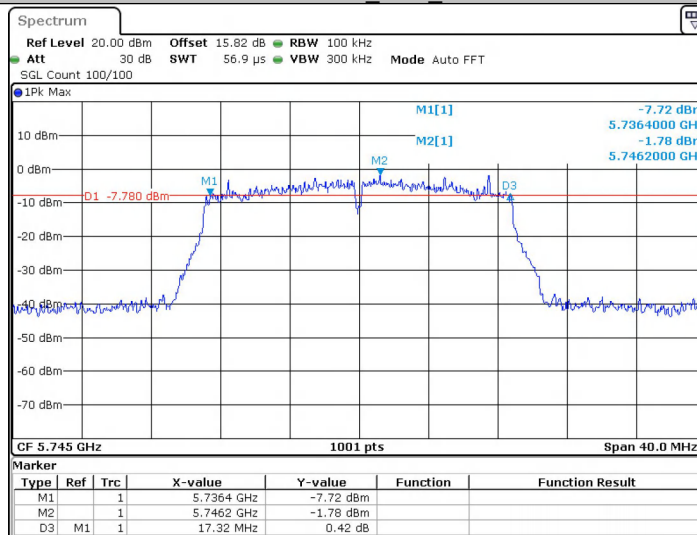
11N40SISO_Ant1_5755



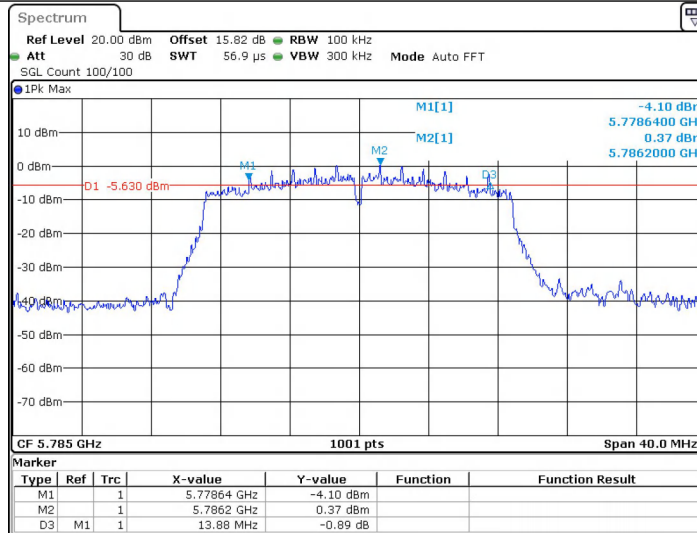
11N40SISO_Ant1_5795



11AC20SISO_Ant1_5745

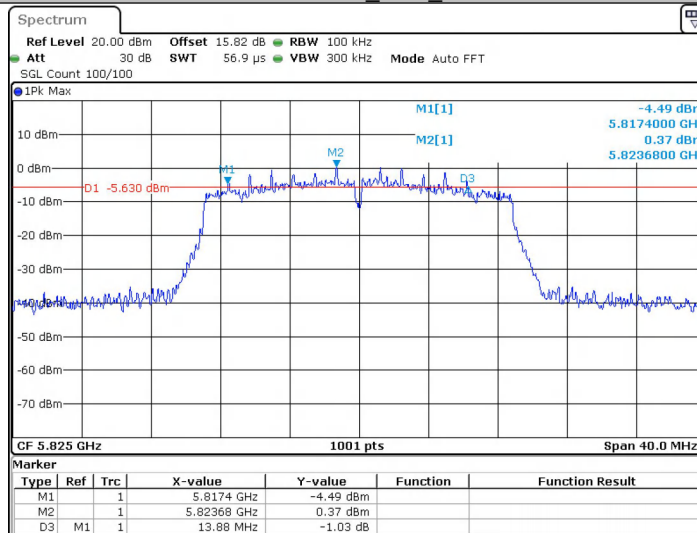


11AC20SISO_Ant1_5785



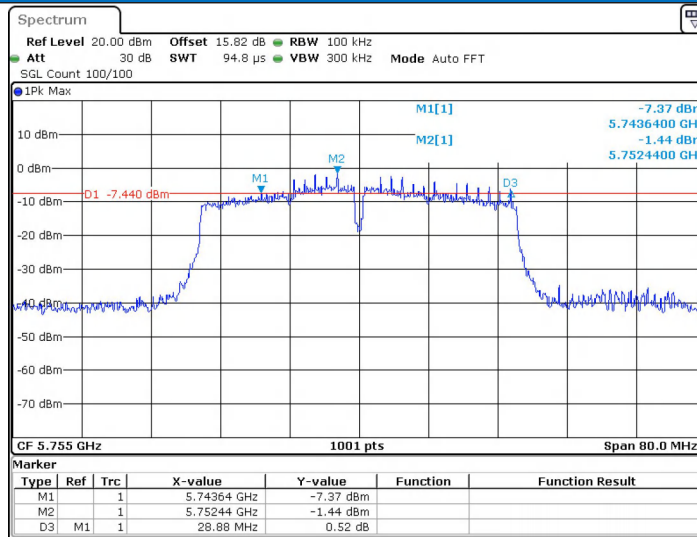
Date: 23.APR.2026 21:36:53

11AC20SISO_Ant1_5825



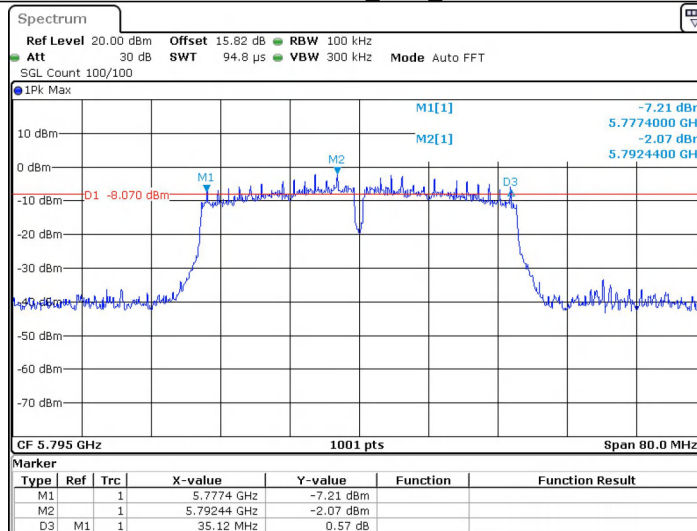
Date: 23.APR.2026 21:38:50

11AC40SISO_Ant1_5755



Date: 23 APR 2026 21:55:30

11AC40SISO_Ant1_5795



Date: 23 APR 2026 21:57:10

Appendix B): Maximum Conduct Output Power

1.Duty Cycle (x)

Test Requirement KDB 789033 D02 II B 1

Test Method: KDB 789033 II B 1

Test Procedure:

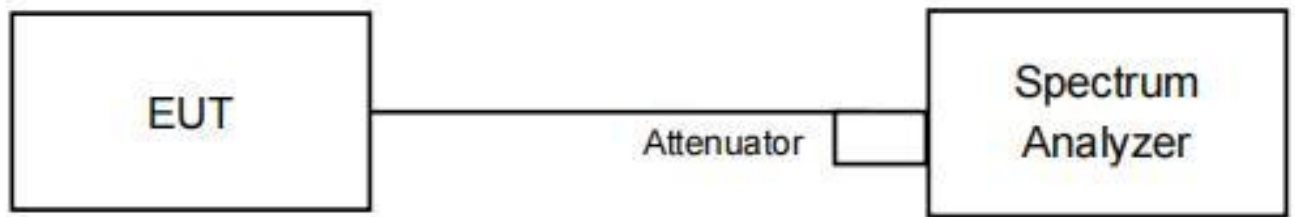
Set RBW = 20MHz

Set VBW = 40MHz

Set detector = peak.

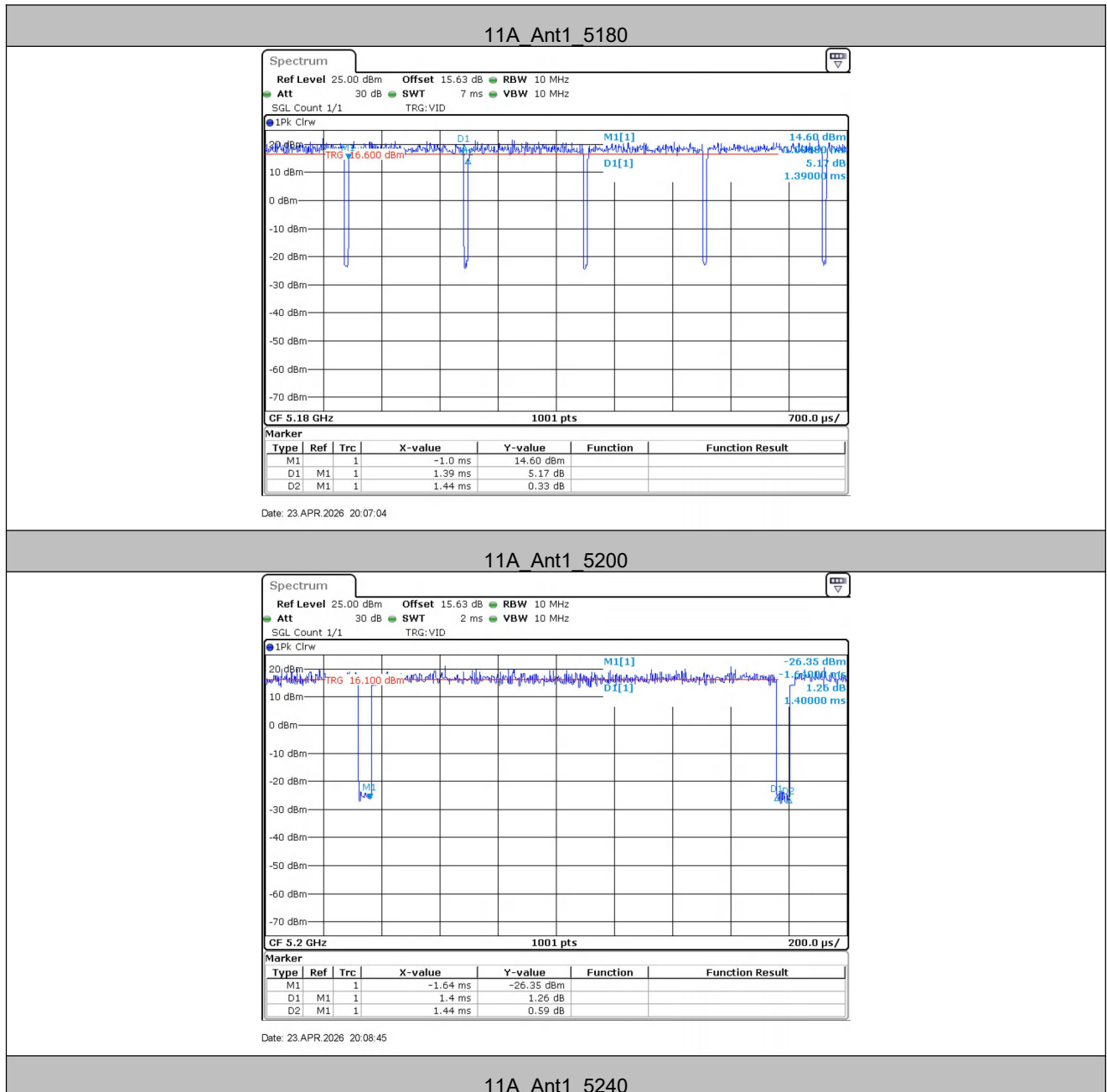
Set span =0Hz

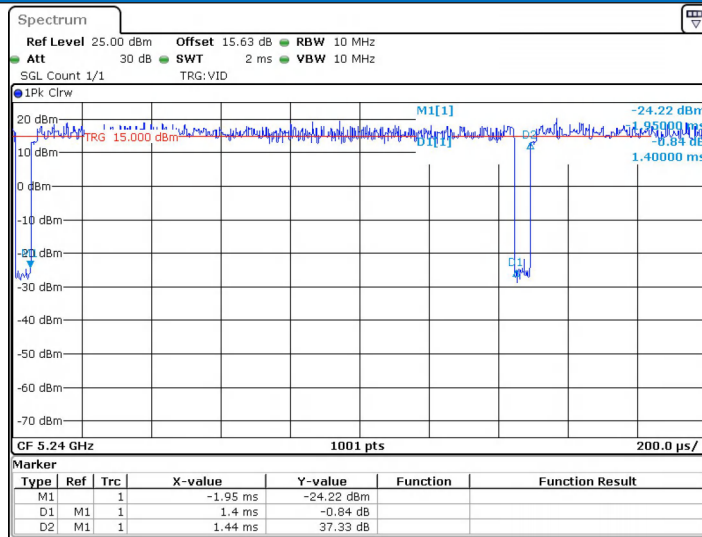
Test Setup Diagram



Measurement Data

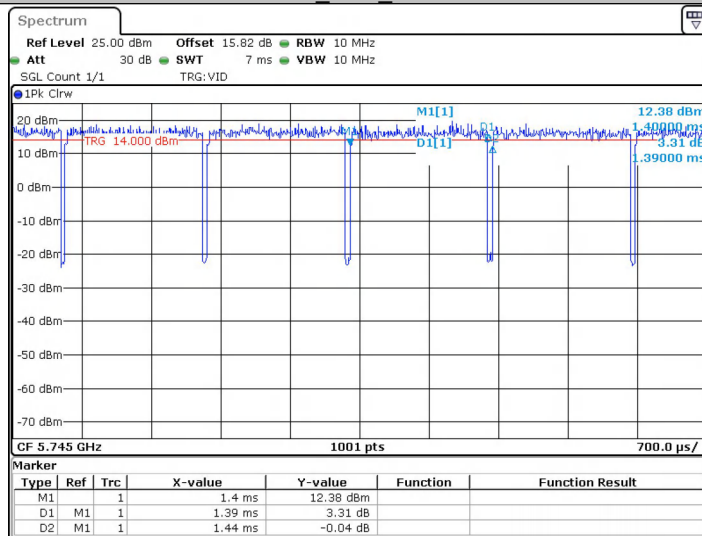
TestMode	Freq(MHz)	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]
11A	5180	1.39	1.44	96.53
	5200	1.40	1.44	97.22
	5240	1.40	1.44	97.22
	5745	1.39	1.44	96.53
	5785	1.39	1.44	96.53
	5825	1.39	1.44	96.53
11N20SISO	5180	1.30	1.35	96.30
	5200	1.30	1.34	97.01
	5240	1.30	1.34	97.01
	5745	1.30	1.34	97.01
	5785	1.30	1.35	96.30
	5825	1.30	1.34	97.01
11N40SISO	5190	0.65	0.69	94.20
	5230	0.65	0.69	94.20
	5755	0.65	0.69	94.20
	5795	0.65	0.69	94.20
11AC20SISO	5180	1.30	1.35	96.30
	5200	1.30	1.35	96.30
	5240	1.31	1.35	97.04
	5745	1.30	1.35	96.30
	5785	1.30	1.34	97.01
	5825	1.30	1.34	97.01
11AC40SISO	5190	0.65	0.69	94.20
	5230	0.64	0.69	92.75
	5755	0.65	0.69	94.20
	5795	0.65	0.69	94.20





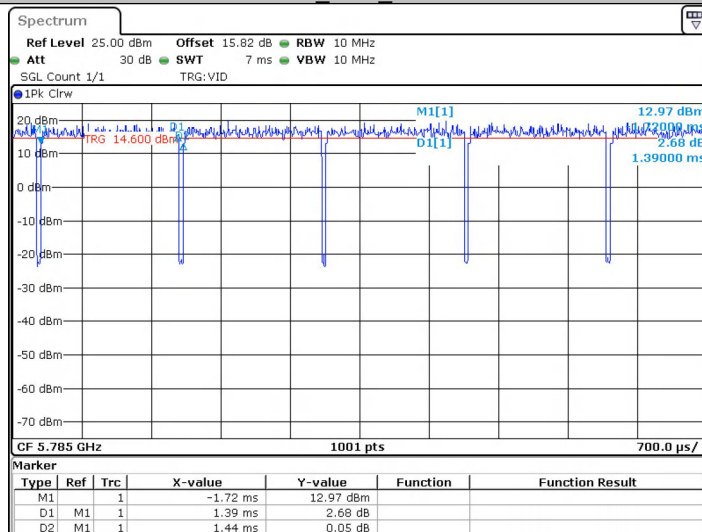
Date: 23.APR.2026 20:10:32

11A_Ant1_5745



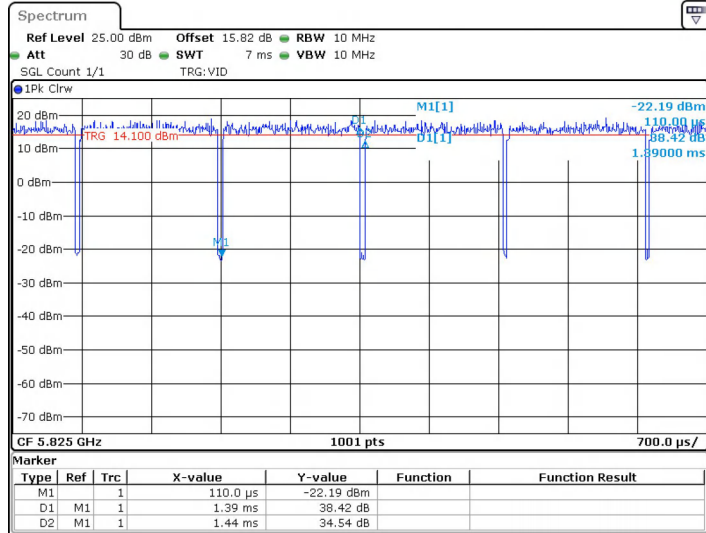
Date: 23.APR.2026 20:31:45

11A_Ant1_5785



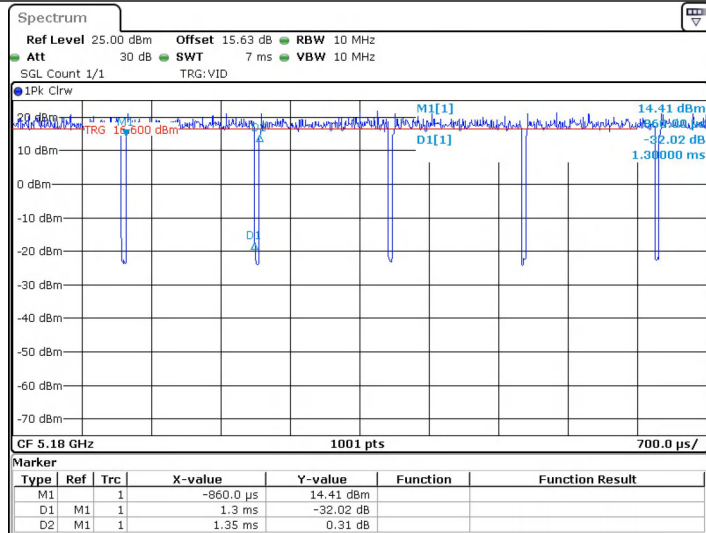
Date: 23.APR.2026 20:33:40

11A_Ant1_5825



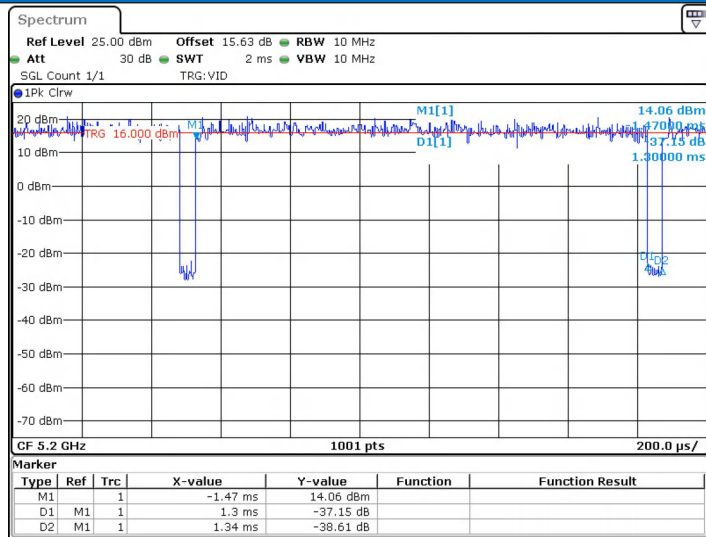
Date: 23.APR.2026 20:34:50

11N20SISO_Ant1_5180



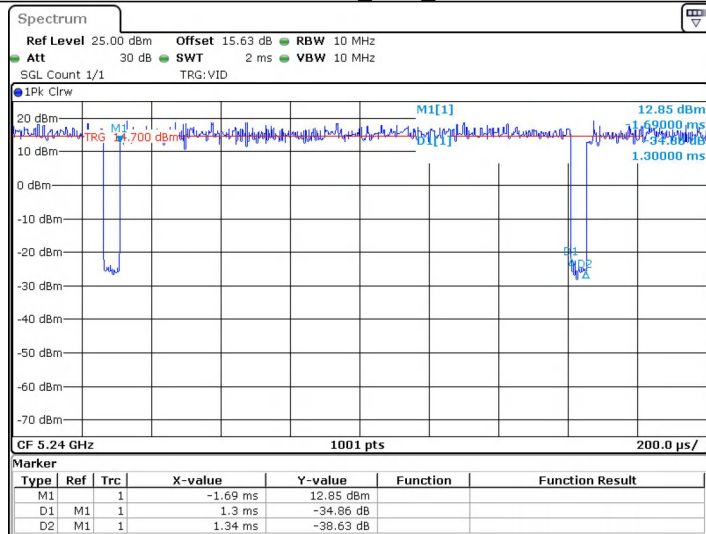
Date: 23.APR.2026 20:36:25

11N20SISO_Ant1_5200



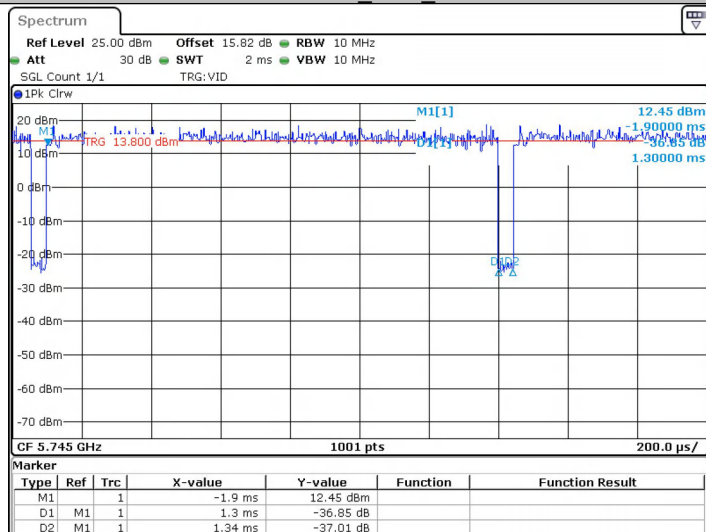
Date: 23.APR.2026 20:38:04

11N20SISO_Ant1_5240



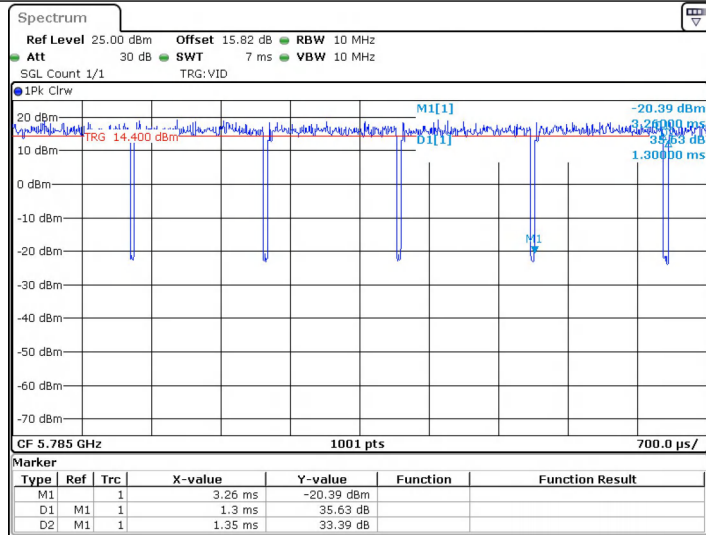
Date: 23.APR.2026 20:39:18

11N20SISO_Ant1_5745



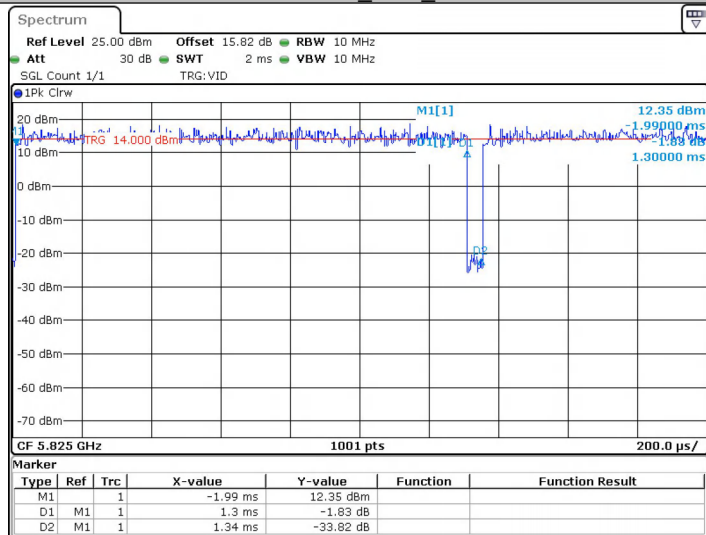
Date: 23.APR.2026 20:48:36

11N20SISO_Ant1_5785



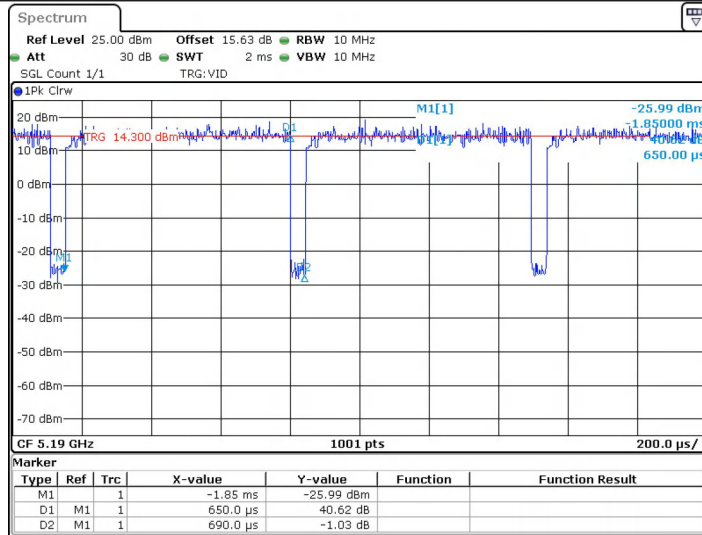
Date: 23.APR.2026 20:50:12

11N20SISO_Ant1_5825



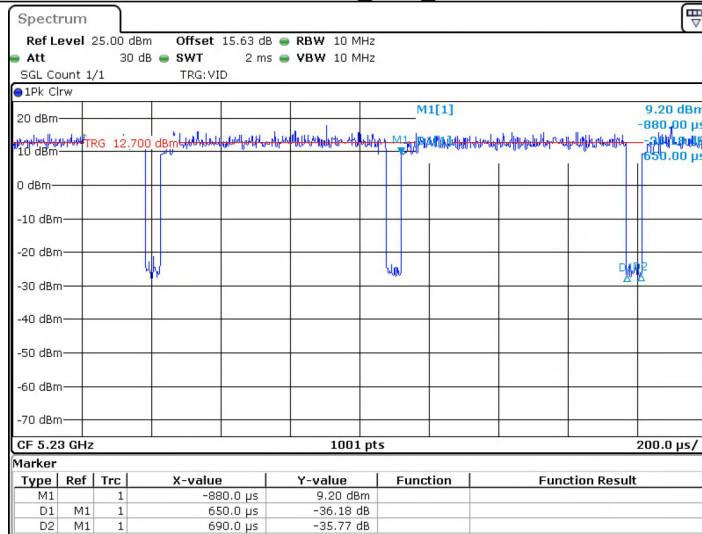
Date: 23.APR.2026 20:51:18

11N40SISO_Ant1_5190



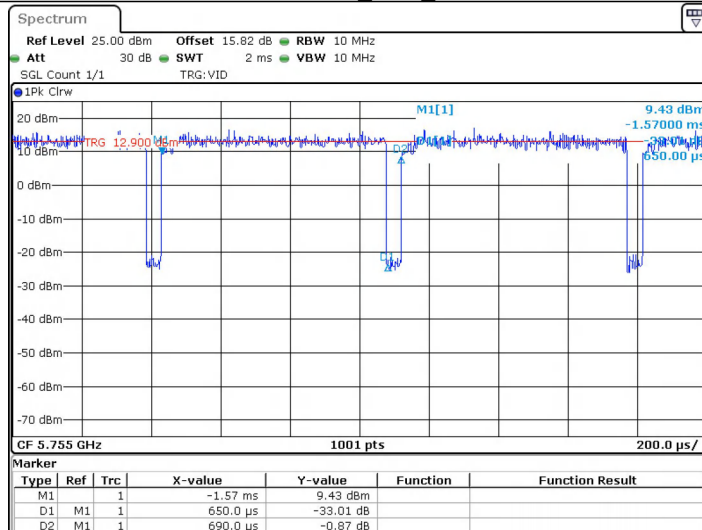
Date: 23.APR.2026 20:53:28

11N40SISO_Ant1_5230



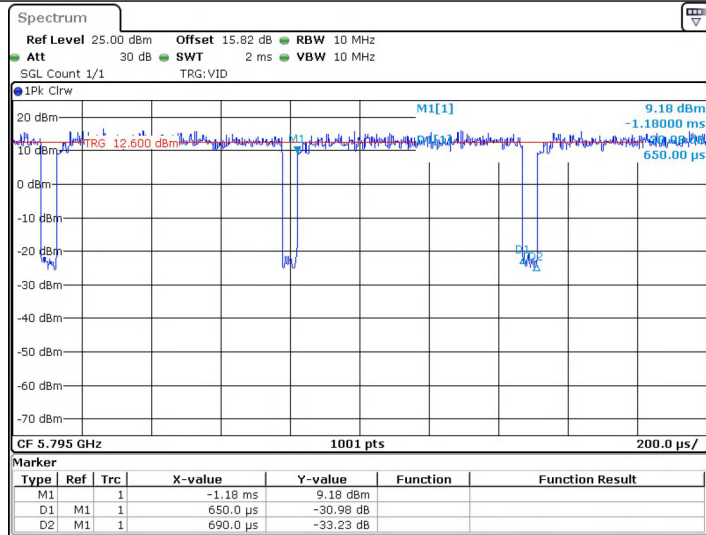
Date: 23.APR.2026 20:56:02

11N40SISO_Ant1_5755



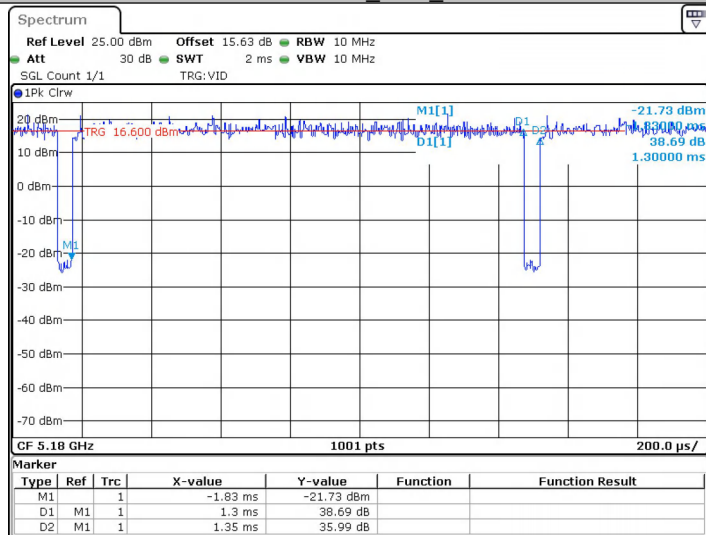
Date: 23.APR.2026 21:08:02

11N40SISO_Ant1_5795



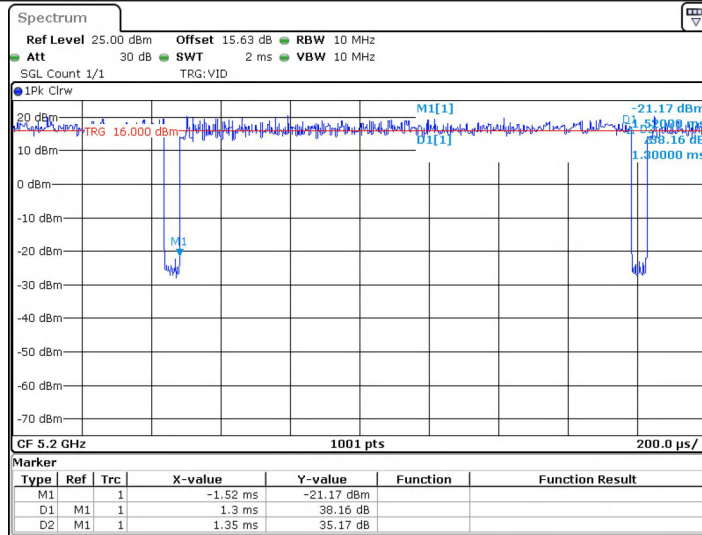
Date: 23.APR.2026 21:11:07

11AC20SISO_Ant1_5180



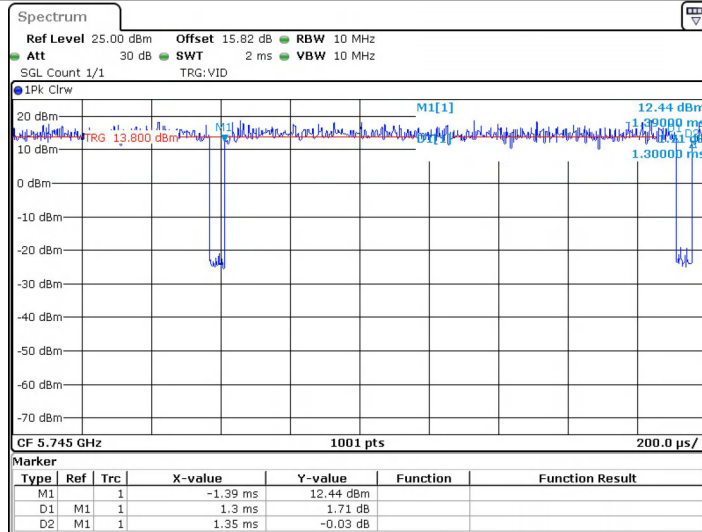
Date: 23.APR.2026 21:14:16

11AC20SISO_Ant1_5200



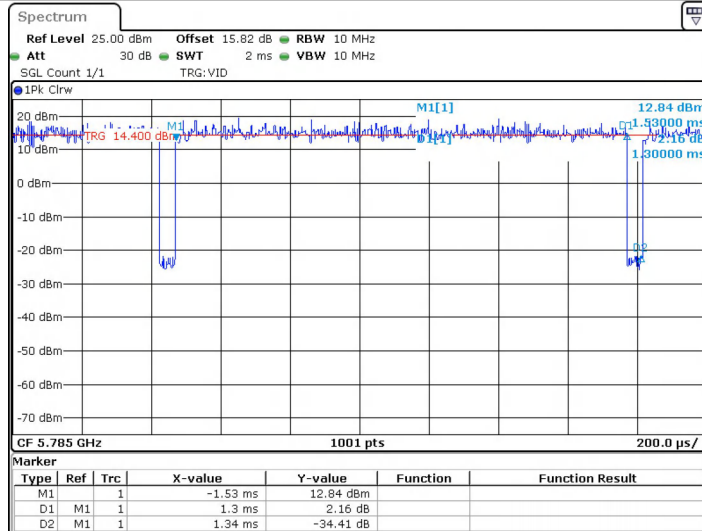
Date: 23.APR.2026 21:18:22

11AC20SISO_Ant1_5745



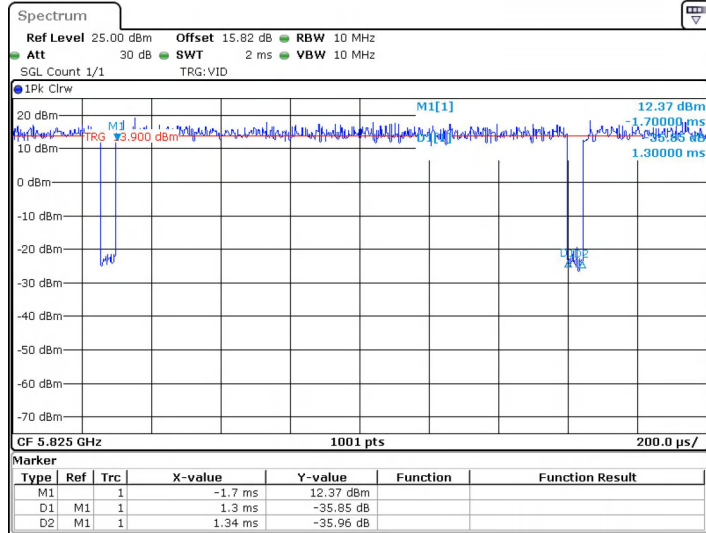
Date: 23.APR.2026 21:35:40

11AC20SISO_Ant1_5785



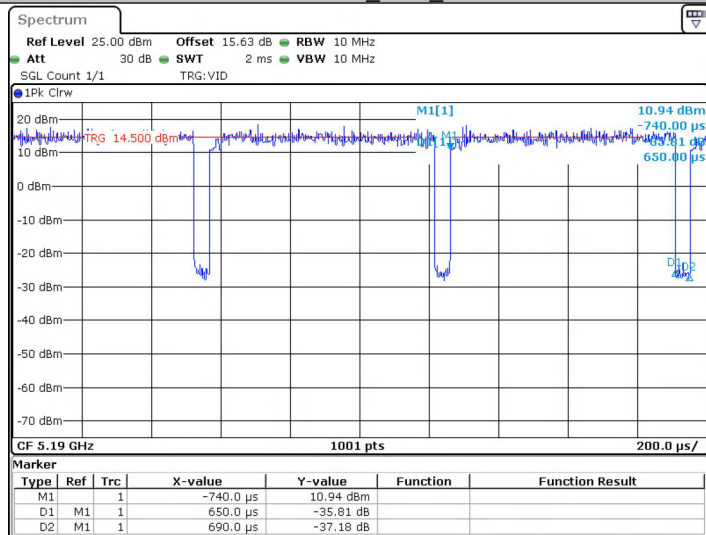
Date: 23.APR.2026 21:37:12

11AC20SISO_Ant1_5825



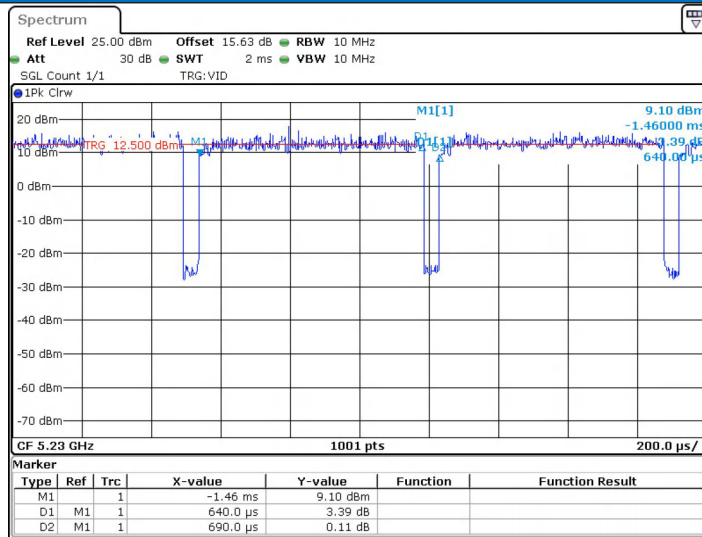
Date: 23.APR.2026 21:39:09

11AC40SISO_Ant1_5190



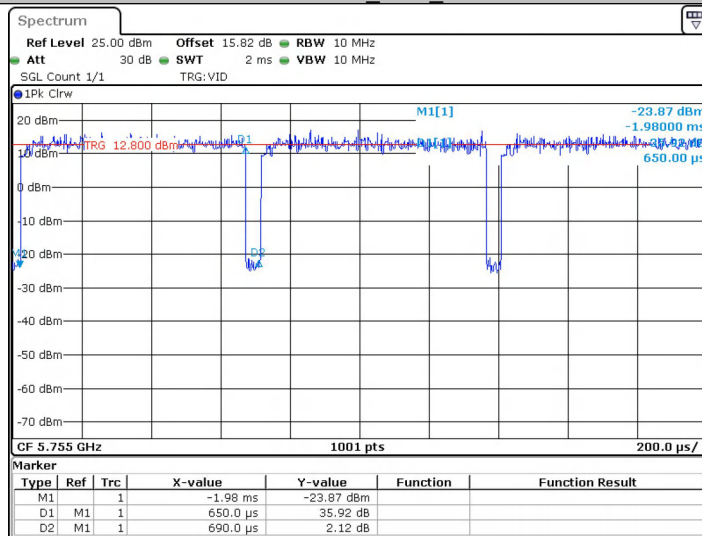
Date: 23.APR.2026 21:40:51

11AC40SISO_Ant1_5230



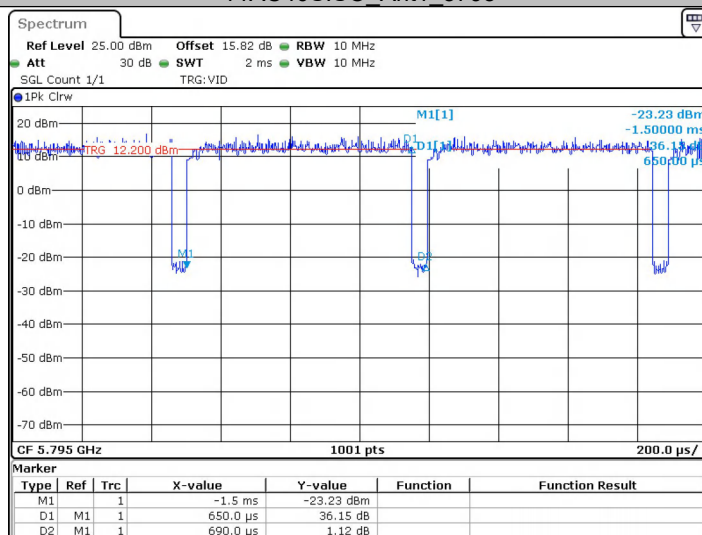
Date: 23.APR.2026 21:45:19

11AC40SISO_Ant1_5755



Date: 23.APR.2026 21:55:51

11AC40SISO_Ant1_5795



Date: 23.APR.2026 21:57:31

2. Maximum Conducted Output Power

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II E

Limit:

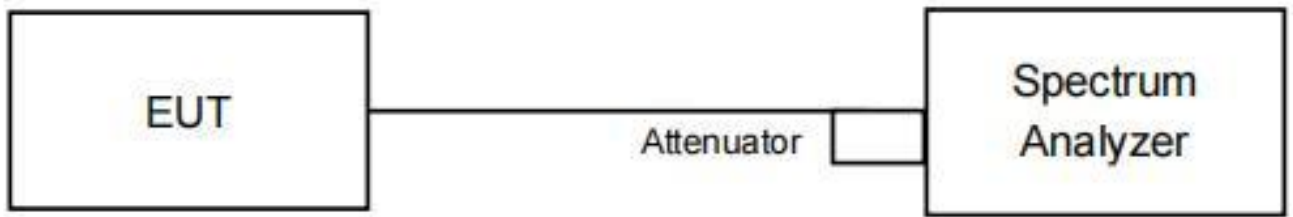
Frequency band(MHz)	Limit
5150-5250	$\leq 1\text{W}(30\text{dBm})$ for master device
	$\leq 250\text{mW}(24\text{dBm})$ for client device
5250-5350	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5470-5725	$\leq 250\text{mW}(24\text{dBm})$ for client device or $11\text{dBm}+10\log B^*$
5725-5850	$\leq 1\text{W}(30\text{dBm})$
Remark:	* Where B is the 26dB emission bandwidth in MHz. The maximum conducted output power must be measured over any interval of continuous transmission using instrumentation calibrated in terms of an rms-equivalent voltage.

Test Procedure:

Method SA-2 (trace averaging across on and off times of the EUT transmissions, followed by duty cycle correction).

- (1) Set RBW = 1 MHz.
- (2) Set VBW $\geq$ 3 MHz.
- (3) Detector = power average
- (4) Sweep time = auto.
- (5) Add duty cycle to the measured average power.

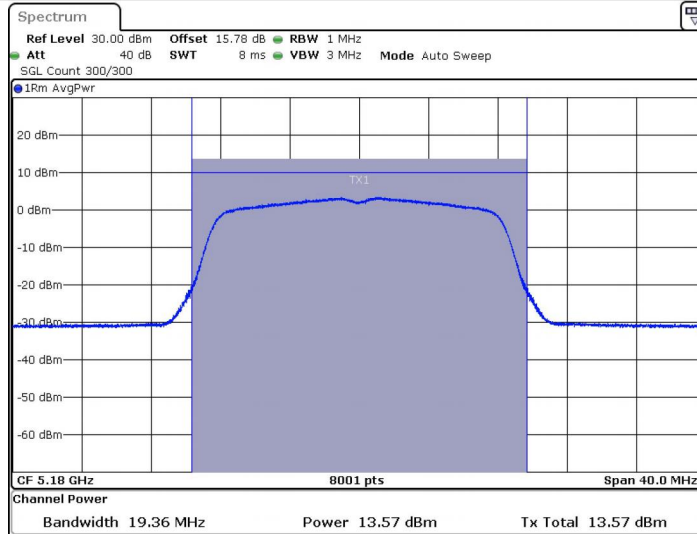
Test Setup Diagram



Measurement Data

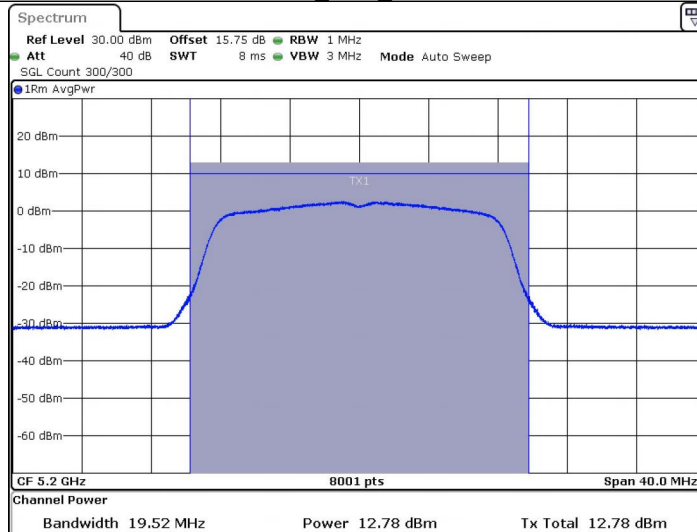
Test Mode	Antenna	Freq (MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant1	5180	13.42	96.53	0.15	13.57	≤23.98	PASS
		5200	12.66	97.22	0.12	12.78	≤23.98	PASS
		5240	11.75	97.22	0.12	11.87	≤23.98	PASS
		5745	11.20	96.53	0.15	11.35	≤30.00	PASS
		5785	11.40	96.53	0.15	11.55	≤30.00	PASS
		5825	10.86	96.53	0.15	11.01	≤30.00	PASS
11N20SISO	Ant1	5180	13.10	96.30	0.16	13.26	≤23.98	PASS
		5200	12.63	97.01	0.13	12.76	≤23.98	PASS
		5240	11.41	97.01	0.13	11.54	≤23.98	PASS
		5745	11.07	97.01	0.13	11.20	≤30.00	PASS
		5785	11.20	96.30	0.16	11.36	≤30.00	PASS
		5825	10.72	97.01	0.13	10.85	≤30.00	PASS
11N40SISO	Ant1	5190	12.68	94.20	0.26	12.94	≤23.98	PASS
		5230	11.36	94.20	0.26	11.62	≤23.98	PASS
		5755	11.05	94.20	0.26	11.31	≤30.00	PASS
		5795	11.08	94.20	0.26	11.34	≤30.00	PASS
11AC20SISO	Ant1	5180	12.92	96.30	0.16	13.08	≤23.98	PASS
		5200	12.55	96.30	0.16	12.71	≤23.98	PASS
		5240	11.47	97.04	0.13	11.60	≤23.98	PASS
		5745	10.98	96.30	0.16	11.14	≤30.00	PASS
		5785	11.09	97.01	0.13	11.22	≤30.00	PASS
		5825	10.70	97.01	0.13	10.83	≤30.00	PASS
11AC40SISO	Ant1	5190	12.67	94.20	0.26	12.93	≤23.98	PASS
		5230	11.28	92.75	0.33	11.61	≤23.98	PASS
		5755	10.98	94.20	0.26	11.24	≤30.00	PASS
		5795	11.04	94.20	0.26	11.30	≤30.00	PASS

11A_Ant1_5180



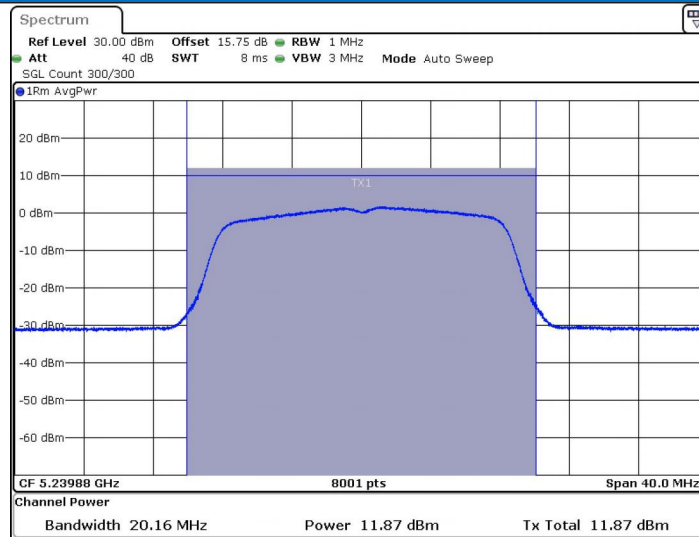
Date: 23 APR.2026 20:07:14

11A_Ant1_5200



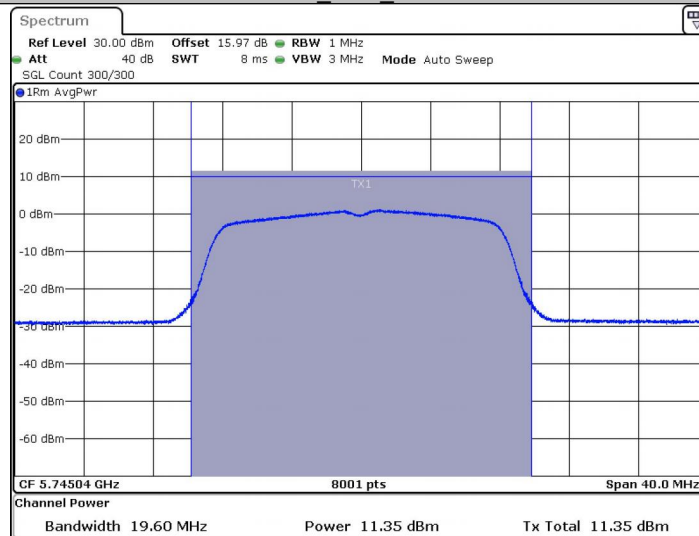
Date: 23 APR.2026 20:08:54

11A_Ant1_5240



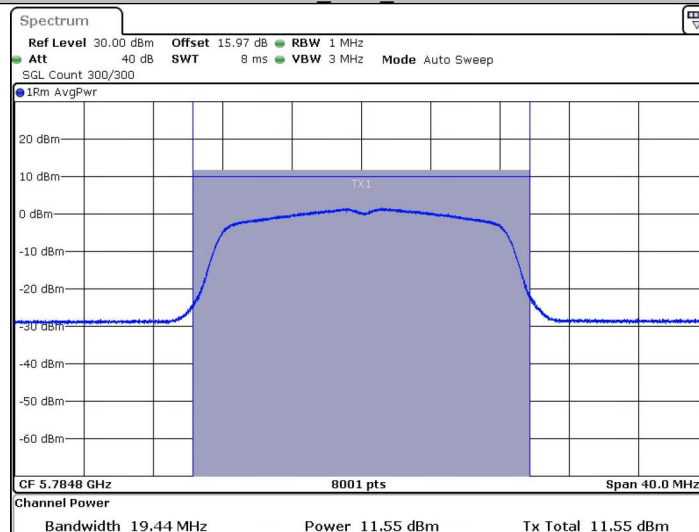
Date: 23 APR 2026 20:10:40

11A_Ant1_5745



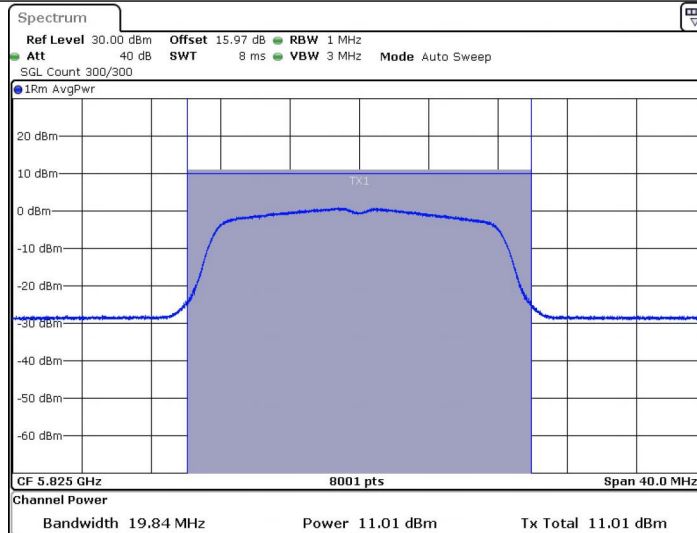
Date: 23 APR 2026 20:31:54

11A_Ant1_5785



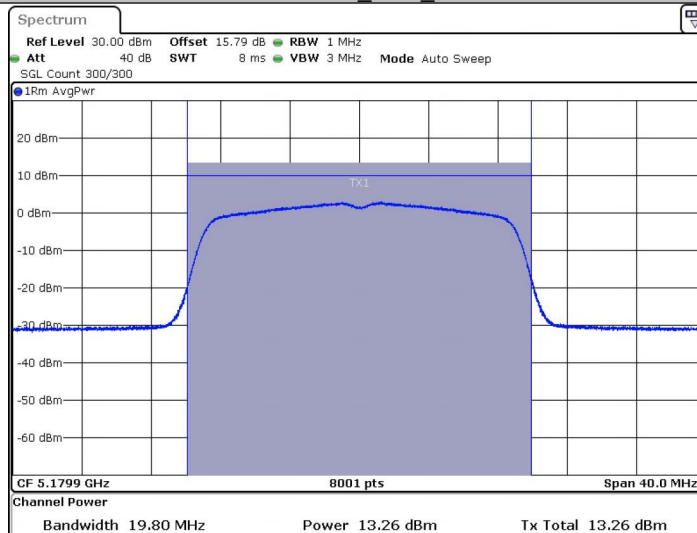
Date: 23 APR 2026 20:33:49

11A_Ant1_5825



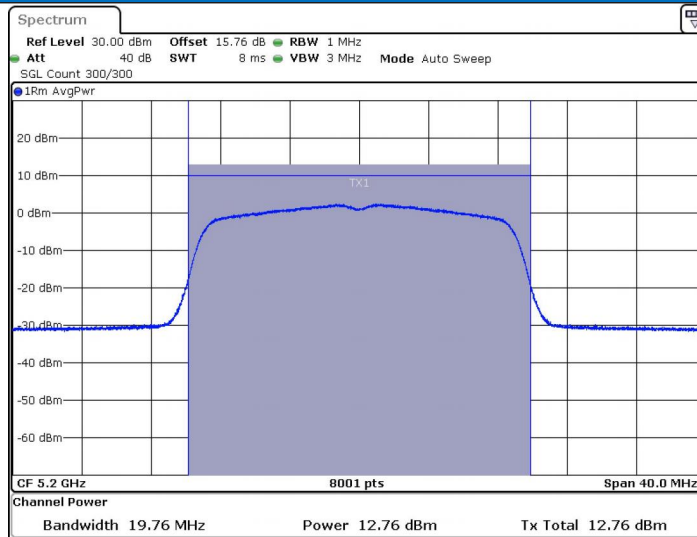
Date: 23 APR 2026 20:35:00

11N20SISO_Ant1_5180



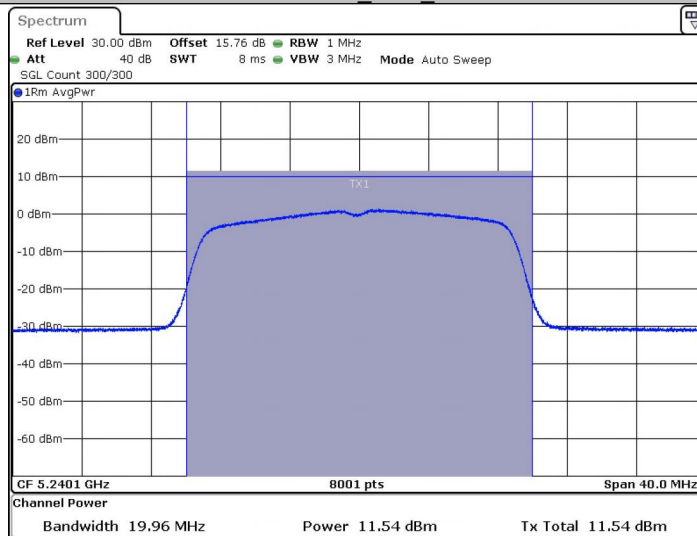
Date: 23 APR 2026 20:36:35

11N20SISO_Ant1_5200



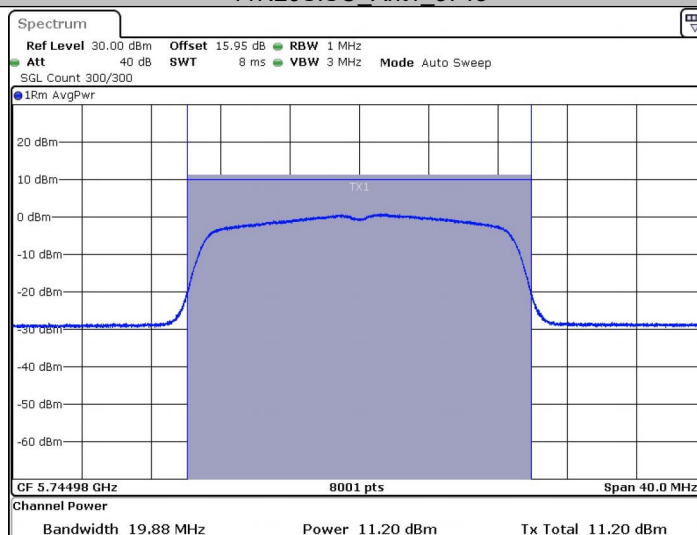
Date: 23 APR 2026 20:38:12

11N20SISO_Ant1_5240



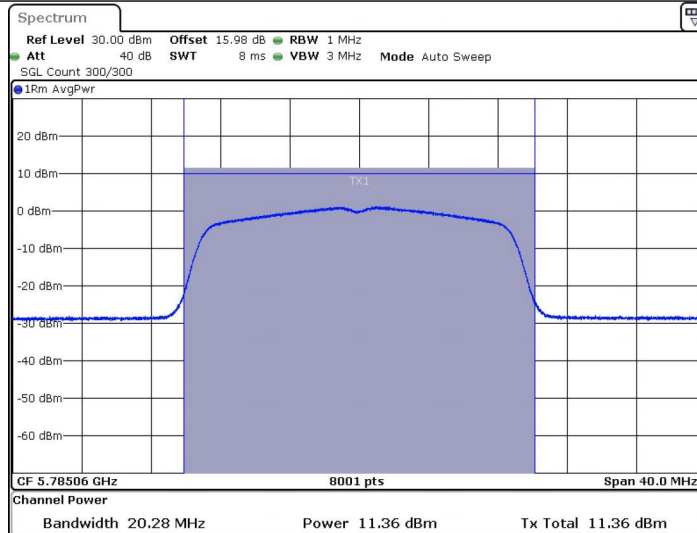
Date: 23 APR 2026 20:39:26

11N20SISO_Ant1_5745



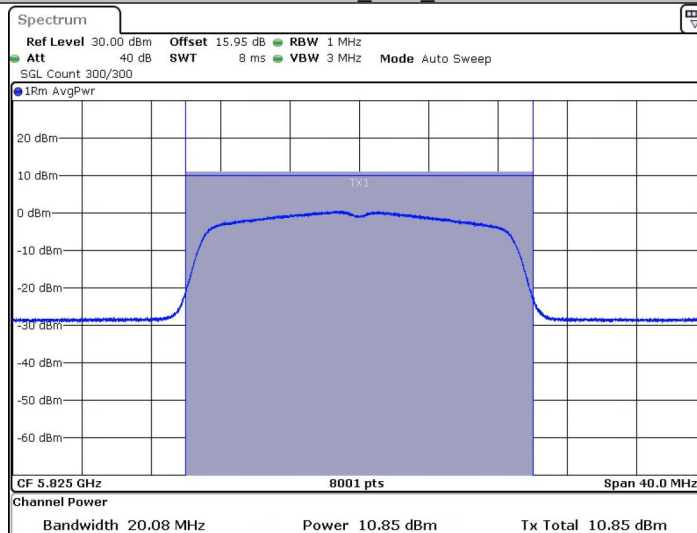
Date: 23 APR 2026 20:48:45

11N20SISO_Ant1_5785



Date: 23 APR 2026 20:50:21

11N20SISO_Ant1_5825



Date: 23 APR 2026 20:51:27

11N40SISO_Ant1_5190