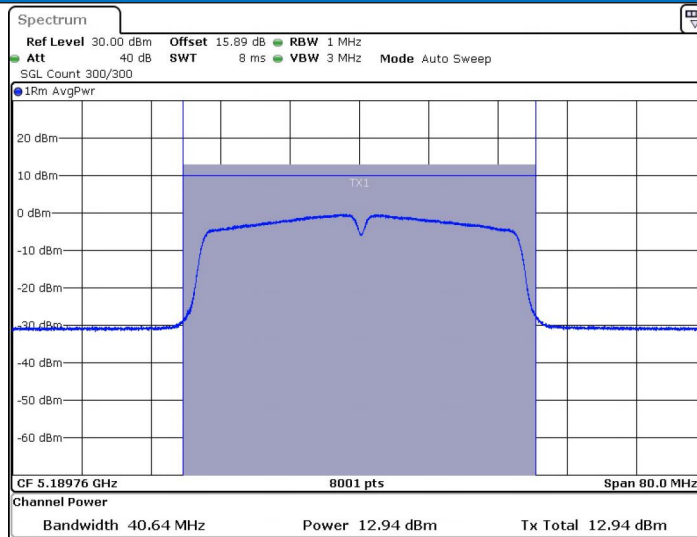
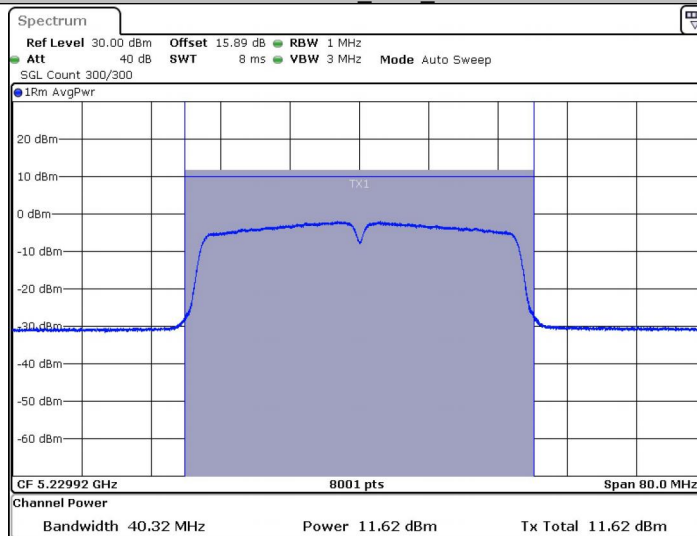




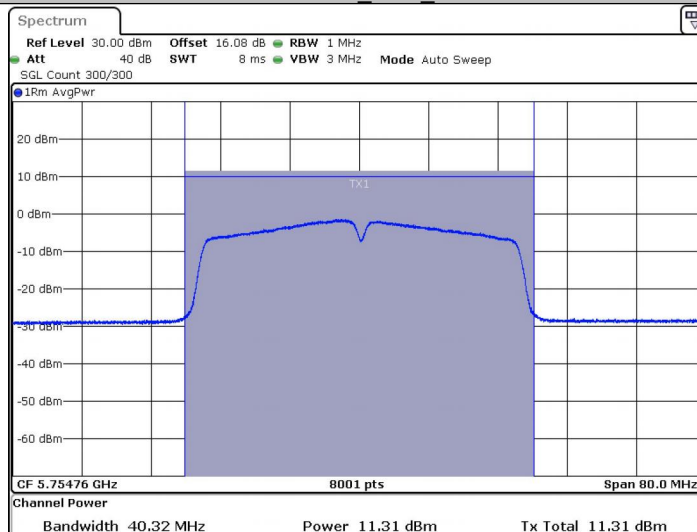
Date: 23 APR 2026 20:53:38

11N40SISO_Ant1_5230



Date: 23 APR 2026 20:56:11

11N40SISO_Ant1_5755



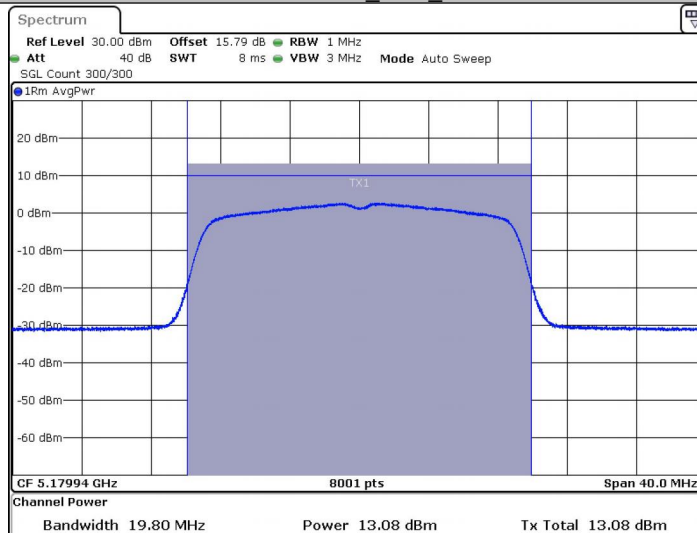
Date: 23 APR 2026 21:08:11

11N40SISO_Ant1_5795



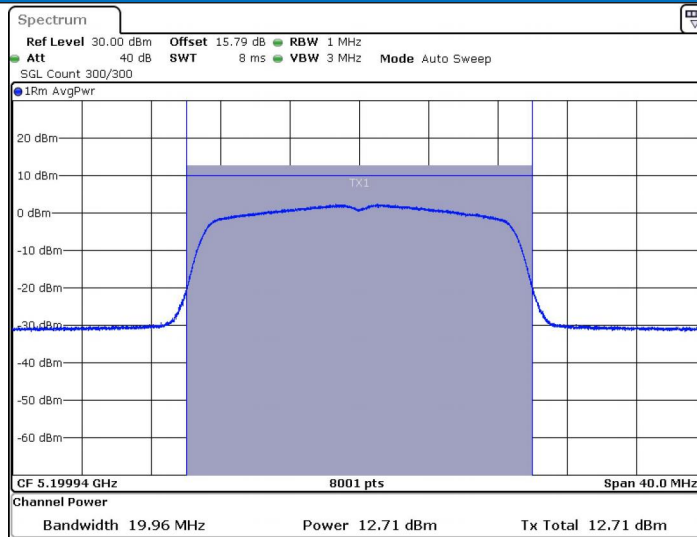
Date: 23.APR.2026 21:11:17

11AC20SISO_Ant1_5180



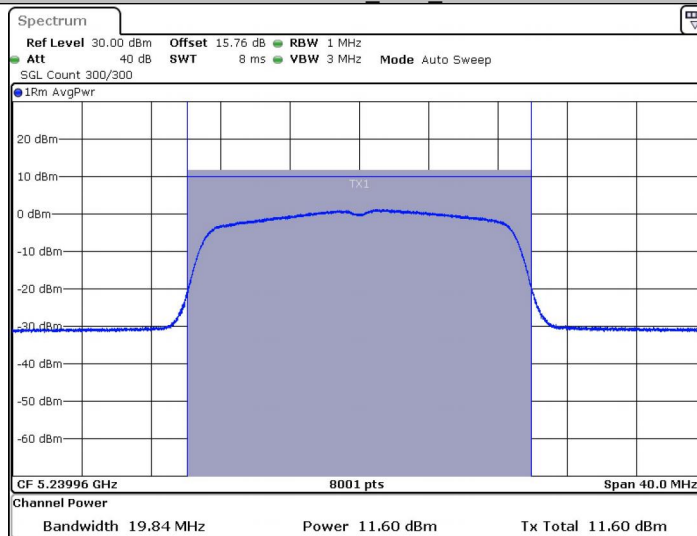
Date: 23.APR.2026 21:14:26

11AC20SISO_Ant1_5200



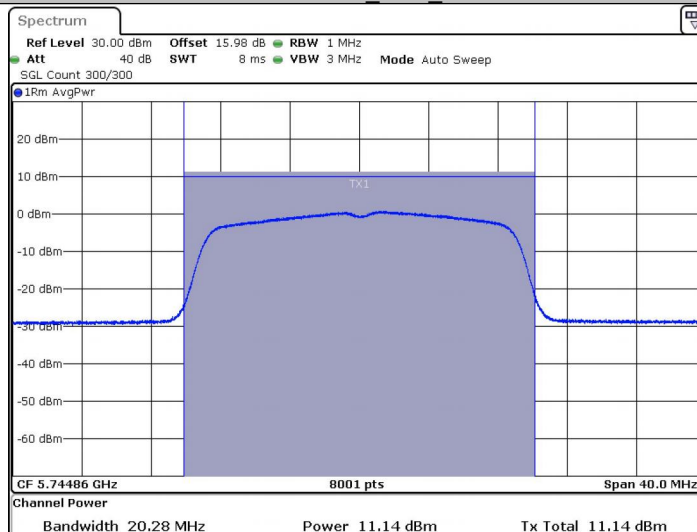
Date: 23 APR 2026 21:18:31

11AC20SISO_Ant1_5240



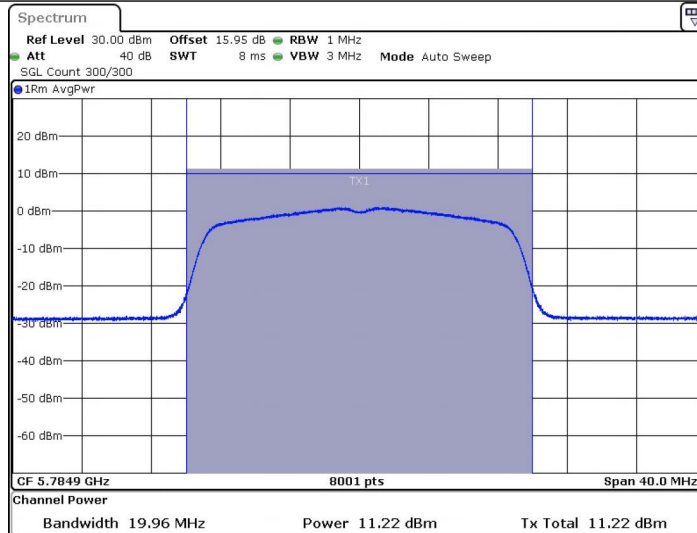
Date: 23 APR 2026 21:20:06

11AC20SISO_Ant1_5745



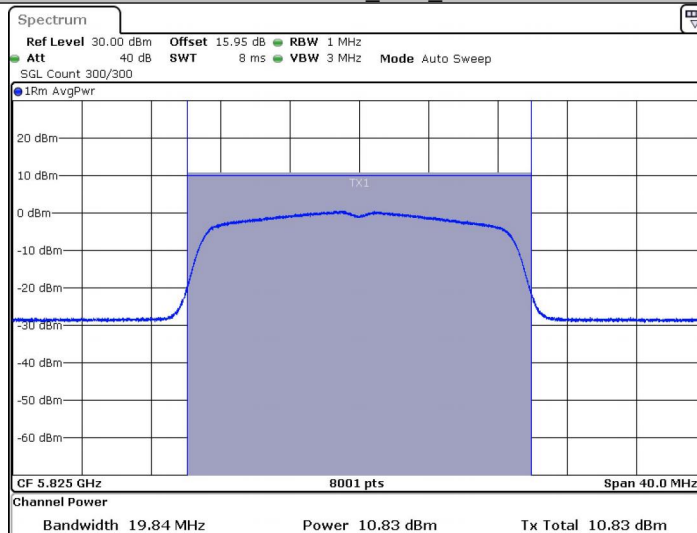
Date: 23 APR 2026 21:35:49

11AC20SISO_Ant1_5785



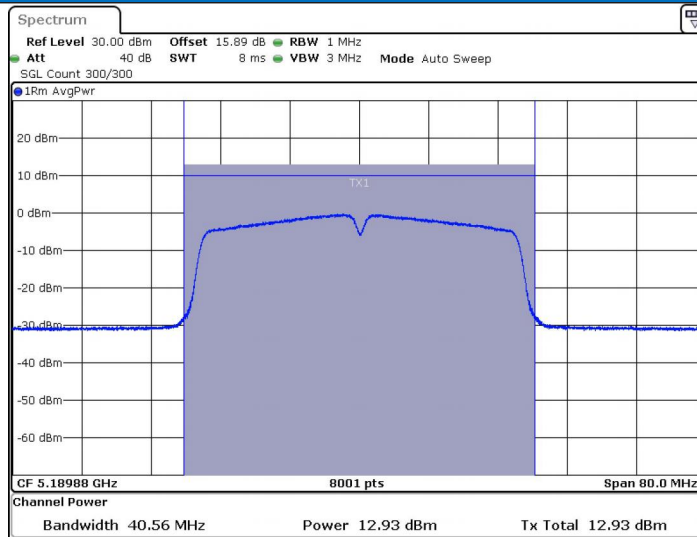
Date: 23 APR 2026 21:37:21

11AC20SISO_Ant1_5825



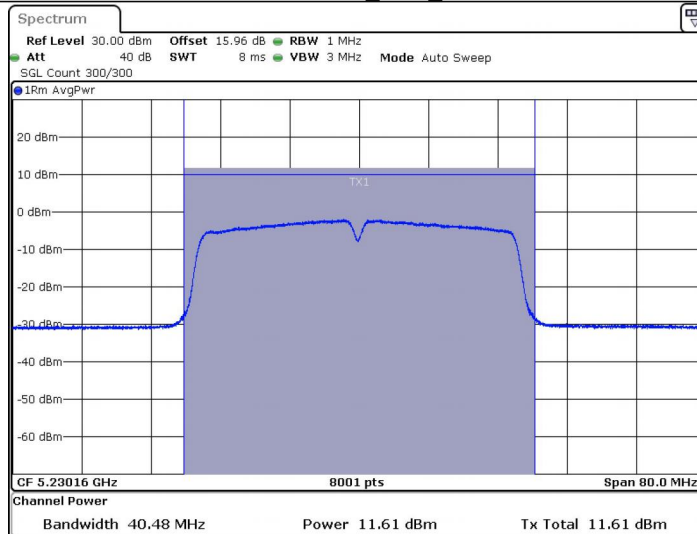
Date: 23 APR 2026 21:39:18

11AC40SISO_Ant1_5190



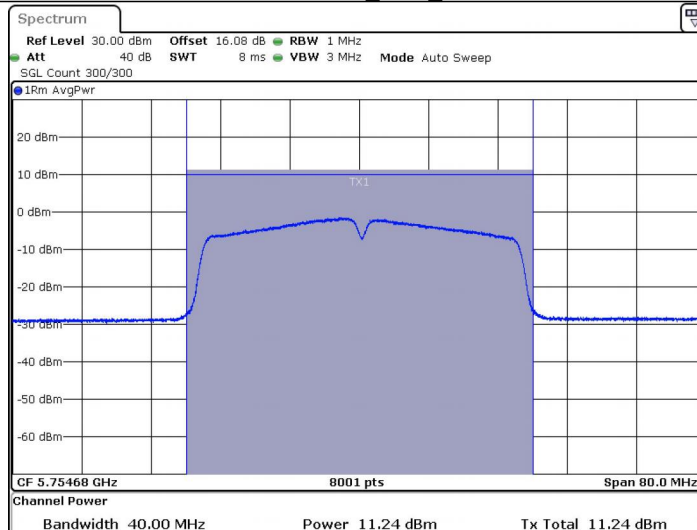
Date: 23 APR 2026 21:41:01

11AC40SISO_Ant1_5230

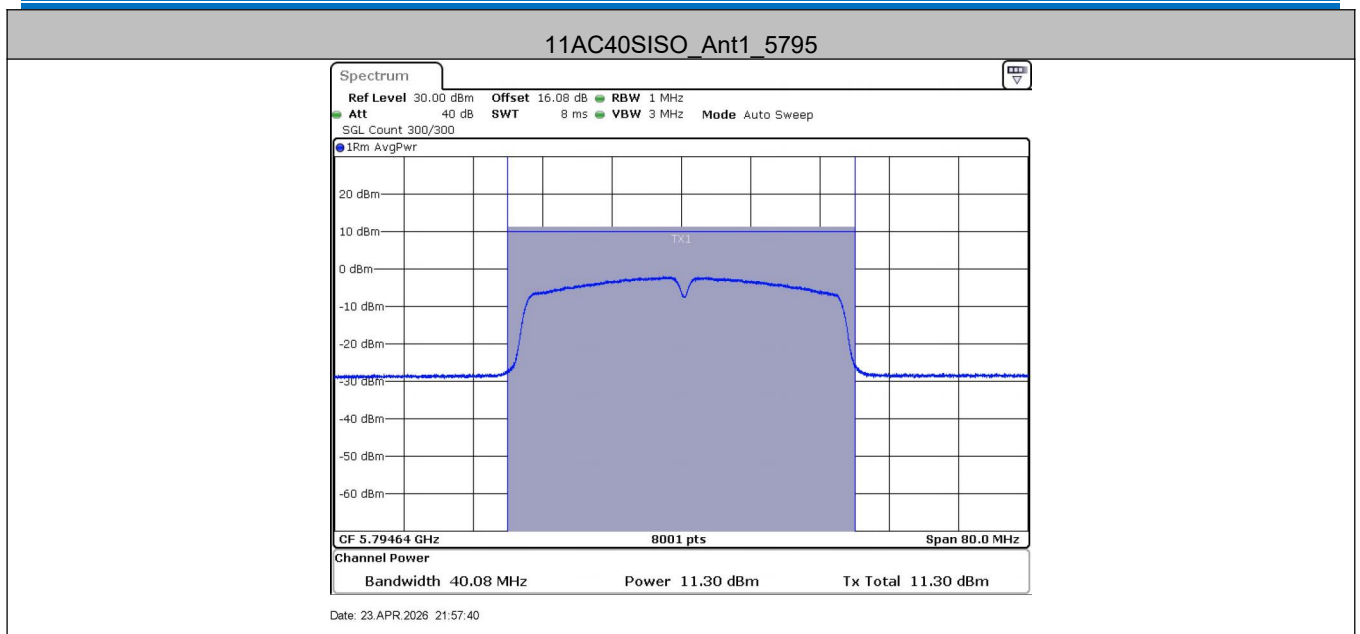


Date: 23 APR 2026 21:45:29

11AC40SISO_Ant1_5755



Date: 23 APR 2026 21:56:00



Remark:

$Av.Power = Meas.Level + 10 \log(1/duty\ cycle)$

$E.i.r.p = Av.Power + G$,

G = antenna gain in dBi.

Appendix C): Maximum Power Spectral Density

Test Requirement 47 CFR Part 15, Subpart C 15.407 (a)

Test Method: KDB 789033 D02 II F

Test Procedure:

For 5150-5725MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 1MHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

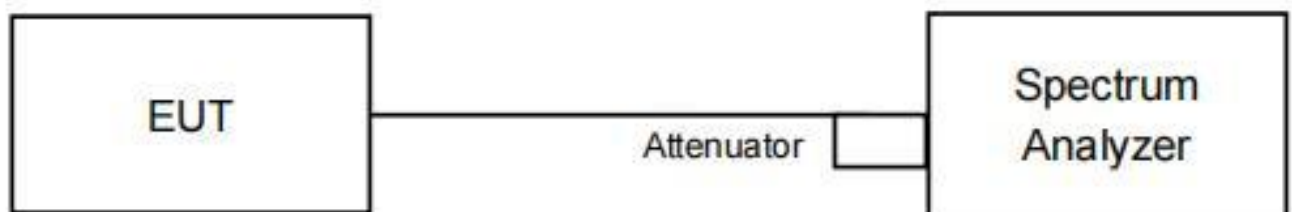
For 5725-5850MHz:

1. Connect EUT RF output port to the Spectrum Analyzer through an RF attenuator
2. Set the EUT Work on operation frequency individually.
3. Set RBW = 500KHz.
4. Set the VBW $\geq 3 \times$ RBW. Detector = Peak. Trace mode = max hold.

Limit:

Frequency band(MHz)	Limit
5150-5250	$\leq 17\text{dBm}$ in 1MHz for master device
	$\leq 11\text{dBm}$ in 1MHz for client device
5250-5350	$\leq 11\text{dBm}$ in 1MHz for client device
5470-5725	$\leq 11\text{dBm}$ in 1MHz for client device
5725-5850	$\leq 30\text{dBm}$ in 500 kHz
Remark:	The maximum power spectral density is measured as a conducted emission by direct connection of a calibrated test instrument to the equipment under test.

Test Setup Diagram



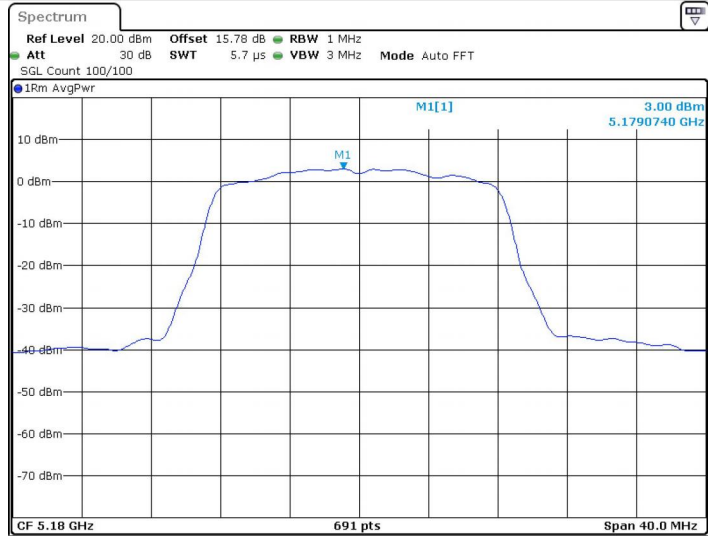
Result Table

Remark:

TestMode	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	5180	3.00	≤11.00	PASS
	5200	2.36	≤11.00	PASS
	5240	1.43	≤11.00	PASS
	5745	-2.05	≤30.00	PASS
	5785	-1.17	≤30.00	PASS
	5825	-2.01	≤30.00	PASS
11N20SISO	5180	2.74	≤11.00	PASS
	5200	2.44	≤11.00	PASS
	5240	1.28	≤11.00	PASS
	5745	-2.35	≤30.00	PASS
	5785	-1.75	≤30.00	PASS
	5825	-2.06	≤30.00	PASS
11N40SISO	5190	-0.45	≤11.00	PASS
	5230	-2.24	≤11.00	PASS
	5755	-4.13	≤30.00	PASS
	5795	-4.84	≤30.00	PASS
11AC20SISO	5180	2.62	≤11.00	PASS
	5200	1.68	≤11.00	PASS
	5240	0.69	≤11.00	PASS
	5745	-2.43	≤30.00	PASS
	5785	-1.83	≤30.00	PASS
	5825	-2.68	≤30.00	PASS
11AC40SISO	5190	-0.48	≤11.00	PASS
	5230	-2.20	≤11.00	PASS
	5755	-4.69	≤30.00	PASS
	5795	-4.79	≤30.00	PASS

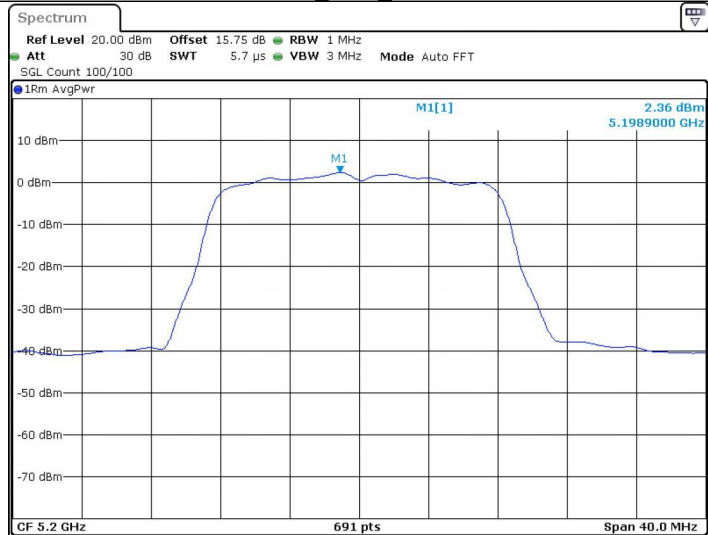
PSD = Meas PSD + Duty Cycle Factor

11A_Ant1_5180



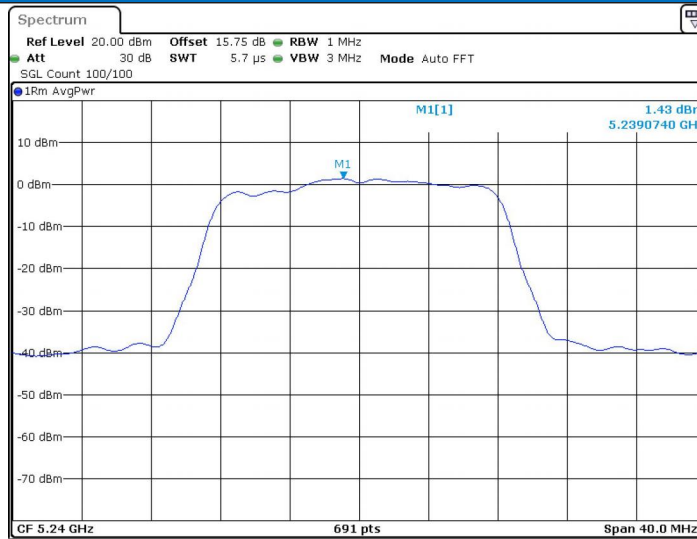
Date: 23 APR 2026 20:07:19

11A_Ant1_5200



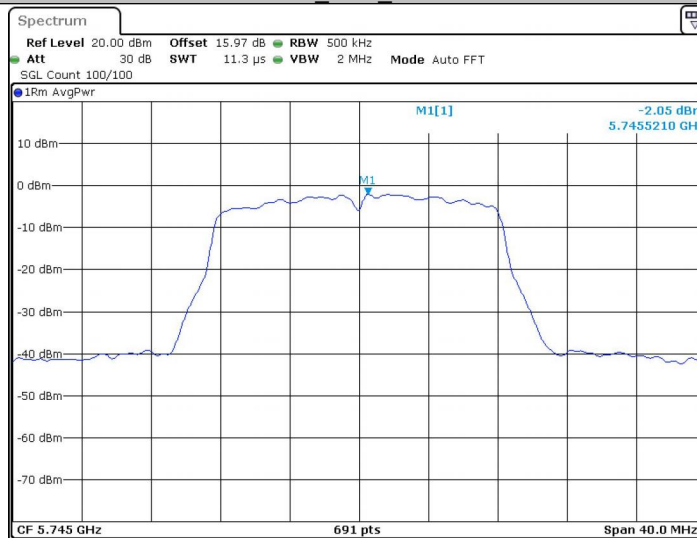
Date: 23 APR 2026 20:09:00

11A_Ant1_5240



Date: 23 APR 2026 20:10:46

11A Ant1 5745



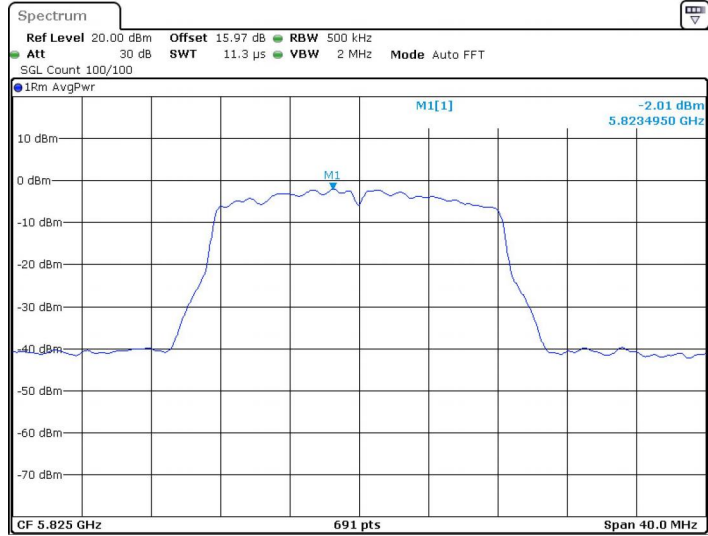
Date: 23 APR 2026 20:32:00

11A Ant1 5785



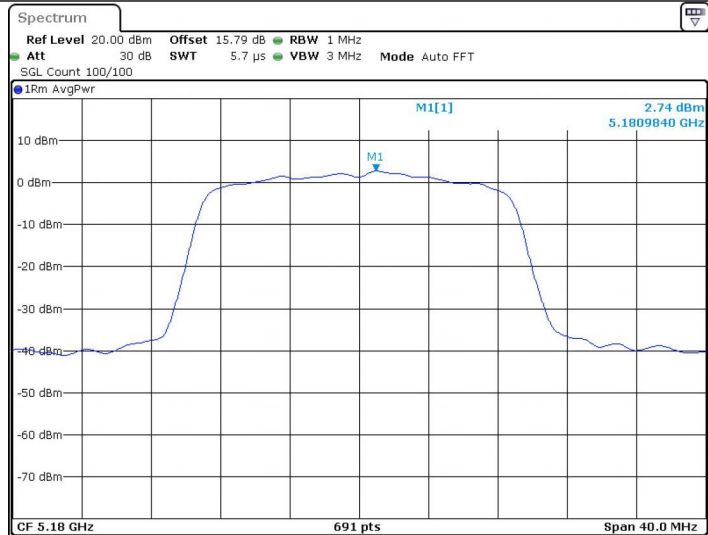
Date: 23 APR 2026 20:33:55

11A_Ant1_5825



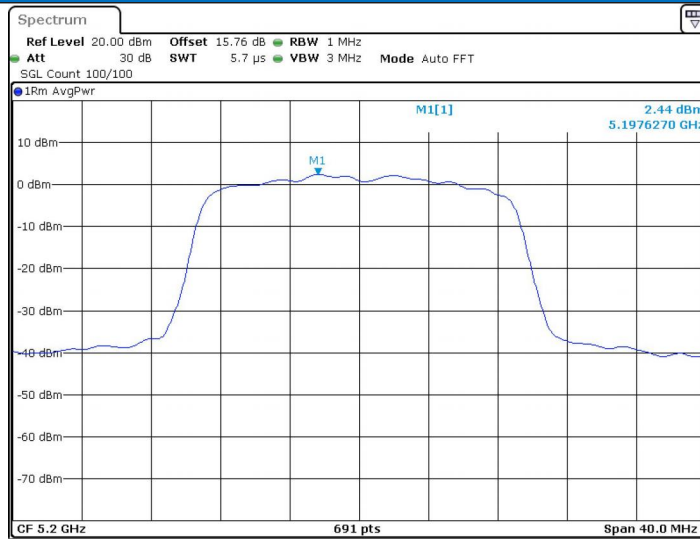
Date: 23.APR.2026 20:35:06

11N20SISO_Ant1_5180



Date: 23.APR.2026 20:36:40

11N20SISO_Ant1_5200



Date: 23 APR 2026 20:38:18

11N20SISO_Ant1_5240



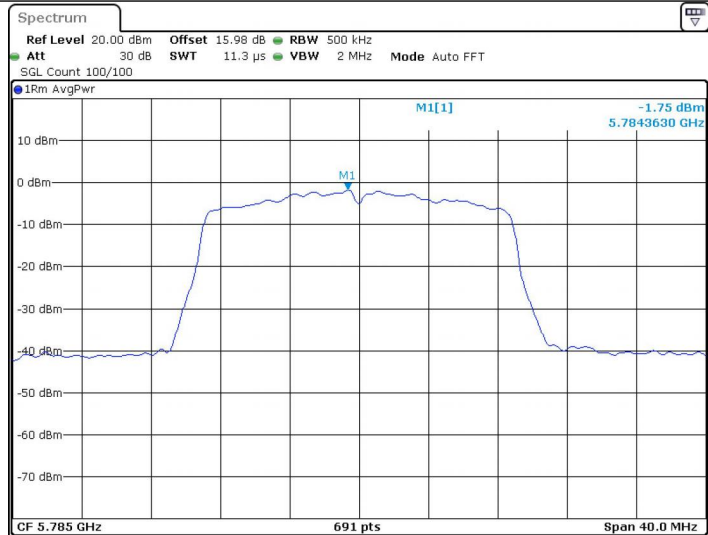
Date: 23 APR 2026 20:39:32

11N20SISO_Ant1_5745



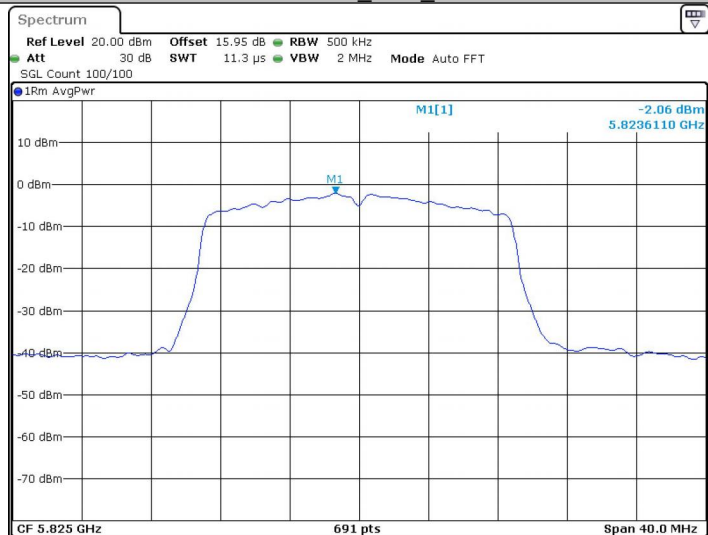
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11N20SISO_Ant1_5785



Date: 23 APR 2026 20:50:27

11N20SISO_Ant1_5825



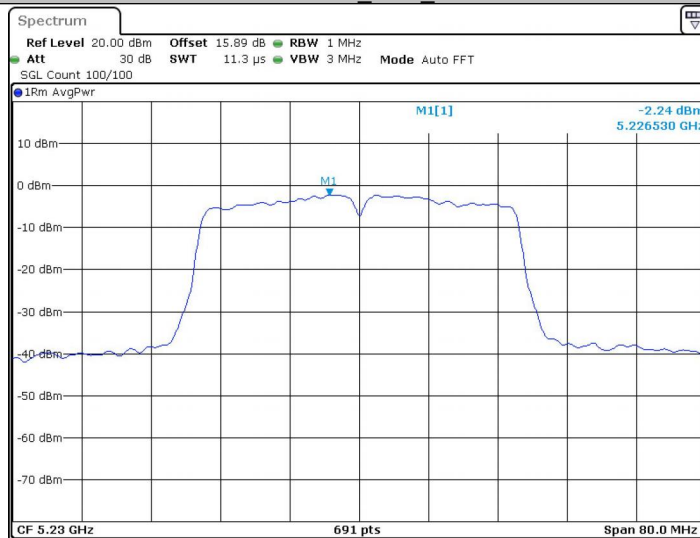
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11N40SISO_Ant1_5190



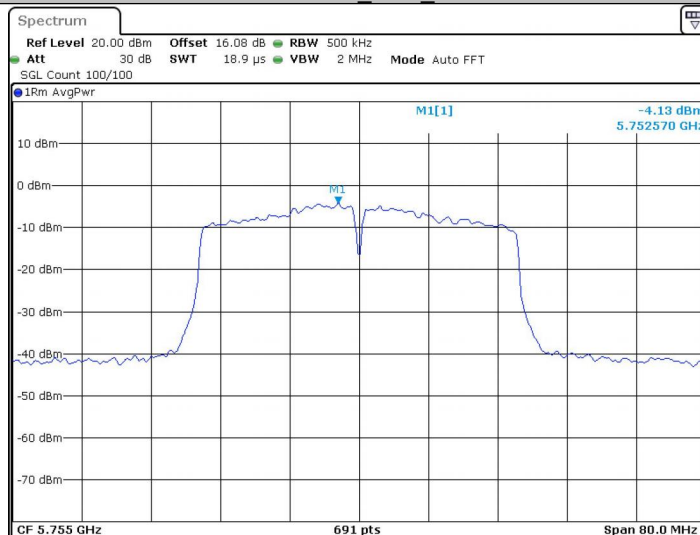
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11N40SISO_Ant1_5230



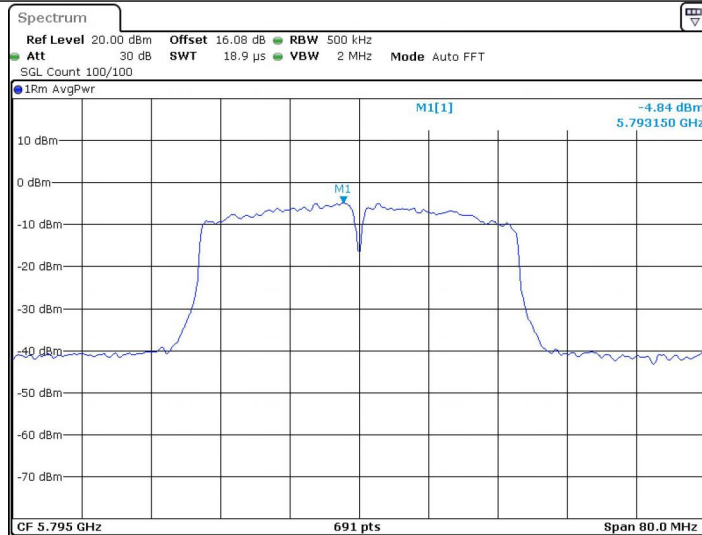
Date: 23 APR 2026 20:56:18

11N40SISO_Ant1_5755



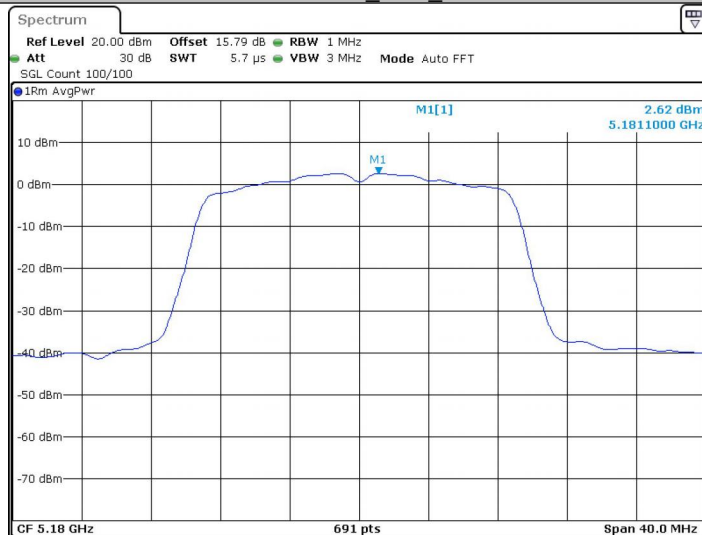
Date: 23 APR 2026 21:08:18

11N40SISO_Ant1_5795



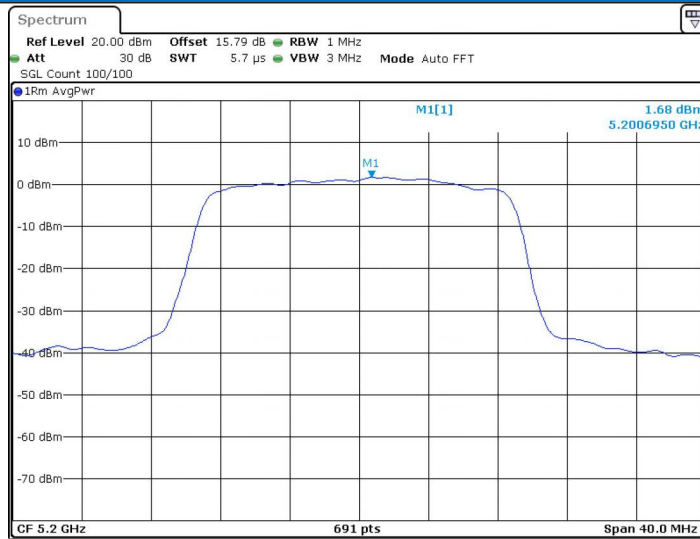
Date: 23.APR.2026 21:11:23

11AC20SISO_Ant1_5180



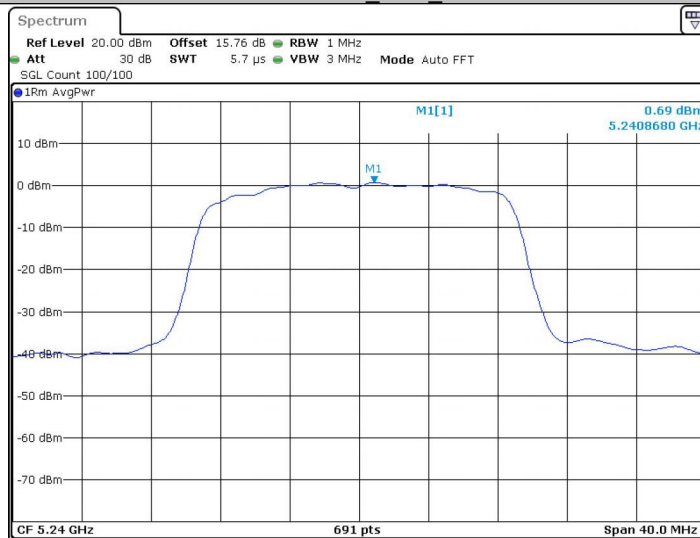
Date: 23.APR.2026 21:14:32

11AC20SISO_Ant1_5200



Date: 23 APR 2026 21:18:37

11AC20SISO_Ant1_5240



Date: 23 APR 2026 21:20:12

11AC20SISO_Ant1_5745



Date: 23 APR 2026 21:35:55

11AC20SISO_Ant1_5785



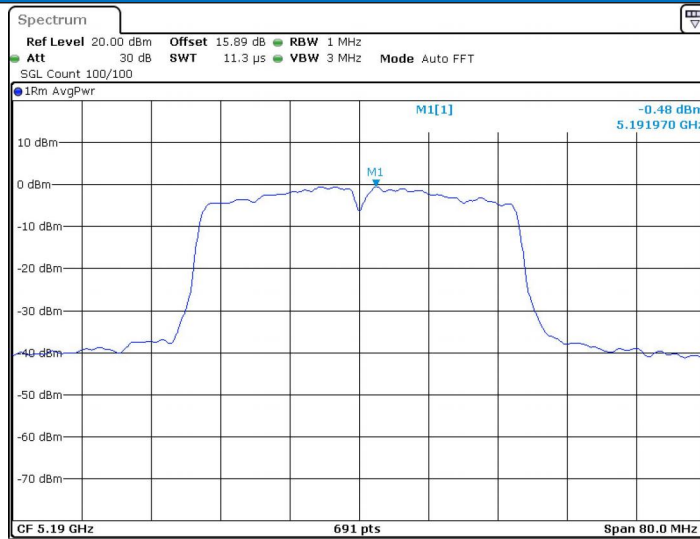
Date: 23 APR 2026 21:37:27

11AC20SISO_Ant1_5825



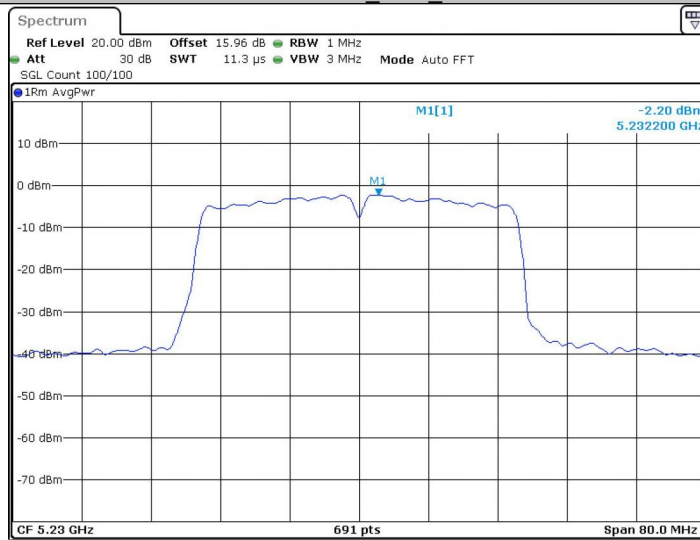
Date: 23 APR 2026 21:39:24

11AC40SISO_Ant1_5190



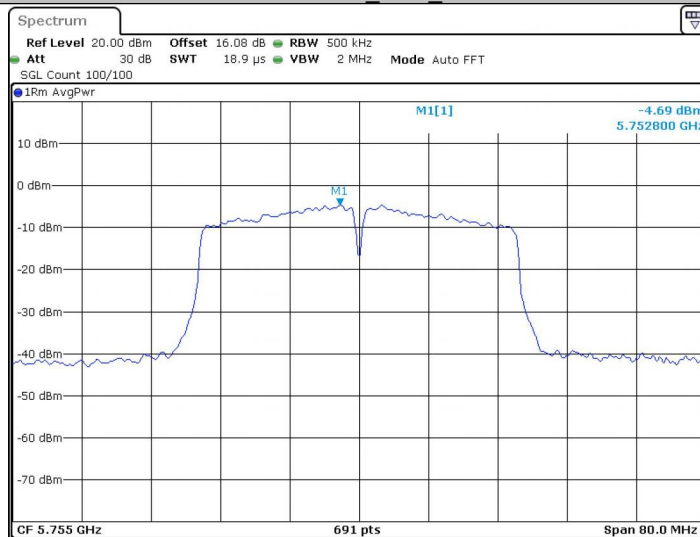
Date: 23 APR 2026 21:41:07

11AC40SISO_Ant1_5230



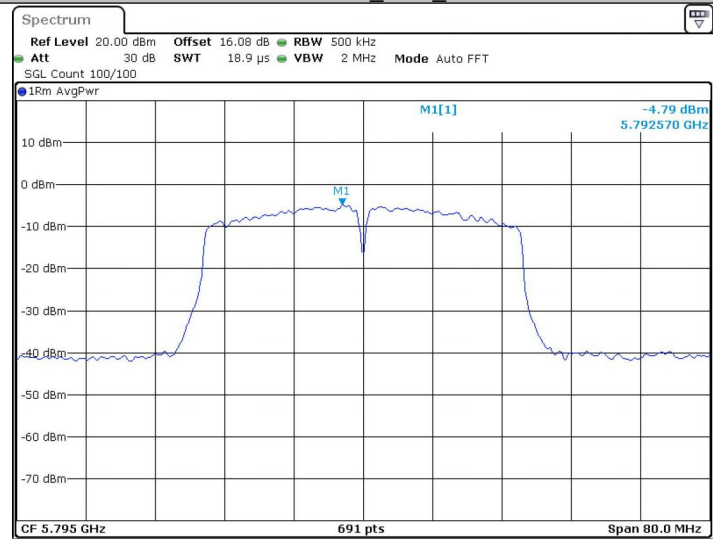
Date: 23 APR 2026 21:45:35

11AC40SISO_Ant1_5755



Date: 23 APR 2026 21:56:06

11AC40SISO_Ant1_5795



Date: 23 APR 2026 21:57:47

Appendix D): Band Edge Measurements

Test Requirement 47 CFR Part 15, Subpart C 15.209 & 15.407(b)

Test Method: KDB 789033 D02 II G

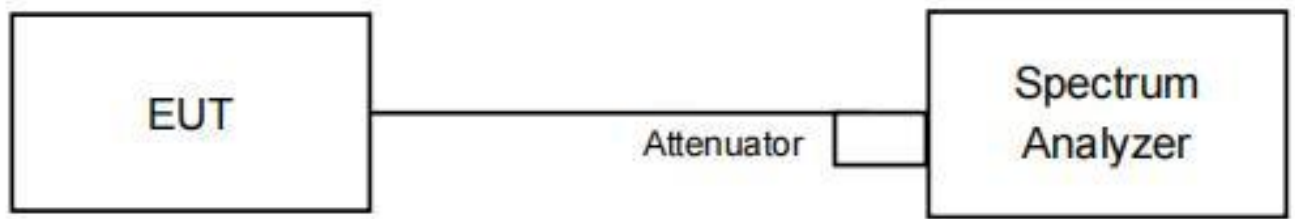
Test Procedure:

1. The EUT operates at transmitting mode. The operate channel is tested to verify the largest transmission and spurious emissions power at the continuous transmission mode.
2. Set the spectrum analyzer in the following setting in order to capture the lower and upper band-edges of the emission:
 - (a) PEAK: RBW=VBW=1MHz / Sweep=AUTO
 - (b) AVERAGE: RBW=1MHz ; VBW=1/on time(1KHz) / Sweep=AUTO

Limit:

For transmitters operating in the 5.15-5.25 GHz band:	All emissions outside of the 5.15-5.35 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.25-5.35 GHz band:	All emissions outside of the 5.15-5.35 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.47-5.725 GHz band:	All emissions outside of the 5.47-5.725 GHz band shall not exceed an e.i.r.p. of -27 dBm/MHz (68.2dBuV/m).
For transmitters operating in the 5.725-5.85 GHz band:	(i) All emissions shall be limited to a level of -27 dBm/MHz (68.2dBuV/m) at 75 MHz or more above or below the band edge increasing linearly to 10 dBm/MHz (105.2dBuV/m) at 25 MHz above or below the band edge, and from 25 MHz above or below the band edge increasing linearly to a level of 15.6 dBm/MHz (110.8dBuV/m) at 5 MHz above or below the band edge, and from 5 MHz above or below the band edge increasing linearly to a level of 27 dBm/MHz (122.2dBuV/m) at the band edge.

Test Setup Diagram



Test Result B1

TestMode	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Low	5180	-41.94	≤ -27	PASS
	High	5240	-46.35	≤ -27	PASS
11N20SISO	Low	5180	-35.95	≤ -27	PASS
	High	5240	-45.55	≤ -27	PASS
11N40SISO	Low	5190	-29.41	≤ -27	PASS
	High	5230	-45.74	≤ -27	PASS
11AC20SISO	Low	5180	-33.95	≤ -27	PASS
	High	5240	-46.29	≤ -27	PASS
11AC40SISO	Low	5190	-29.13	≤ -27	PASS
	High	5230	-46.09	≤ -27	PASS

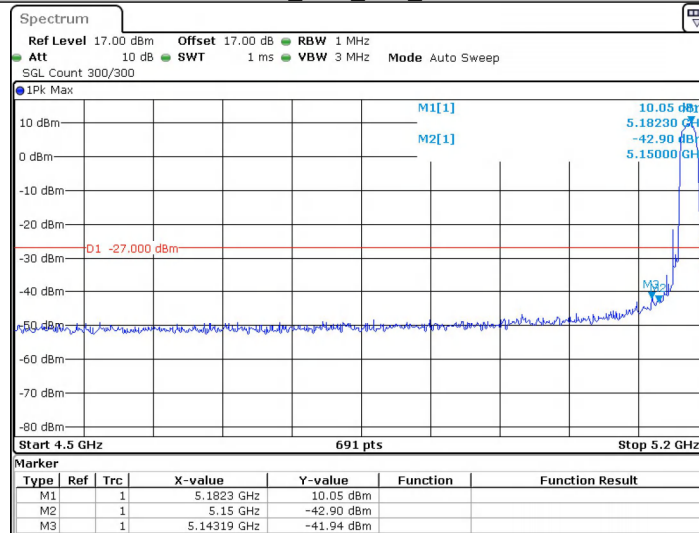
Test Result B4

TestMode	ChName	Freq(MHz)	FreqRange [MHz]	Result [dBm]	Limit [dBm]	Verdict
11A	Low	5745	5650~5700	-46	≤ -26.88	PASS
			5700~5720	-31.47	≤ 15.23	PASS
			5720~5725	-35.65	≤ 15.98	PASS
			5760~5650	-45.94	≤ -27	PASS
	High	5825	5850~5855	-42.85	≤ 15.65	PASS
			5855~5875	-44.18	≤ 10.46	PASS
			5875~5925	-46.71	≤ -26.87	PASS
			5925~5935	-45.73	≤ -27	PASS
11N20SISO	Low	5745	5650~5700	-45.03	≤ -26.01	PASS
			5700~5720	-44.01	≤ 11.17	PASS
			5720~5725	-27.89	≤ 25.86	PASS
			5760~5650	-44.86	≤ -27	PASS
	High	5825	5850~5855	-38.66	≤ 15.65	PASS
			5855~5875	-37.09	≤ 14.35	PASS
			5875~5925	-47.19	≤ -26.73	PASS
			5925~5935	-45.17	≤ -27	PASS
11N40SISO	Low	5755	5650~5700	-46.17	≤ -26.36	PASS
			5700~5720	-20.92	≤ 15.47	PASS
			5720~5725	-20.84	≤ 15.90	PASS
			5780~5650	-45.29	≤ -27	PASS
	High	5795	5850~5855	-43.41	≤ 15.85	PASS

			5855~5875	-42.55	≤ 11.61	PASS
			5875~5925	-47.04	≤ -26.61	PASS
			5925~5935	-47.42	≤ -27	PASS
11AC20SIS O	Low	5745	5650~5700	-45.39	≤ -25.40	PASS
			5700~5720	-42.16	≤ 12.10	PASS
			5720~5725	-31.64	≤ 20.92	PASS
			5760~5650	-44.11	≤ -27	PASS
	High	5825	5850~5855	-42.79	≤ 15.65	PASS
			5855~5875	-43.78	≤ 10.02	PASS
			5875~5925	-45.67	≤ -25.71	PASS
			5925~5935	-46.08	≤ -27	PASS
11AC40SIS O	Low	5755	5650~5700	-45.04	≤ -25.49	PASS
			5700~5720	-36.17	≤ 13.28	PASS
			5720~5725	-35.44	≤ 15.90	PASS
			5780~5650	-44.72	≤ -27	PASS
	High	5795	5850~5855	-42.06	≤ 18.03	PASS
			5855~5875	-38.76	≤ 11.48	PASS
			5875~5925	-48.23	≤ -26.97	PASS
			5925~5935	-46.61	≤ -27	PASS

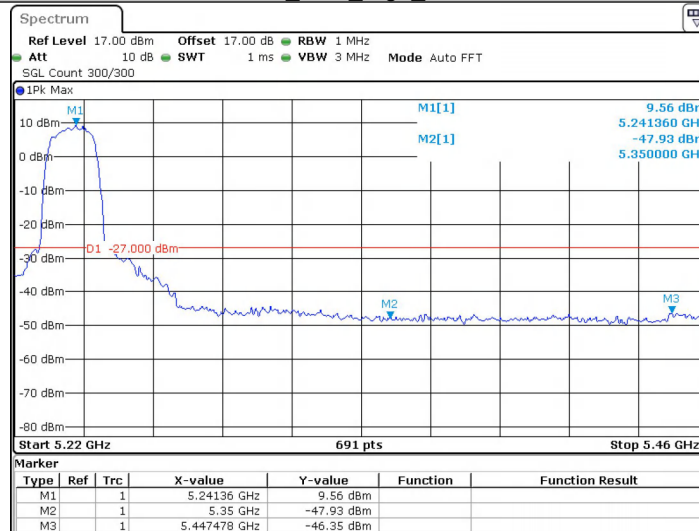
Test Graphs B1

11A_Ant1_Low_5180



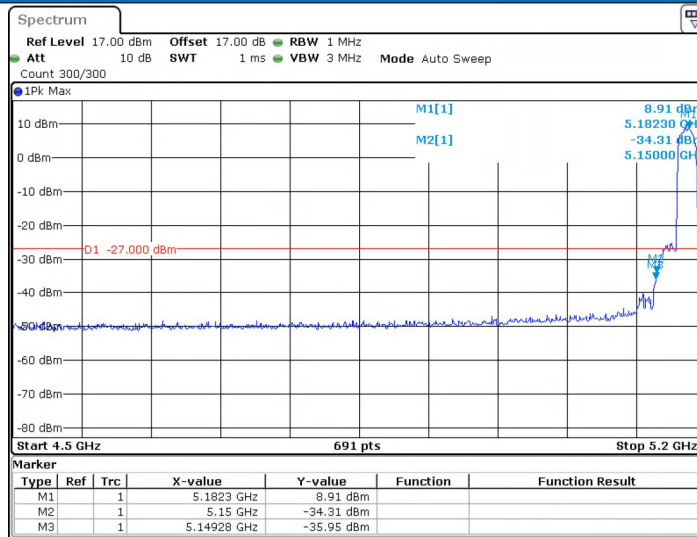
Date: 30 JUL 2026 11:28:54

11A_Ant1_High_5240



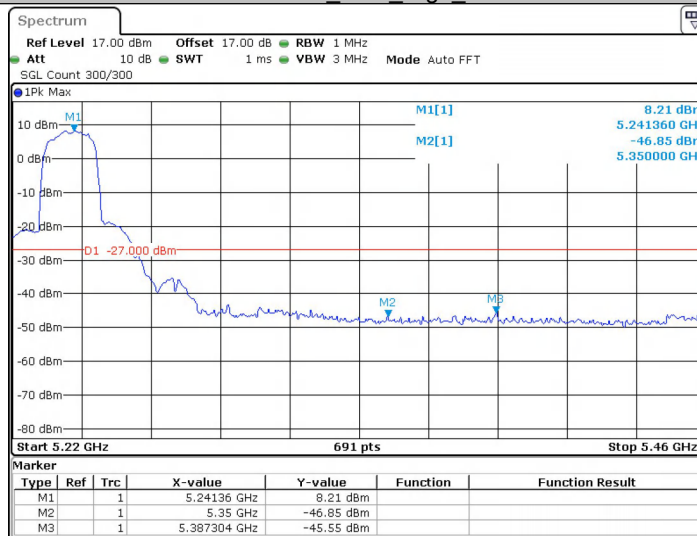
Date: 30 JUL 2026 11:36:05

11N20SISO_Ant1_Low_5180



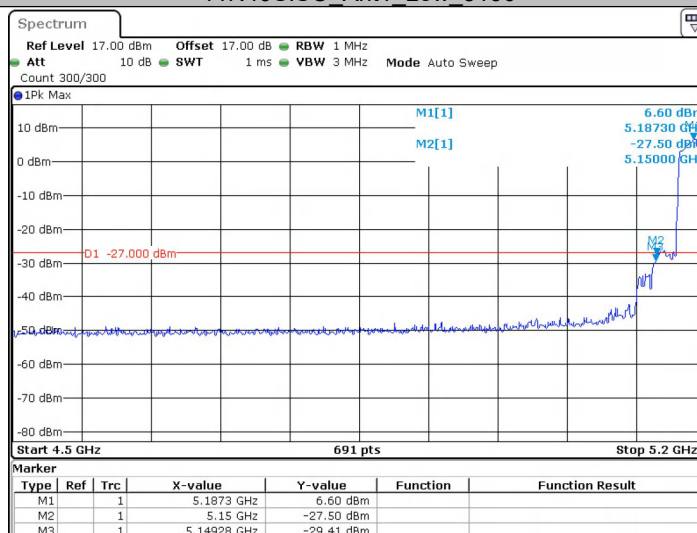
Date: 30 JUL 2026 11:56:41

11N20SISO_Ant1_High_5240



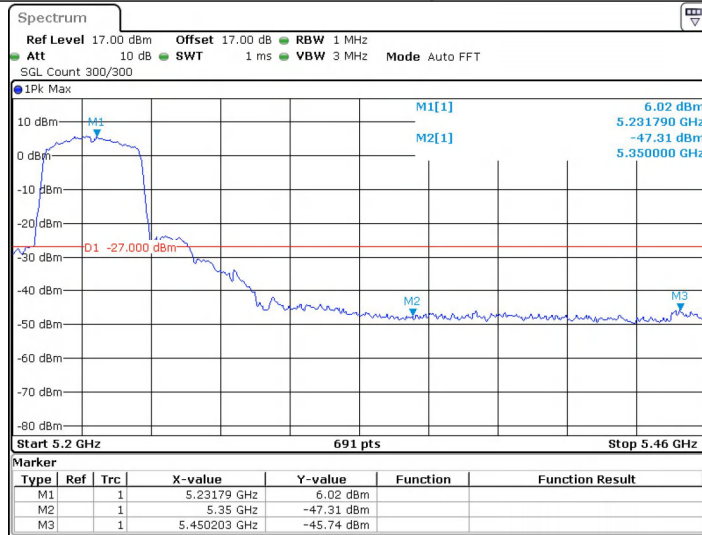
Date: 30 JUL 2026 11:39:21

11N40SISO_Ant1_Low_5190



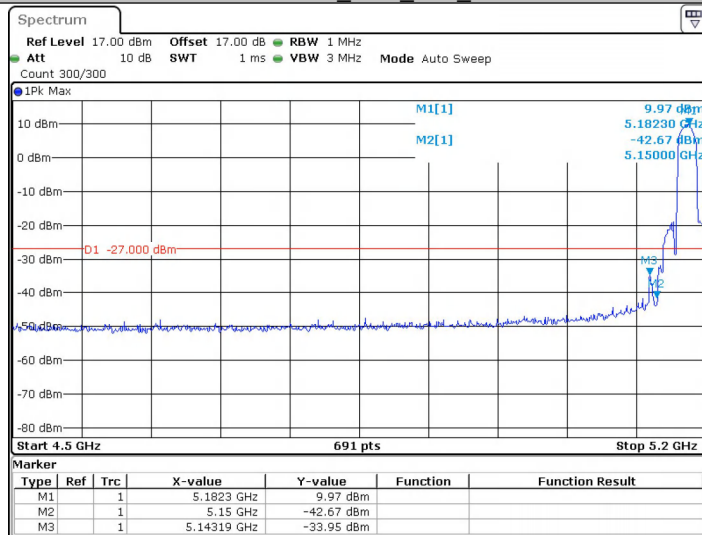
Date: 30 JUL 2026 11:41:28

11N40SISO_Ant1_High_5230



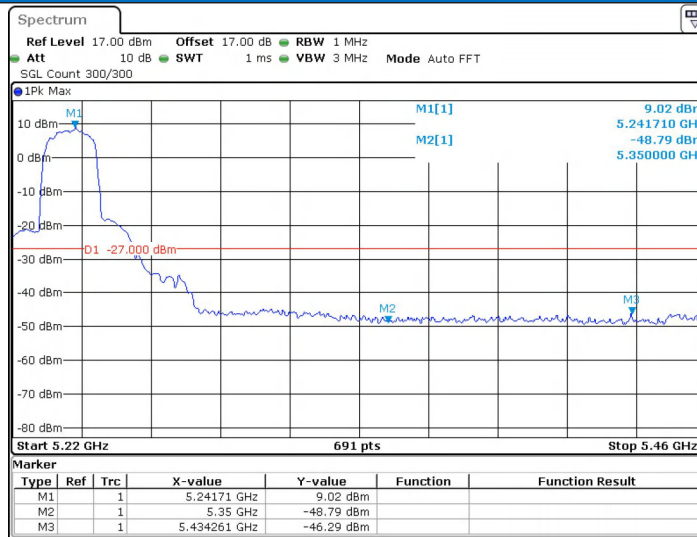
Date: 30 JUL 2026 11:42:57

11AC20SISO_Ant1_Low_5180



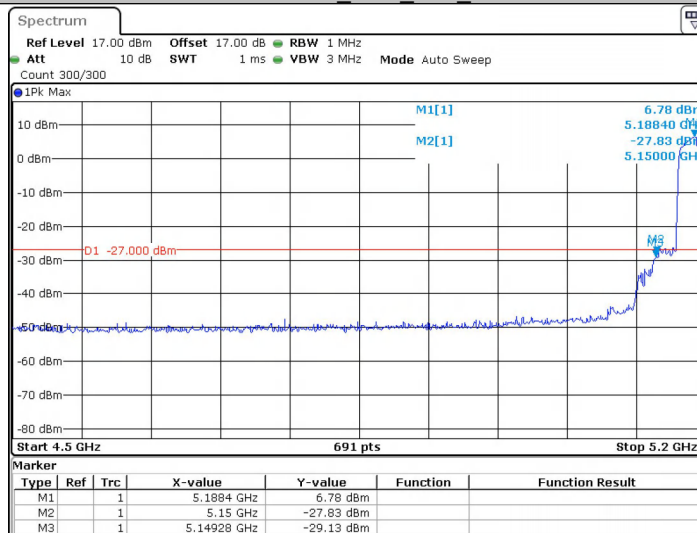
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11AC20SISO_Ant1_High_5240



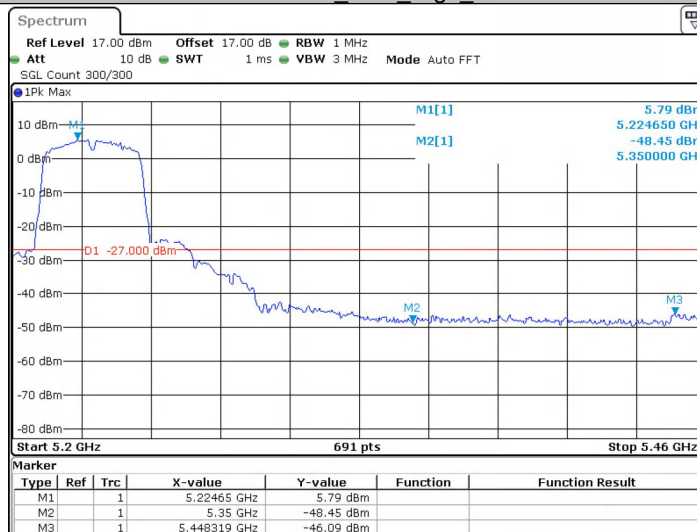
Date: 30 JUL 2026 11:46:28

11AC40SISO_Ant1_Low 5190



Date: 30 JUL 2026 11:50:10

11AC40SISO_Ant1_High 5230



Date: 30 JUL 2026 11:51:29