

Appendix for 71A_n66A

Product Name: 5G CPE

Model No: S3

Appendix A: Average Power Output for SA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	0.87	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Inner_Full	2.42	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1.33	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	20.71	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	20.74	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Inner_Full	21.75	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	20.34	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	20.71	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	20.46	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Inner_Full	22.07	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	20.54	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	20.34	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	20.47	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Inner_Full	21.59	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	20.18	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	20.47	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	20.81	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Inner_Full	22.39	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	20.61	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	20.68	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	20.88	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Inner_Full	21.97	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	20.49	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	20.66	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	20.88	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Inner_Full	22.21	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	20.95	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	20.75	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	20.85	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Inner_Full	21.92	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	20.54	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	20.92	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	20.73	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Inner_Full	22.21	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	20.58	PC3	PASS

DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	20.58	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	20.74	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Inner_Full	21.67	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	20.60	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	20.80	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	20.90	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Inner_Full	22.39	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	20.89	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	20.82	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	20.87	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Inner_Full	21.92	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.00	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	20.84	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	20.83	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Inner_Full	22.29	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	20.81	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	20.73	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	20.79	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Inner_Full	21.79	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	20.88	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	20.65	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	20.69	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Inner_Full	22.22	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	20.94	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	20.60	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	20.75	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Inner_Full	21.73	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	20.69	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.01	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	20.82	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Inner_Full	22.40	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.04	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	20.69	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	20.83	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Inner_Full	21.80	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	20.99	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	20.50	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	20.78	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Inner_Full	22.23	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	20.64	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	20.66	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	20.86	PC3	PASS

DC_71A_n66A	15	5+20	CP-16QAM	M+L	Inner_Full	21.82	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	20.47	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	20.70	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	20.71	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Inner_Full	22.06	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	20.85	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	20.46	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	20.75	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Inner_Full	21.67	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	20.81	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	20.66	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	20.93	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Inner_Full	22.59	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	20.82	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	20.84	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	21.02	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Inner_Full	21.94	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	20.89	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	20.77	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

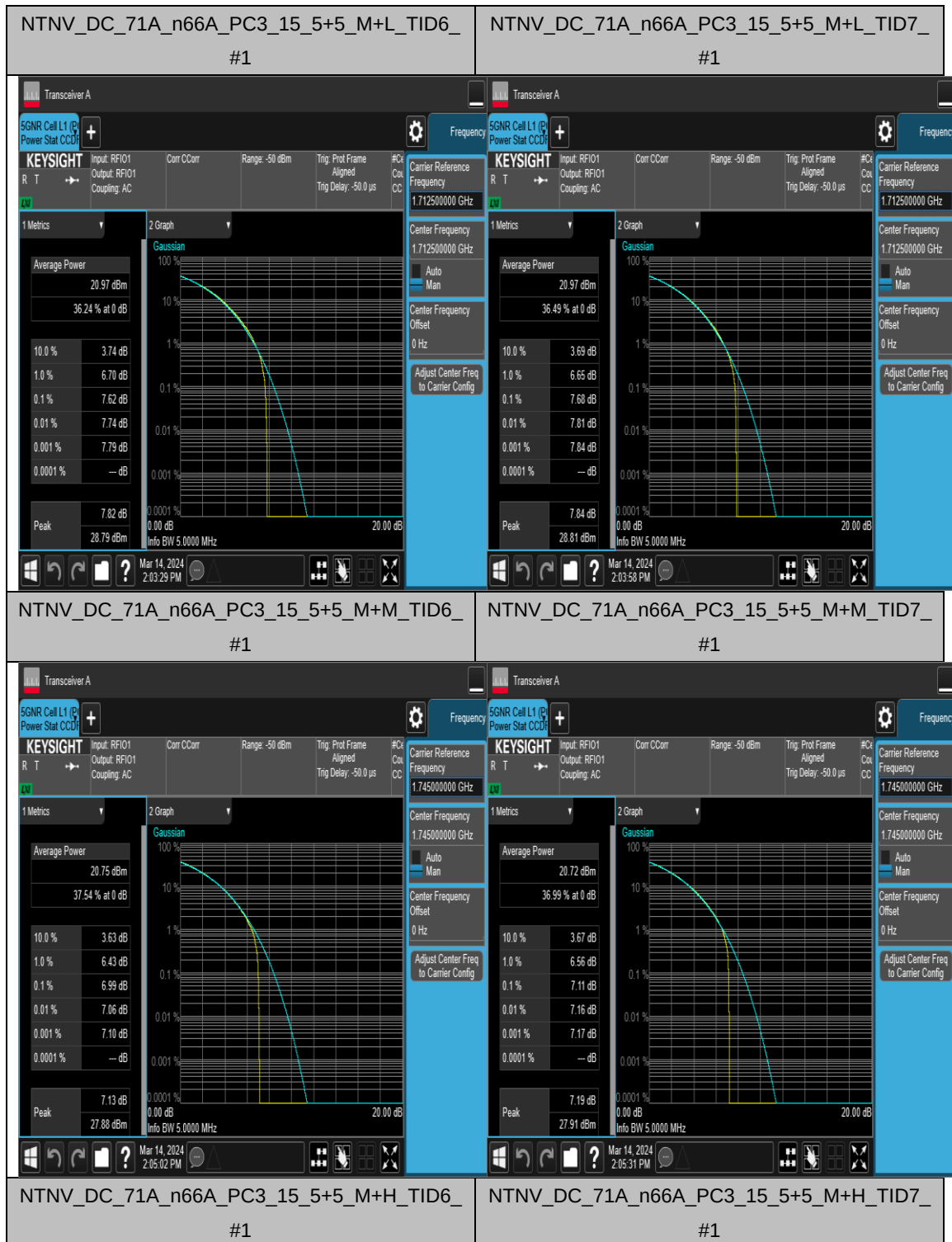
Peak-to-Average Ratio(CCDF)

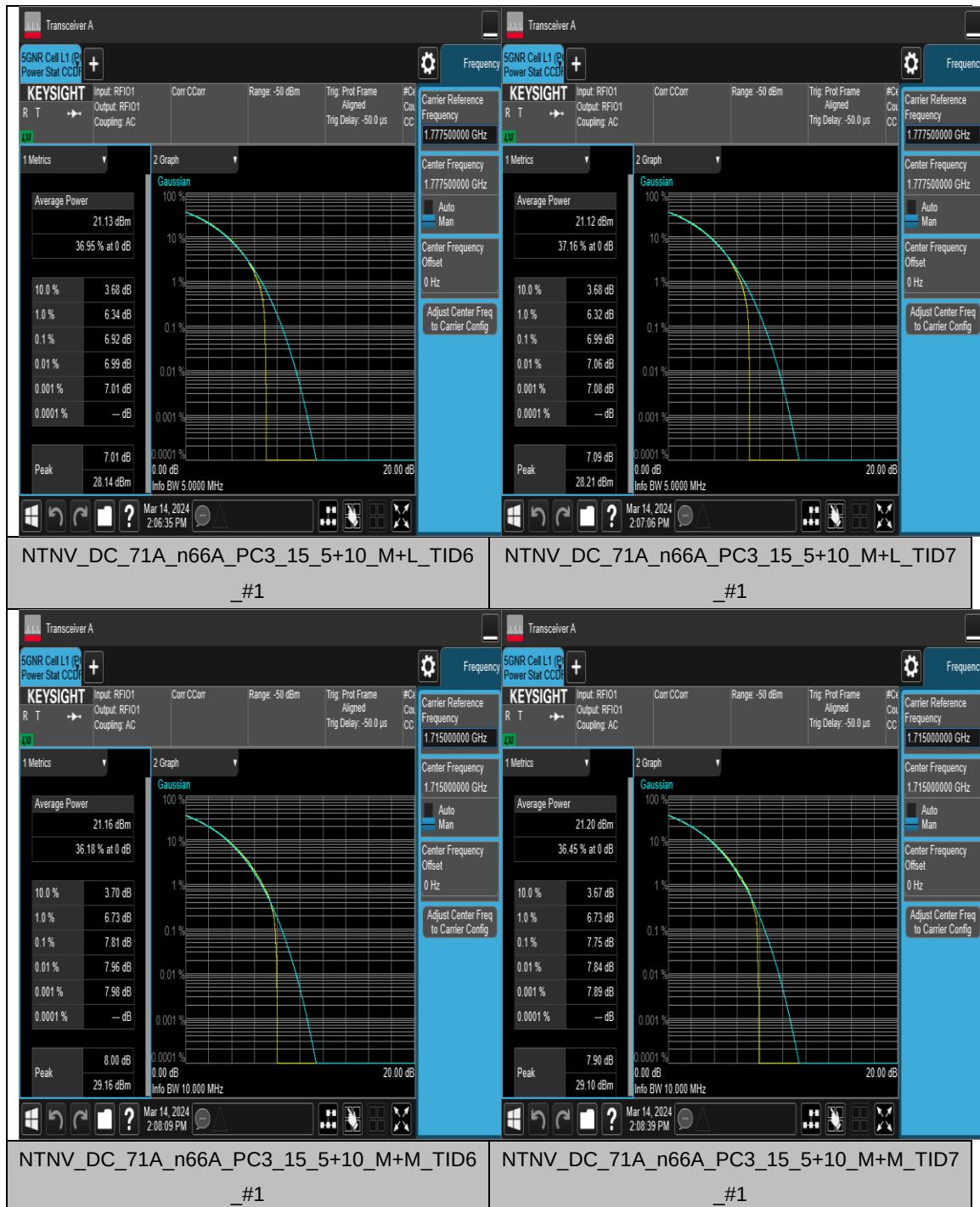
Test Result

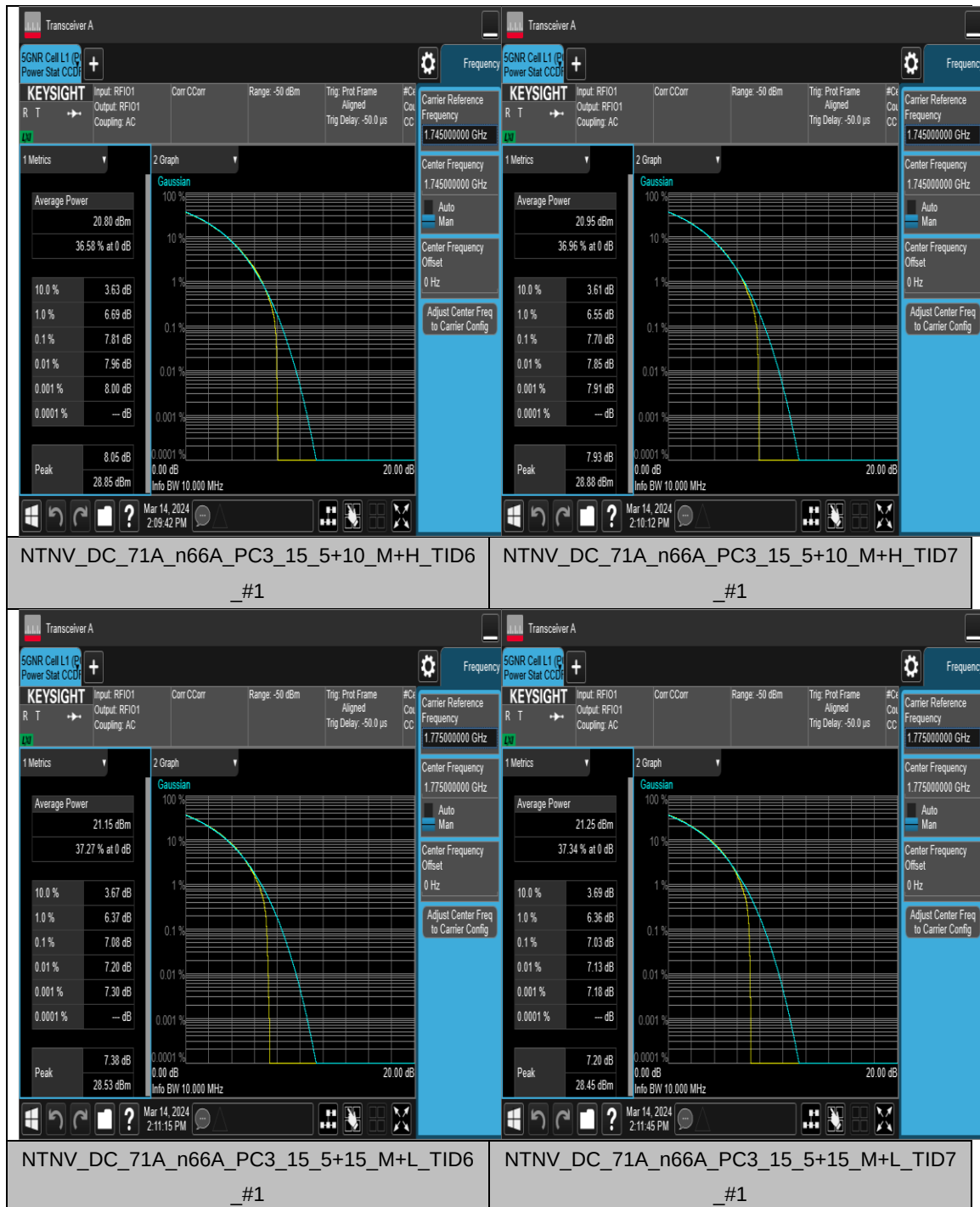
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_71A_n 66A	15	5+5	CP-QPSK	M+L	Outer_Full	7.62	≤13	PASS
DC_71A_n 66A	15	5+5	CP-16QAM	M+L	Outer_Full	7.68	≤13	PASS
DC_71A_n 66A	15	5+5	CP-QPSK	M+M	Outer_Full	6.99	≤13	PASS
DC_71A_n 66A	15	5+5	CP-16QAM	M+M	Outer_Full	7.11	≤13	PASS
DC_71A_n 66A	15	5+5	CP-QPSK	M+H	Outer_Full	6.92	≤13	PASS
DC_71A_n 66A	15	5+5	CP-16QAM	M+H	Outer_Full	6.99	≤13	PASS
DC_71A_n 66A	15	5+10	CP-QPSK	M+L	Outer_Full	7.81	≤13	PASS
DC_71A_n 66A	15	5+10	CP-16QAM	M+L	Outer_Full	7.75	≤13	PASS
DC_71A_n 66A	15	5+10	CP-QPSK	M+M	Outer_Full	7.81	≤13	PASS
DC_71A_n 66A	15	5+10	CP-16QAM	M+M	Outer_Full	7.70	≤13	PASS
DC_71A_n 66A	15	5+10	CP-QPSK	M+H	Outer_Full	7.08	≤13	PASS
DC_71A_n 66A	15	5+10	CP-16QAM	M+H	Outer_Full	7.03	≤13	PASS
DC_71A_n 66A	15	5+15	CP-QPSK	M+L	Outer_Full	7.08	≤13	PASS
DC_71A_n 66A	15	5+15	CP-16QAM	M+L	Outer_Full	6.95	≤13	PASS
DC_71A_n 66A	15	5+15	CP-QPSK	M+M	Outer_Full	7.54	≤13	PASS
DC_71A_n 66A	15	5+15	CP-16QAM	M+M	Outer_Full	7.52	≤13	PASS
DC_71A_n 66A	15	5+15	CP-QPSK	M+H	Outer_Full	6.95	≤13	PASS

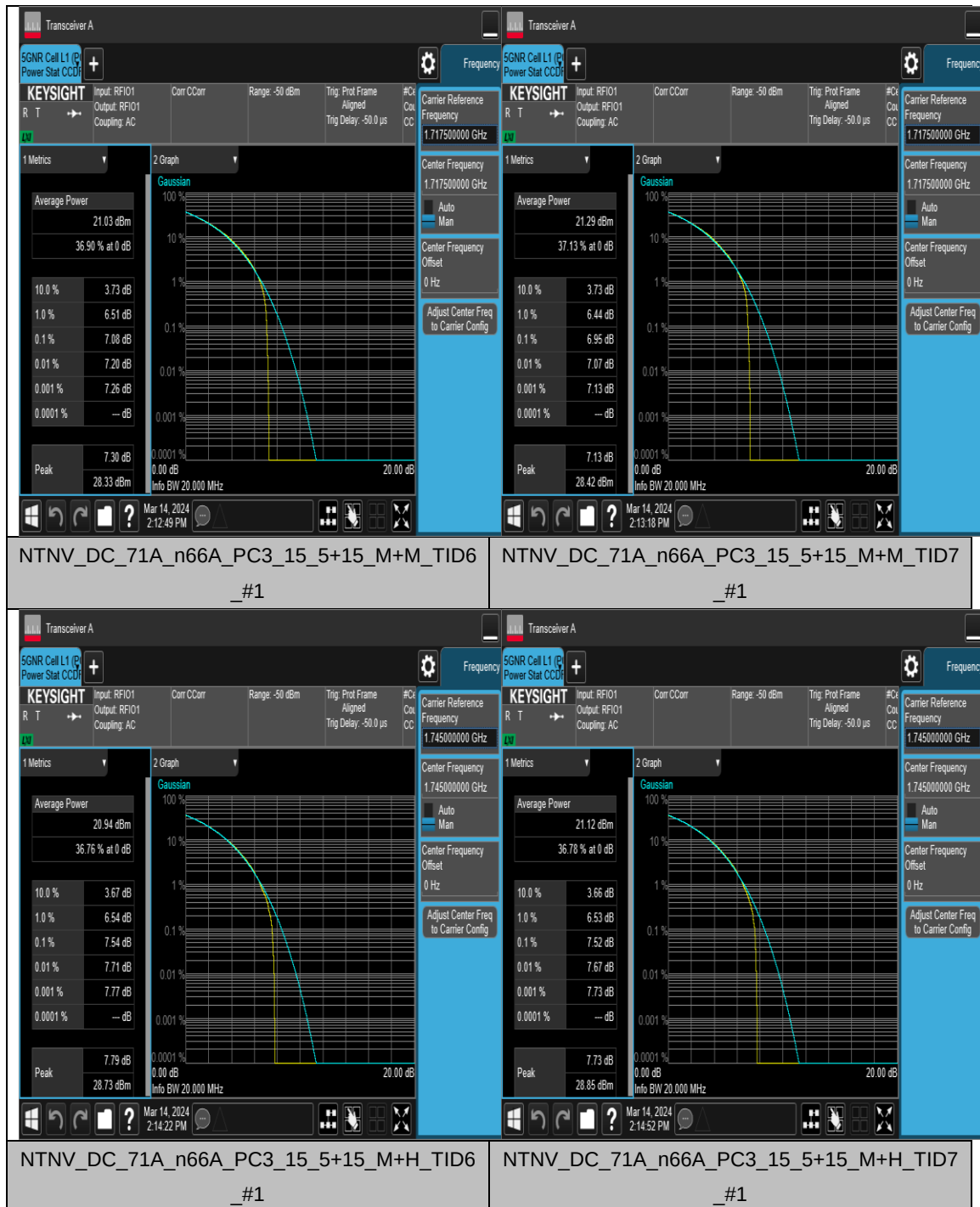
DC_71A_n 66A	15	5+15	CP-16QAM	M+H	Outer_Full	6.92	≤13	PASS
DC_71A_n 66A	15	5+20	CP-QPSK	M+L	Outer_Full	7.60	≤13	PASS
DC_71A_n 66A	15	5+20	CP-16QAM	M+L	Outer_Full	7.58	≤13	PASS
DC_71A_n 66A	15	5+20	CP-QPSK	M+M	Outer_Full	7.41	≤13	PASS
DC_71A_n 66A	15	5+20	CP-16QAM	M+M	Outer_Full	7.43	≤13	PASS
DC_71A_n 66A	15	5+20	CP-QPSK	M+H	Outer_Full	6.91	≤13	PASS
DC_71A_n 66A	15	5+20	CP-16QAM	M+H	Outer_Full	6.94	≤13	PASS

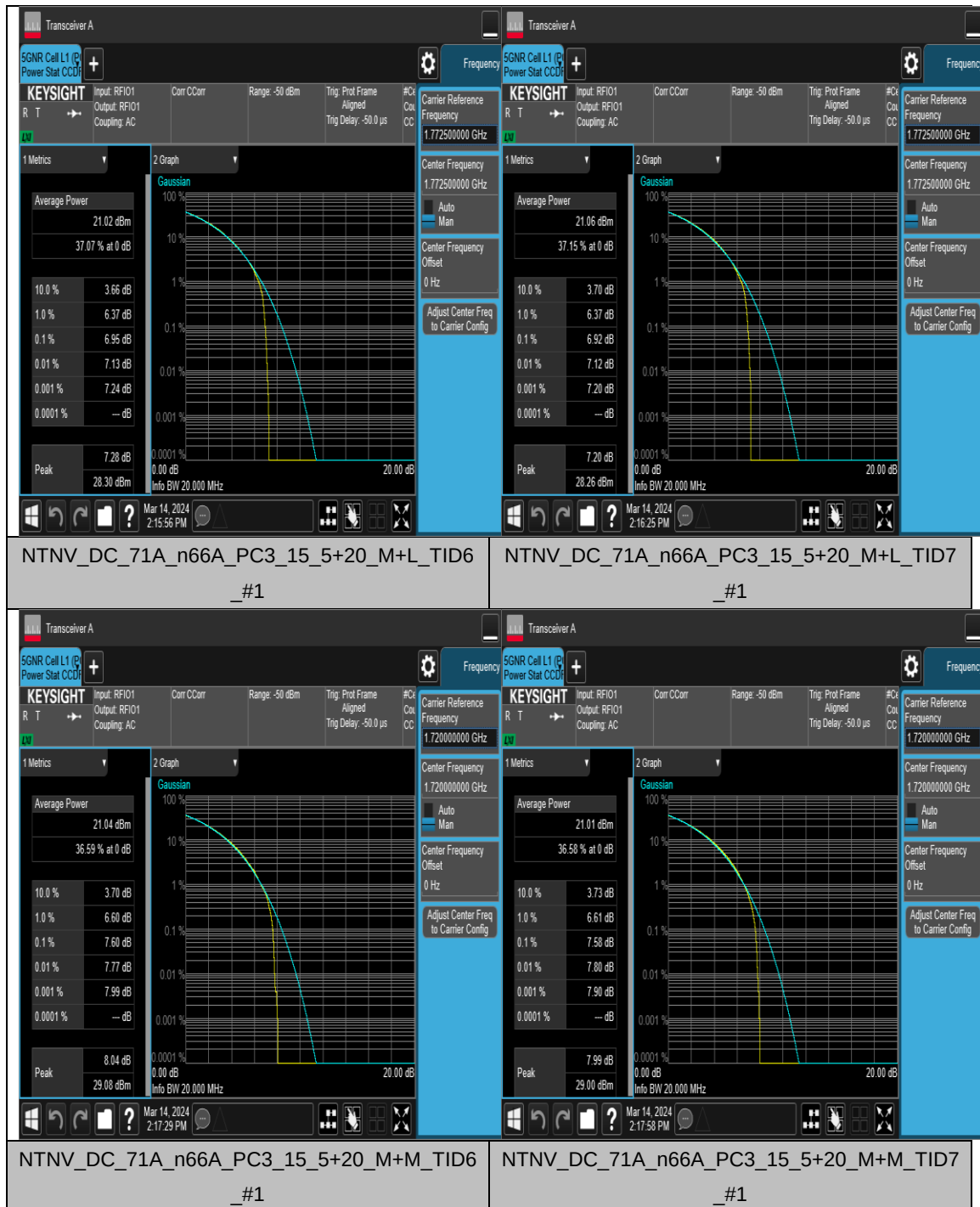
Test Graphs

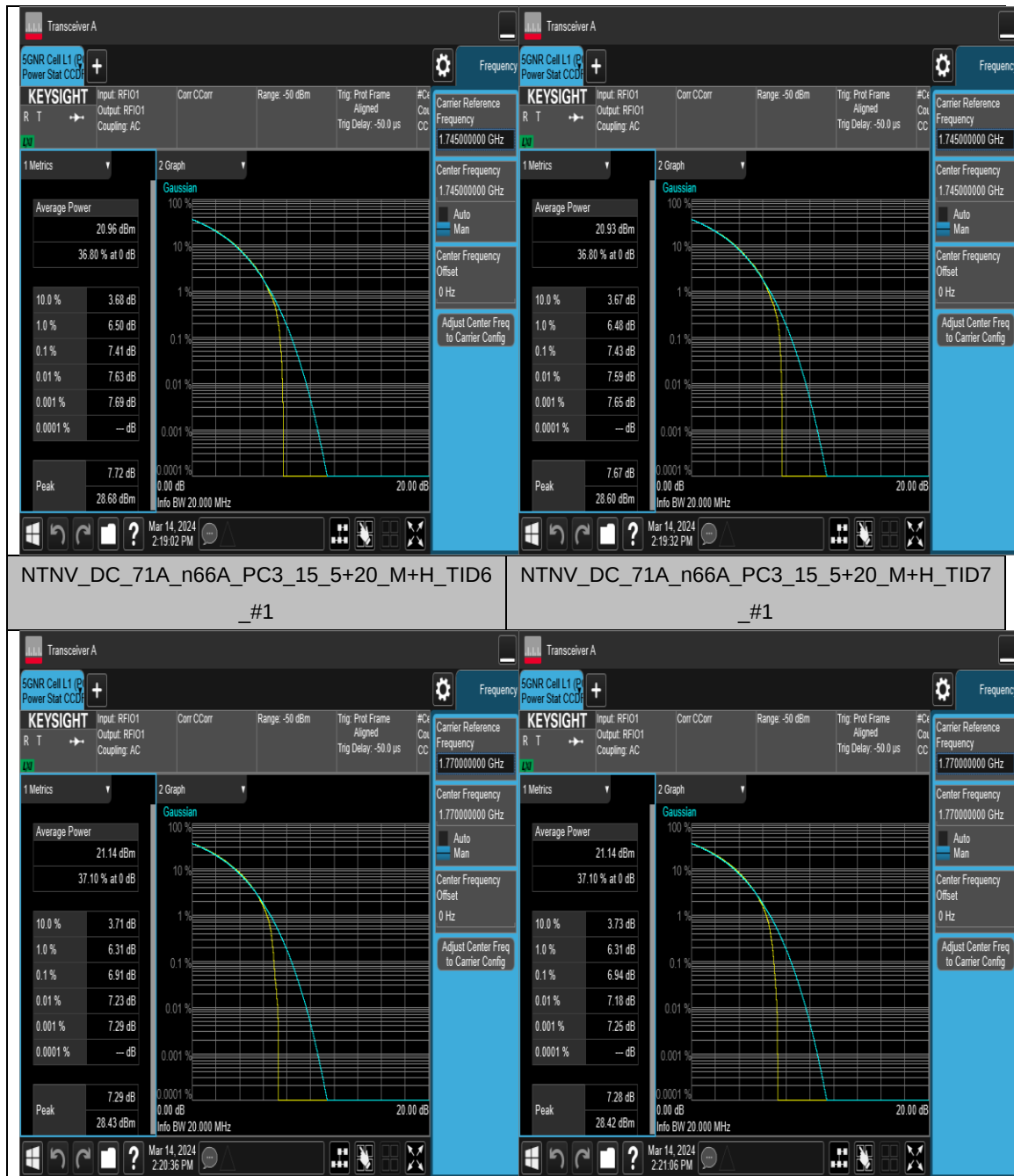












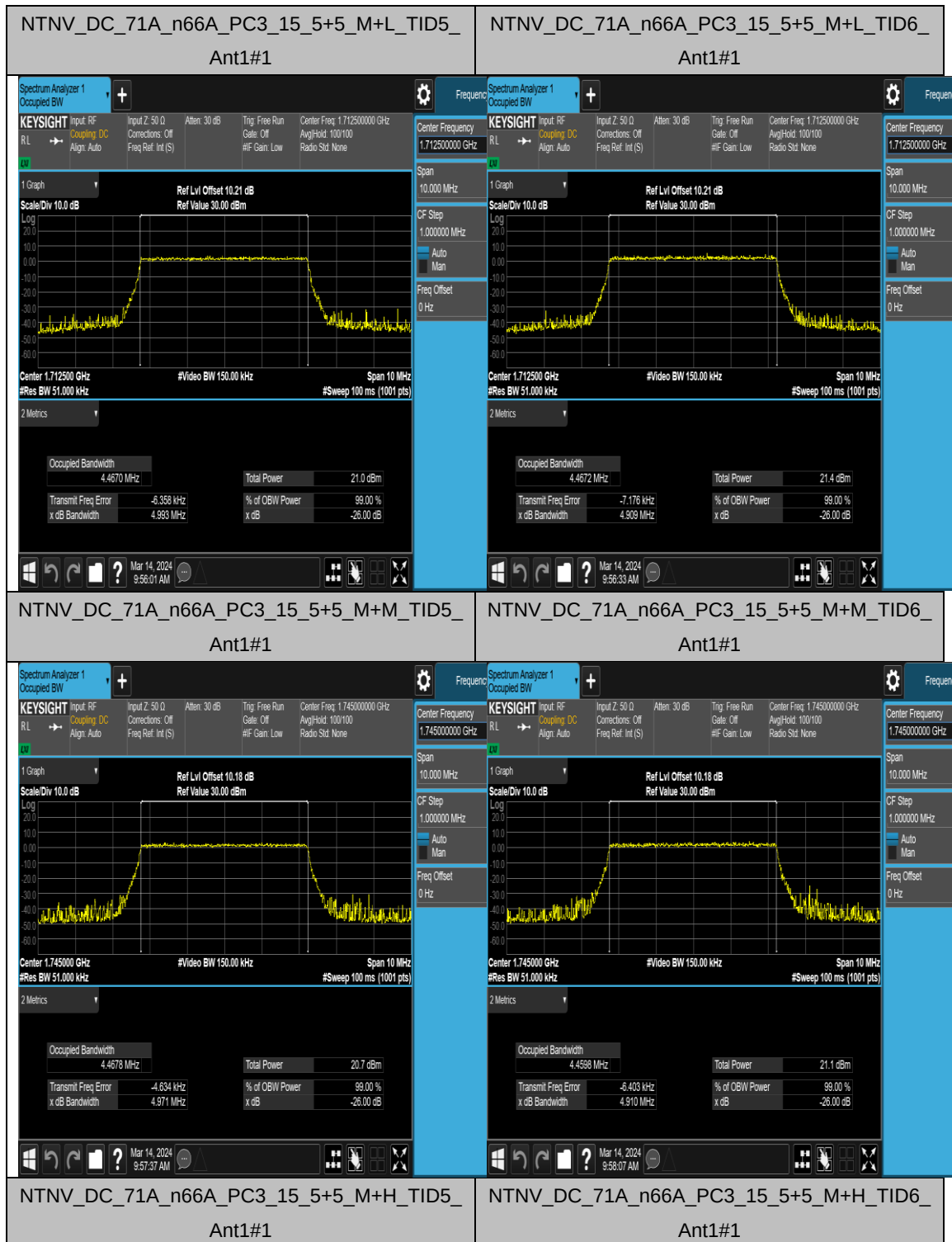
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

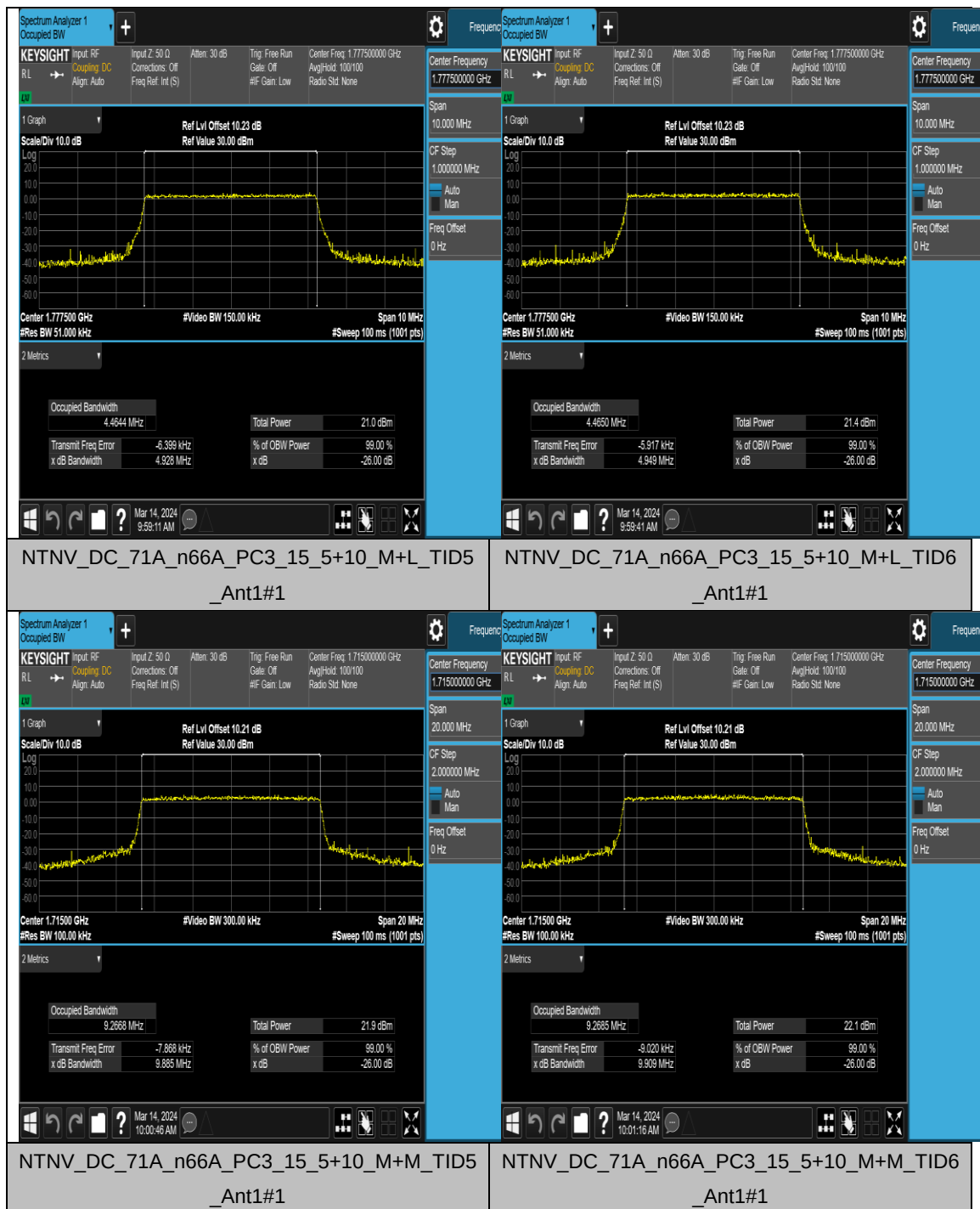
NSA

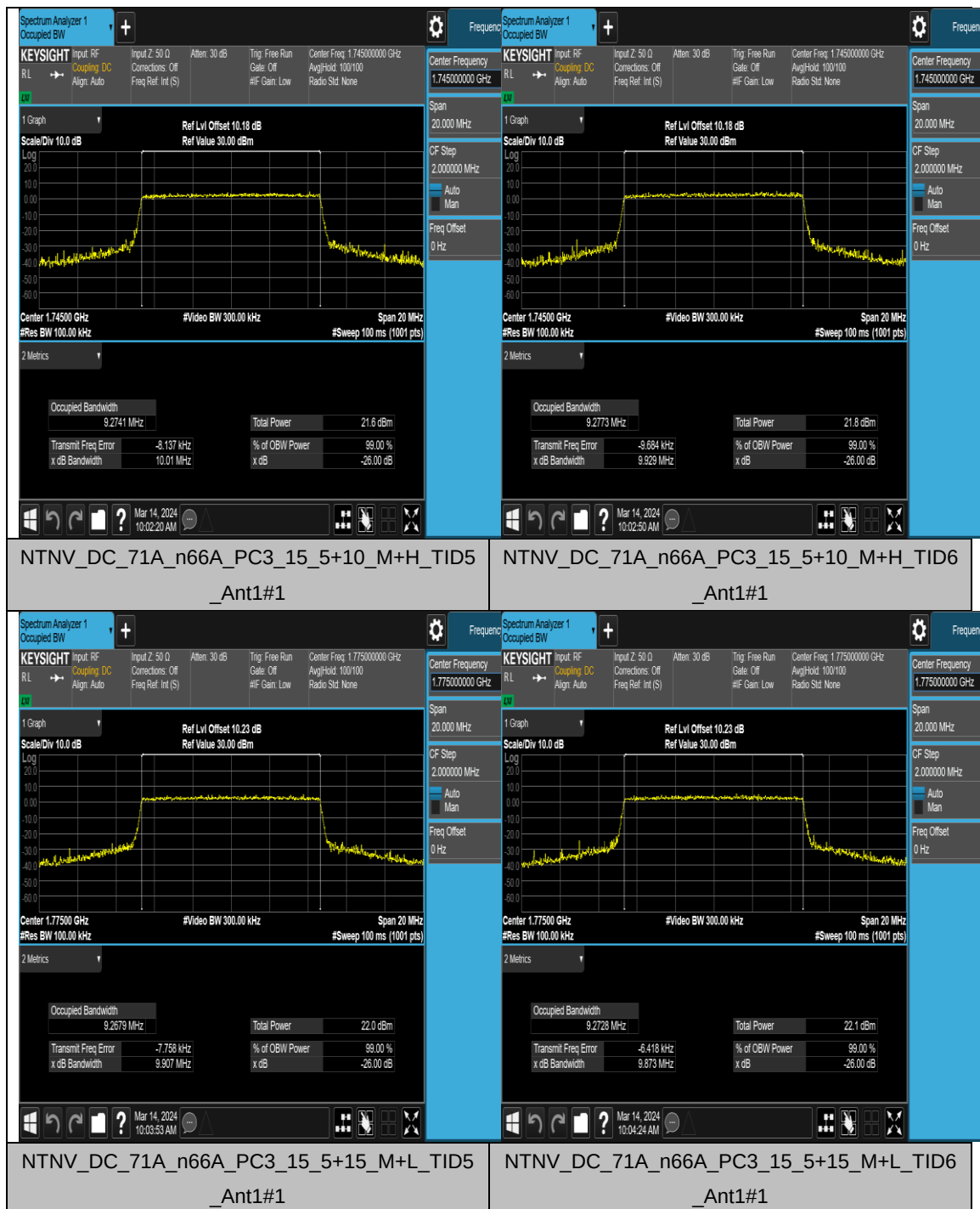
Test Result

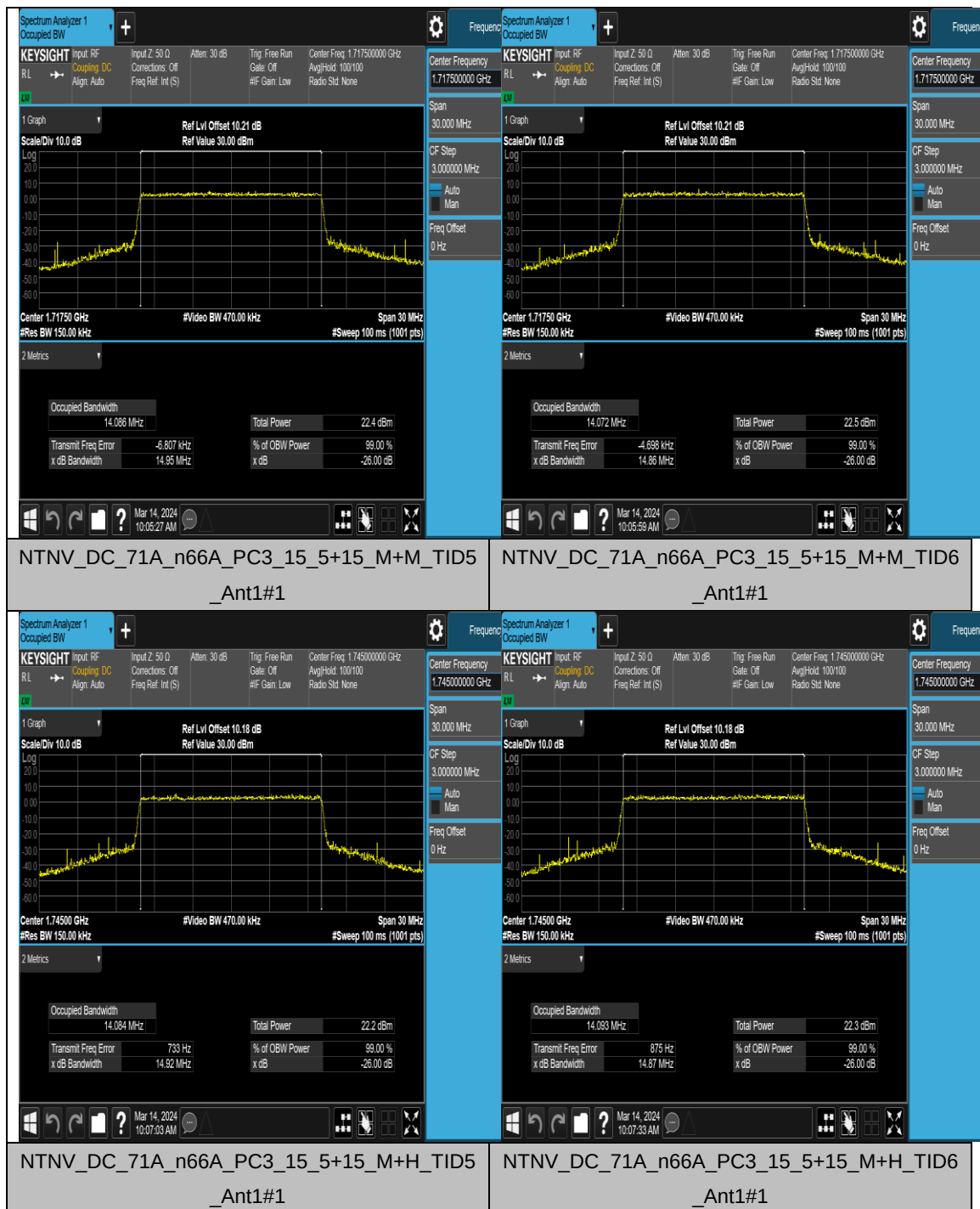
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4670	4.993	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4672	4.909	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4678	4.971	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4598	4.910	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4644	4.928	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4650	4.949	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2668	9.885	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2685	9.909	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2741	10.01	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2773	9.929	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2679	9.907	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2728	9.873	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	14.086	14.95	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	14.072	14.86	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	14.084	14.92	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	14.093	14.87	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	14.078	14.83	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	14.080	14.87	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	18.892	19.80	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	18.896	19.84	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	18.881	19.81	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	18.903	20.10	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	18.902	19.80	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	18.898	19.80	PASS

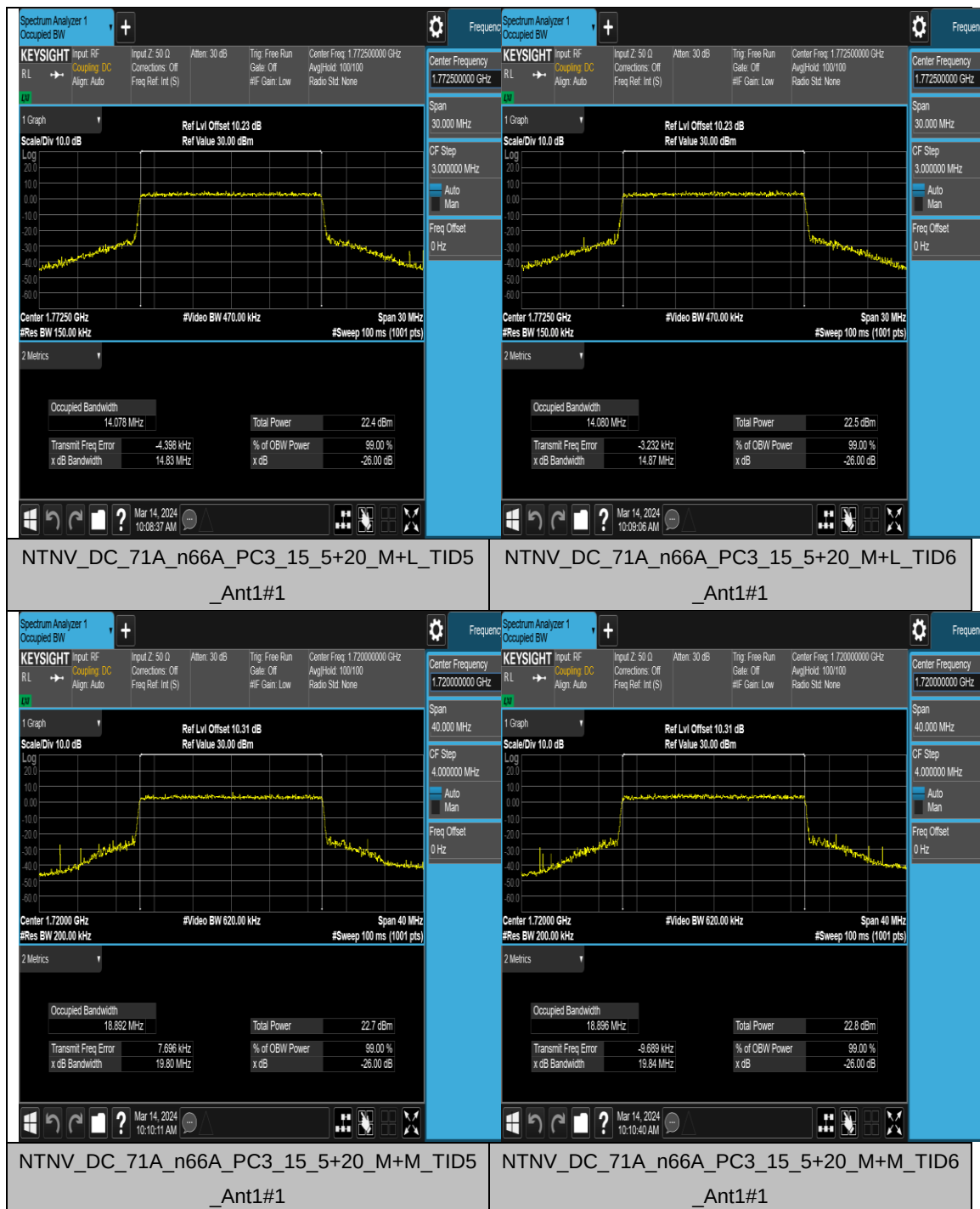
Test Graphs

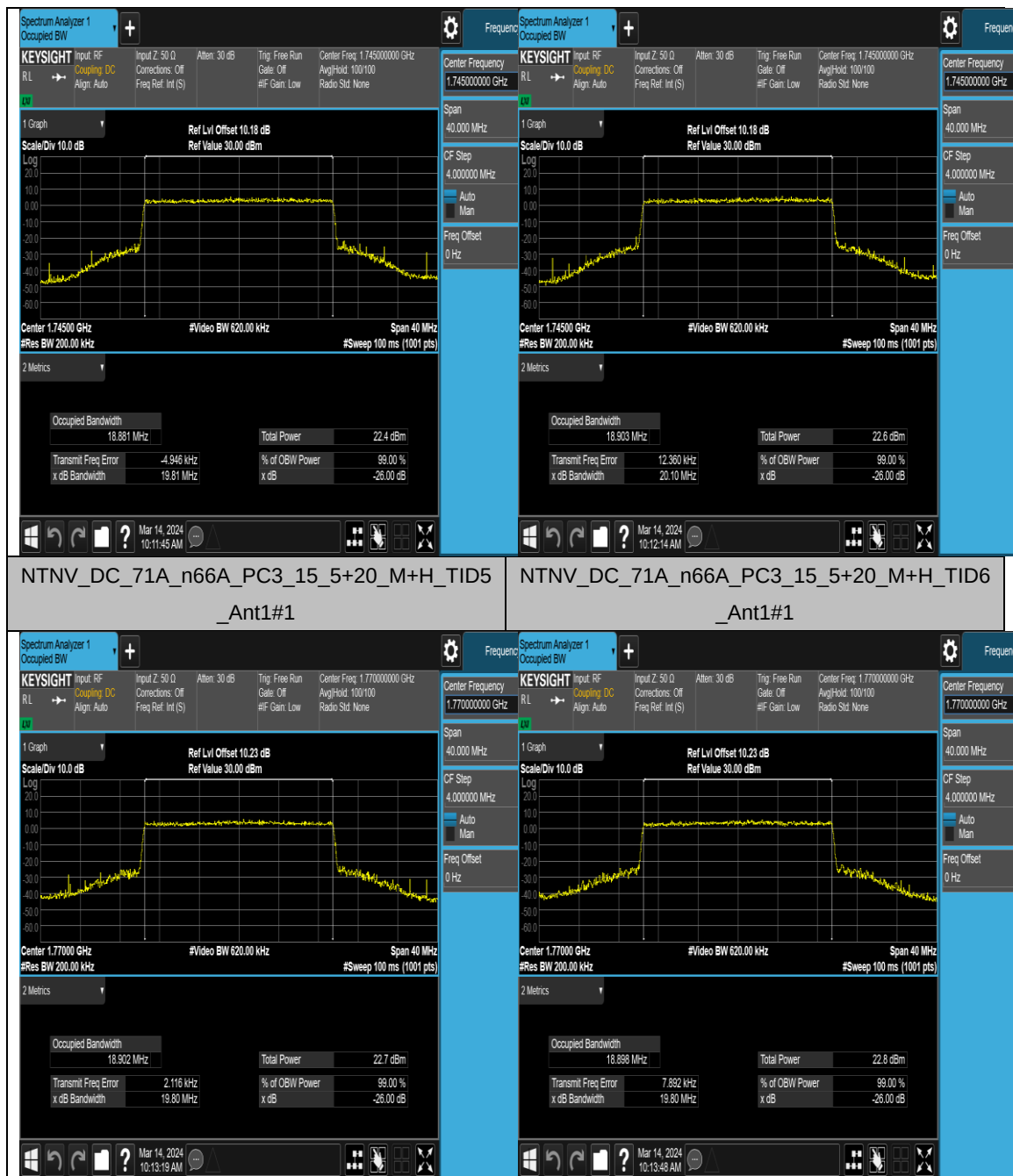










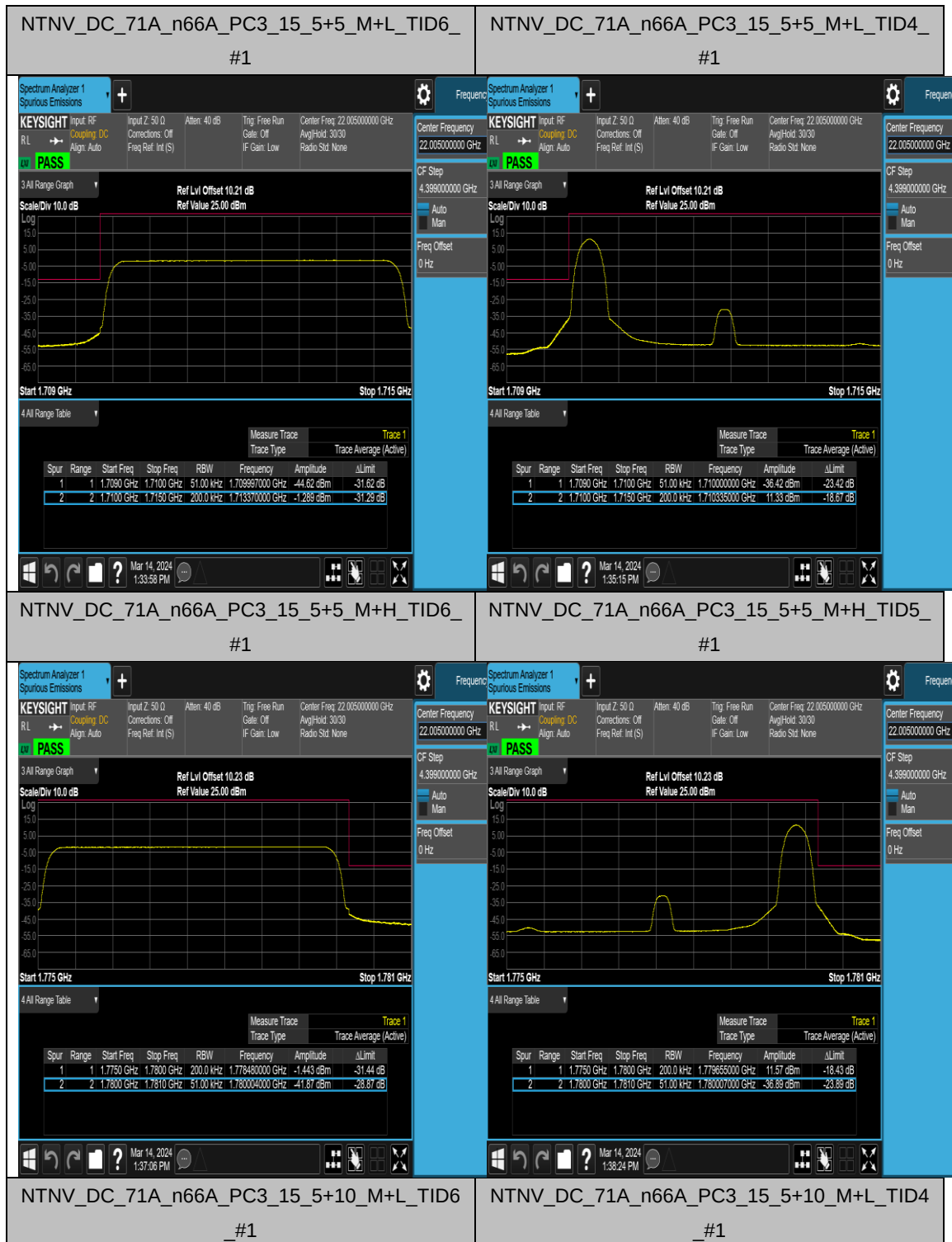


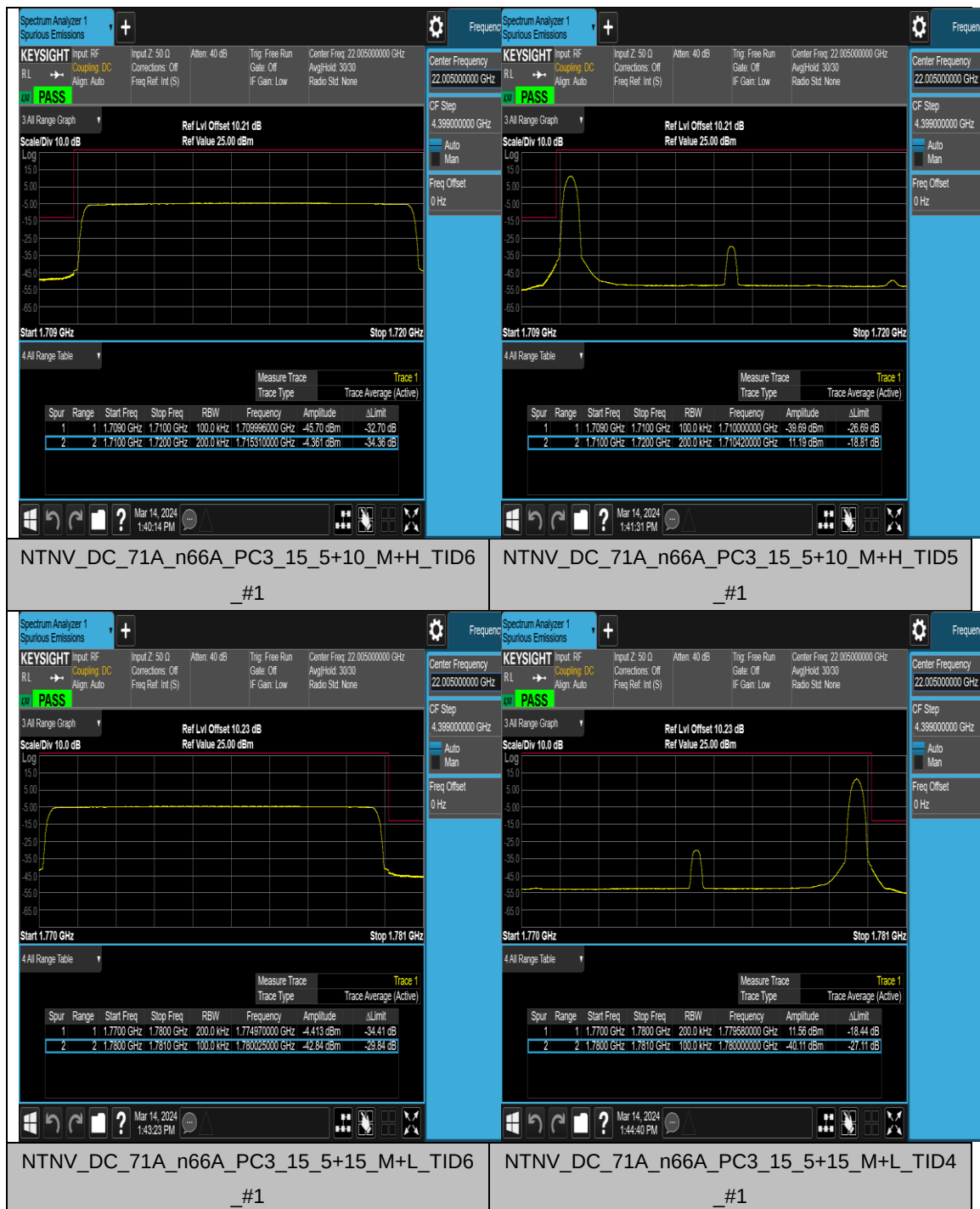
Appendix D: Band Edge for NSA

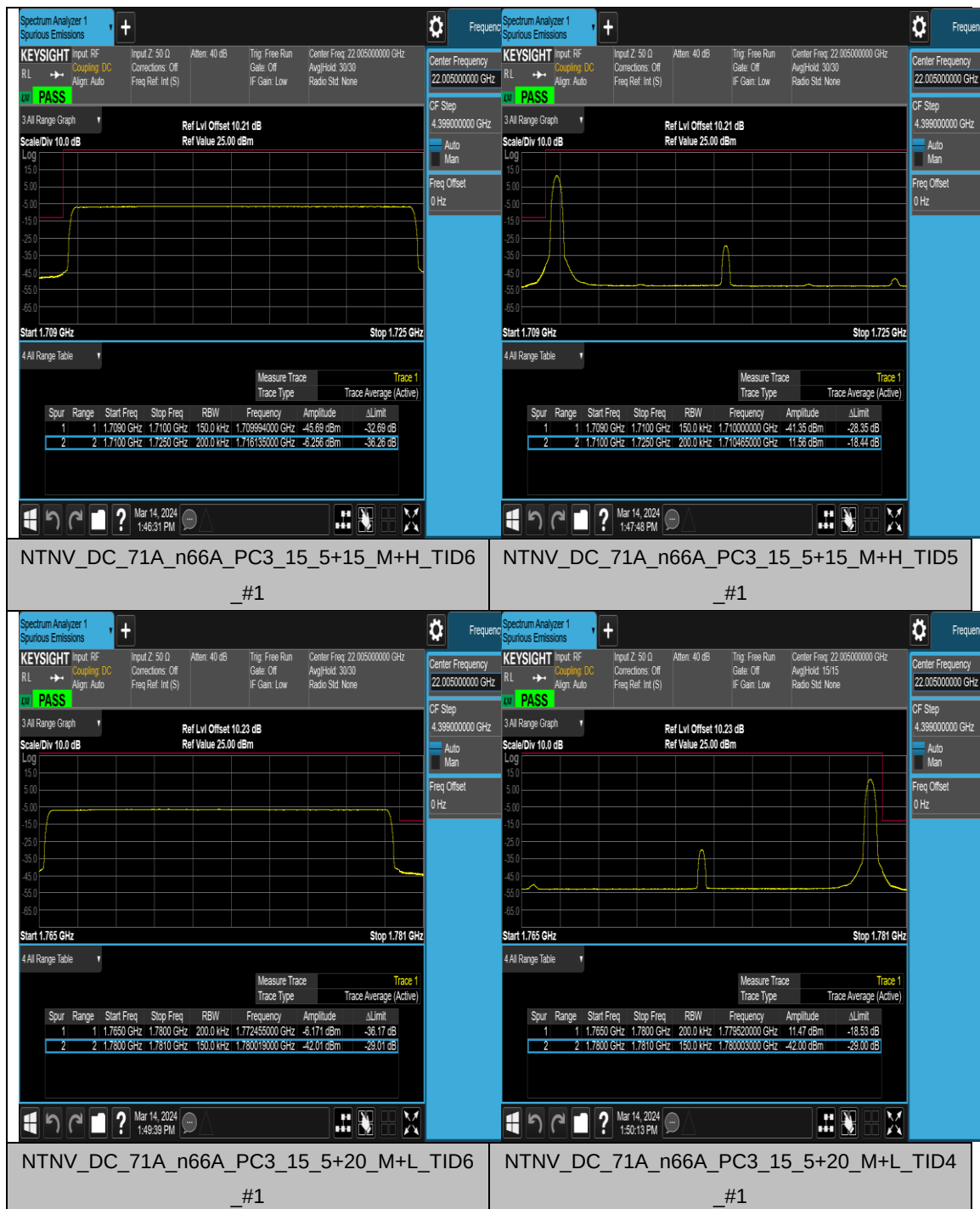
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.39	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-72.13	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-52.21	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-35.81	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-45.38	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.25	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.56	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.33	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-35.19	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.13	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.59	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-71.47	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-50.61	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.65	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.54	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.54	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-62.98	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-70.66	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-52.61	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-35.44	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-43.71	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-45.33	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.10	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.21	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.25	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-35.05	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.61	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.41	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.98	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.29	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-50.52	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.70	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.55	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.45	-13	PASS

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DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.64	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-73.21	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-52.17	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-43.39	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-45.31	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-63.48	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-72.86	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-51.26	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.69	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.20	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.51	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.95	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.28	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.63	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.55	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.34	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-63.17	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-71.90	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-52.41	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-37.77	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-43.28	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-45.34	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.17	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.44	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.42	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.34	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.04	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.06	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.68	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.30	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.46	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-72.04	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-50.20	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-31.74	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-45.32	-13	PASS

DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.03	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.40	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-52.06	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.65	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.47	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.34	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.47	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.60	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-51.60	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-36.20	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.81	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.30	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.63	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-71.89	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-52.40	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-31.23	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-45.33	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.95	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-72.42	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-52.02	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.75	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.78	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.32	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.88	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.68	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-50.55	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-36.20	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.66	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.37	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.59	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-72.66	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-38.15	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-34.19	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-45.30	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.65	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-71.40	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-42.82	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.75	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.28	-13	PASS

DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.95	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.67	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.57	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.77	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.43	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.31	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.77	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-71.91	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-52.31	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-33.56	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-45.24	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-63.14	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-73.35	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.62	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.50	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.56	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.50	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.21	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.74	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.52	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.33	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.81	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-71.37	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-51.93	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-37.67	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-43.63	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-45.34	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.17	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-70.71	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.46	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.59	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.35	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-61.97	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-71.90	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.66	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.57	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.32	-13	PASS

DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.70	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-71.65	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-52.14	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-37.68	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-45.28	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.78	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.82	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-51.00	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.40	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.13	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-73.39	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-52.56	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.57	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.23	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.45	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-71.89	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-51.10	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-30.58	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-43.71	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-45.39	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-62.97	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.39	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.80	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.68	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.57	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.37	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.45	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-74.10	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.36	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.70	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.62	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.37	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-62.43	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-73.23	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-52.19	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-31.72	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-43.78	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-45.22	-13	PASS

DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.76	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-73.51	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.14	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.62	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.76	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.46	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.70	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.99	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-52.54	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.62	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.30	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-61.78	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-72.94	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-52.46	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-32.86	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-45.42	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-62.23	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-73.80	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-51.95	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.63	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.62	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.23	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-61.74	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-70.63	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.32	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.69	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.51	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.39	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.89	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-70.98	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-51.75	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-33.05	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-43.32	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-45.31	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-61.18	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-71.90	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-49.93	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.54	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.70	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.22	-13	PASS

DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.64	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-71.92	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-51.48	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.72	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.47	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-62.57	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-72.57	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-52.30	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-43.52	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-45.36	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-62.22	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-71.59	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-50.52	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.49	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.23	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.16	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.83	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-52.59	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.77	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.55	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.42	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-62.43	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-73.25	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-52.40	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-43.55	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-45.40	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-61.93	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-72.18	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-52.07	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.51	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.13	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-62.10	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-70.05	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-51.99	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.72	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.66	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.33	-13	PASS

DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-62.17	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-71.36	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-51.98	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-30.81	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-45.39	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-61.75	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-71.92	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-51.72	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.70	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.61	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.33	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-62.37	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.31	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-51.72	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.51	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.35	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-61.60	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-71.93	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-51.89	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-30.15	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-43.55	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-45.30	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-62.50	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-74.05	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.53	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.66	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.49	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.41	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.52	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-71.66	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-51.44	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.68	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.74	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.38	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-62.00	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-72.32	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-52.28	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-32.23	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-43.60	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-45.32	-13	PASS

DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-63.05	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-72.56	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-52.06	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-37.64	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-43.59	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-45.42	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-62.84	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-72.56	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-52.23	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-37.74	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-43.58	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-45.45	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-61.98	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-72.64	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-52.50	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-30.89	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-43.64	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-45.35	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-62.68	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-72.95	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-52.47	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-37.63	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-43.56	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-45.36	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-62.50	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-72.06	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-52.57	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-37.53	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-43.53	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-45.49	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-61.71	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-73.78	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-52.23	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-37.60	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-43.57	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-45.46	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-60.89	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-73.61	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-52.29	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-37.73	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-43.76	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-45.38	-13	PASS

DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-62.60	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-72.57	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-51.44	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-37.68	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-43.53	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-45.47	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-63.19	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-71.80	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-51.96	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-37.65	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-43.75	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-45.37	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-62.71	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-71.65	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-51.73	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-37.72	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-43.75	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-45.46	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-61.99	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-72.46	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-50.53	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-37.69	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-43.57	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-45.46	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-61.77	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-72.93	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-50.55	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-32.25	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-43.49	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-45.28	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-63.12	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-72.69	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-50.27	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-37.69	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-43.46	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-45.38	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-61.40	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-72.19	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-52.31	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-37.62	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-43.72	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-45.43	-13	PASS

DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-61.87	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-73.05	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-50.77	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-31.70	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-43.69	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-45.38	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-61.47	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-70.41	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-50.63	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-37.70	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-43.70	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-45.40	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-62.73	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-72.51	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-37.66	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-37.61	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-43.45	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-45.43	-13	PASS