

5GHz AUDIO TRANSCEIVER

PANDA / ATM210

DATASHEET

REV 00

June 10, 2019



SEONG JI INDUSTRIAL CO., LTD

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1. Revision History

No.	Revision	Content	Date	Revision Page
1	00	Initial release	2019-06-10	

2. Scope

This document is the specifications of 5GHz wireless audio transceiver module, ATM210.

3. Product Information

3-1. Product Information

ITEMS	DESCRIPTION
Manufacturer	SEONG JI INDUSTRIAL CO.,LTD
Product type	Wireless Audio Transceiver
Product application	Mono/Stereo wireless audio system
Project Name	PANDA
Model Name	ATM210
Part Number	WSATM210x00
Regulatory Certification	
● FCC	2AS8LATM210
● IC	25119-ATM210
● CE	TBD

3-2. Part Number

W	S	A	T	M	2	1	0	x	0	0
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)	(11)

No.	EXPLANATION
(1),(2)	Wireless Solution
(3),(4)	Application (AT: Audio Transceiver)
(5)	Type (M:Module)
(6)	Group model : Main Chip (2:Avnera AV5100)
(7)	Development Code (0: w/PA, 1: w/o PA)
(8)	Development Code (0:default, 1: expansion)
(9)	Customer Code
(10),(11)	Firmware application 00 : TX 10 : RX

4. Introduction

The ATM210 is 5GHz AUDIO TRANSCEIVER that optimized for building point to multi-point digital wireless audio solutions such as wireless subwoofers and wireless rear speakers in home theater system. This wireless audio module provides a highly-integrated and flexible wireless audio solution based on the AV5100 SoC.

The AV5100 SoC has the VMI RF protocol, providing a excellent coexistence performance and also provide the capabilities of world-class wireless audio protocol by supporting increased digital I/O and OTA (over-the air) signal routing flexibility to accommodate a wide range of wireless audio application. It features low fixed latency, multiple OTA audio configurations, uncompressed CD quality mono or stereo audio, excellent interference immunity, and inherent coexistence with WIFI.

The IC contains all the necessary radio transceiver and digital baseband circuitry to form a complete digital wireless node without the need for external processing as well as including power management and analog circuitry.

The IC is able to support both the lower and upper bands of 5GHz band RF spectrum, enabling worldwide coverage.

The highly integrated nature of the AV5100 transceiver IC results in few external components being required for the ATM210 module design. 2 printed PCB antennas are used to achieve increased range, and to achieve antenna spatial diversity. The extended-range RF path consists of the antennas, associated tuning components, shield can, the RF switch, and two baluns, one connected to each of the RF input/output ports on the AV5100 IC.

A 16MHz crystal oscillator generates the AV5100 fundamental system clock used as the basis for all RF and digital audio clocks.

A 2Mb flash memory chip is used to store the module's application firmware. The AV5100 is able to boot from internal ROM upon first power up, which enables programming the flash chip with the application firmware through USB. In addition, Over-the-air Firmware upgrade capability can be enabled through the application firmware.

The module can be controlled from an external host device via the I2C Slave or the SPI Slave data interfaces. The I2C master port allows the module to control other system audio devices such as a sub-woofer amplifier system without having to add another MCU to the product design.

Up to 9 additional GPIOs are available on the ATM210 module (not including I2C and I2S signals) for implementing different UI features on the target application.

The ATM210 module is powered from +5V supply input and the internal AV5100 IC is powering the internal +3.3V output LDO.

The ATM210 module is available in two variations; digital input transmitter or digital output receiver.

There are three available I2S digital audio data inputs/outputs, each of these can be configured to operate as either a master or a slave – depending on the application, the I2S ports can operate simultaneously as either inputs or outputs. When configured as slaves, the I2S inputs/outputs can be independently clocked by up to two external masters. In addition, MCLK can be output from the module to provide a reference clock source to an external ADC or DAC. MCLK can also be input to the module to provide a reference clock from an external source.

The hardware for the audio input (transmit) and audio output (receive) versions of the module is identical and only the firmware loaded onto the module determines its function.

Features

- Product Size
 - Module Size (mm): 40.4 (W) X 26.0 (L) X 3.2 (H)
 - PCB size (mm): 40.0 (W) X 2.6.0 (L) x 1.1 (H)
 - Shield can (mm): 24.7 (W) X 21.9 (L) X 2.0(H)
 - Antenna Type : PCB Printed Antenna
- Audio Interfaces
 - I2S Digital Input/Output interface with >93dB end-to-end digital audio path
- Frequency range : 5.15~5.25GHz, 5725-5.825 GHz, continuous dynamic frequency selection
- Low, fixed latency (14.8ms typ. for stereo)
- 3Mbps OTA data rate
- Multiple OTA audio configurations
- Forward error correction coding, error detection, and audio-specific error concealment
- Auto-search/sync and dynamic channel selection
- Capability to detect and avoid wideband interferences such as 5GHz band WLAN
- Sample rate converter: Support for 32 - 96kHz input sample rates
- Dual printed PCB diversity antennas for multipath and fading migrations
- Wireless Range (typ.)
 - LOS (Line of Sight) range : <110m
- Control interfaces: I2C, SPI
- Up to 9 additional GPIOs
- Customizable firmware for simple, low-cost, sub-woofer amplifier implementations
- Dual printed PCB diversity antennas for multipath and fading mitigation
- RF parts can-shield
- 24pin FPC connector
- To be certified FCC/IC & CE compliance

Applications

- Wireless Subwoofers
- Stereo Wireless Rear Speakers
- Soundbar / Audio Video Receiver / Home theater system
- Mono/Stereo Audio Channel Transmission

5. Electrical Specifications

5-1. Absolute Maximum Ratings

Absolute Maximum Ratings (AMR) are stress ratings only. AMR corresponds to the maximum value that can be applied without leading to instantaneous or very short-term unrecoverable hard failure (destructive breakdown). Stresses beyond those listed under AMR may cause permanent damage to the device.

Functional operation of the device at these or any other conditions beyond those indicated under “Recommended Operating Range” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may adversely affect device reliability.

Device functional operating limits and guaranteed performance specifications are given under Electrical Characteristics at the test conditions specified.

CONDITION	MIN	MAX
+5V Supply Voltage Input	-0.3V	6.0V
Input Voltage Range – Digital Inputs	-0.3V	3.6V
Input Voltage Range – Analog Inputs	-0.3V	3.6V
Operating Temperature	-40°C	+85°C
Storage Temperature	-40°C	+85°C
Static Discharge Voltage*	TBD	TBD

Notes;

* System level ESD : IEC 61000-4-2; C = 150pF, R = 330Ω

5-2. Recommended operating Range

PARAMETER	MIN	TYP	MAX	UNIT
VDD, +5V Supply pin voltage	4.5	5.0	5.5	V
Ambient Temperature (TA)	0		60	°C
RESET pin hold time	10			ms
Power Supply Rise Time (to 3.0V)	0		10	ms

5-3. Electrical Characteristics – DC

Typical specifications at TA = 25°C, VDD = 5.0V

PARAMETER	CONDITION	MIN	TYP	MAX	UNIT
Supply Current (IVDDA)	Shutdown (chip disabled)			1	uA
	Standby (also USB suspend)			TBD	mA

	Continuous TX		110		mA
	Linked status for TX		49		
	Searching for TX		46		
	Continuous RX		110		
	Linked status for RX		93		
	Searching for RX		93		
CMOS I/O Logic Levels – VDDIO 3.3V	Input Voltage Logic Low, VIL			0.6	V
	Input Voltage Logic High, VIH	VDDIO - 0.6V			
	Output Voltage Logic Low, VOL			0.3	
	Output Voltage Logic High, VOH	VDDIO - 0.3V			

5-4. Electrical Characteristics – RF TX

Operating Conditions: VDD = 4.5 to 5.5V, TA = 0°C to +60 °C, RF Freq = 5725-5825MHz, measured to the RF conducted 2 ports. Typical specifications at TA = 25°C, VDD = 5.0V

PARAMETER	CONDITION	MIN	TYP	MAX	UNIT
RF Channel Frequency Range	Low band	5150		5250	MHz
	High band	5725		5825	
Channel Bandwidth [OBW]	SSC (Single Sub-Carrier)		2		MHz
	DSC (Dual Sub-Carrier)		4		
TX Output power	SSC / Low band		5		dBm
	DSC / Low band		4		
	SSC / High band		7		dBm
	DSC / High band		6		
TX Spurious(harmonic)	2nd			-55	dBm
	3rd			-55	dBm
RF I/O Impedance	ANT0, ANT1		50		ohm
LO leakage			-20		dBc

5-5. Electrical Characteristics – RF RX

Operating Conditions: VDD = 4.5 to 5.5V, TA = 0°C to +60 °C, RF Freq = 5725-5825MHz, measured to the RF conducted 2 ports. Typical specifications at TA = 25°C, VDD = 5.0V

PARAMETER	CONDITION	MIN	TYP	MAX	UNIT
RF Channel Frequency Range	Low band	5150		5250	MHz
	High band	5725		5825	
RX Sensitivity*	SSC (Single Sub-Carrier)		-89		dBm
	DSC (Dual Sub-Carrier)		-86		
Max input signal	LNA = low gain mode, min IF gain		-5		
Out-of-band blocker level	<5150 MHz, >5850 MHz		-45		
	2400-2483.5 MHz		-20		
Spurious RF outputs	5150-5850 MHz			TBD	dBm
	<5150 MHz, >5850 MHz			TBD	
RF I/O Impedance	ANT0, ANT1		50		ohm

* The sensitivity been defined with BER <= 0.002.

5-6. Electrical Characteristics – RF PLL

Operating Conditions: VDD = 4.5 to 5.5V, TA = 0°C to +60 °C, RF Freq = 5725-5825MHz, measured relative to the RF balun single-ended I/O. Typical specifications at TA = 25°C, VDD = 5.0V

PARAMETER	CONDITION	MIN	TYP	MAX	UNIT
RF Channel Frequency Range	Low band	5150		5250	MHz
	High band	5725		5825	
RF Channel frequency resolution (raster)			1		MHz
Local Frequency error			5	10	ppm
Crystal Oscillator Frequency	External crystal		16		MHz
RF I/O Impedance	ANT0, ANT1		50		ohm

5-7. Electrical Characteristics – Audio C/CS

PARAMETER	CONDITION	MIN	TYP	MAX	UNIT
Frequency Response (-3dB)	16bit audio, 11KSps over-the-air sample rate	20		5K	Hz

	16bit audio, 14.8KSps over-the-air sample rate	20		6.5K	
	16bit audio, 14.8KSps over-the-air sample rate	20		10K	
	16bit audio, 14.8KSps over-the-air sample rate	20		13K	
	16bit audio, 14.8KSps over-the-air sample rate	20		20K	
Gain Flatness	0dB Input / Output Gain		+/- 0.2		dB
SNR	I2S Input / Output	93*			dB
THD+N			94		dB

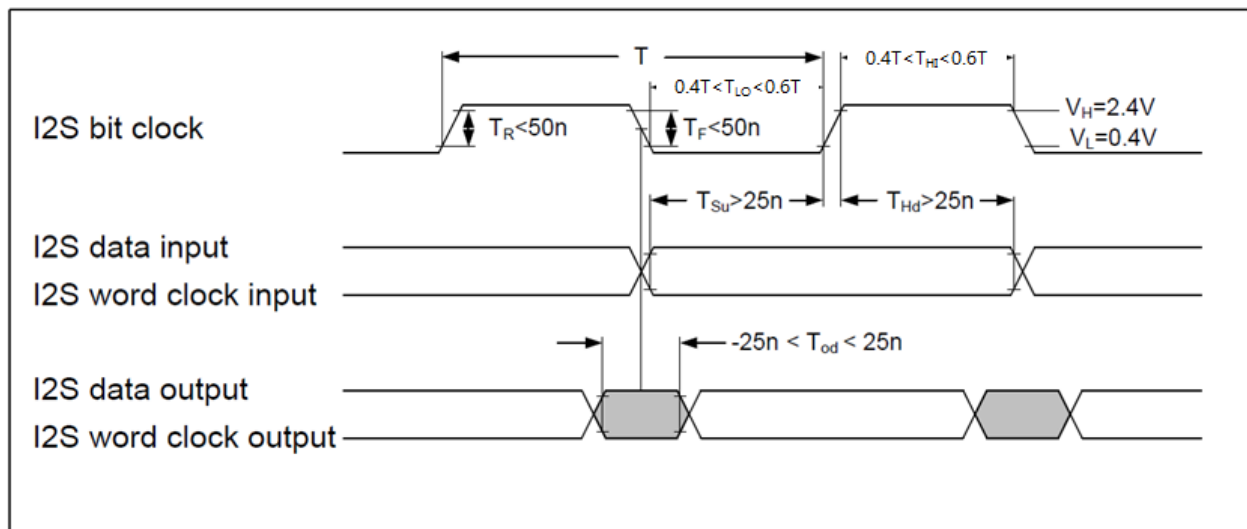
* 16bit audio, all OTA sample rate. OTA 12-bit path for voice is possible, but will limit the SNR to 72dB

5-8. AV5100 Rate Converter Characteristics

SRC Block	Input Rates	Output Rates	SNR (dB)	SRC BW (-3dB)
SRC 0 (Audio)	<u>I2S</u> 32-96K <u>USB</u> 8K-48K <u>ECU (TX)</u> "11K" "14.8K" "22K" "29.6K" "44K"	<u>I2S</u> Maser : 48K Slave : 44.1K-96K <u>ECU (TX)</u> "11K" "14.8K" "22K" "29.6K" "44K"	All rate support 16bit, > 93dB	Actual bandwidth is dependent on the lower of the input or output rate <u>Output BW vs OTA</u> "11K" = 5KHz "14.8K" = 6.5KHz "22K" = 10KHz "29.6K" = 13KHz "44K" = 20KHz
SRC 1 (LFE)	<u>I2S</u> 32-96K <u>USB</u> 8K-48K <u>ECU (TX)</u> "11K" "14.8K" "22K" "29.6K" "44K"	<u>I2S</u> Maser : 48K Slave : 44.1K-96K <u>ECU (TX)</u> "11K" "14.8K" "22K" "29.6K" "44K"	All rate support 16bit, > 93dB	Actual bandwidth is dependent on the lower of the input or output rate <u>Output BW vs OTA</u> "11K" = 5KHz "14.8K" = 6.5KHz "22K" = 10KHz "29.6K" = 13KHz "44K" = 20KHz

SRC 2 (Audio)	<u>I2S</u> 32-96K	<u>I2S</u> Master : 48K Slave : 44.1K-96K	All rate support 16bit, > 93dB, but the OTA 12bit path will limit SNR to 72dB	Actual bandwidth is dependent on the lower of the input or output rate <u>Output BW vs OTA</u> "11K" = 5KHz "14.8K" = 6.5KHz "22K" = 10KHz "29.6K" = 13KHz "44K" = 20KHz
	<u>USB</u> 8K-48K	<u>USB</u> 8K-48K		
	<u>ECU (TX)</u> "14.8K"	<u>ECU (TX)</u> "14.8K"		

5-9. I²S Communication Interface Timing

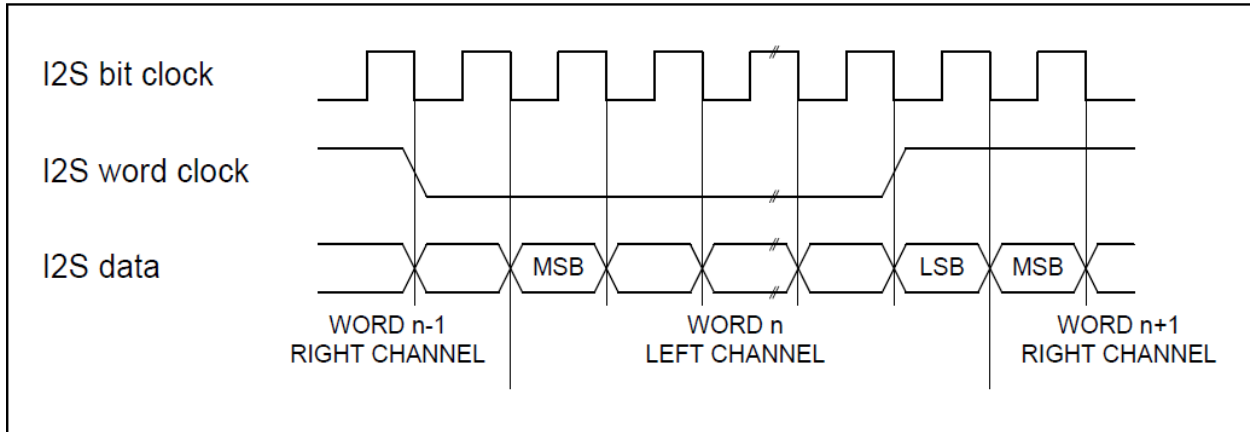


I²S Interface Timing

	Parameter	MIN	TYP	MAX	UNIT	Notes
V _L	Low voltage level	-0.3	0	0.4	V	
V _H	High voltage level	2.4	3.3	3.6	V	
T	Clock period		325.5		ns	1/3.072MHz
T _{LO}	Clock low period	0.4T		0.6T		
T _{HI}	Clock high period	0.4T		0.6T		
T _R	Rise time			50	ns	
T _F	Fall time			50	ns	
T _{Su}	Setup time	25			ns	

T_{Hd}	Hold time	25			ns	
T_{Od}	Output delay	-25		25	ns	
	Bit clocks/Word clock		64			

I2S protocol is "I2S Justified" as shown below.



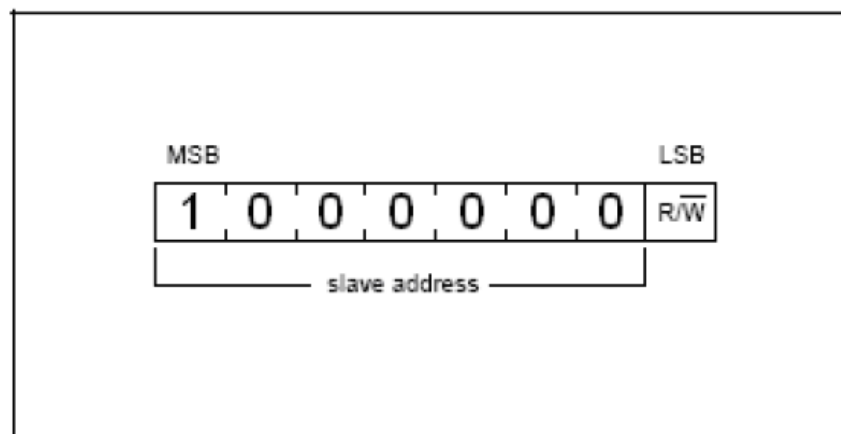
*The timing specified for the rise and fall times represents the edge rates on the module itself. The rise and fall times of the I2S signals are determined by ESD/EMI mitigation components on the modules, as well as external loading, and will be higher than the specified numbers

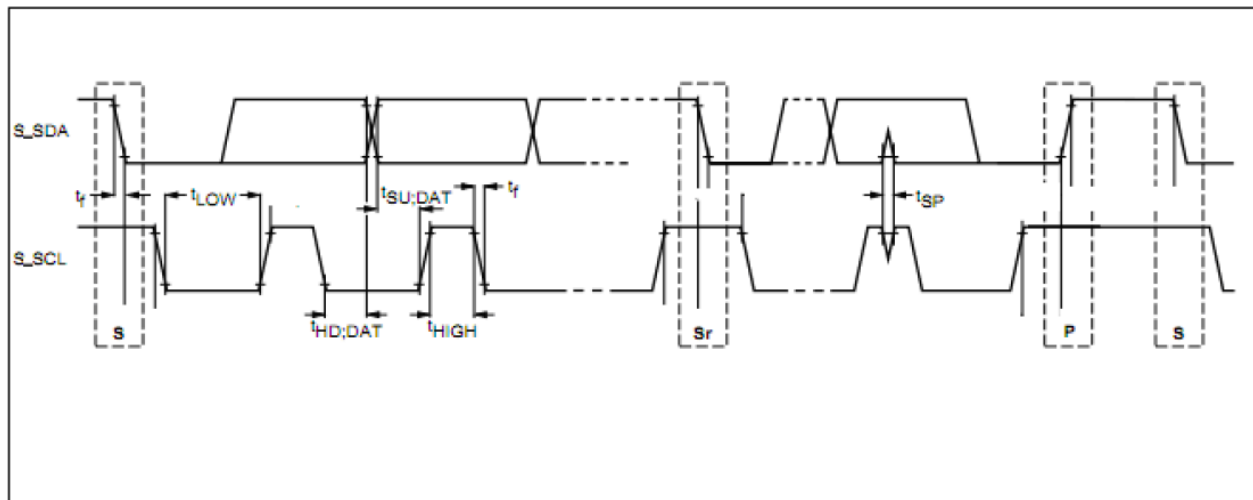
5-10. I²C Slave Communication Interface Timing

The ATM210 has both I2C slave and master interfaces available with their respective pins S_SCL, S_SDA and M_SCL, M_SDA. The interfaces operate in I2C fast-mode and can receive and transmit at up to 400 kbit/s. Bytes are 8 bits long and are transferred with the most significant bit (MSB) first. Each byte has to be followed by an acknowledge bit. The SWA52 will apply clock-stopping (by holding the clock line S_SCL LOW to force the master into a wait state) if necessary due to internal high-priority tasks.

The slave/master interface can be used both for writing (e.g. sending commands) or reading (e.g. requesting status). An additional GPIO pin on the SWA52 (Ex. GPIO24), can be used to notify the I2C master when a pending message is ready to be sent.

The ATM210 slave interface responds to the 7-bit slave address 1000000 (0x40) as shown in picture below.



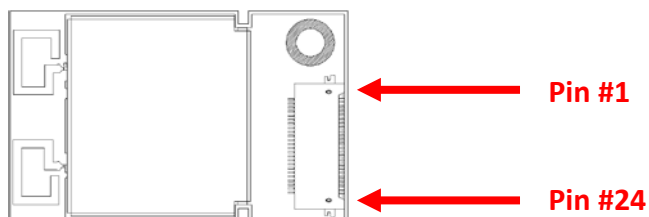


I²C Slave Interface Timing (Fast-Mode)

	Parameter	MIN	MAX	UNIT
V _{IL}	Low level input voltage	-0.3	0.8	V
V _{IH}	High level input voltage	2.0	3.6	V
V _{OH}	Low level output voltage At 1mA sink current	0	0.4	V
t _{of}	Output fall time from V _{IHmin} to V _{ILmax} with a bus capacitance from 10pF to 400pF	0	250	ns
t _{SP}	Pulse width of spike which must be suppressed by the input filter	0	50	ns
f _{SCL}	S_SCL clock frequency	0	400	KHz
t _{LOW}	Low period of the S_SCL clock	1.3		ns
T _{HIGH}	High period of the S-SCL clock	0.6		ns
t _{HD;DAT}	Data hold time	100		ns
T _{SU;DAT}	Data set-up time	100		ns

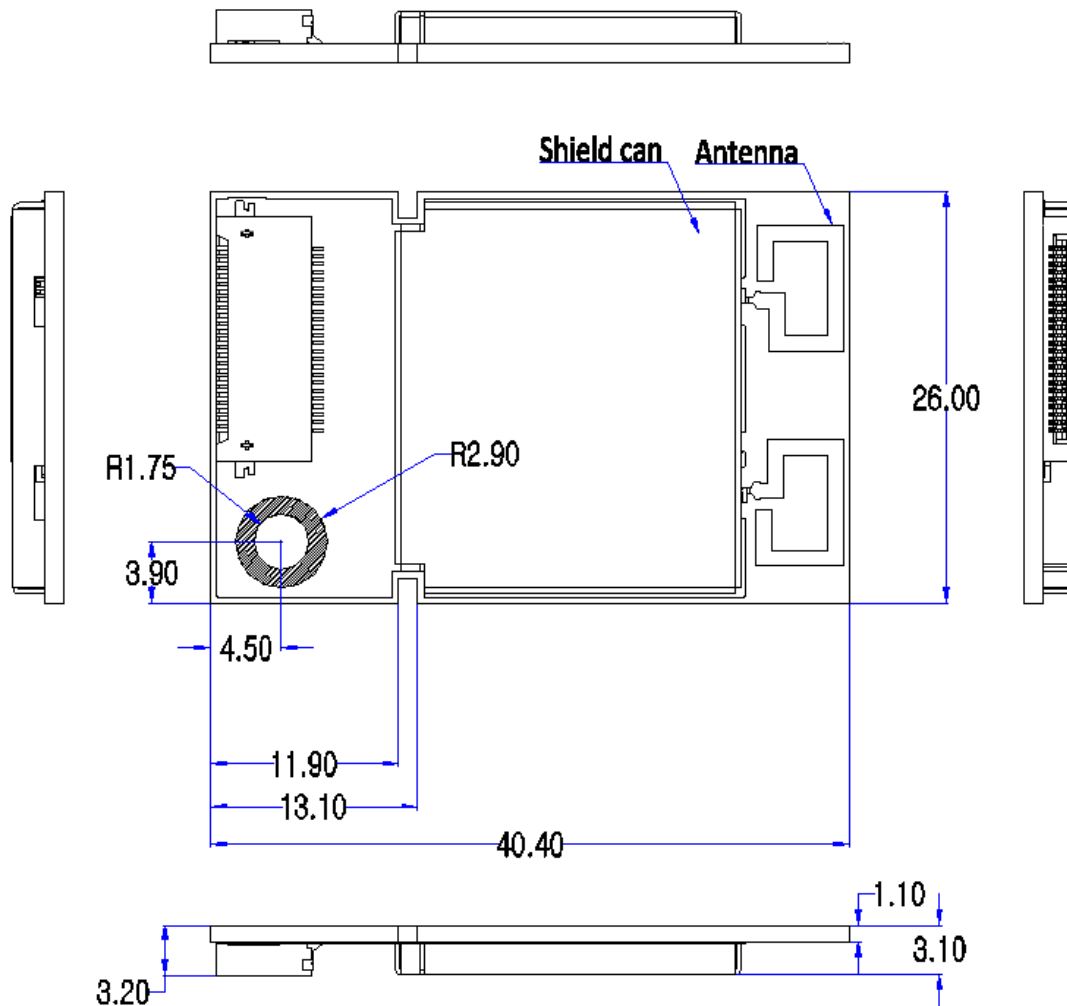
6. I/O Connector Pin Description

No	Pin Name	I/O	ATM210-TX Pin Description	ATM210-RX Pin Description
1	GPIO2 /S_SSB	I/O	GPIO or SPI slave chip select	GPIO or SPI slave chip select
2	GPIO3 /S_SCLK	I/O	GPIO or SPI slave serial clock	GPIO or SPI slave serial clock
3	GPIO4 / I2CS_SDA/S_MOSI	I/O	GPIO, I2C slave serial data or SPI slave data In	GPIO, I2C slave serial data or SPI slave data In
4	GPIO5 / I2CS_SCL/S_MISO	I/O	GPIO, I2C slave serial clock or SPI slave data out	GPIO, I2C slave serial clock or SPI slave data out
5	GPIO16 / I2CM_SDA	I/O	GPIO or I2C master serial data	GPIO or I2C master serial data
6	GPIO17 / I2CM_SCL	I/O	GPIO or I2C master serial clock	GPIO or I2C master serial clock
7	GPIO20 /LINK_LED	I/O	GPIO or Link_LED output	GPIO or Link_LED output
8	GPIO21/ PAIR	I/O	GPIO or Input from PAIR Button	GPIO or Input from PAIR Button
9	GPIO18 /BCK1	I/O	GPIO or I2S port1 bit clock	GPIO or I2S port1 bit clock
10	GPIO19 / WCLK1	I/O	GPIO or I2S port1 word clock	GPIO or I2S port1 word clock
11	GPIO10 /MCLK	I/O	GPIO or Master clock out	GPIO or Master clock out
12	GND	GND	GND	GND
13	GPIO11 / BCK0	I/O	GPIO or I2S port0 bit clock	GPIO or I2S port0 bit clock
14	GPIO12 /WCLK0	I/O	GPIO or I2S port0 word clock	GPIO or I2S port0 word clock
15	GPIO13 /ADAT0	I/O	GPIO or I2S port0 audio data	GPIO or I2S port0 audio data
16	GPIO14 / ADAT1	I/O	GPIO or I2S port1 audio data	GPIO or I2S port1 audio data
17	GPIO15 / ADAT2/CEN	I/O	GPIO, I2S port2 audio data or Chip enable	GPIO, I2S port2 audio data or Chip enable
18	GPIO22 / D+	I/O	GPIO or USB data plus	GPIO or USB data plus
19	GPIO23 / D-	I/O	GPIO or USB data plus	GPIO or USB data plus
20	GPIO24	I/O	GPIO	GPIO
21	RESETN_EXT	I	Reset signal active low	Reset signal active low
22	GND	GND	GND	GND
23	VDD	Supply	+5.0V input supply voltage	+5.0V input supply voltage
24	VDD	Supply	+5.0V input supply voltage	+5.0V input supply voltage

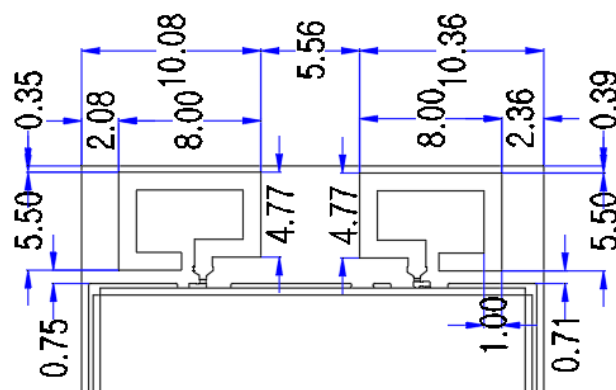


7. Mechanical Specifications

7-1. Module dimension



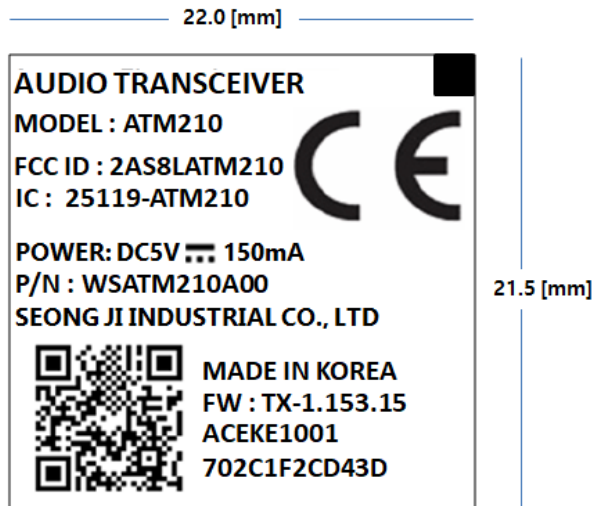
7-2. Antenna dimension



8. Label Specification

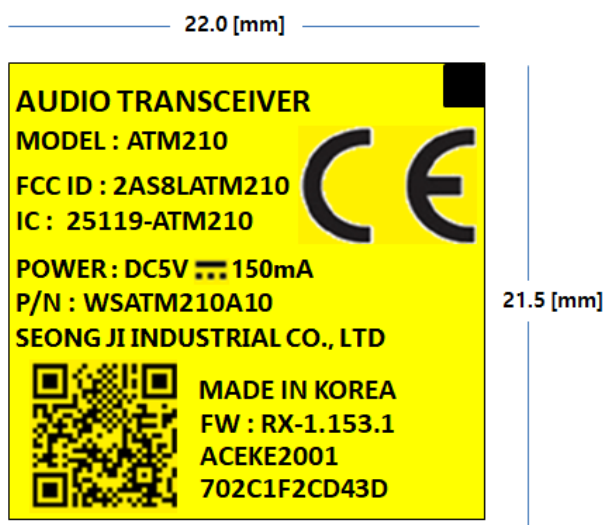
ATM210 TX Module Label

Size: 22 X 21.5 [mm]
color : WHITE



ATM210 RX Module Label

Size: 22 X 21.5 [mm]
color : YELLOW



9. Certificate Statement

FCC & IC Statement

This AUDIO TRANSCEIVER has been granted modular approval for mobile applications. OEM integrators for host products may use the module in their final products without additional FCC / IC (Industry Canada) certification if they meet the following conditions. Otherwise, additional FCC / IC approvals must be obtained.

- The host product with the module installed must be evaluated for simultaneous transmission requirements.
- The user's manual for the host product must clearly indicate the operating requirements and conditions that must be observed to ensure compliance with current FCC / IC RF exposure guidelines.
- To comply with FCC / IC regulations limiting both maximum RF output power and human exposure to RF radiation, use this module only with the included onboard antenna.
- A label must be affixed to the outside of the host product with the following statements:

Product Name : AUDIO TRANSCEIVER

Contains FCC ID: **2AS8LATM210**

Contains IC: **25119-ATM210**

The final host / module combination may also need to be evaluated against the FCC Part 15B criteria for unintentional radiators in order to be properly authorized for operation as a Part 15 digital device.

This equipment has been tested and found to comply within the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation.

This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna
- Increase the separation between the equipment and receiver
- Connect the equipment into an outlet on a different circuit from that to which the receiver is connected
- Consult the dealer or an experienced radio/TV technician for help.

Device Classifications

Since host devices vary widely with design features and configurations module integrators shall follow the guidelines below regarding device classification and simultaneous transmission, and seek guidance from their preferred regulatory test lab to determine how regulatory guidelines will impact the device compliance. Proactive management of the regulatory process will minimize unexpected schedule delays and costs due to unplanned testing activities.

The module integrator must determine the minimum distance required between their host device and the user's body. The FCC provides device classification definitions to assist in making the correct determination. Note that these classifications are guidelines only; strict adherence to a device classification may not satisfy the regulatory requirement as near-body device design details may vary widely. Your preferred test lab will be able to assist in determining the appropriate device category for your host product and if a KDB or PBA must be submitted to the FCC.

Note, the module you are using has been granted modular approval for mobile applications. Portable applications may require further RF exposure (SAR) evaluations. It is also likely that the host / module combination will need to undergo testing for FCC Part 15 regardless of the device classification. Your preferred test lab will be able to assist in determining the exact tests which are required on the host / module combination.

FCC Definitions

Portable: (§2.1093) — A portable device is defined as a transmitting device designed to be used so that the radiating structure(s) of the device is / are within 20 centimeters of the body of the user.

Mobile: (§2.1091) (b) — A mobile device is defined as a transmitting device designed to be used in other than fixed locations and to generally be used in such a way that a separation distance of at least 20 centimeters is normally maintained between the transmitter's radiating structure(s) and the body of the user or nearby persons. Per §2.1091d(4) In some cases (for example, modular or desktop transmitters), the potential conditions of use of a device may not allow easy classification of that device as either Mobile or Portable. In these cases, applicants are responsible for determining minimum distances for compliance for the intended use and installation of the device based on evaluation of either specific absorption rate (SAR), field strength, or power density, whichever is most appropriate.

Simultaneous Transmission Evaluation

This module has **not** been evaluated or approved for simultaneous transmission as it is impossible to determine the exact multi-transmission scenario that a host manufacturer may choose. Any simultaneous transmission condition established through module integration into a host product **must** be evaluated per the requirements in KDB447498D01(8) and KDB616217D01,D03 (for laptop, notebook, netbook, and tablet applications).

These requirements include, but are not limited to:

- Transmitters and modules certified for mobile or portable exposure conditions can be incorporated in mobile host devices without further testing or certification when:

- The closest separation among all simultaneous transmitting antennas is >20 cm,

Or

- Antenna separation distance and MPE compliance requirements for **ALL** simultaneous transmitting antennas have been specified in the application filing of at least one of the certified transmitters within the host device.

In addition, when transmitters certified for portable use are incorporated in a mobile host device, the antenna(s) must be >5 cm from all other simultaneous transmitting antennas.

- All antennas in the final product must be at least 20 cm from users and nearby persons.

OEM Instruction Manual Content

Consistent with §2.909(a), the following text must be included within the user's manual or operator instruction guide for the final commercial product (*OEM-specific content is displayed in italics.*)

Operating Requirements and Conditions:

The design of **ATM210** complies with U.S. Federal Communications Commission (FCC) guidelines respecting safety levels of radio frequency (RF) exposure for Mobile devices.

Note: In the case where the Host / Module combination has been re-certified the FCCID shall appear in the product manual as follows:

FCCID: **2AS8LATM210**

Mobile Device RF Exposure Statement (If Applicable):

RF Exposure - This device is only authorized for use in a mobile application. At least 20 cm of separation distance between the transmitting antenna device and the user's body must be maintained at all times.

Caution Statement for Modifications:

Caution: Any changes or modifications to the equipment not expressly approved by the party responsible for compliance could void user's authority to operate the equipment.

FCC Part 15 Statement (Only Include if FCC Part 15 is Required on the End Product):

Note: This equipment has been tested and found to comply with the limits for a **Class B** digital device, pursuant to Part 15 of the FCC Rules. (*OEM must follow Part 15 guidelines (§15.105 and §15.19) to determine additional statements required in this section for their device class*)

Note 2 : This device complies with part 15 of the FCC Rules.

Operation is subject to the following two conditions.

- 1) This device may not cause harmful interference, and
 - 2) This device must accept any interference, including interference that may cause undesired operation of the device.
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- a. That module is limited to OEM installation ONLY.
 - b. That OEM integrators are responsible for ensuring that the end-user has no manual instructions to remove or install module.
 - c. That module is limited to installation in mobile or fixed applications, according to Part 2.1091(b).
 - d. That separate approval is required for all other operating configurations, including portable configurations with respect to Part 2.1093 and different antenna configurations.
 - e. That grantee shall provide guidance to the host manufacturer for compliance with Part 15 subpart B requirements.

Information

This device complies with Industry Canada's license-exempt RSSs. Operation is subject to the following two conditions:

- (1) This device may not cause interference; and
- (2) This device must accept any interference, including interference that may cause undesired operation of the device.

Le présent appareil est conforme aux CNR d'Industrie Canada applicable aux appareils radio exempts de licence.

L'exploitation est autorisée aux deux conditions suivantes :

- (1) l'appareil ne doit pas produire de brouillage, et
- (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Cet équipement est conforme aux limites d'exposition aux rayonnements énoncées pour un environnement non contrôlé et respecte les règles des radioélectriques (RF) de la FCC lignes directrices d'exposition et d'exposition aux fréquences radioélectriques (RF) CNR-102 de l'IC. Cet équipement émet une énergie RF très faible qui est considérée conforme sans évaluation du débit d'absorption spécifique (DAS).

CE Statement

Hereby, SEONG JI INDUSTRIAL CO., LTD declares that the radio equipment type AUDIO TRANSCEIVER is in compliance with Directive 2014/53/EU. The full text of the EU declaration of conformity is available at the providing datasheet or approval sheet.

The antenna(s) must be installed such that a minimum separation distance of at least 20 cm is maintained between the radiator (antenna) and all persons at all times. This device must not be collocated or operating in conjunction with any other antenna or transmitter.