

Cover

PLH100.3.2 4WD_Control_Board

Table of Content

Page 1	Cover
Page 2	Change List
Page 3	Block Diagram
Page 4	Power Tree
Page 5	Board PWR
Page 6	SOC PMIC
Page 7	SOC PWR
Page 8	SOC eMMC
Page 9	SOC LPDDR4
Page 10	SOC USB
Page 11	SOC GPIO
Page 12	SOC MIPI
Page 13	SOC MISC
Page 14	WIFI + BT
Page 15	RS485
Page 16	RTC+PPS
Page 17	USB HUB
Page 18	Camera connector
Page 19	Board connectors

Revision History

Rev. Code	Date	By	Description	
V2.0	2025/06/13	Zhiqun Shi		

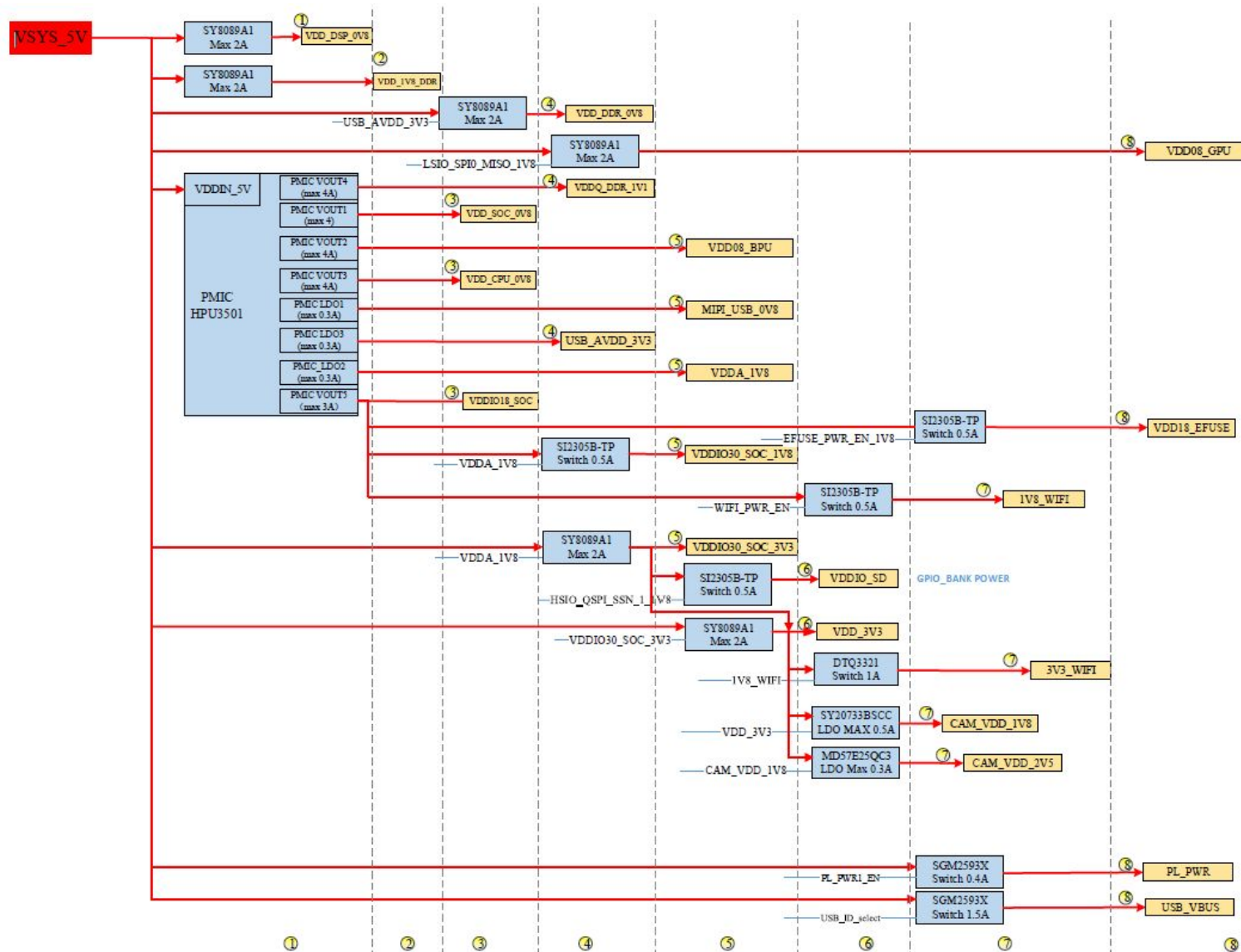


HW ID Change List

Date	HW Version	ADC_Voltage		Resistance state	
2025/04/28	PCB_H100.3.06 X5H + FLC5330SXSA-B	ADC_VINS0 = 1V	ADC_VINS1 = 0.6V	R197 = 16K R868 = 20K	R198 = 16K R190 = 8.2K

Block Diagram

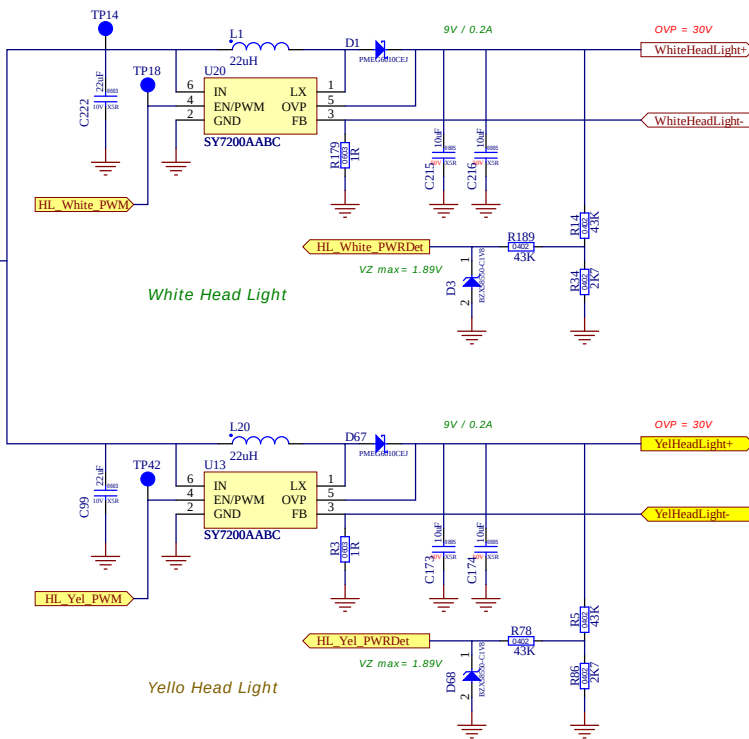
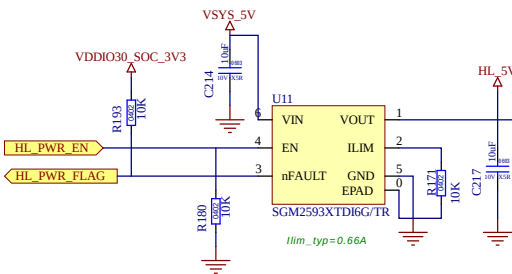
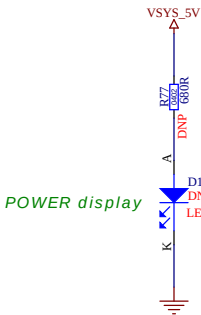
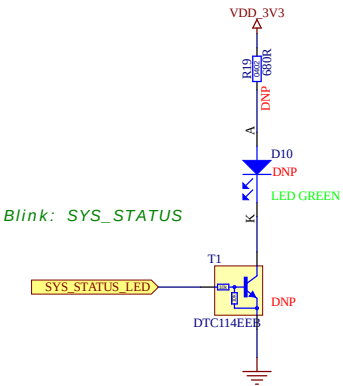
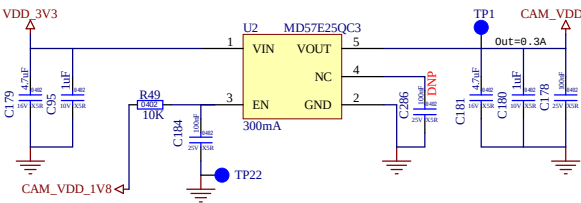
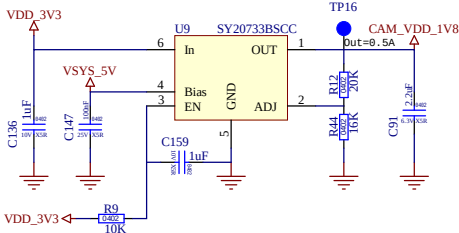
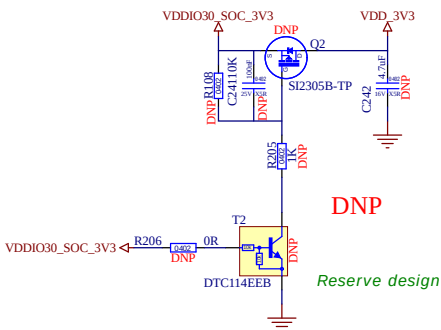
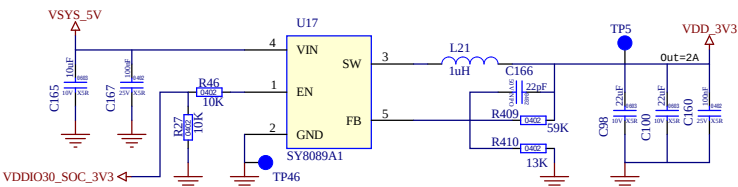
Power Tree



Group 8 Control by Software

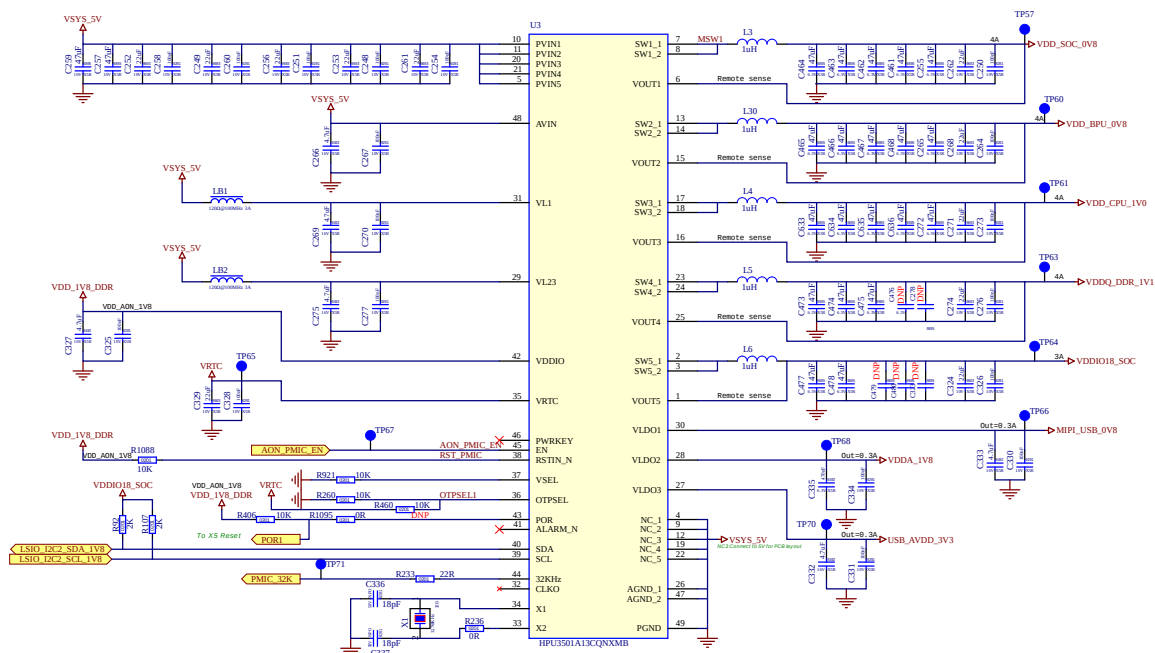
X5 SOC平台电源只关注时序，不关注每组上电时序间隔时间

Board PWR

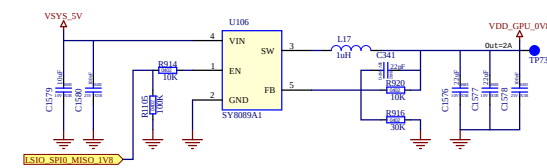
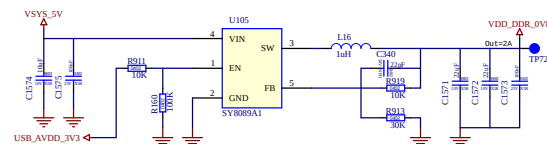
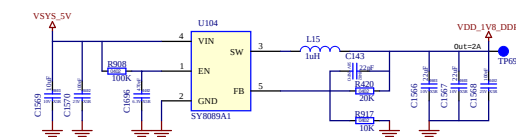
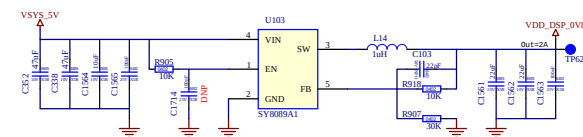
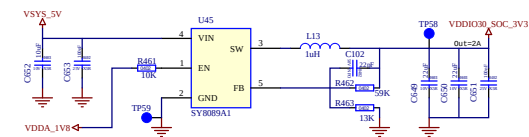
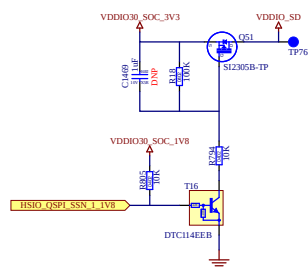
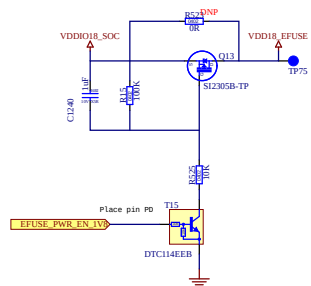
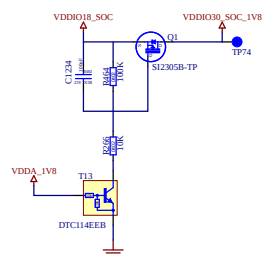


Head Light Status List			
HeadLightDET	HL_PWR_FLAG	HeadLightPWRDet	Status
H	x	x	No Head Light
L	H	H	OK
L	L	L	Light Fault - Open circuit
L	L	x	Light Fault - Short circuit

SOC *PMIC*



Soft boot

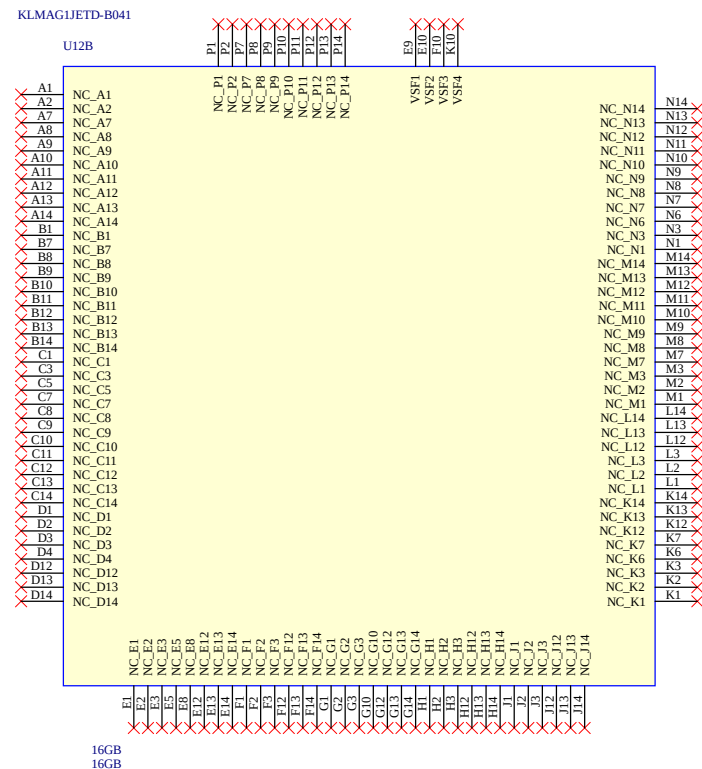
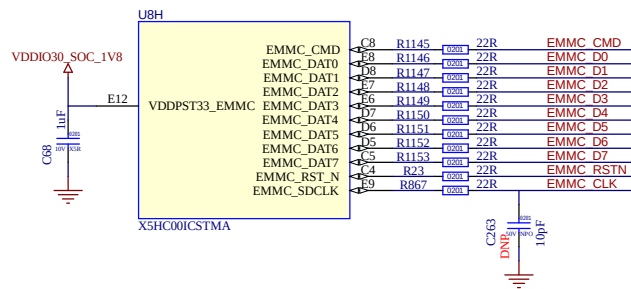
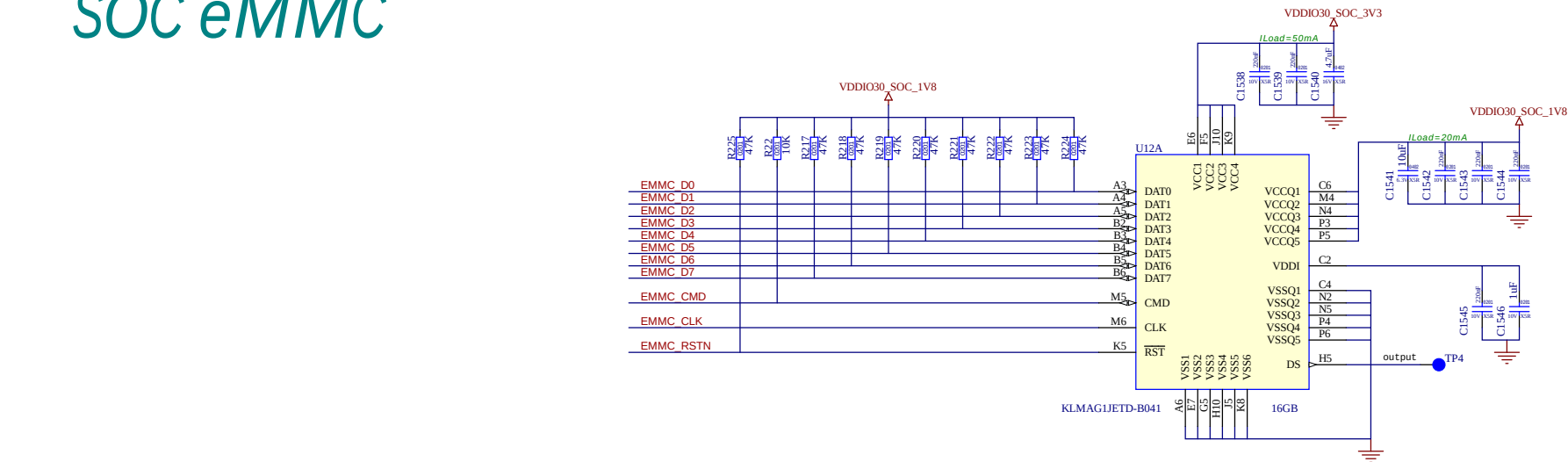


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C

C |

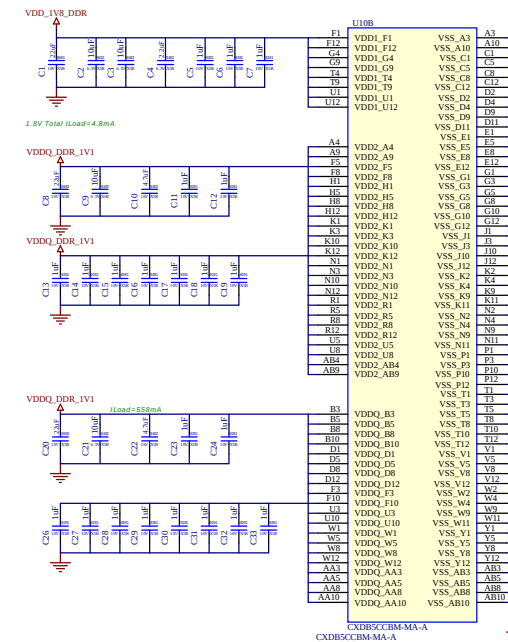
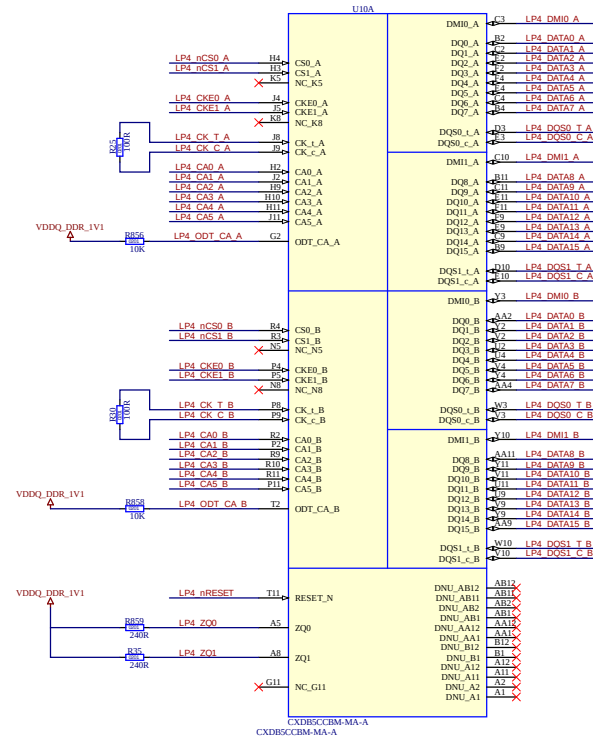
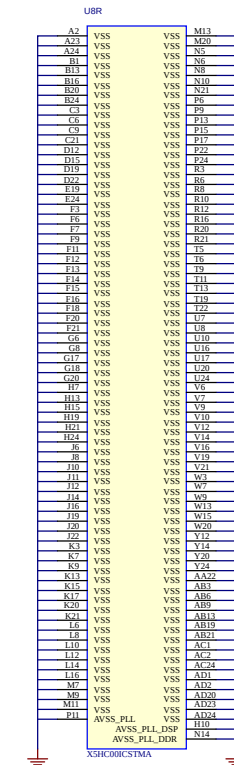
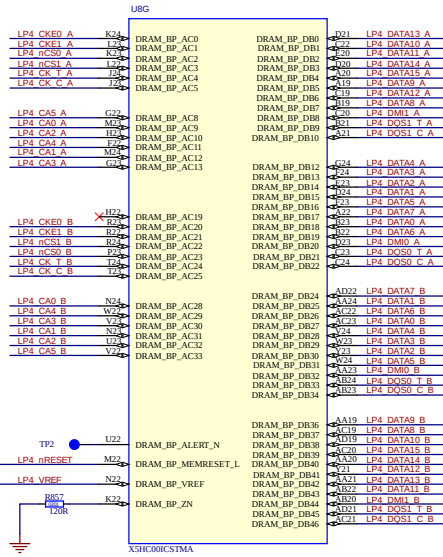
SOC eMMC



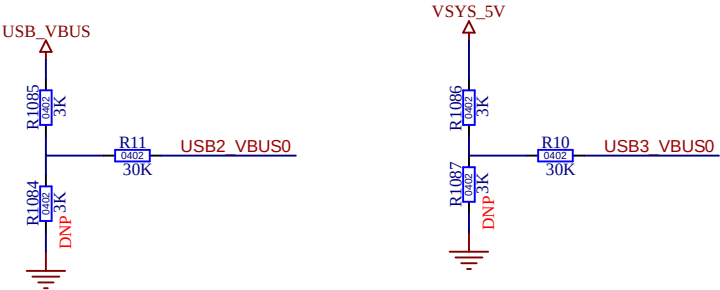
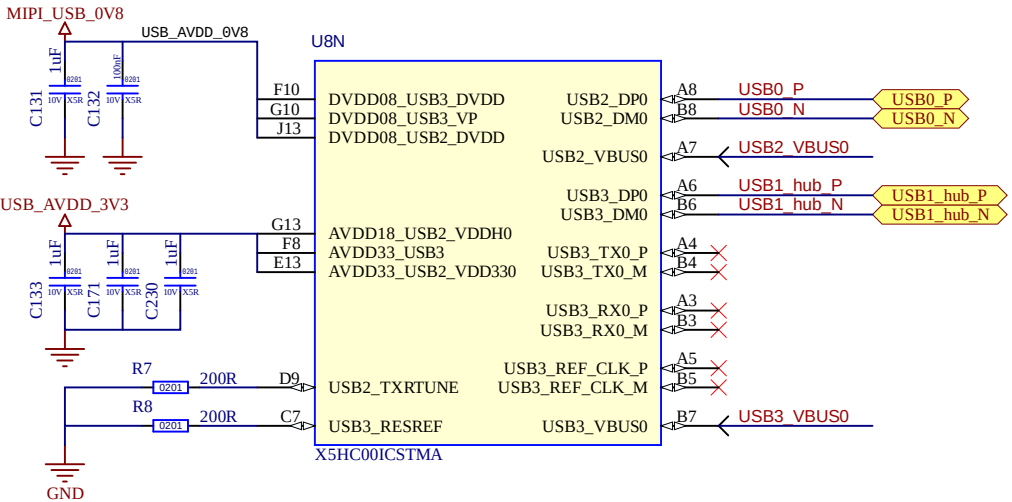
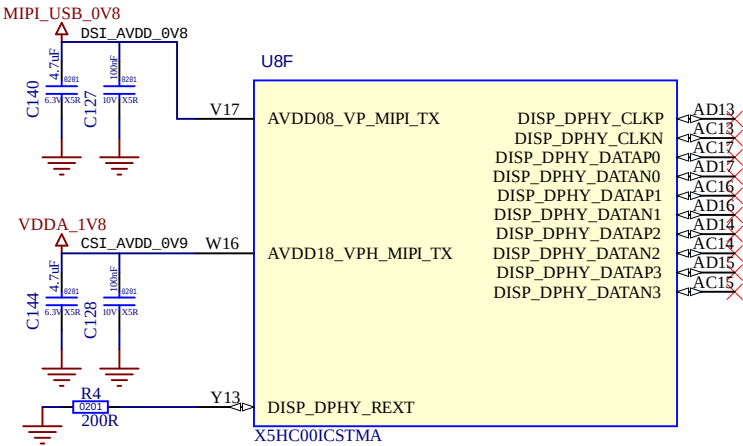
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Date: 2025/6/13	Time: 9:44:18	Sheet 8 of 19
File: 06-SOC eMMC_SchDoc		



LPDDR4



SOC USB



Title **SOC USB**

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Number: **PLH100.3**

Revision: **06**

Date: 2025/6/13

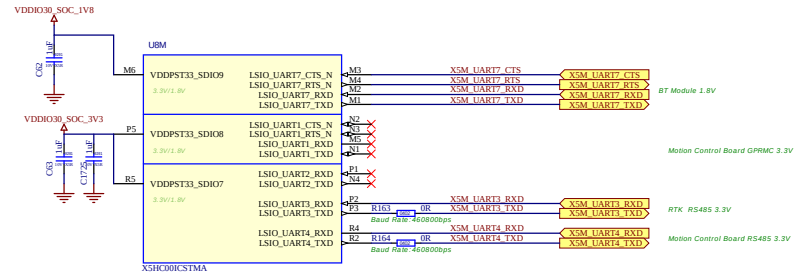
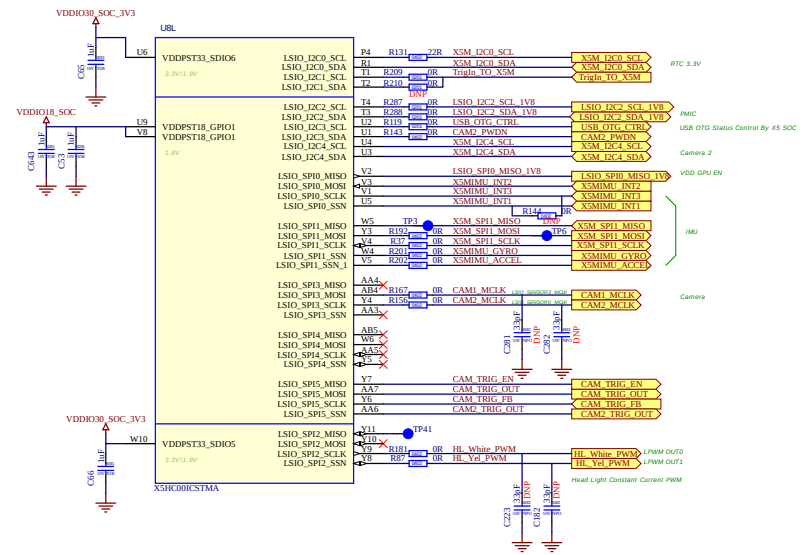
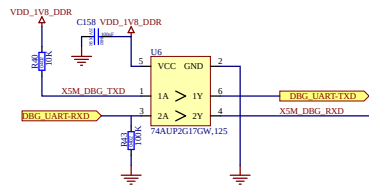
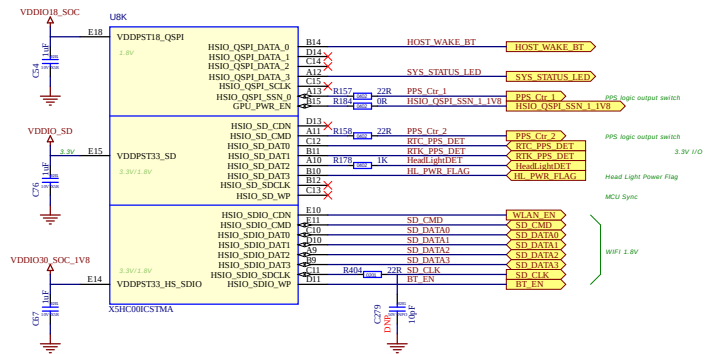
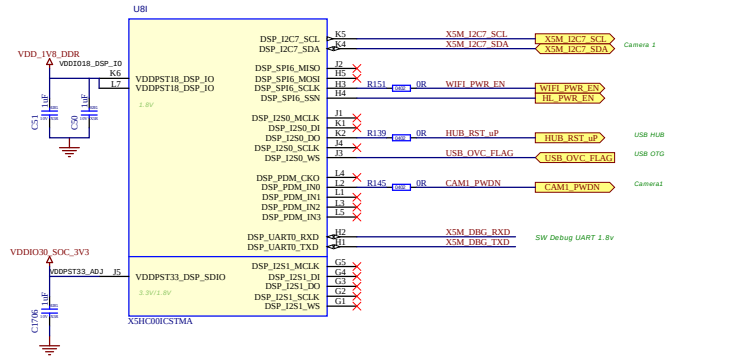
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Sheet 10 of 19

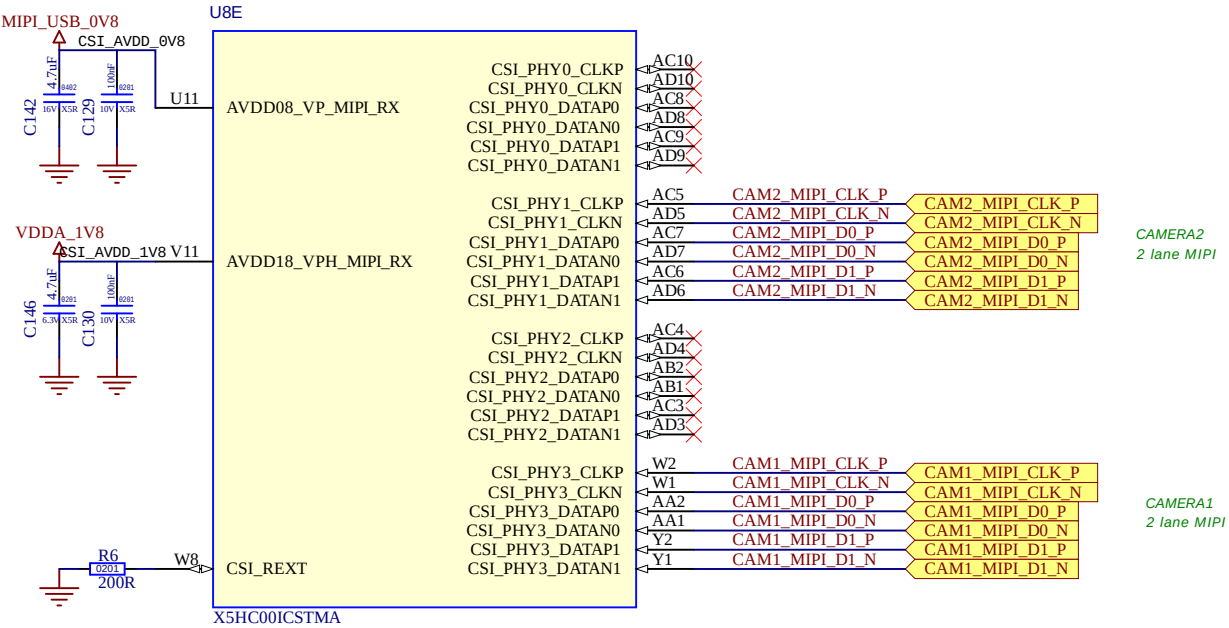
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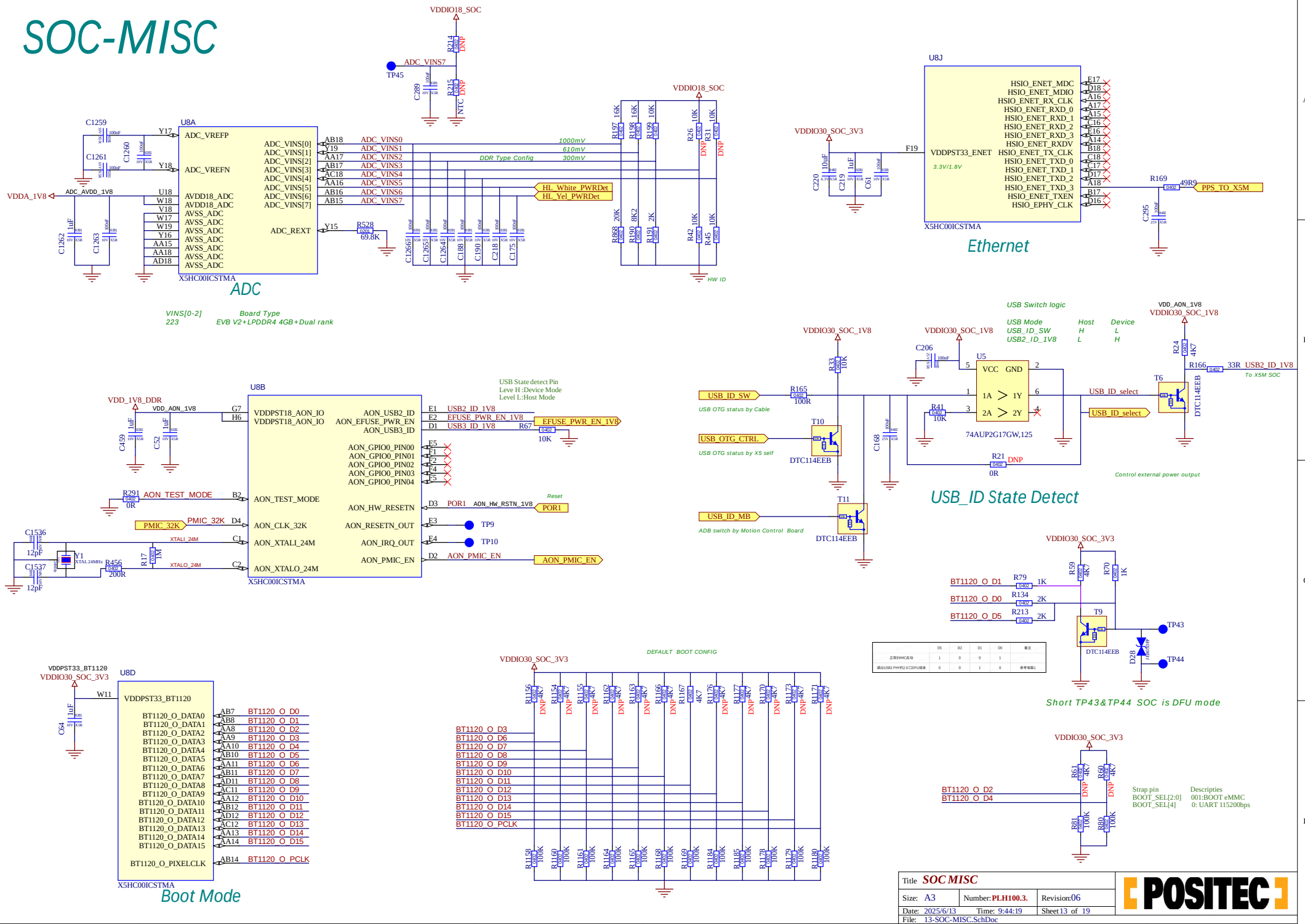
SOC GPIO



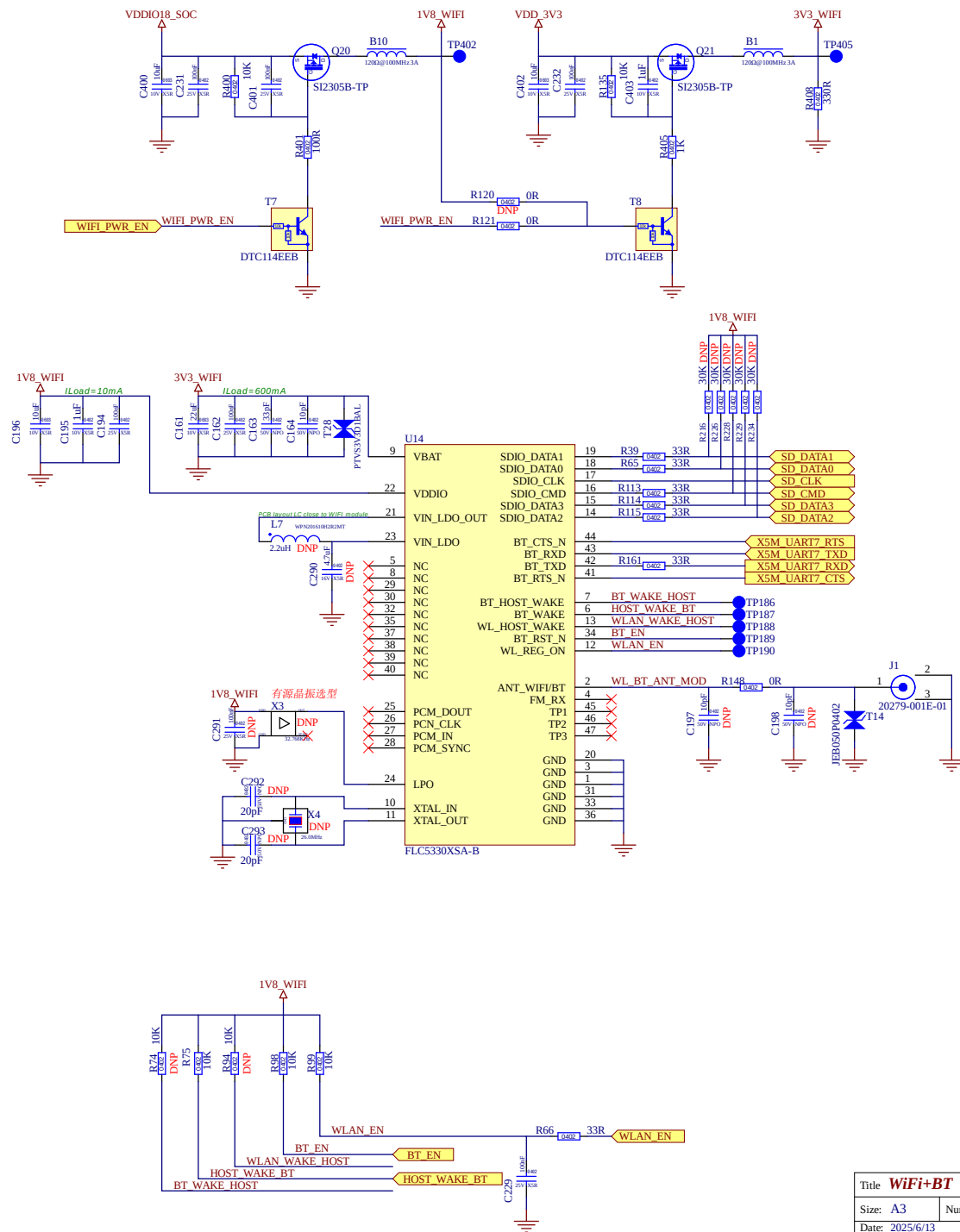
SOC MIPI



SOC-MISC



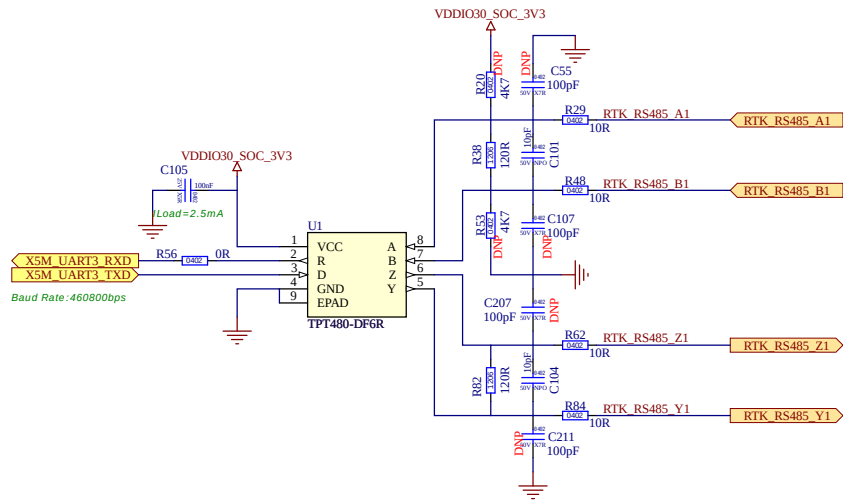
WI-FI + BT



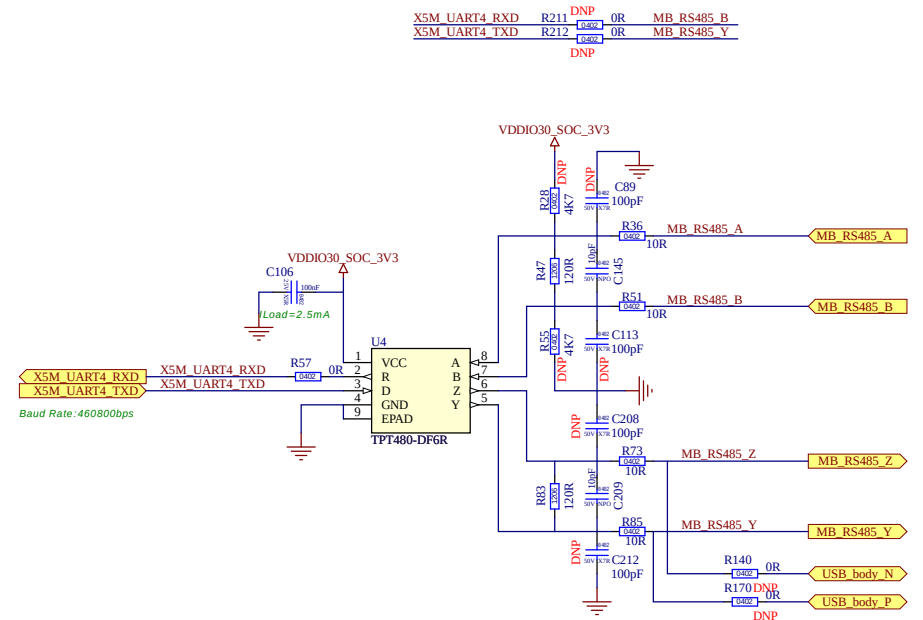
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RS485



RTK RS485



Motion Control Board RS485

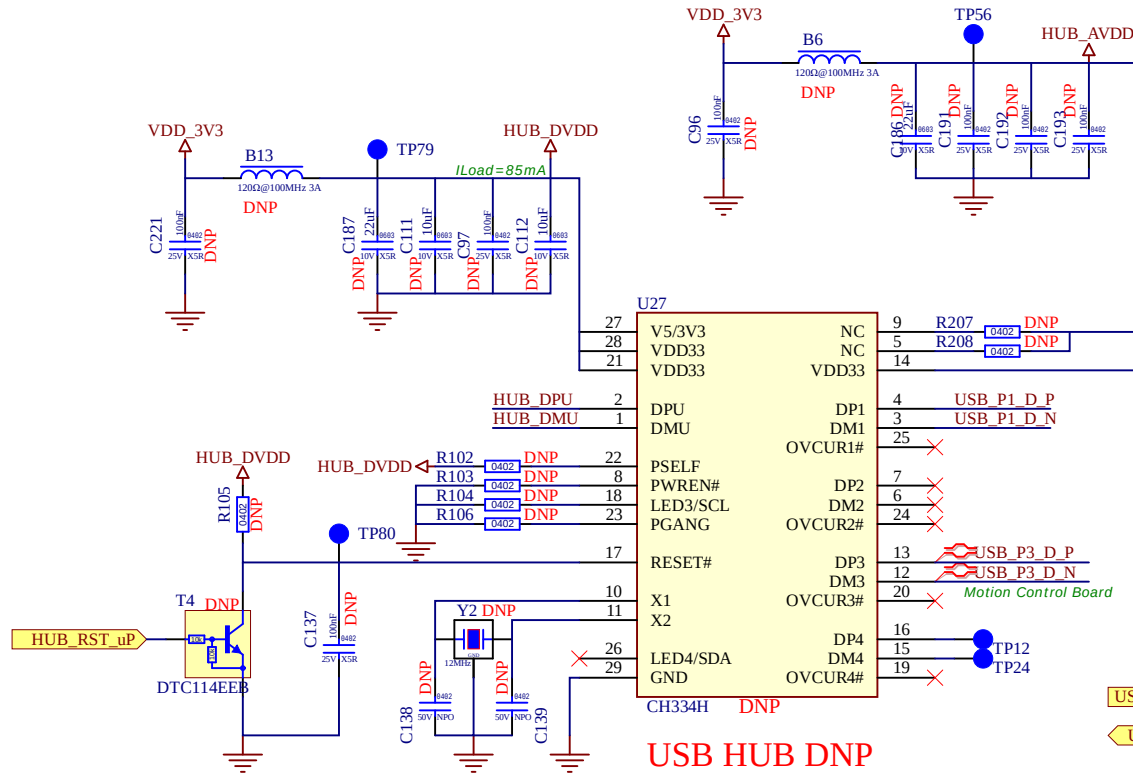
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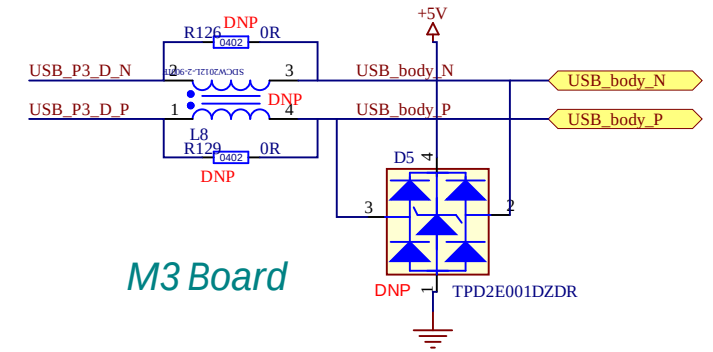
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**POSITEC**

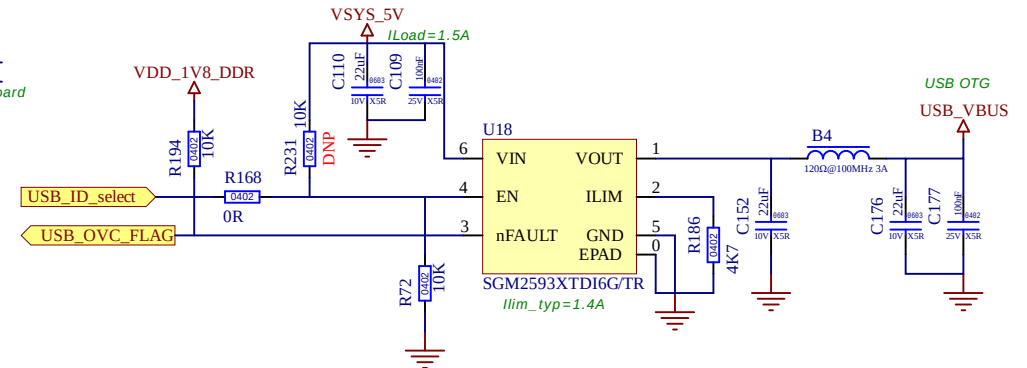
USB_HUB



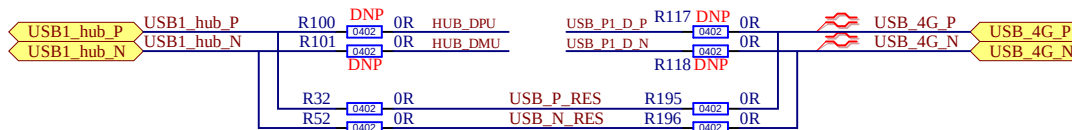
USB HUB DNP



M3 Board

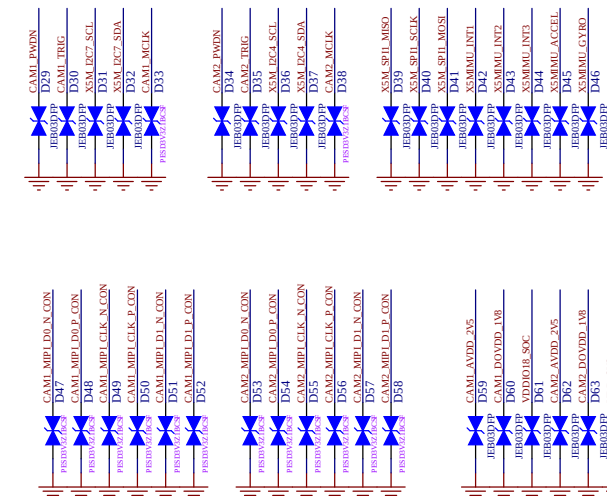


Power output for USB OTG



Title USB-HUB			
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Date: 2025/6/13	Time: 9:44:19	Sheet 17 of 19	
File: 17-USB_HUB.SchDoc			

A

**POSITEC**

D

