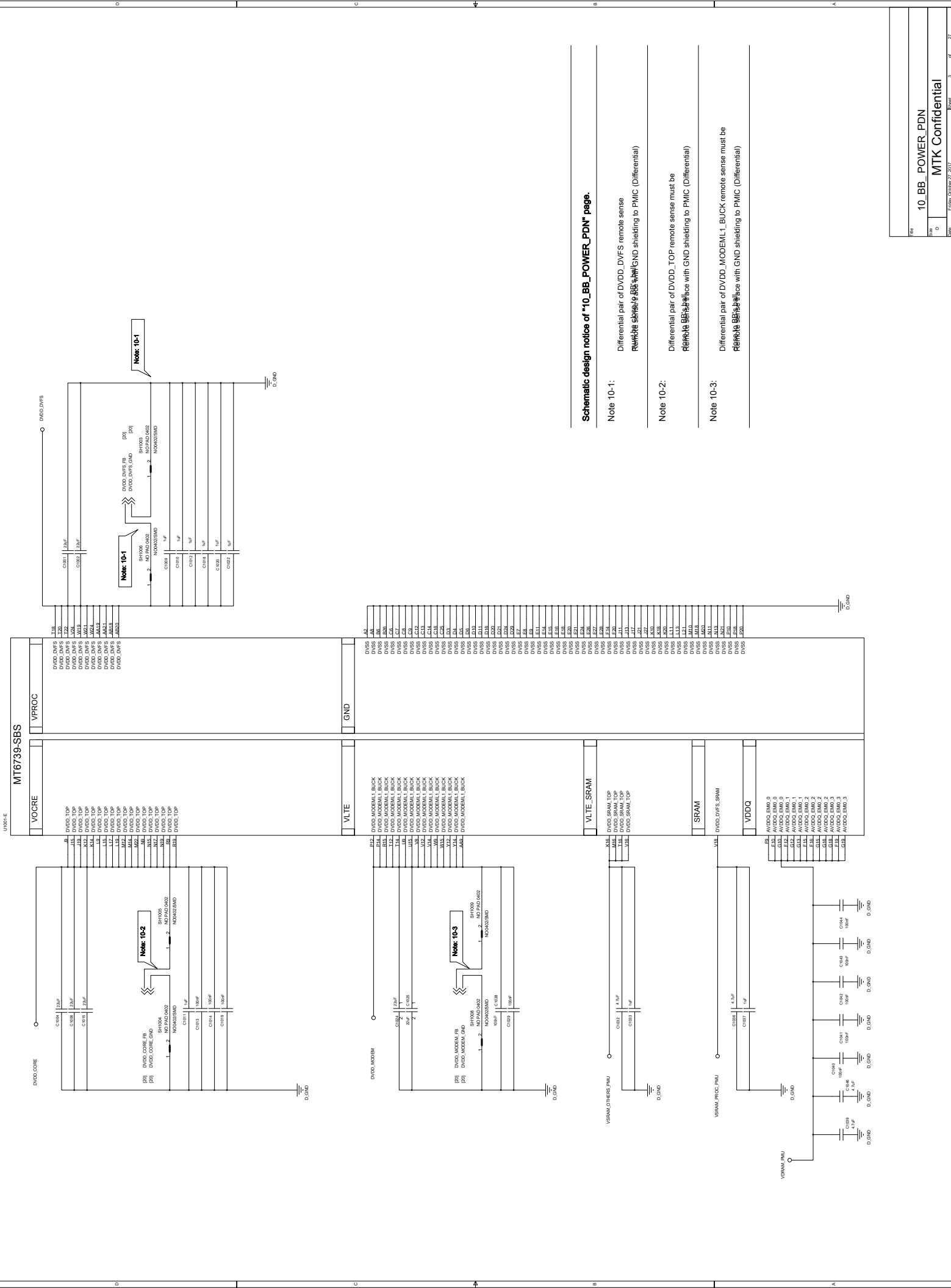
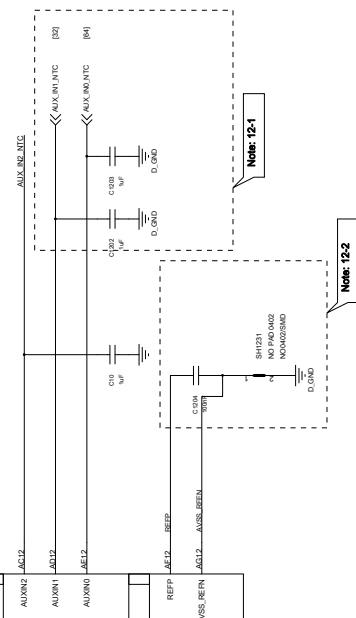
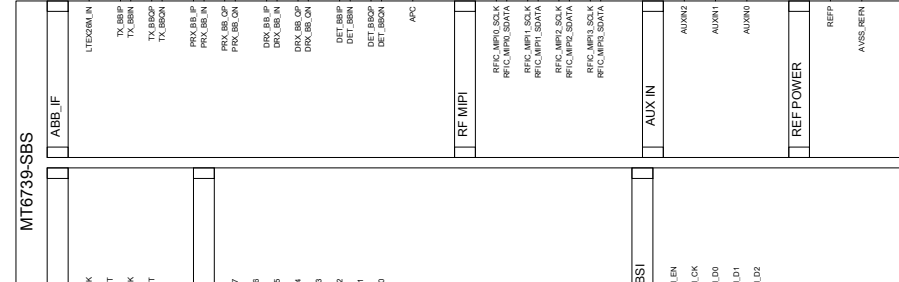
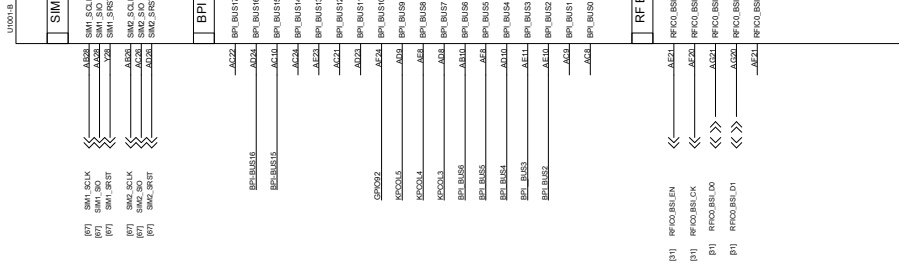
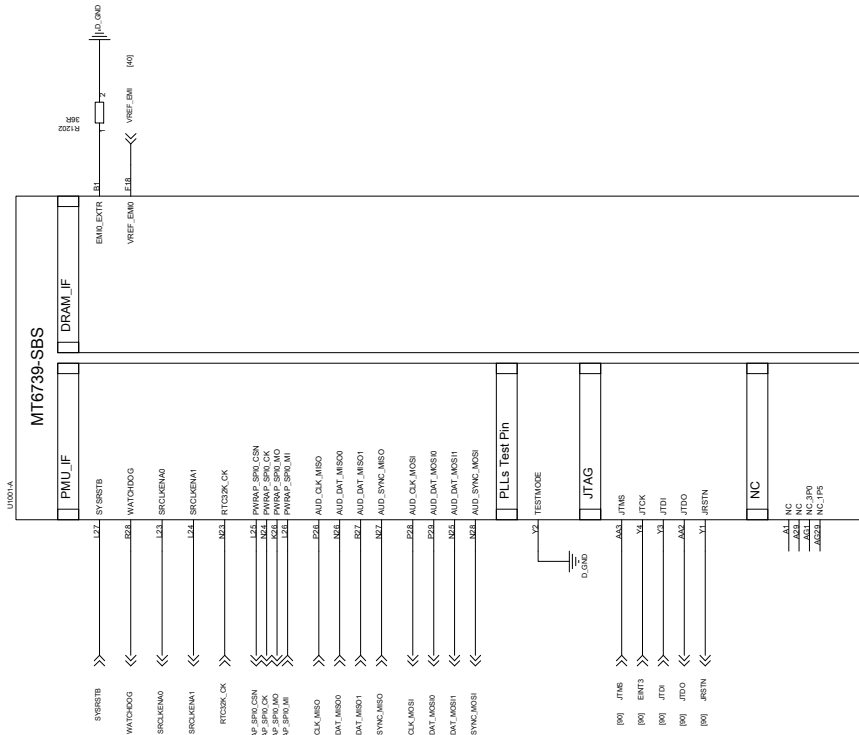




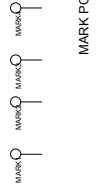
Schematic design notice of "10_BB_POWER_PDN" page.

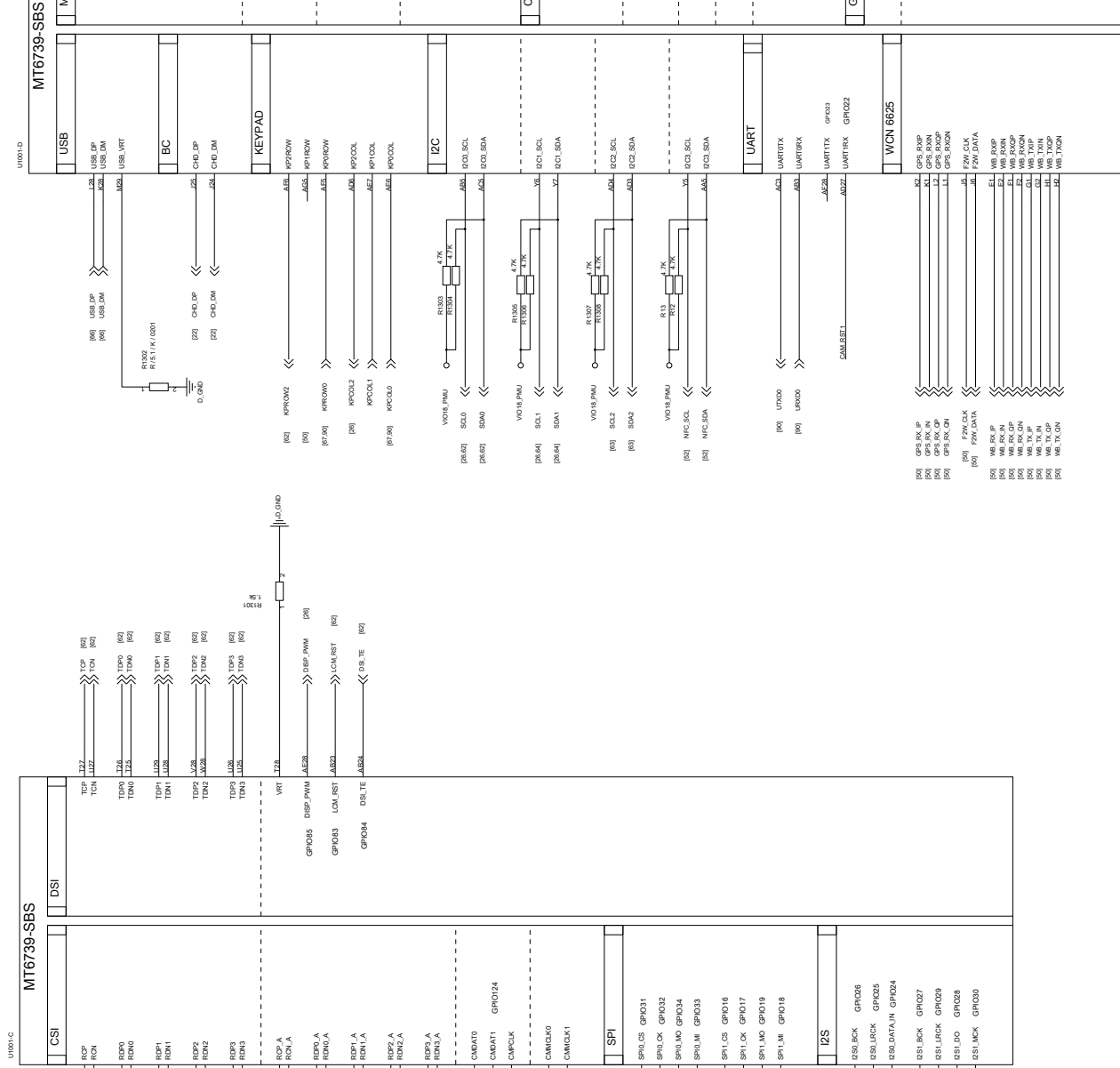
- Note 10-1: Differential pair of DVDD_DVFS remote sense
Remote sense place with GND shielding to PMIC (Differential)
- Note 10-2: Differential pair of DVDD_TOP remote sense must be
Remote sense place with GND shielding to PMIC (Differential)
- Note 10-3: Differential pair of DVDD_MODEM1_1 BUCK remote sense must be
Remote sense place with GND shielding to PMIC (Differential)

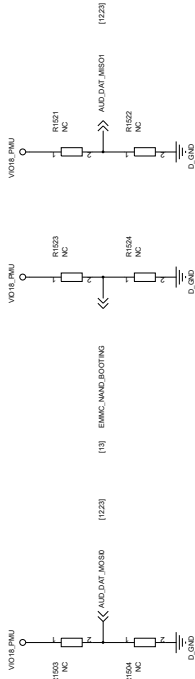


Note 12-1:
To shunt a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible.
Connect the unused AUX ADC input to GND.

Note 12-2:
The de-coupling cap. for REFP (AF12 ball) have to be placed as close to BB as possible.







Schematic design notice of "15_BB_HW_Trap" page.

Note 15-1: AUD_DAT_MISO0 is JTAG feature in bootstrap.

AUD_DAT_MISO0 default status is LO		Note
LO	Default	N/A
HI	Aux Func. trap MD_JTAG_AP_JTAG	MD_JTAG = CMDAT0/CMDAT1/CMPCLK/CMMCLK/CMMCLK1

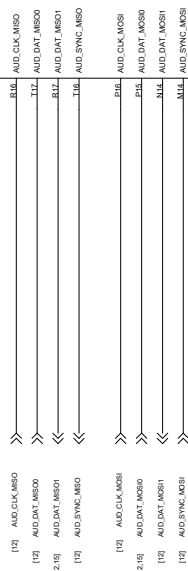
Note 15-2: ANT_SEL2 and AUD_DAT_MISO1 is storage booting feature in bootstrap.

ANT_SEL2 default status is LO		AUD_DAT_MISO1		Storage Booting
AUD_DAT_MISO1 default status is LO		ANT_SEL2 default status is LO		
(EMMC_MAND_BOOTING)	LO	LO	LO	TLC Boot
HI	HI	HI	HI	SLC Boot
LO	LO	LO	LO	eMMC Boot
LO	HI	HI	HI	N/A

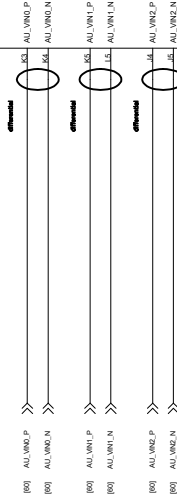
UD001-D

MT6357

AUDIO IF



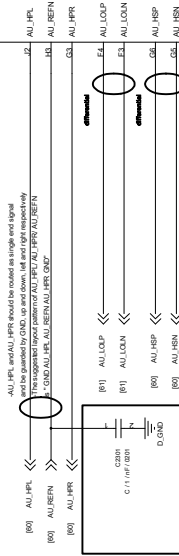
AUDIO INPUT



ACODET

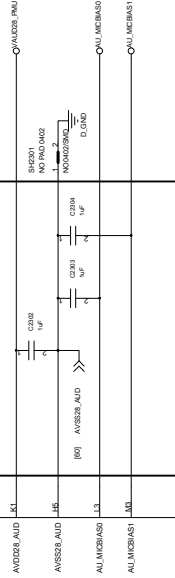


AUDIO OUTPUT



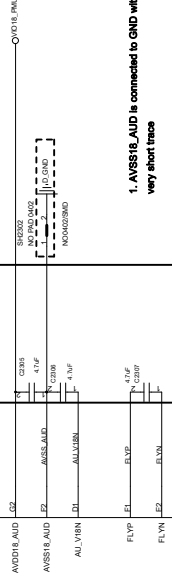
Close to Chip

UL POWER



Close to Chip

CHARGE PUMP



Close to Chip

1. AVSS18_AUD is connected to GND with very short trace
2. AVSS18_AUD is connected to de-couple cap of AVDD18_AUD and AU_LYEN with 6mil trace respectively

SW CHG I2C address: TBD (Write:0xTBC, Read:0xTBC)



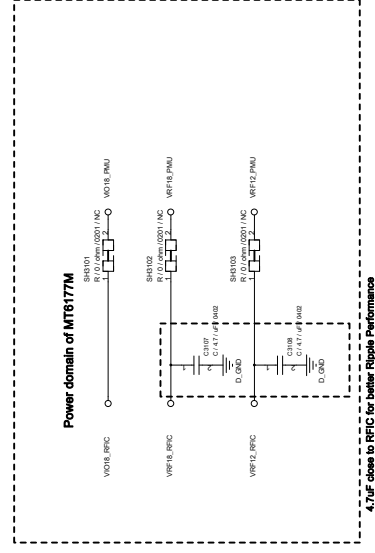
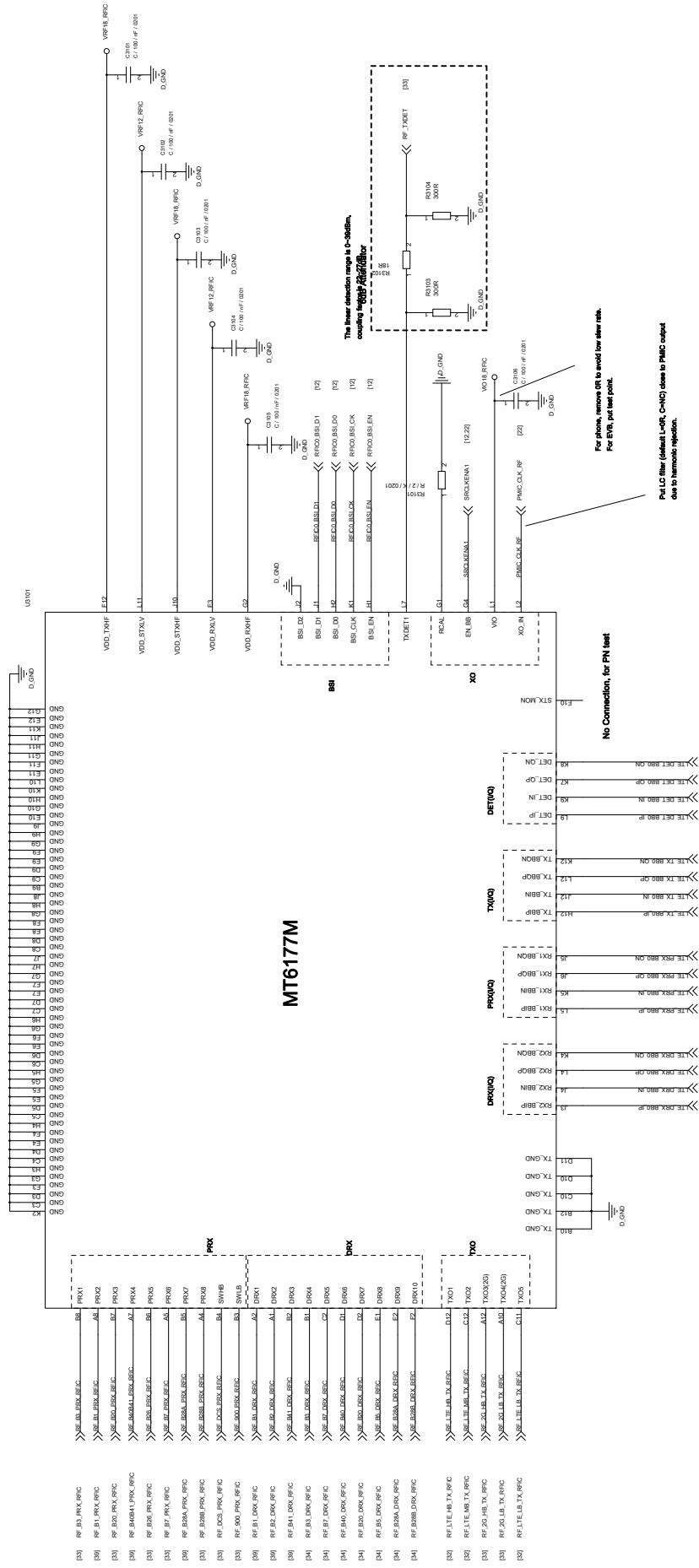
DISP_PWM

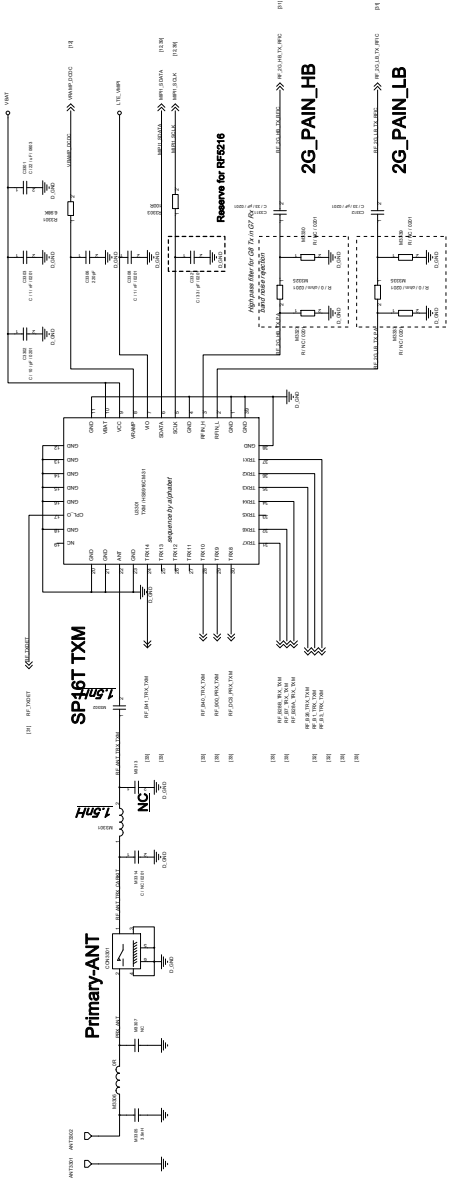
10K

R702

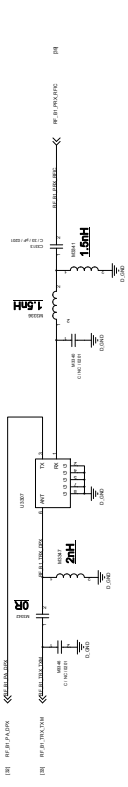
VOUT max : 38V
Switch Frequency : 5KHz~100KHz
VFB : 200mV

For better ESD or surge performance we need choose suitable device for system protection. Please refer to [Surge device selection guide V2.0] provide by MTK.

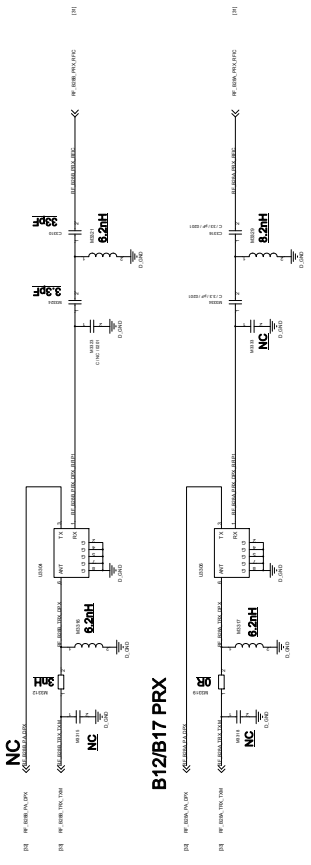




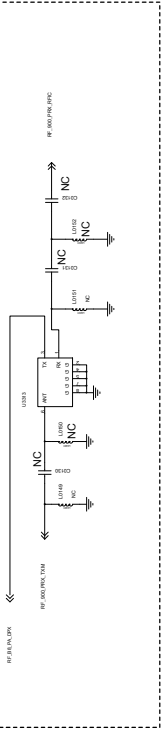
B4/66 TRX



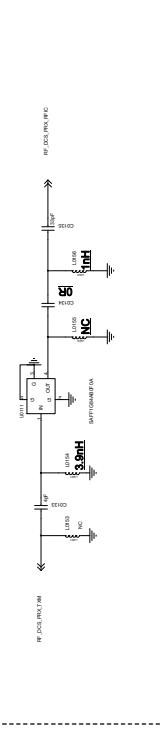
B12/B17 PRX



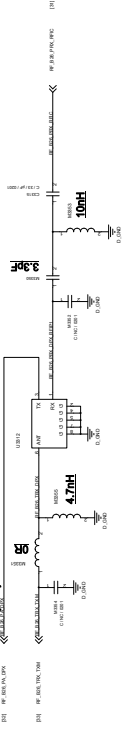
B8 TRX



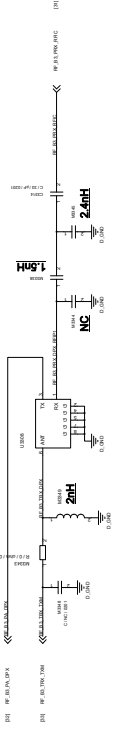
PCS/DCS PRX



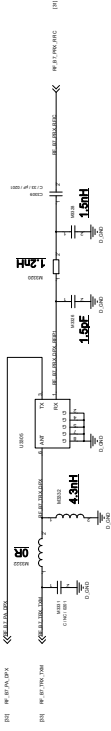
B5(26) TRX

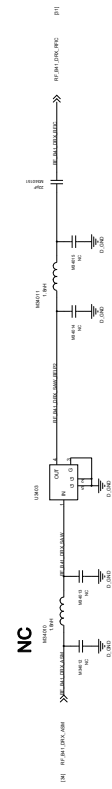
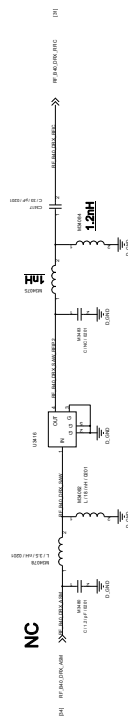
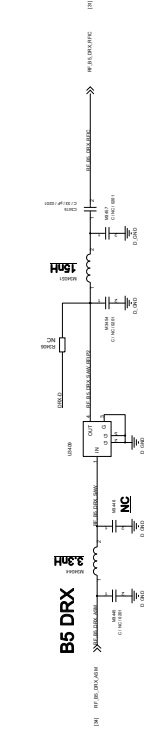
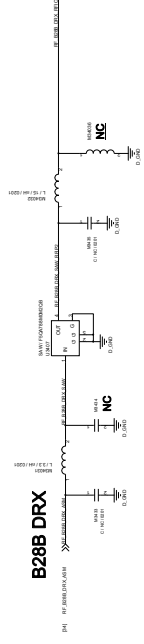
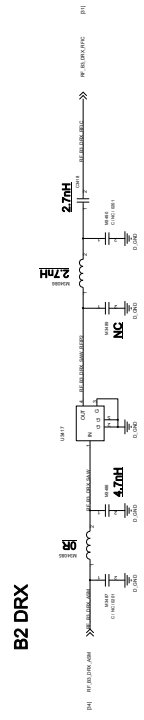
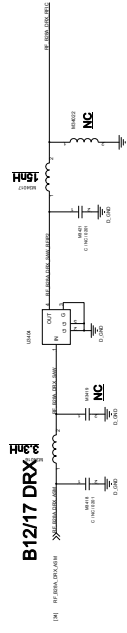
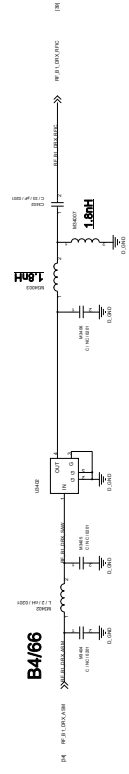
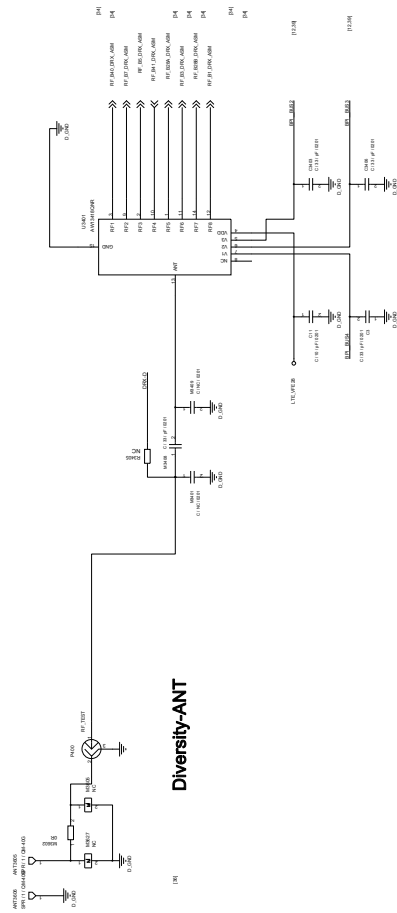


B2 TRX

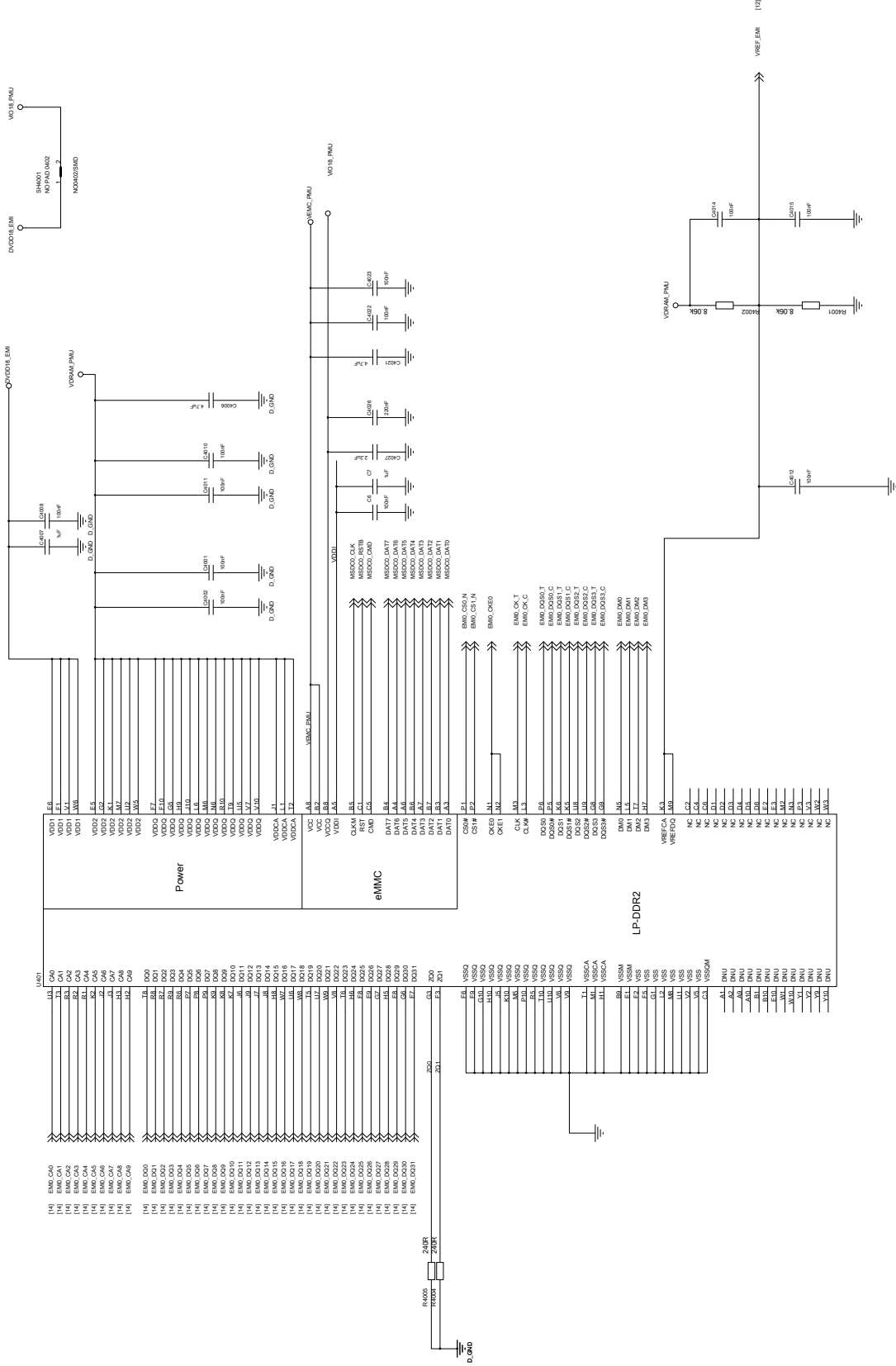


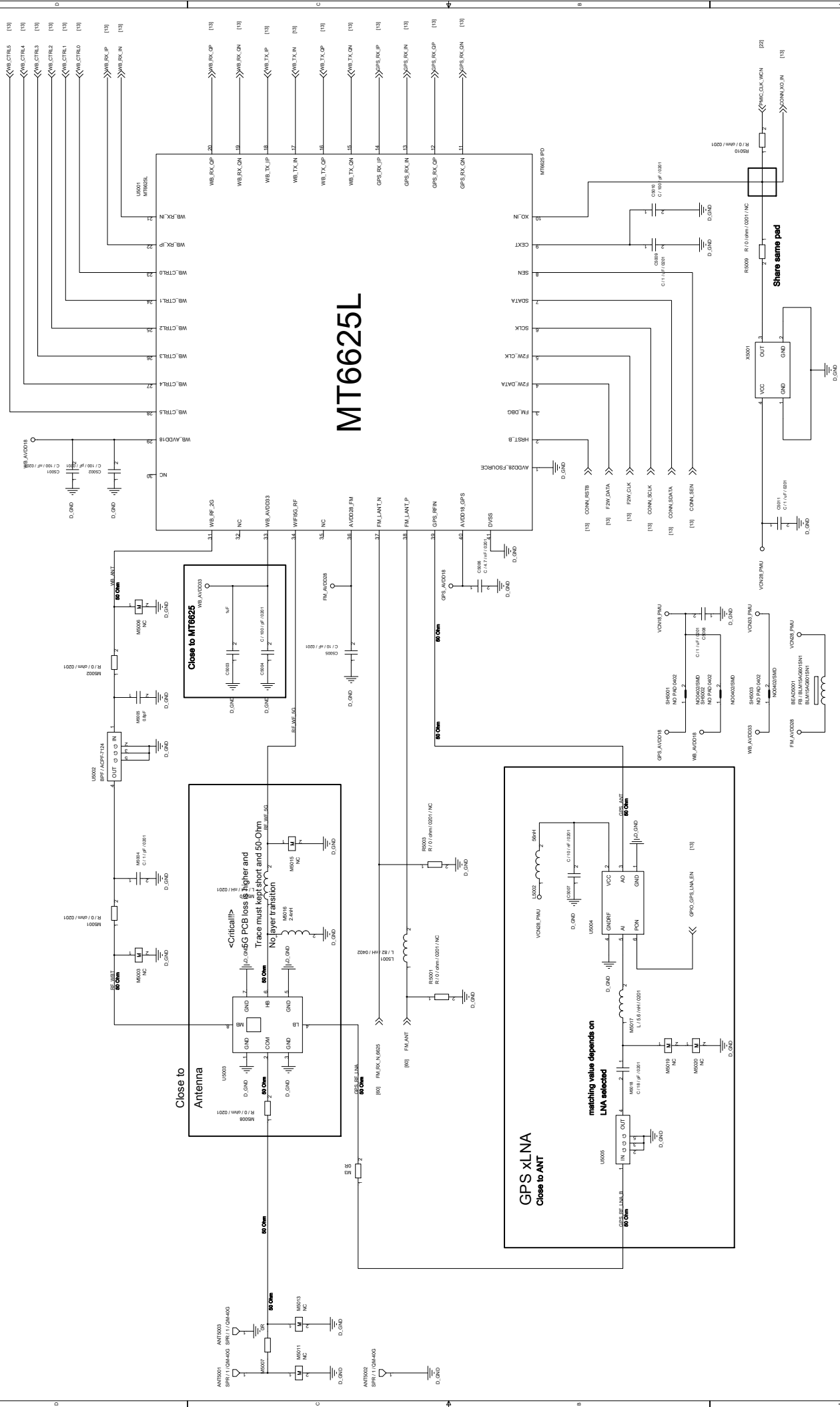
B7 TRX





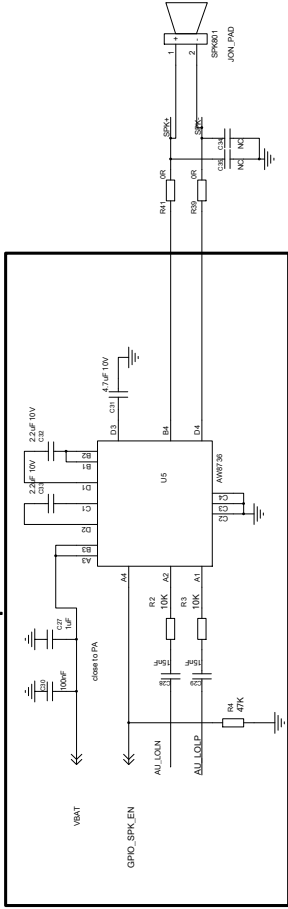
EMCP+LPDDR2



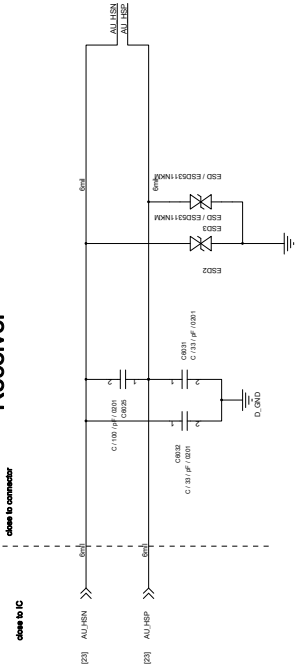


Speaker Amplifier

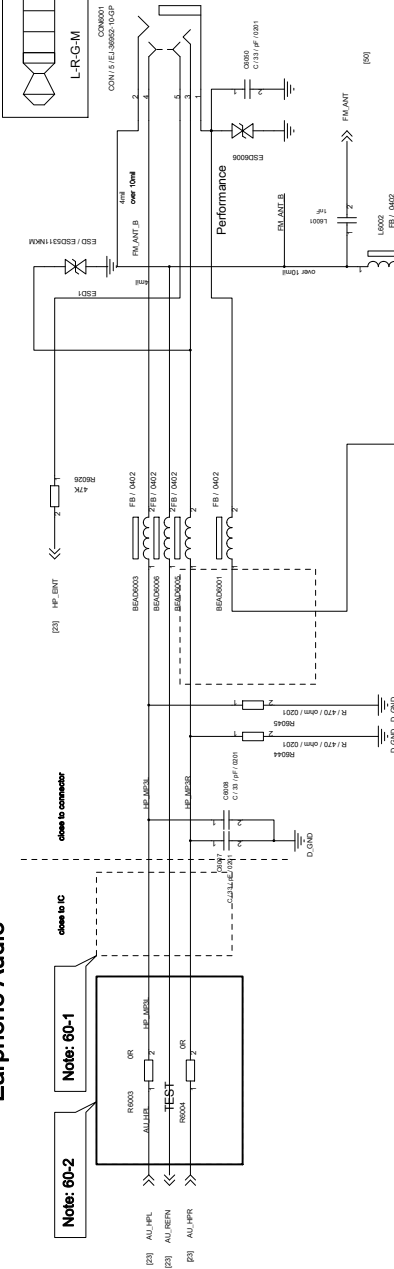
Speaker



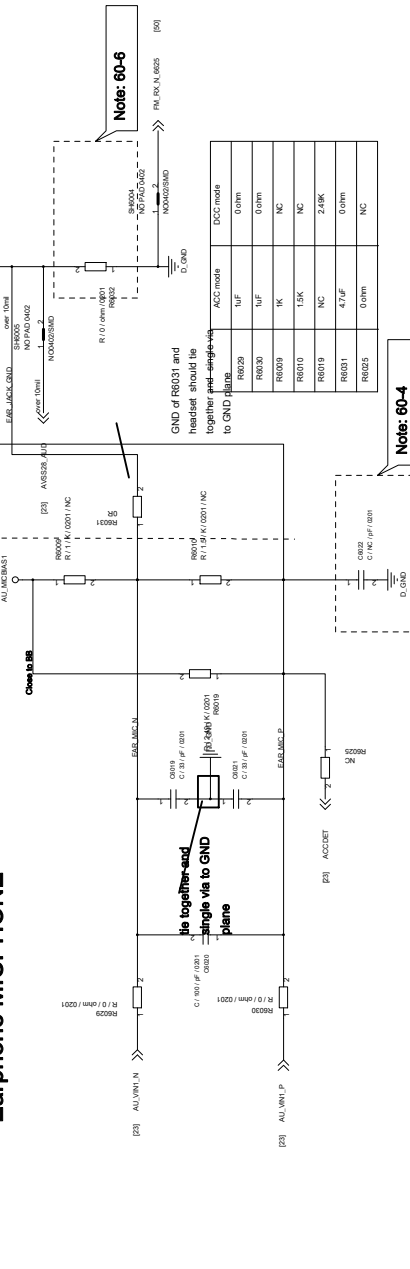
Receiver



Earphone Audio



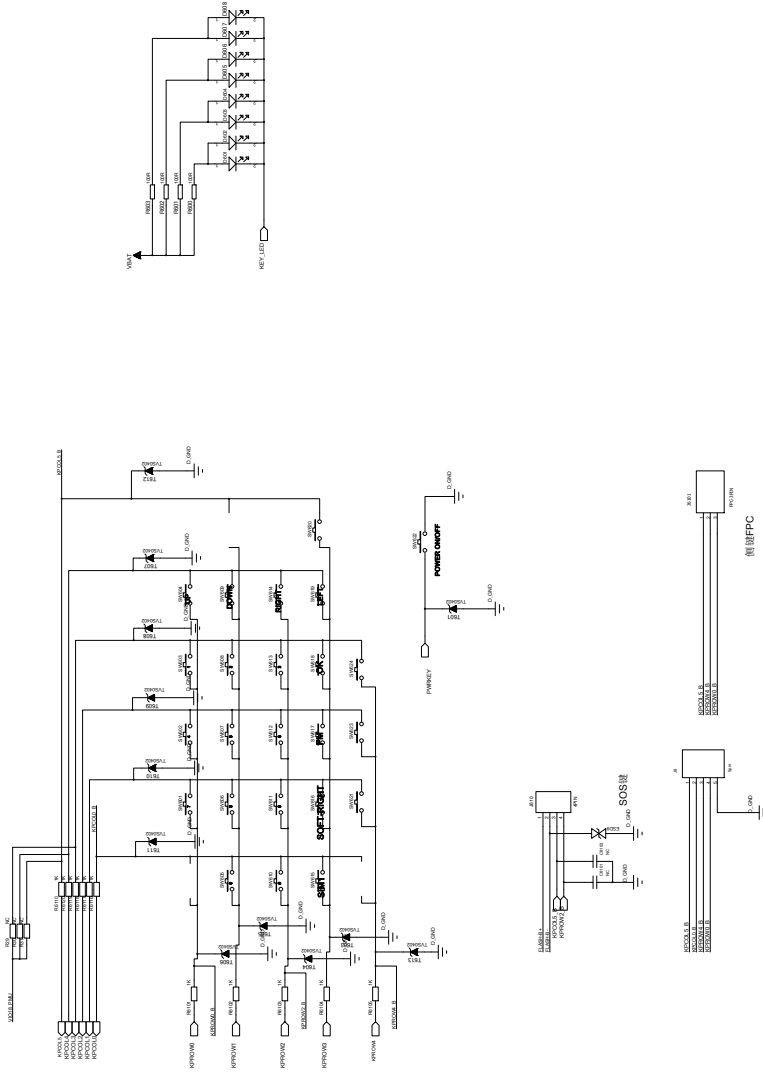
Earphone MICPHONE

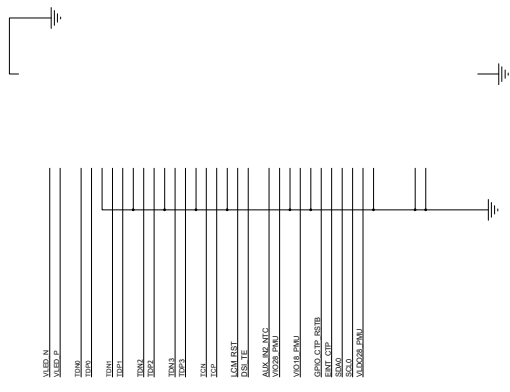


Please refer to [Surge device selection guide V2.0] provide by MTK.

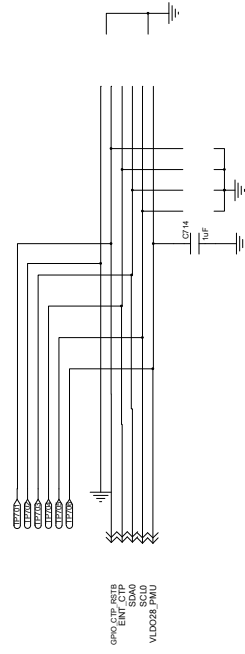
60 PERI_AUDIO_IO
MTK Confidential

KEY

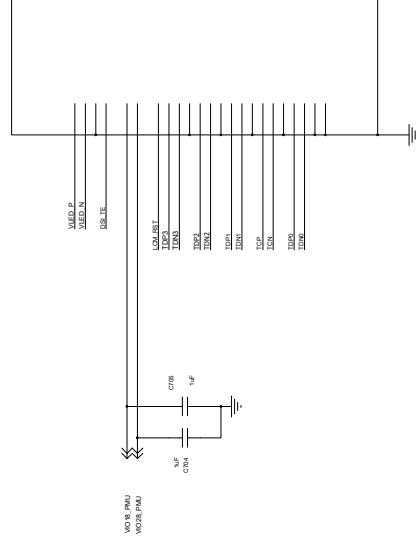
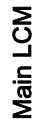




CTP



TEST-LCD-ZIFCONNECT



Schematic design notice of "62_PERI_LCD_CTP" page.

Note 62-1:

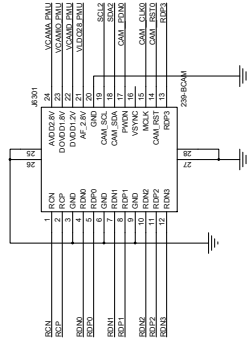
COF GT1151 I2C address: 0X5D (Write:0xBA, Read:0xBB)
Or 0X14 (Write:0x28, Read:0x29)

Note 62-2:

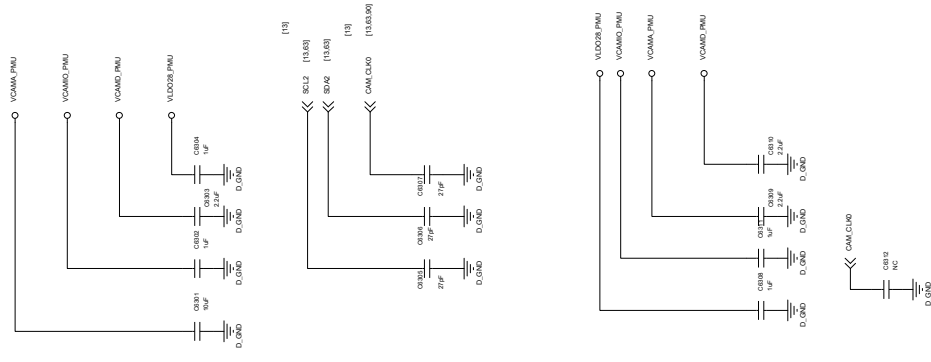
NT35521S 3Lane and 4Lane Selection
4Lane-->R6205=0R
3Lane-->R6205=NC

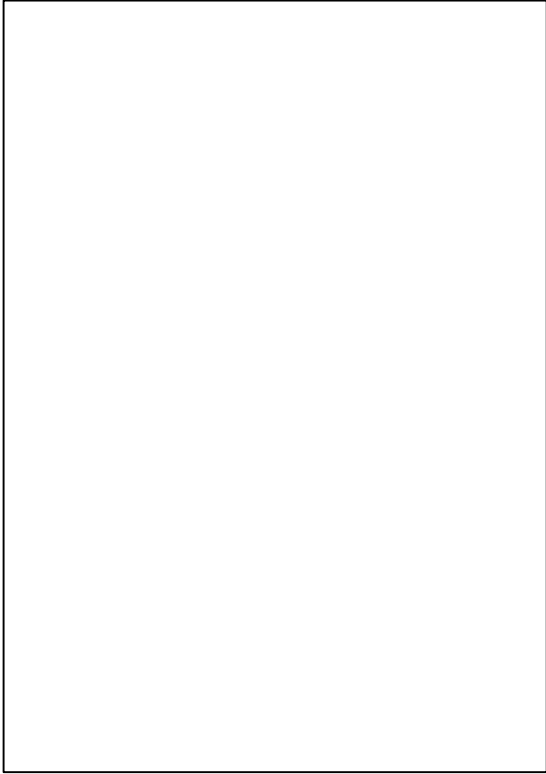
后Rear Camera - 13M

Rear camera (OV13870) I2C address: 0X10 (Write:0x20, Read:0x21)
AF driver (LC-898217) I2C address: 0X72 (Write:0xE4, Read:0xE5)

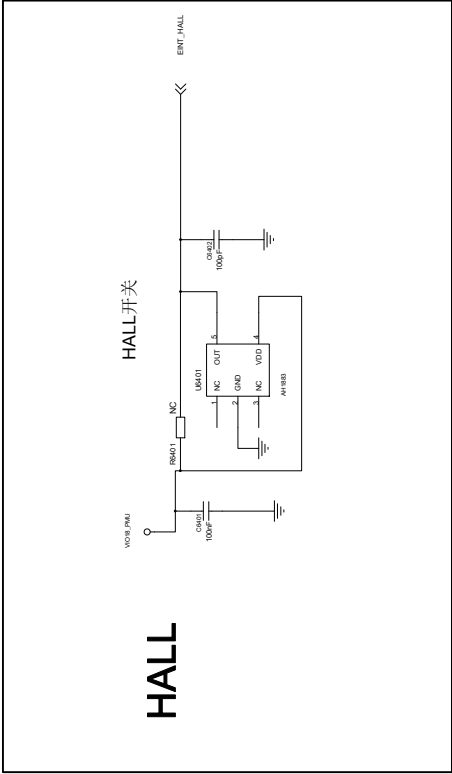
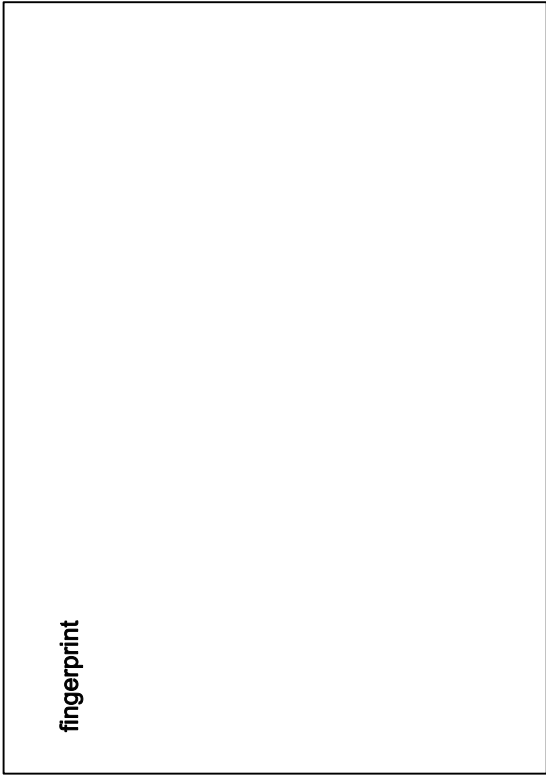


同L239 后摄像头



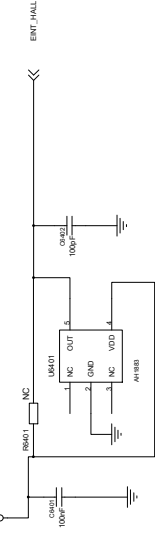


fingerprint



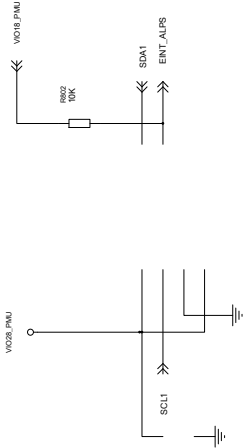
HALL

HALL开关



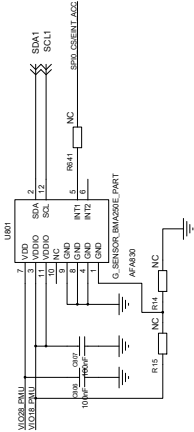
RGB & PS Sensor

CM36558 / ALPS + UV I2C address: 0X51 (Write:0xA2, Read:0xA3)

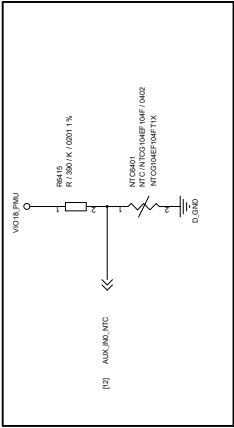


Accerometer

G-Sensor

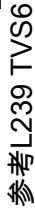


Thermistor / To sense board level temperature

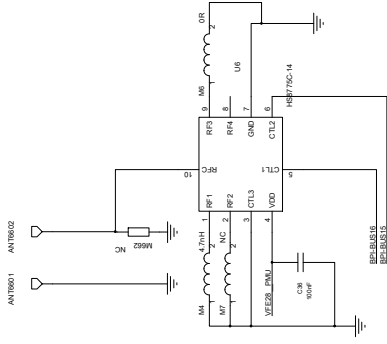


Distance to AP is 6-7mm and away from other heat resources 10mm-12mm

Front camera (OV8856) I2C address: 0X36 (Write:0x6C, Read:0x6D)



ANT6601 ANT6602



Schematic design notice of "66_PERI_USB" page.

Note 66-1: For better ESD or surge performance we need choose suitable device for system protection, please refer to [Surge device selection guide V2.0] provide by MTK.

MTK Debug I/O

Schematic design notice of "90_DEBUG_IO" page.

Note 90-1: UART Default support : UART0, MD_UART0
JTAG Default support : AP JTAG

