

Appendix for 12A_N77A 3700-3980MHz

Product Name: Zenius08
Model No: GMT-08QA-2301

Appendix A: Average Power Output Data for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.67	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.93	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	24.27	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	24.40	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	22.56	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+L	Edge_1RB_Right	21.77	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	23.25	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+L	Inner_Full	24.40	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.68	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.82	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	23.47	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	23.54	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+M	Edge_1RB_Left	21.44	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+M	Edge_1RB_Right	21.77	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+M	Outer_Full	22.56	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+M	Inner_Full	23.51	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.97	PC3	PASS
DC_12A_n77A-3	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	21.73	PC3	PASS

700-3980								
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	23.48	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	23.53	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Left	21.92	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	21.54	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	22.46	PC3	PASS
DC_12A_n77A-3 700-3980	30	5+100	CP-QPSK	M+H	Inner_Full	23.51	PC3	PASS

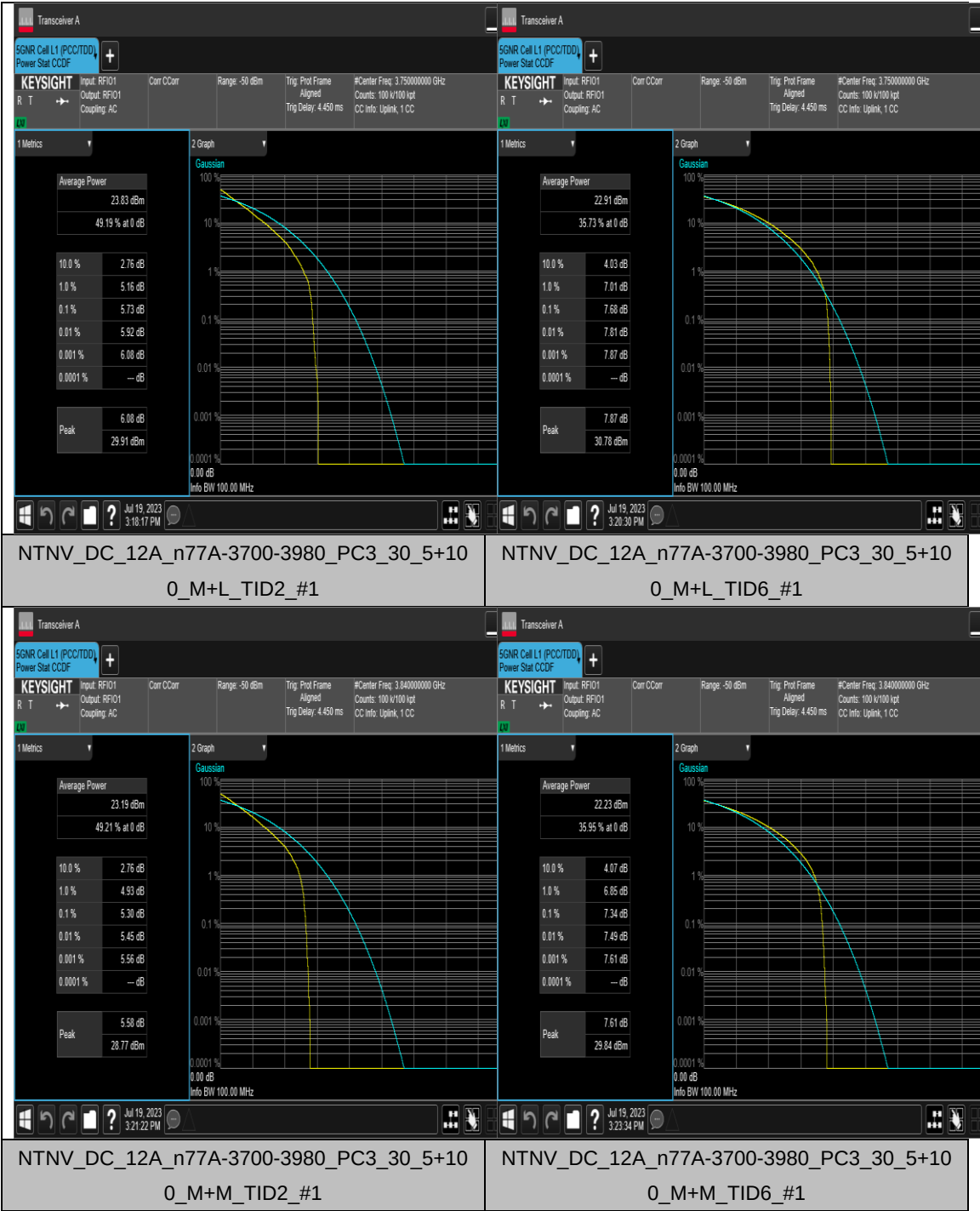
Appendix B: Peak-to-Average Ratio for NSA

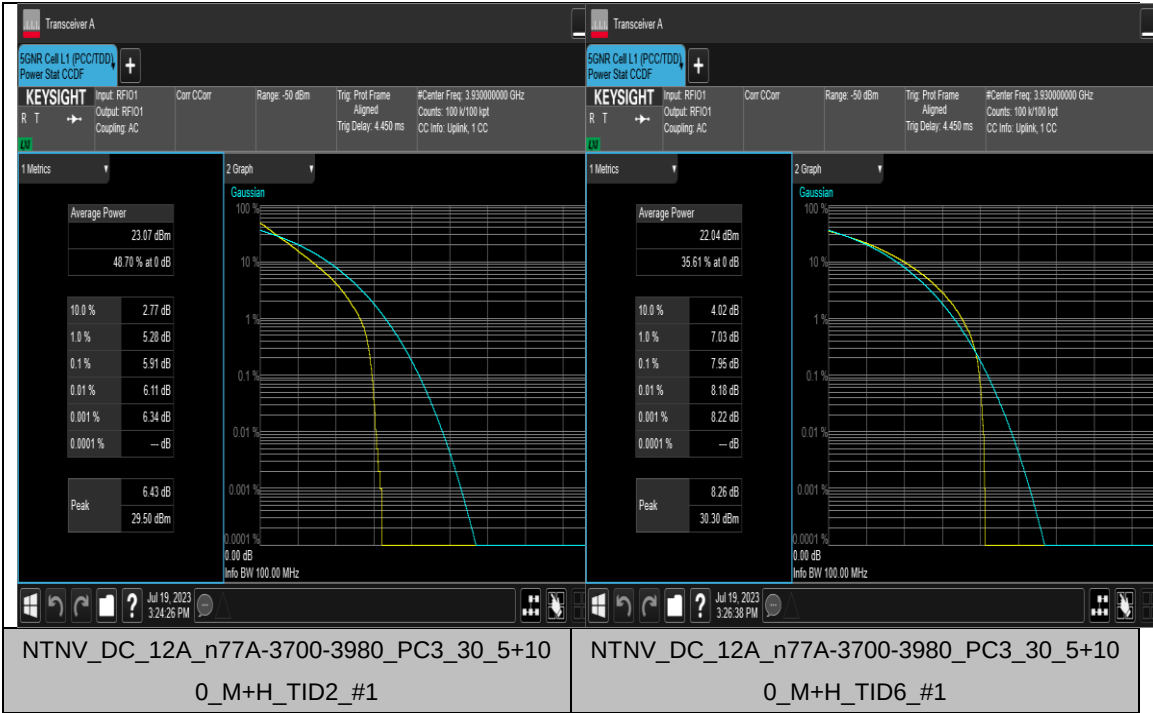
Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_12A_n 77A-3700-3 980	30	5+100	DFT-QPSK	M+L	Outer_Full	5.73	≤13	PASS
DC_12A_n 77A-3700-3 980	30	5+100	CP-QPSK	M+L	Outer_Full	7.68	≤13	PASS
DC_12A_n 77A-3700-3 980	30	5+100	DFT-QPSK	M+M	Outer_Full	5.30	≤13	PASS
DC_12A_n 77A-3700-3 980	30	5+100	CP-QPSK	M+M	Outer_Full	7.34	≤13	PASS
DC_12A_n 77A-3700-3 980	30	5+100	DFT-QPSK	M+H	Outer_Full	5.91	≤13	PASS
DC_12A_n 77A-3700-3 980	30	5+100	CP-QPSK	M+H	Outer_Full	7.95	≤13	PASS

Test Graphs

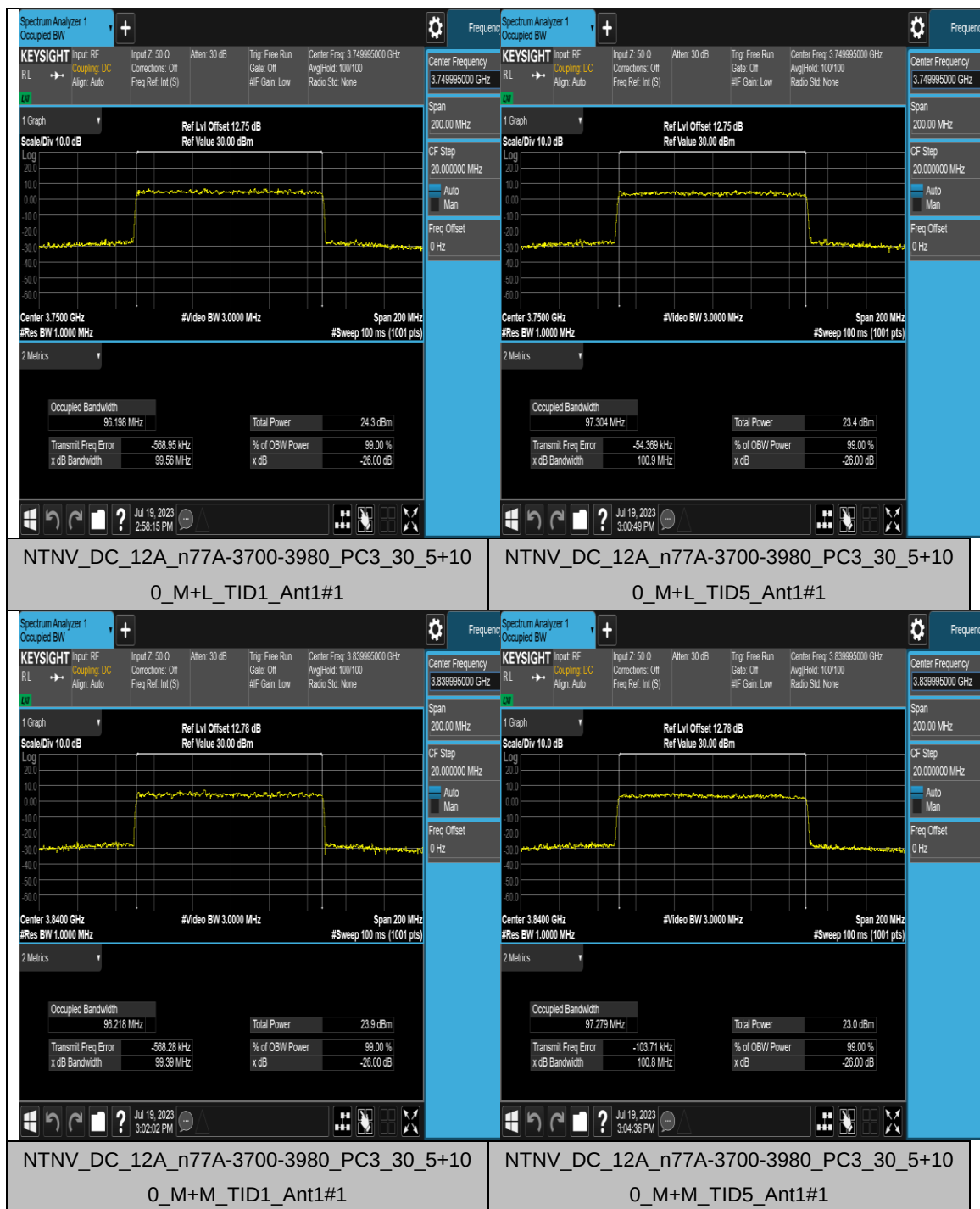


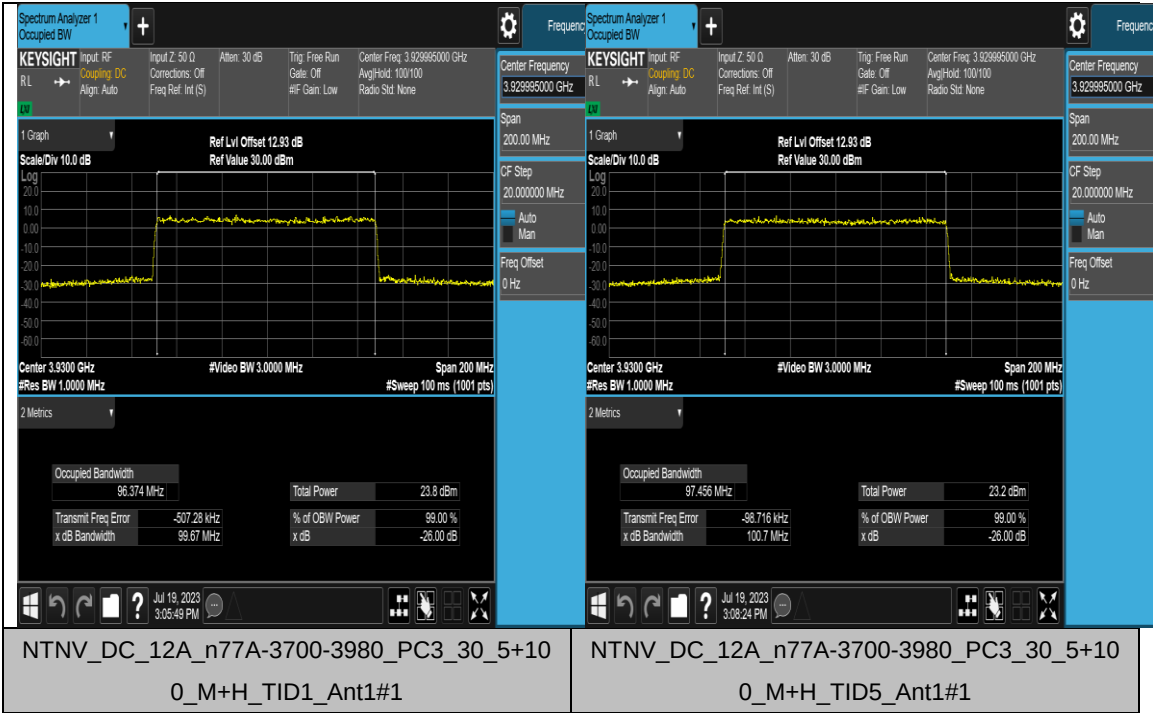


Appendix C: 26dB Bandwidth and Occupied Bandwidth for**NSA****Test Result**

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_12A_n77A-3700-3 980	30	5+100	DFT-QPSK	M+L	Outer_Full	96.198	99.56	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Outer_Full	97.304	100.9	PASS
DC_12A_n77A-3700-3 980	30	5+100	DFT-QPSK	M+M	Outer_Full	96.218	99.39	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+M	Outer_Full	97.279	100.8	PASS
DC_12A_n77A-3700-3 980	30	5+100	DFT-QPSK	M+H	Outer_Full	96.374	99.67	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Outer_Full	97.456	100.7	PASS

Test Graphs



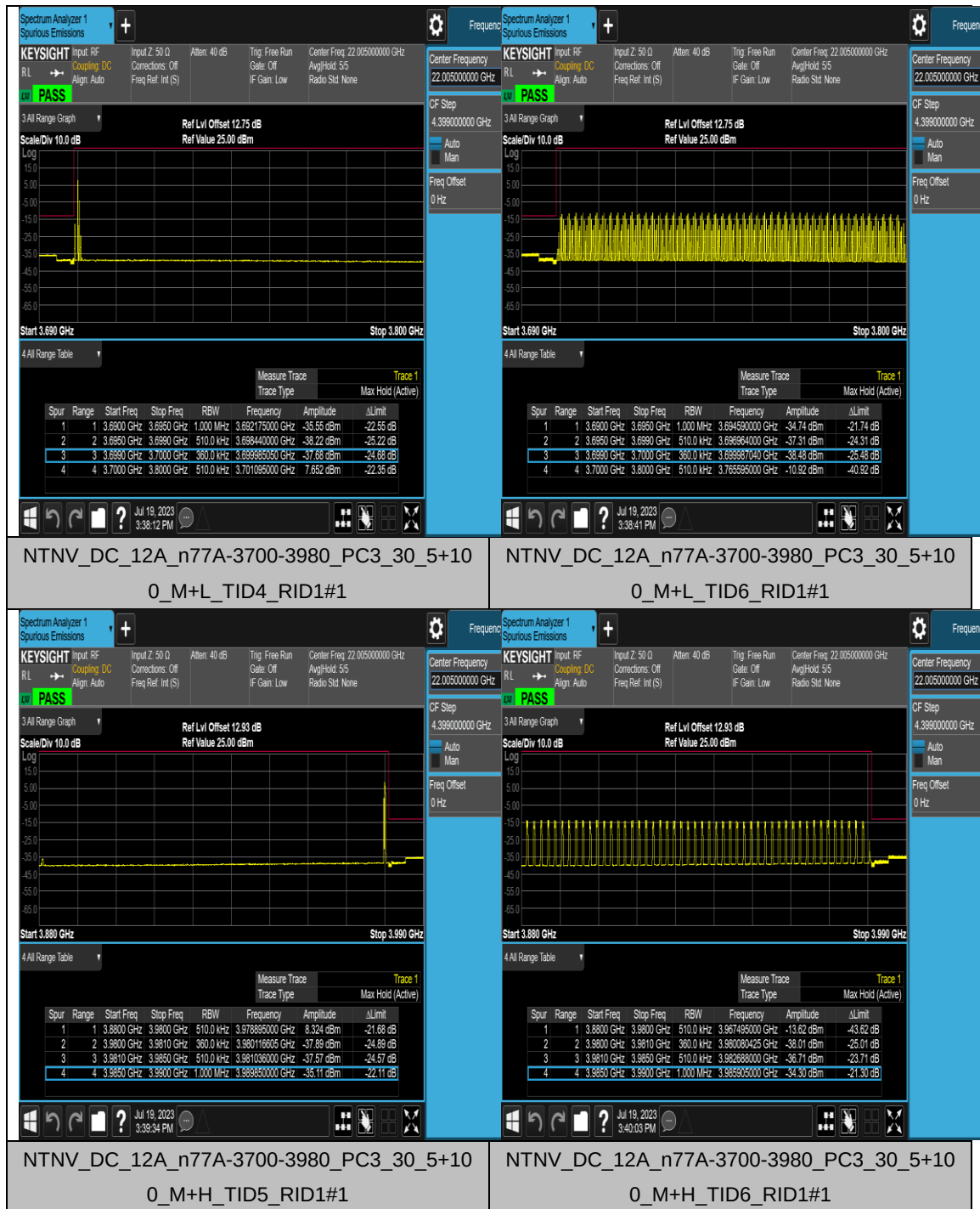


Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Verdict
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Edge_1RB_Left	see graph	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+L	Outer_Full	see graph	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Edge_1RB_Right	see graph	PASS
DC_12A_n77A-3700-3 980	30	5+100	CP-QPSK	M+H	Outer_Full	see graph	PASS

Test Graphs



Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-76.46	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.71	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-54.57	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-57.09	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.15	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.91	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.21	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-75.55	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-77.62	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-26.55	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-57.05	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.68	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-29.79	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.28	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-75.43	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-78.22	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-48.74	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-56.97	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	6000	-46.54	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-30.01	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-33.43	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-73.64	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-78.02	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-55.39	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-56.87	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.70	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	6000	26000	-29.88	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Left	26000	40000	-33.11	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-74.53	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-78.68	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-54.16	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-52.21	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	3000	6000	-49.35	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	6000	26000	-30.11	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Edge_1RB_Right	26000	40000	-33.27	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.009	0.15	-76.26	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	0.15	30	-79.61	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	30	1000	-55.10	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	1000	3000	-56.85	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	3000	6000	-46.58	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	6000	26000	-29.86	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+L	Outer_Full	26000	40000	-33.33	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-77.02	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-78.53	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-55.11	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-56.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.33	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-29.89	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.31	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.77	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-78.13	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-49.14	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-56.78	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.76	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-29.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.34	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-74.96	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-77.04	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-53.11	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-56.91	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	6000	-46.53	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-29.93	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-33.50	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-75.46	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-77.73	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-54.85	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-57.01	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.74	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	6000	26000	-30.04	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Left	26000	40000	-33.20	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-75.34	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-76.96	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-54.24	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-57.00	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	3000	6000	-49.80	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	6000	26000	-30.11	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Edge_1RB_Right	26000	40000	-33.42	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.009	0.15	-74.99	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	0.15	30	-79.13	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	30	1000	-54.34	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	1000	3000	-56.89	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	3000	6000	-46.51	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	6000	26000	-29.91	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+M	Outer_Full	26000	40000	-33.33	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-74.58	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.92	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-54.33	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-57.04	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-50.07	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-30.14	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.30	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-74.77	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-77.56	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-32.64	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-57.00	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	6000	-50.14	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-29.99	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-33.25	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-76.04	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-76.90	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-55.01	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-56.92	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	6000	-46.43	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-30.09	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-33.32	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-75.07	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-77.39	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-54.89	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-57.02	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	3000	6000	-50.03	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	6000	26000	-29.97	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Left	26000	40000	-33.48	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-75.02	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-78.93	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-54.73	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-57.08	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	3000	6000	-49.82	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	6000	26000	-30.16	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Edge_1RB_Right	26000	40000	-32.91	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.009	0.15	-76.31	-43	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	0.15	30	-78.81	-33	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	30	1000	-54.43	-23	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	1000	3000	-56.70	-13	PASS

DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	3000	6000	-44.45	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	6000	26000	-29.85	-13	PASS
DC_12A_n77A -3700-3980	30	5+10 0	CP-QPSK	M+H	Outer_Full	26000	40000	-33.20	-13	PASS

Test Graphs

