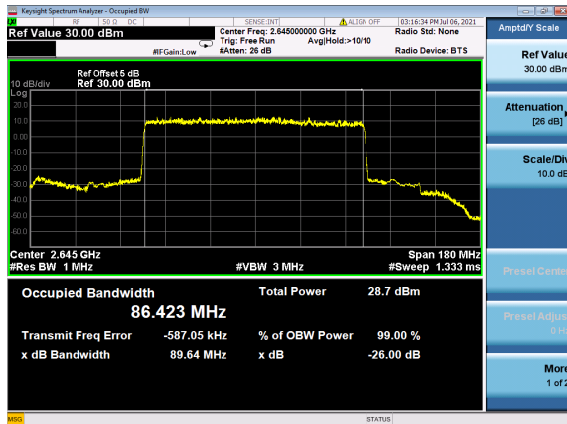
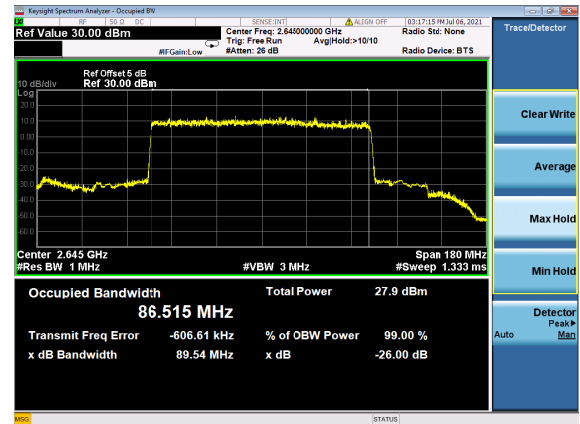
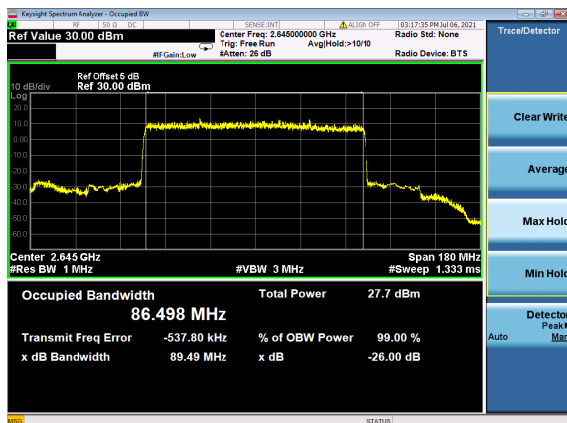
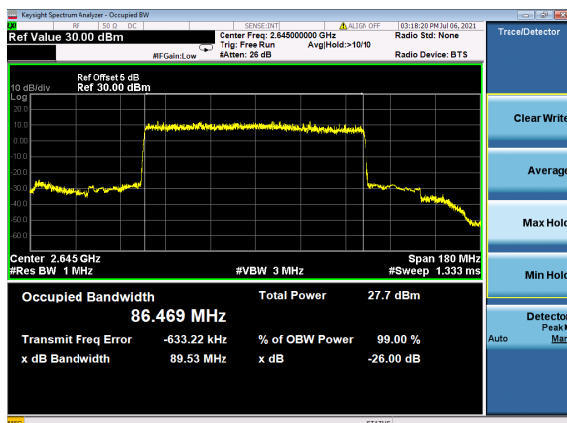
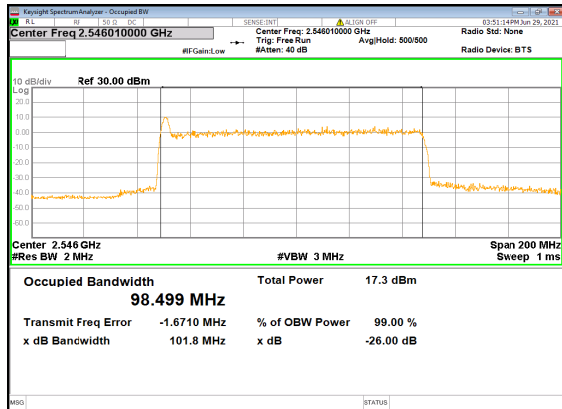
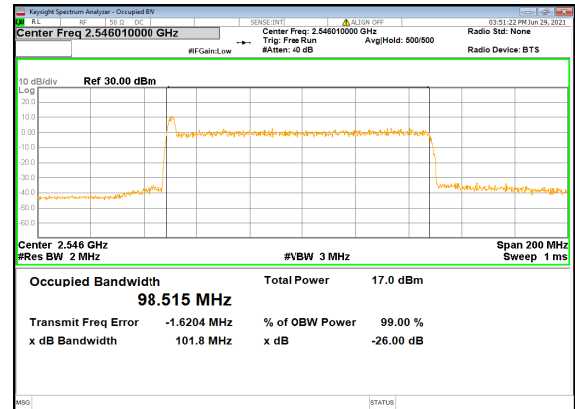
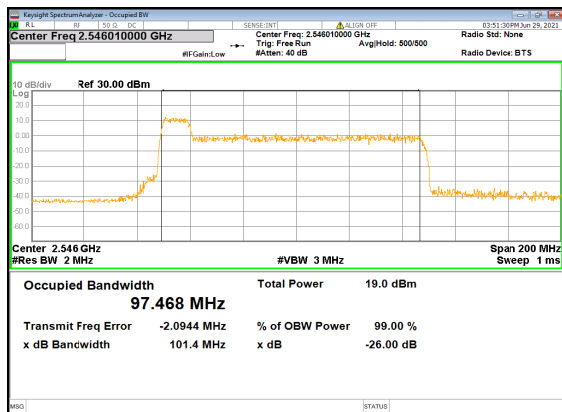
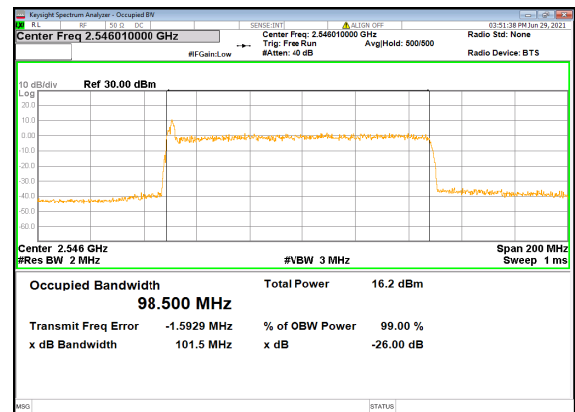
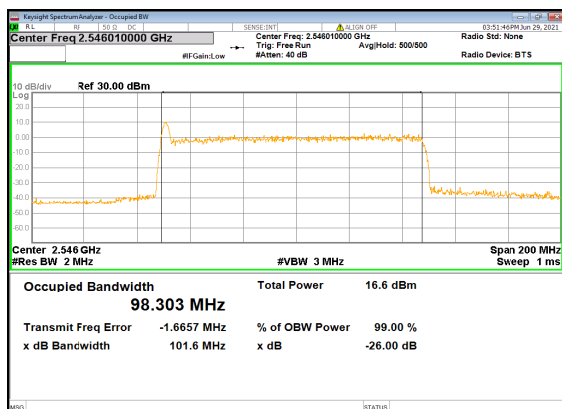
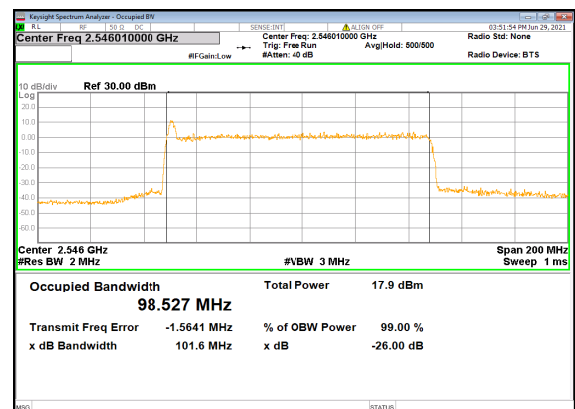
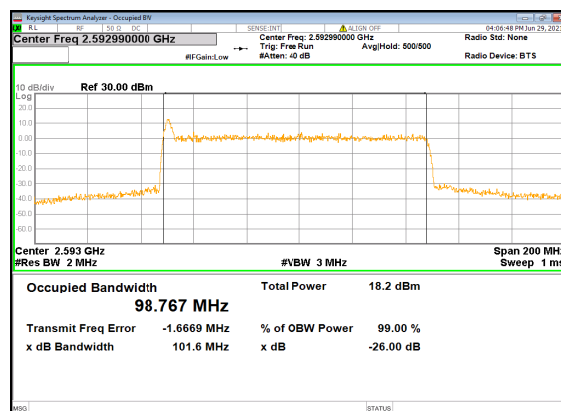


N41(90M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_525996_CHN41(90M)_DFT-s-OFDM_QPSK_Outer_Full
525996_CHN41(90M)_DFT-s-OFDM_16
QAM_Outer_Full_525996_CHN41(90M)_DFT-s-OFDM_64
QAM_Outer_Full_525996_CHN41(90M)_DFT-s-OFDM_256
QAM_Outer_Full_525996_CHN41(90M)_CP-OFDM_QPSK_Outer_Full_52
5996_CH

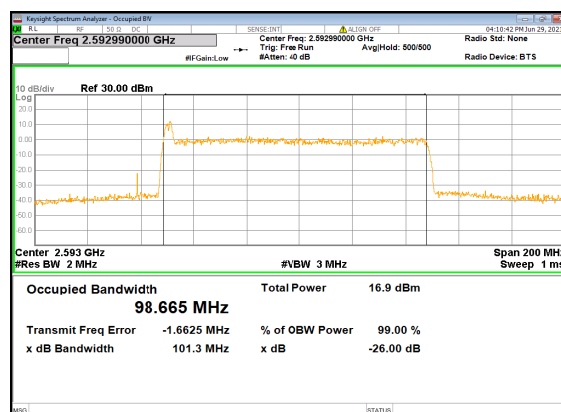
N41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Fu
II_Low_CHN41(100M)_DFT-s-OFDM_QPSK_Outer_Full
_Low_CHN41(100M)_DFT-s-OFDM_16
QAM_Outer_Full_Low_CHN41(100M)_DFT-s-OFDM_64
QAM_Outer_Full_Low_CHN41(100M)_DFT-s-OFDM_256
QAM_Outer_Full_Low_CHN41(100M)_CP-OFDM_QPSK_Outer_Full_L
ow_CH



N41(100M)_DFT-s-OFDM_QPSK_Outer_Full
Mid CH

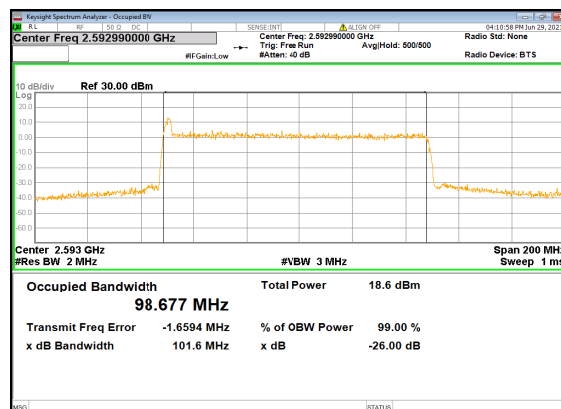


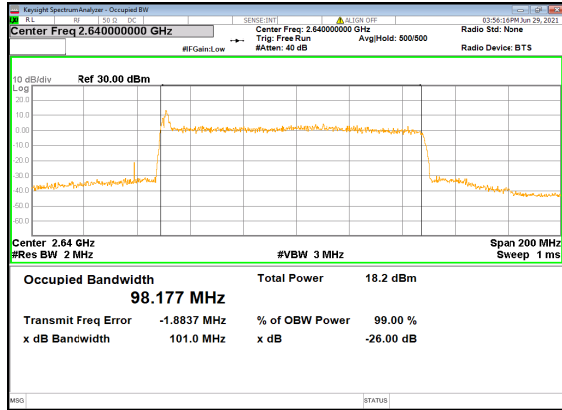
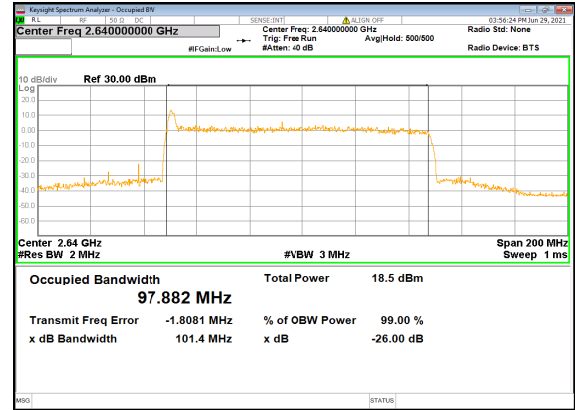
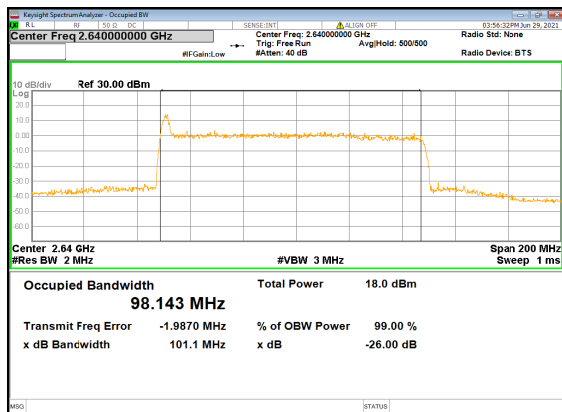
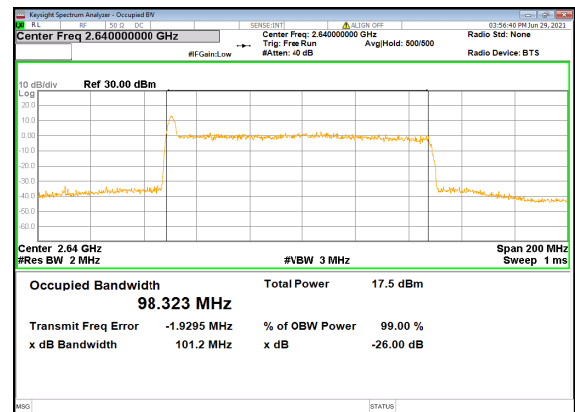
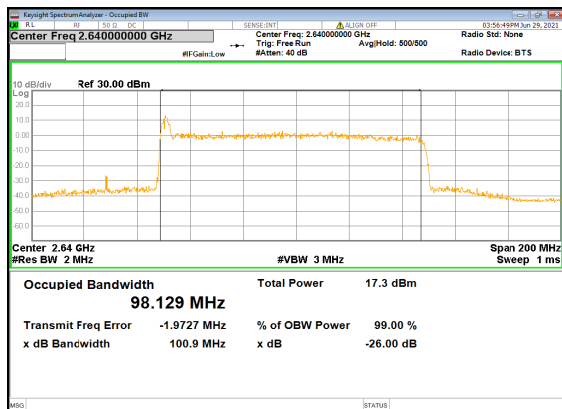
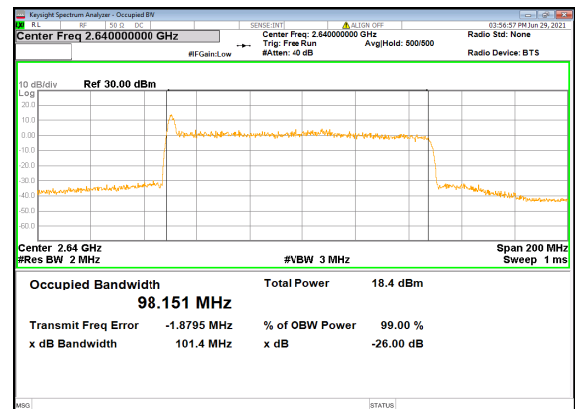
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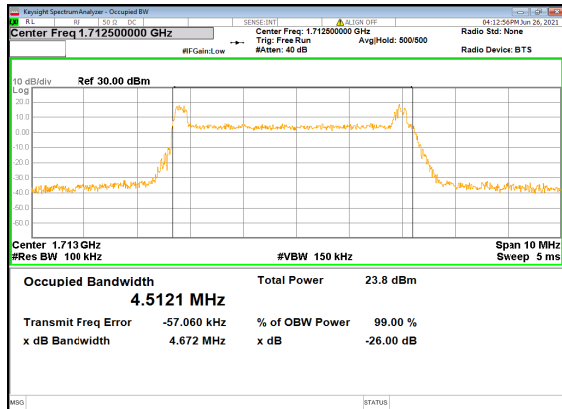
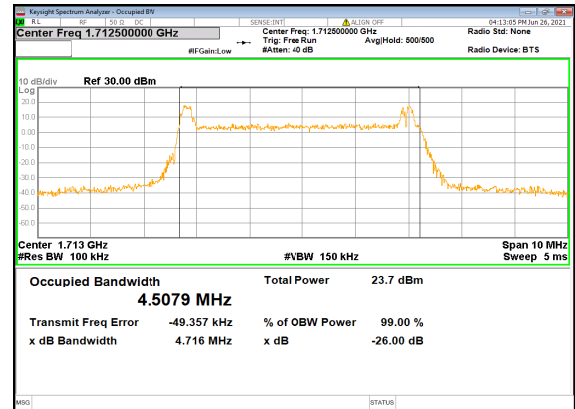
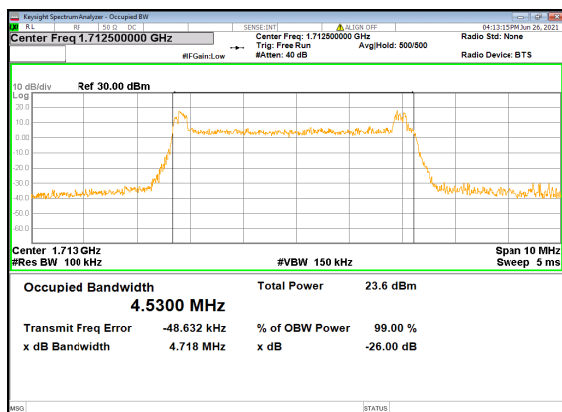
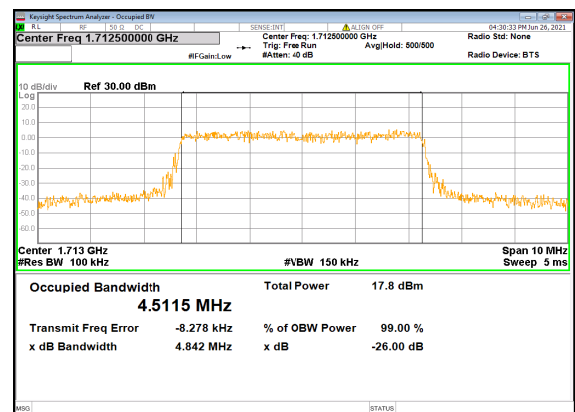
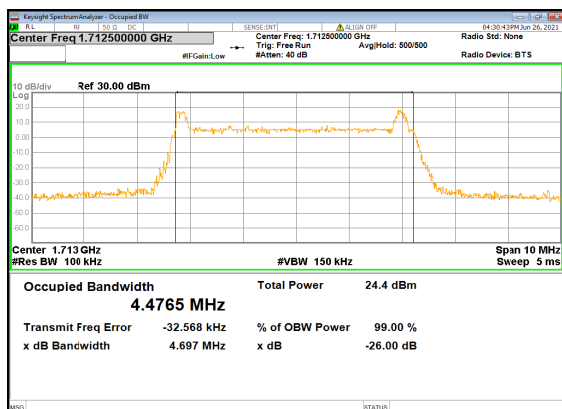
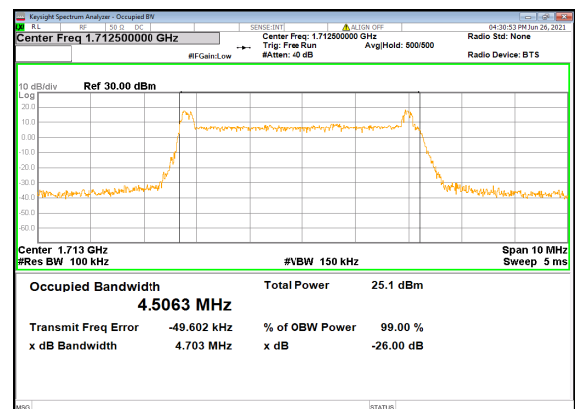


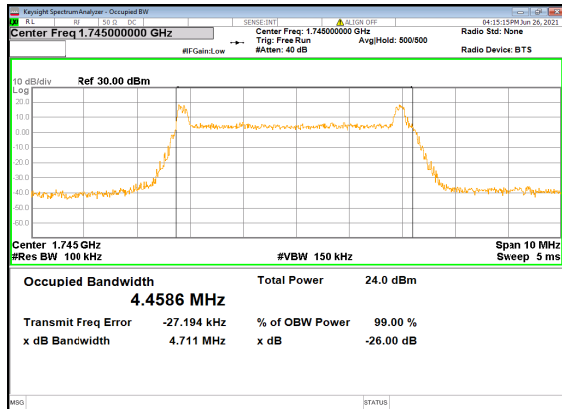
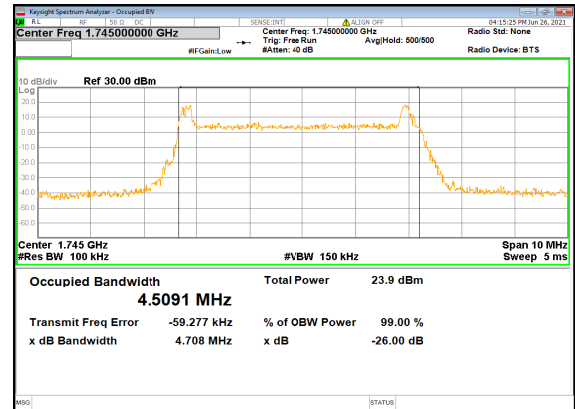
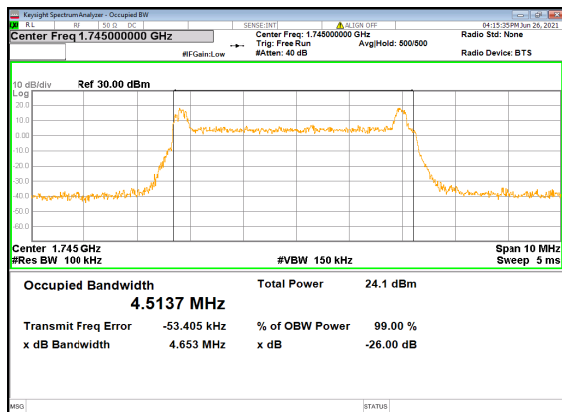
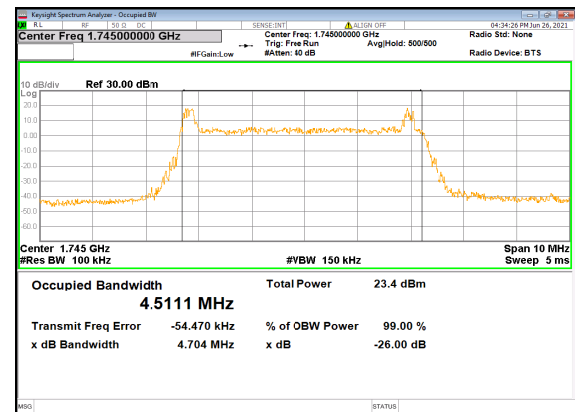
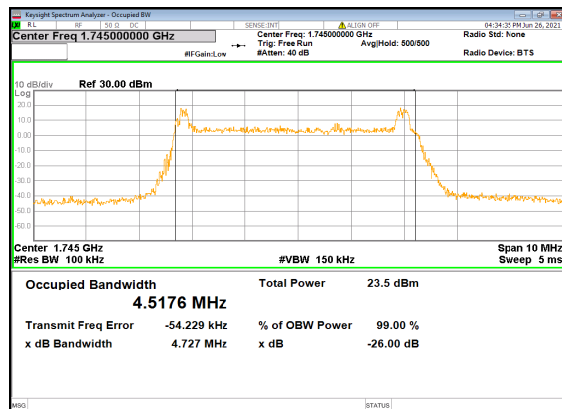
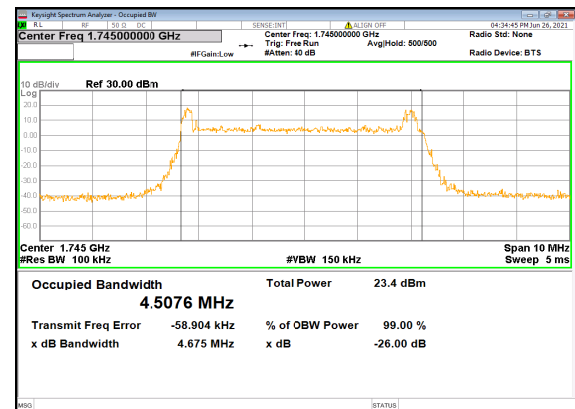
N41(100M)_CP-OFDM_QPSK_Outer_Full_M

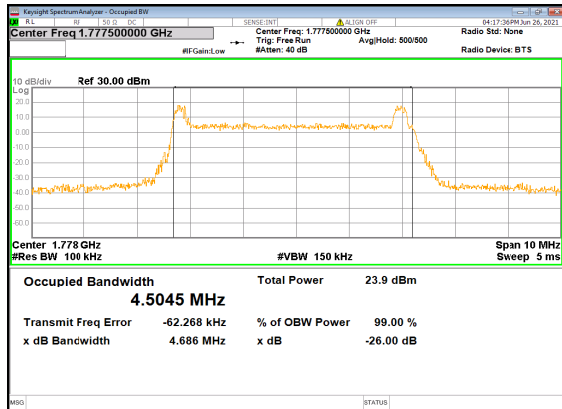
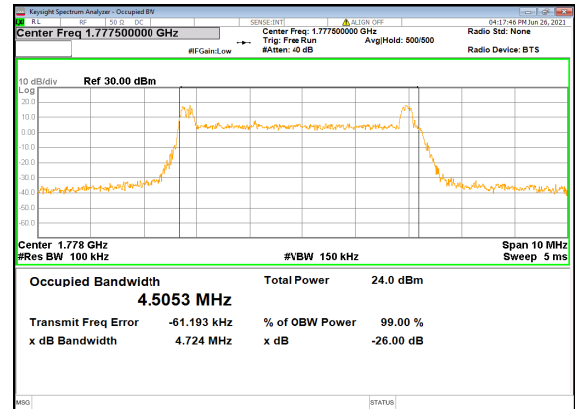
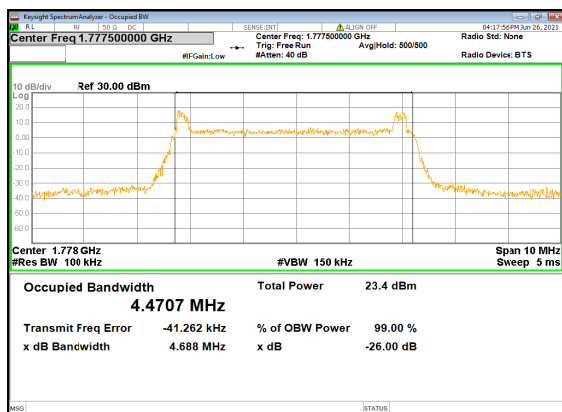
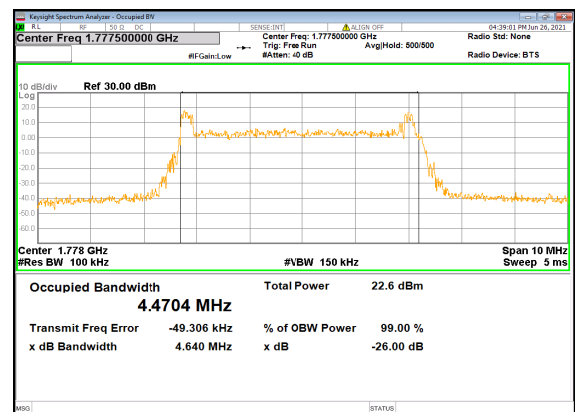
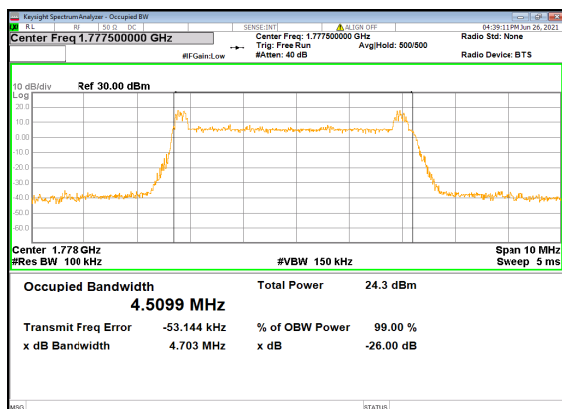
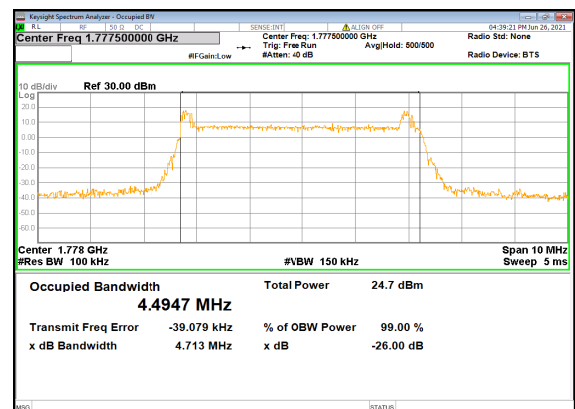
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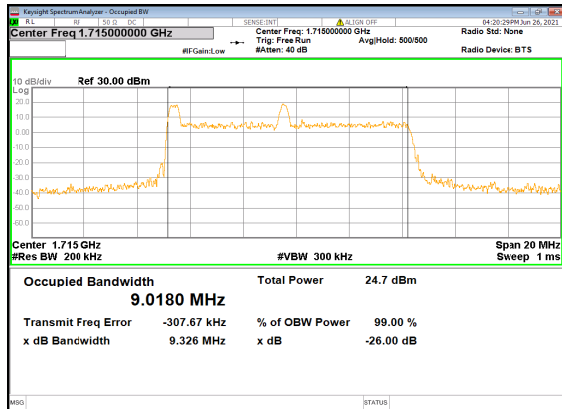
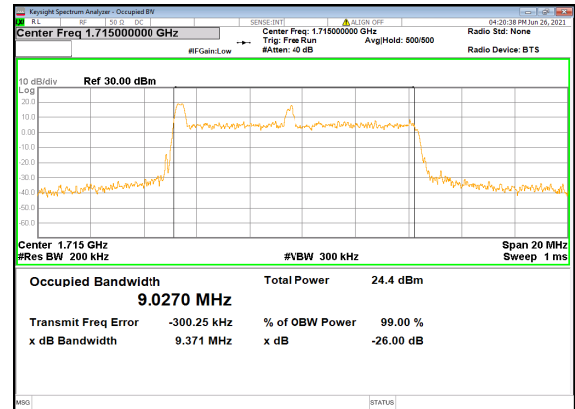
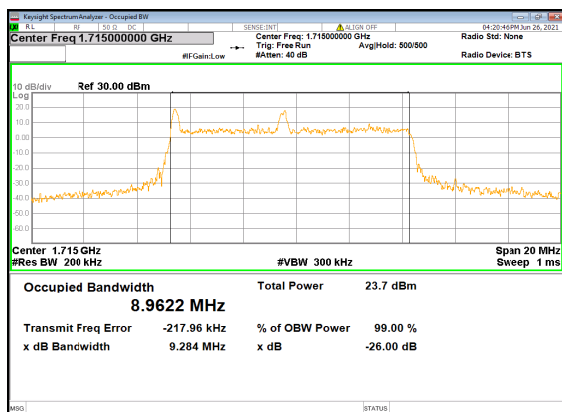
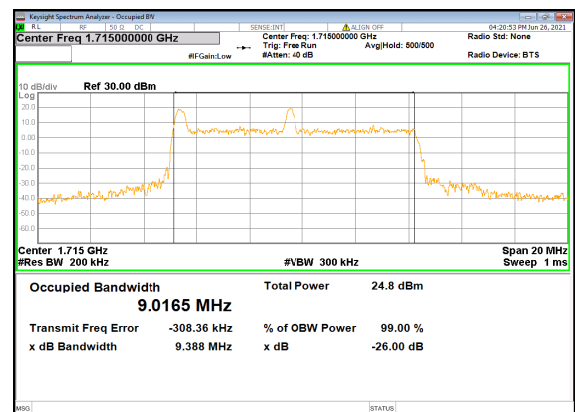
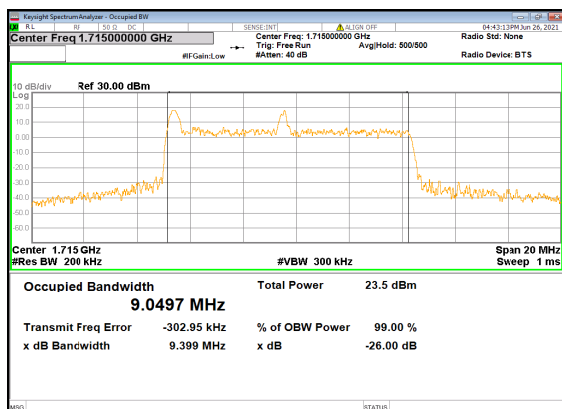
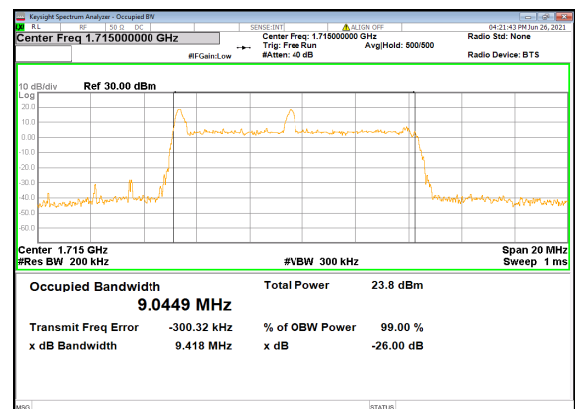


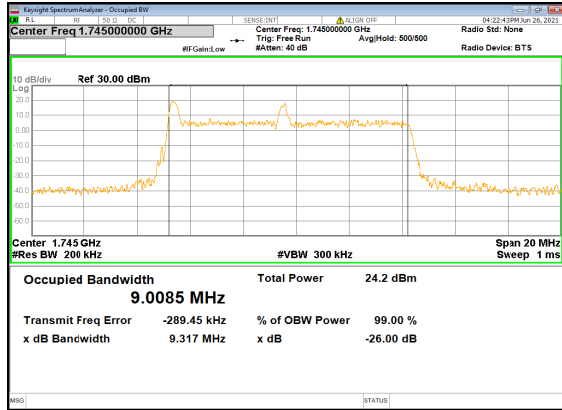
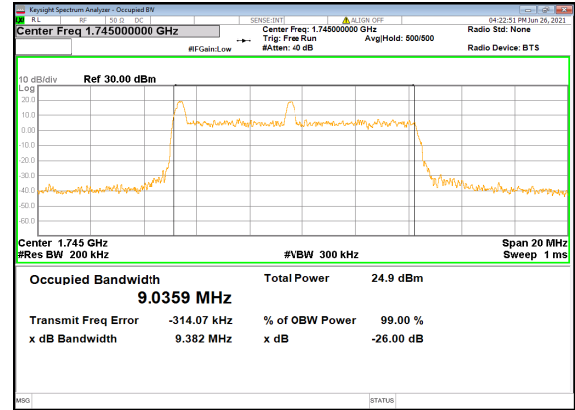
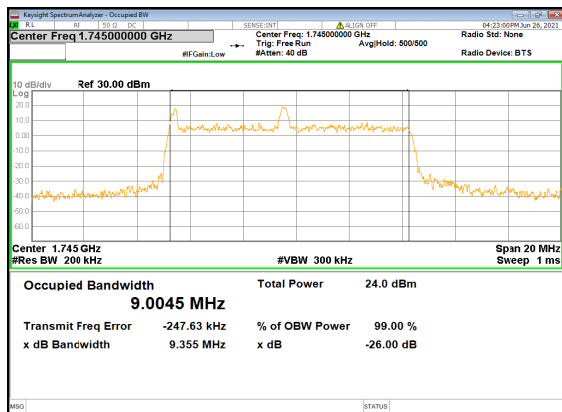
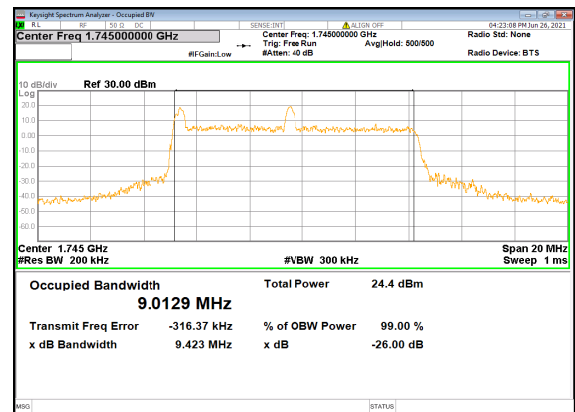
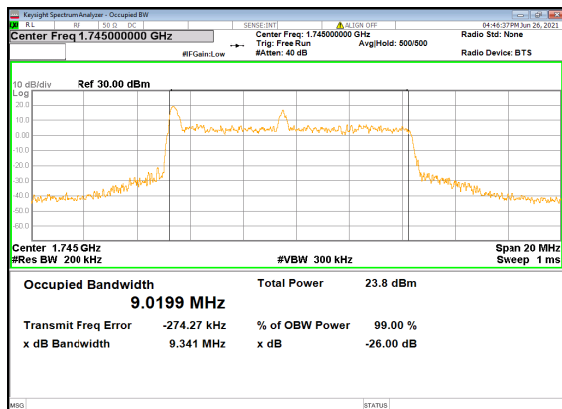
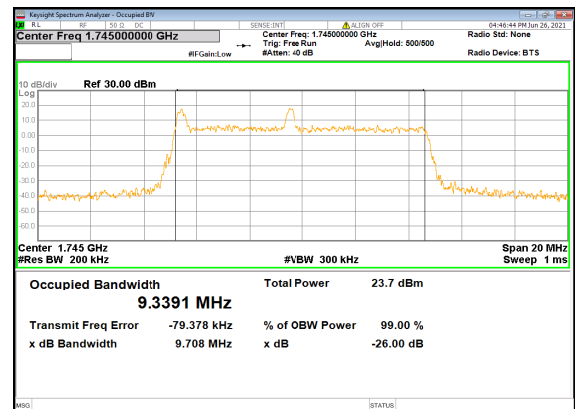
N41(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Fu
II_High_CHN41(100M)_DFT-s-OFDM_QPSK_Outer_Full
_High_CHN41(100M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CHN41(100M)_DFT-s-OFDM_64
QAM_Outer_Full_High_CHN41(100M)_DFT-s-OFDM_256
QAM_Outer_Full_High_CHN41(100M)_CP-OFDM_QPSK_Outer_Full_Hi
gh_CH

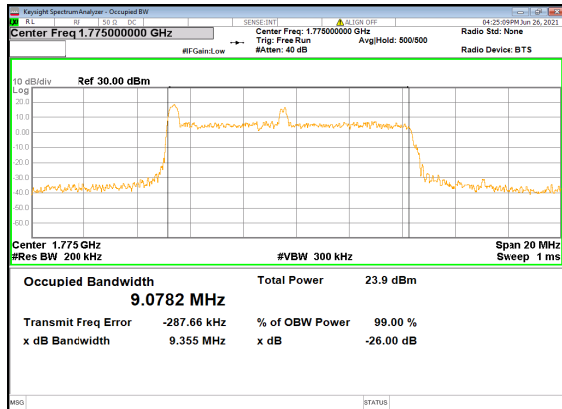
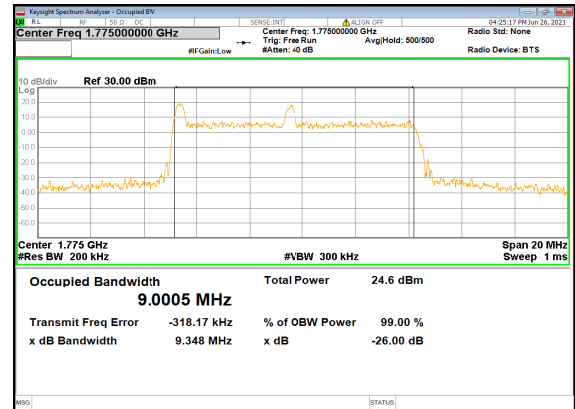
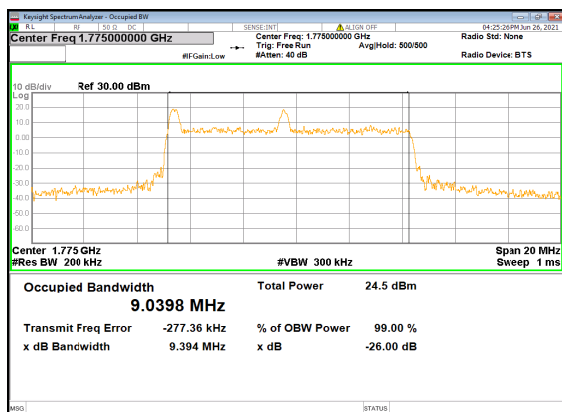
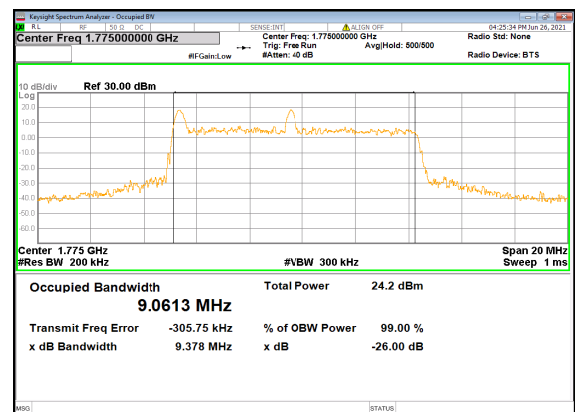
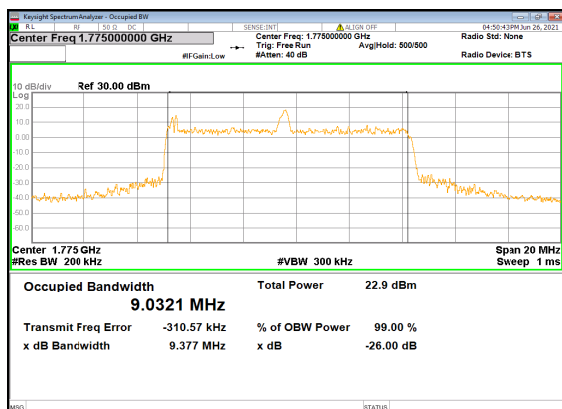
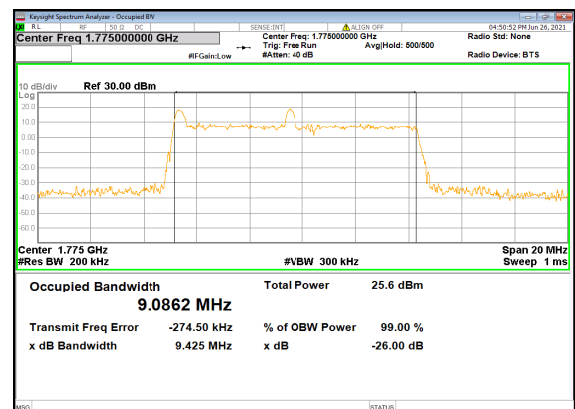
N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_
Low_CHN66(5M)_DFT-s-OFDM_QPSK_Outer_Full_L
ow_CHN66(5M)_DFT-s-OFDM_16
QAM_Outer_Full_Low_CHN66(5M)_DFT-s-OFDM_64
QAM_Outer_Full_Low_CHN66(5M)_DFT-s-OFDM_256
QAM_Outer_Full_Low_CHN66(5M)_CP-OFDM_QPSK_Outer_Full_Low
_CH

N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_
Mid_CHN66(5M)_DFT-s-OFDM_QPSK_Outer_Full_
Mid_CHN66(5M)_DFT-s-OFDM_16
QAM_Outer_Full_Mid_CHN66(5M)_DFT-s-OFDM_64
QAM_Outer_Full_Mid_CHN66(5M)_DFT-s-OFDM_256
QAM_Outer_Full_Mid_CHN66(5M)_CP-OFDM_QPSK_Outer_Full_Mid_
_CH

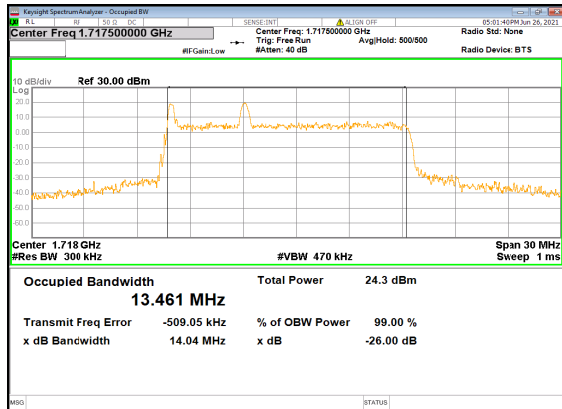
N66(5M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_
High_CHN66(5M)_DFT-s-OFDM_QPSK_Outer_Full_
High_CHN66(5M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CHN66(5M)_DFT-s-OFDM_64
QAM_Outer_Full_High_CHN66(5M)_DFT-s-OFDM_256
QAM_Outer_Full_High_CHN66(5M)_CP-OFDM_QPSK_Outer_Full_High_
CH

N66(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_Low_CHN66(10M)_DFT-s-OFDM_QPSK_Outer_Full
Low_CHN66(10M)_DFT-s-OFDM_16
QAM_Outer_Full_Low_CHN66(10M)_DFT-s-OFDM_64
QAM_Outer_Full_Low_CHN66(10M)_DFT-s-OFDM_256
QAM_Outer_Full_Low_CHN66(10M)_CP-OFDM_QPSK_Outer_Full_Lo
w_CH

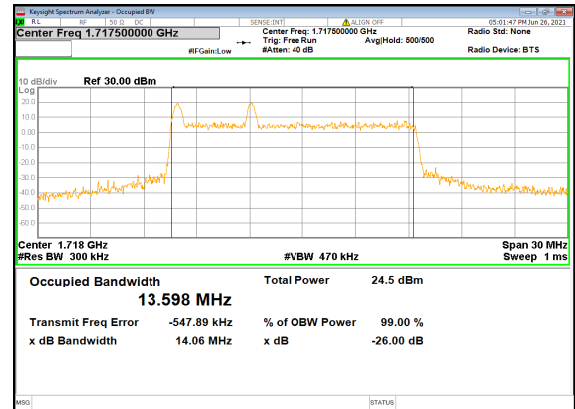
N66(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_Mid_CHN66(10M)_DFT-s-OFDM_QPSK_Outer_Full
Mid_CHN66(10M)_DFT-s-OFDM_16
QAM_Outer_Full_Mid_CHN66(10M)_DFT-s-OFDM_64
QAM_Outer_Full_Mid_CHN66(10M)_DFT-s-OFDM_256
QAM_Outer_Full_Mid_CHN66(10M)_CP-OFDM_QPSK_Outer_Full_Mi
d_CH

N66(10M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_High_CHN66(10M)_DFT-s-OFDM_QPSK_Outer_Full
High_CHN66(10M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CHN66(10M)_DFT-s-OFDM_64
QAM_Outer_Full_High_CHN66(10M)_DFT-s-OFDM_256
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h_CH

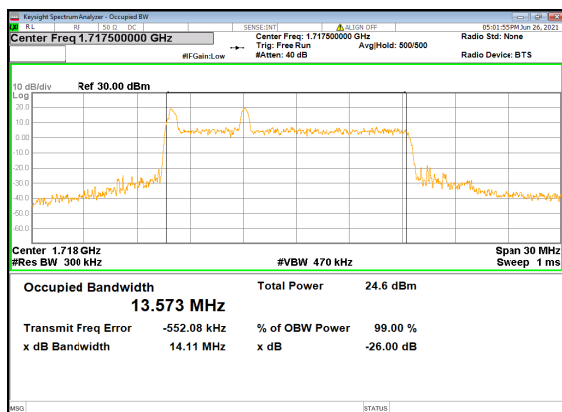
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
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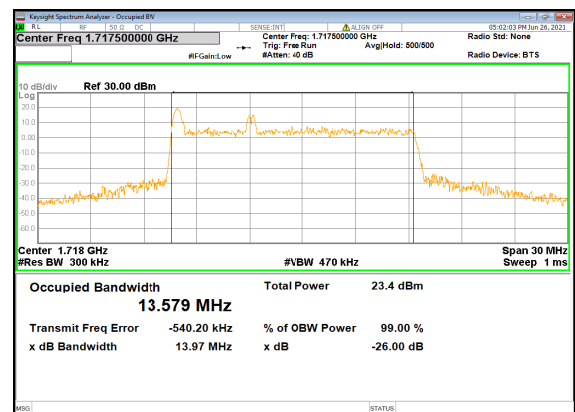
N66(15M)_DFT-s-OFDM_QPSK_Outer_Full_
Low_CH



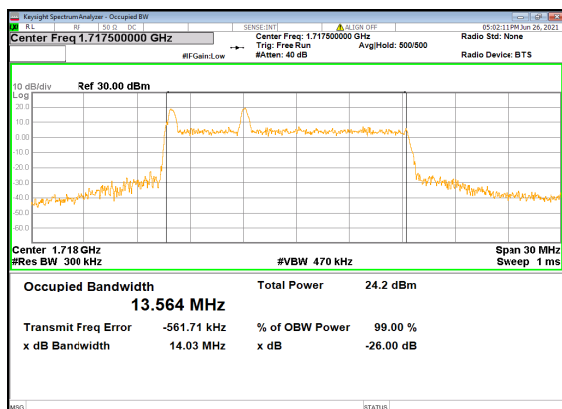
N66(15M)_DFT-s-OFDM_16
QAM Outer Full Low CH



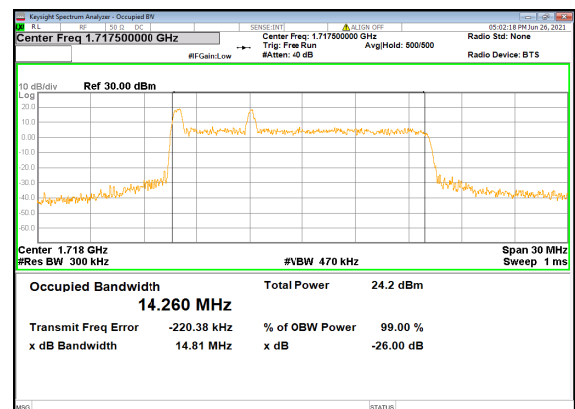
N66(15M)_DFT-s-OFDM_64
QAM Outer Full Low CH



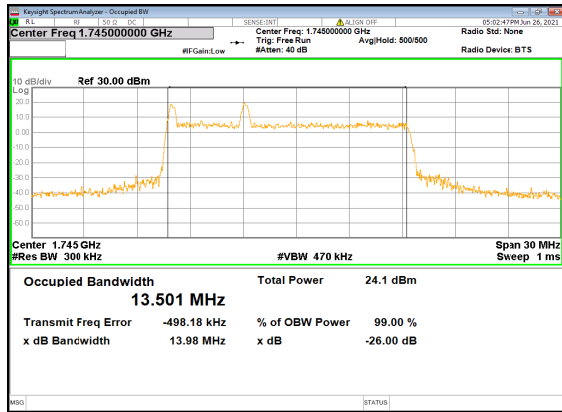
N66(15M)_DFT-s-OFDM_256
QAM Outer Full Low CH



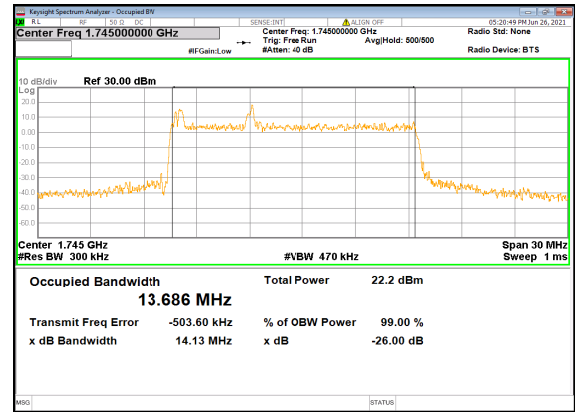
N66(15M)_CP-OFDM_QPSK_Outer_Full_Lo
w CH



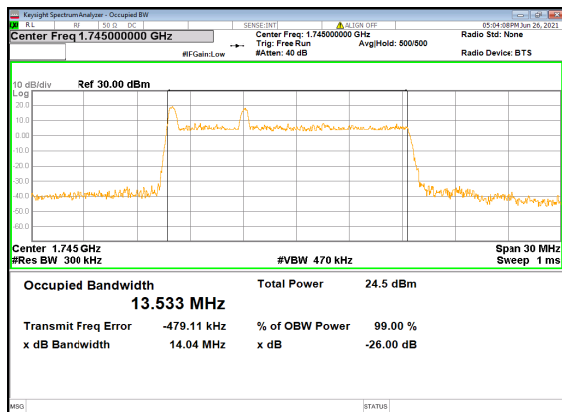
N66(15M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full _Mid_CH



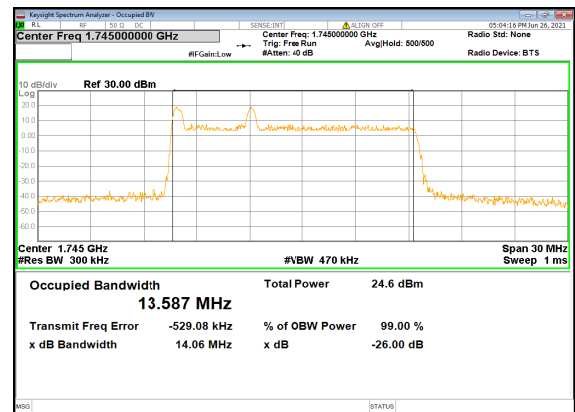
N66(15M)_DFT-s-OFDM_QPSK_Outer_Full Mid_CH



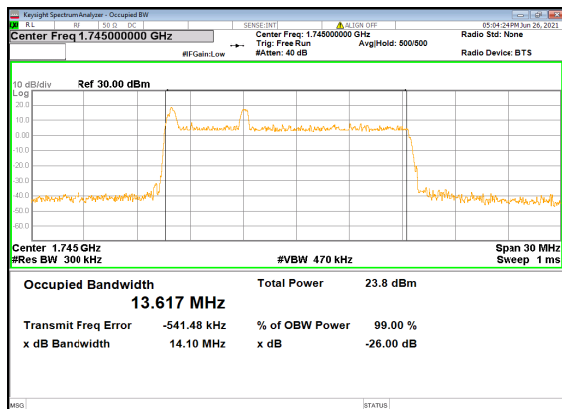
N66(15M)_DFT-s-OFDM_16 QAM_Outer_Full_Mid_CH



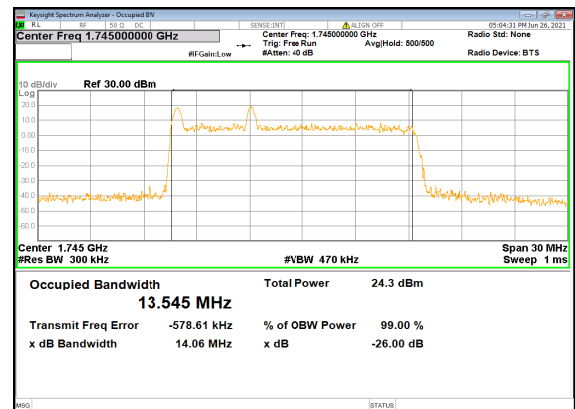
N66(15M)_DFT-s-OFDM_64 QAM_Outer_Full_Mid_CH

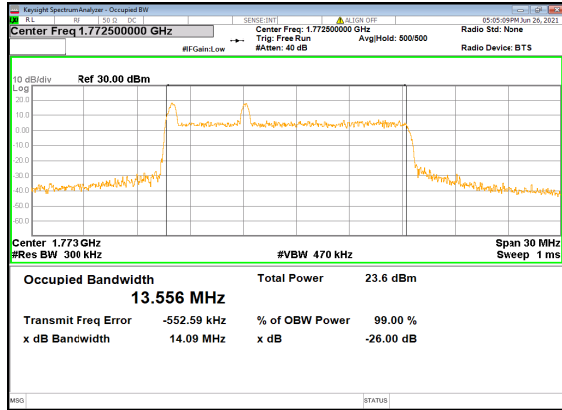
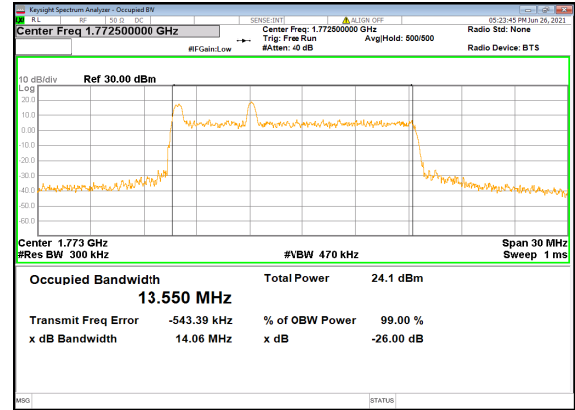
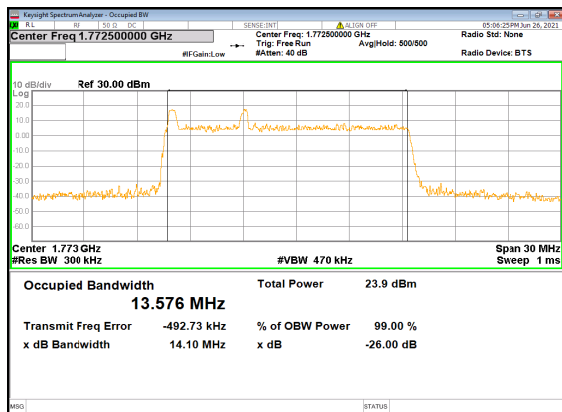
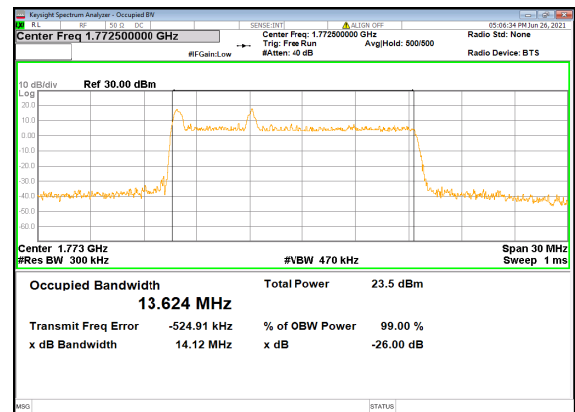
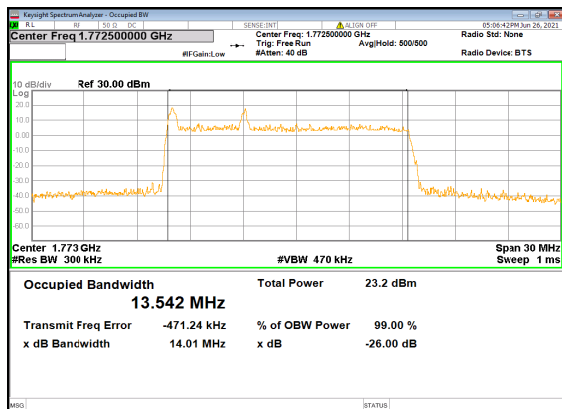
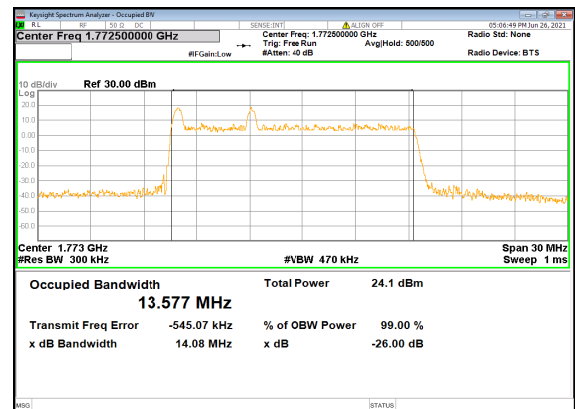


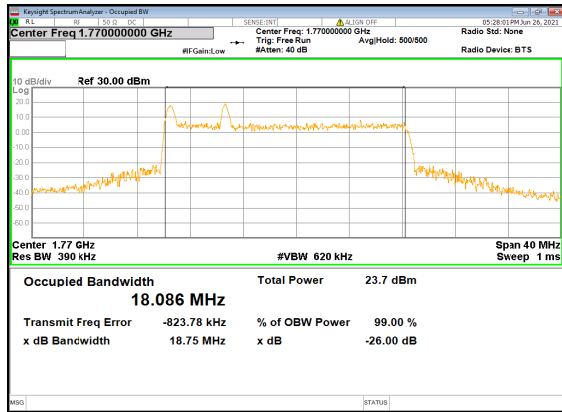
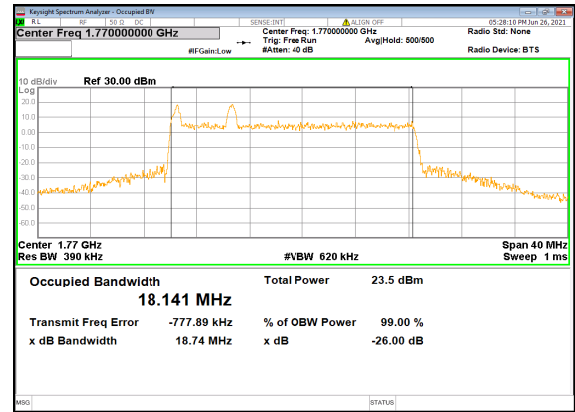
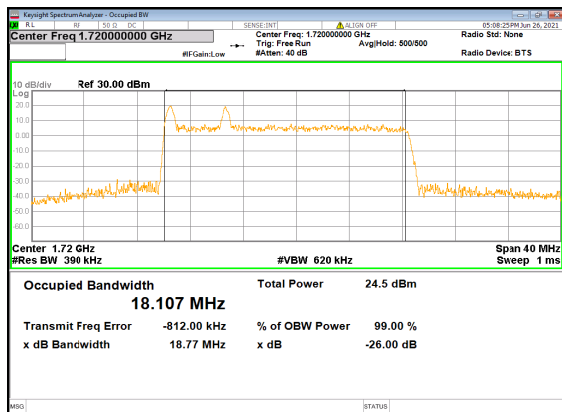
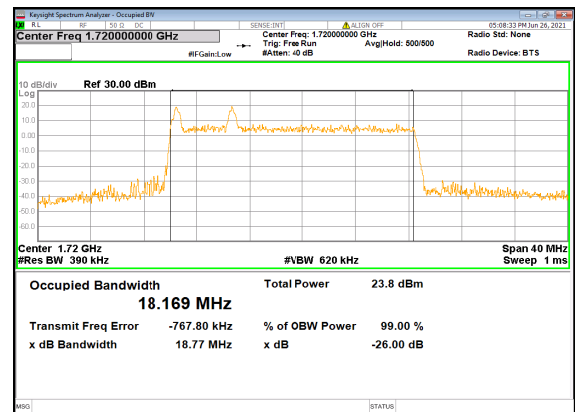
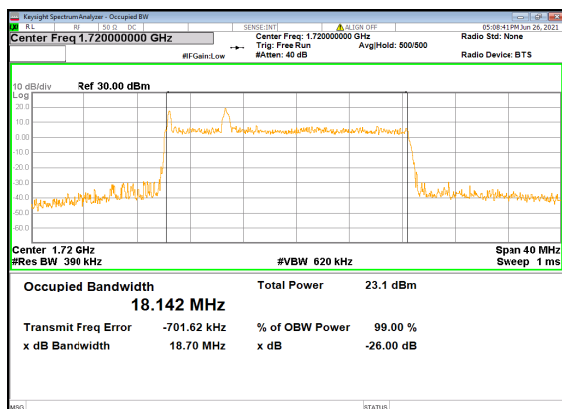
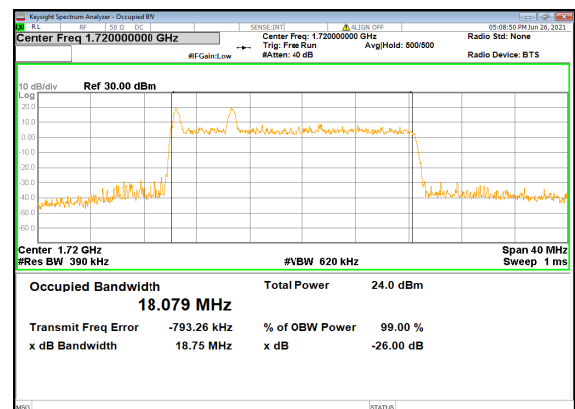
N66(15M)_DFT-s-OFDM_256 QAM_Outer_Full_Mid_CH

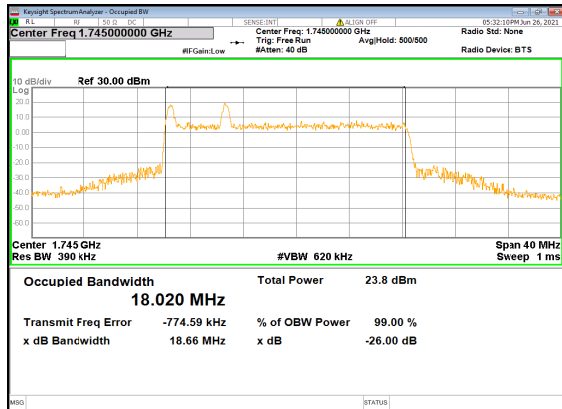
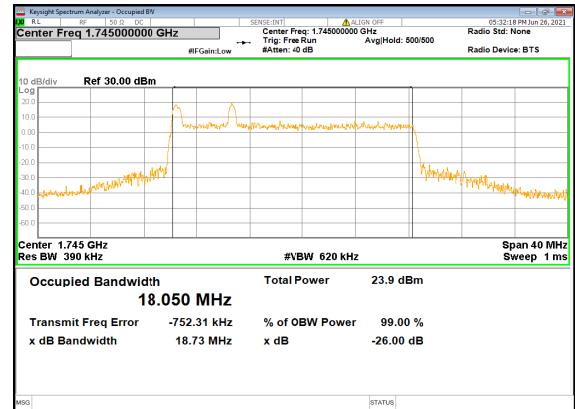
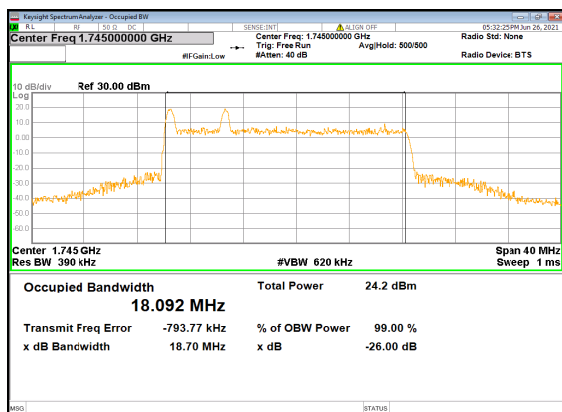
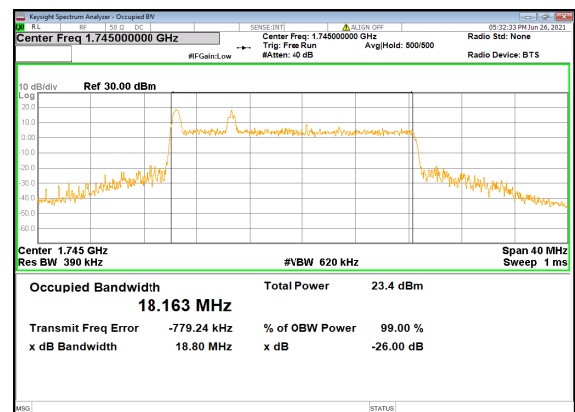
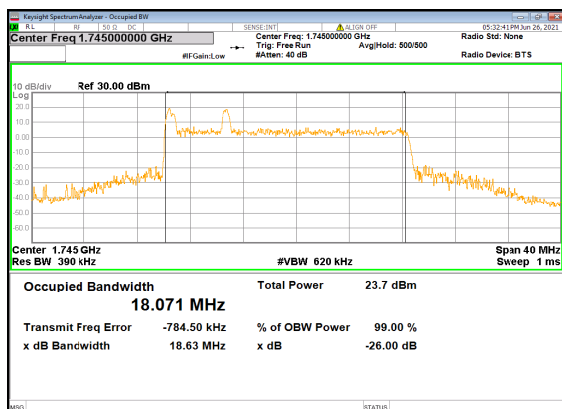
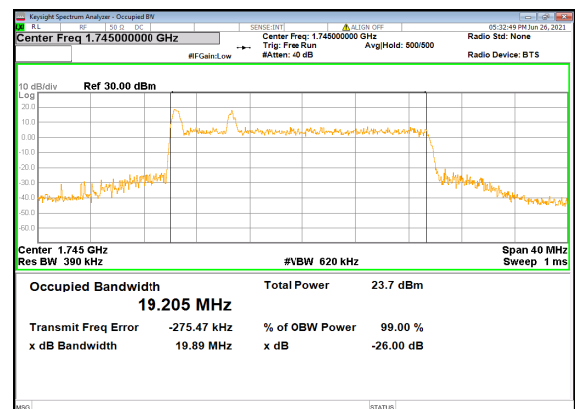


N66(15M)_CP-OFDM_QPSK_Outer_Full_Mi d_CH



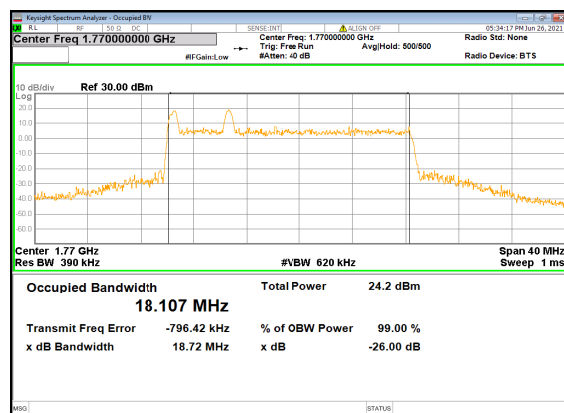
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_High_CHN66(15M)_DFT-s-OFDM_QPSK_Outer_Full
High_CHN66(15M)_DFT-s-OFDM_16
QAM_Outer_Full_High_CHN66(15M)_DFT-s-OFDM_64
QAM_Outer_Full_High_CHN66(15M)_DFT-s-OFDM_256
QAM_Outer_Full_High_CHN66(15M)_CP-OFDM_QPSK_Outer_Full_Hig
h_CH

N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_Low_CHN66(20M)_DFT-s-OFDM_QPSK_Outer_Full
Low_CHN66(20M)_DFT-s-OFDM_16
QAM_Outer_Full_Low_CHN66(20M)_DFT-s-OFDM_64
QAM_Outer_Full_Low_CHN66(20M)_DFT-s-OFDM_256
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w_CH

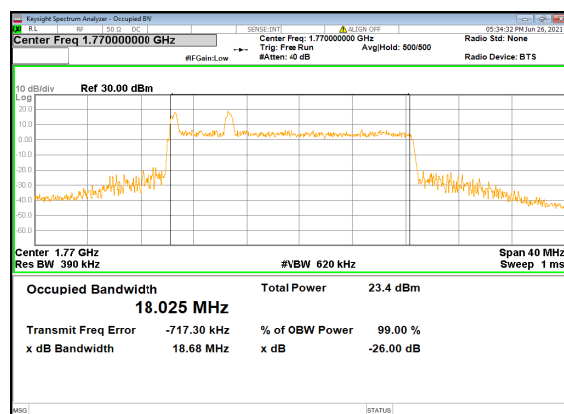
N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full
_Mid_CHN66(20M)_DFT-s-OFDM_QPSK_Outer_Full
Mid_CHN66(20M)_DFT-s-OFDM_16
QAM_Outer_Full_Mid_CHN66(20M)_DFT-s-OFDM_64
QAM_Outer_Full_Mid_CHN66(20M)_DFT-s-OFDM_256
QAM_Outer_Full_Mid_CHN66(20M)_CP-OFDM_QPSK_Outer_Full_Mi
d_CH



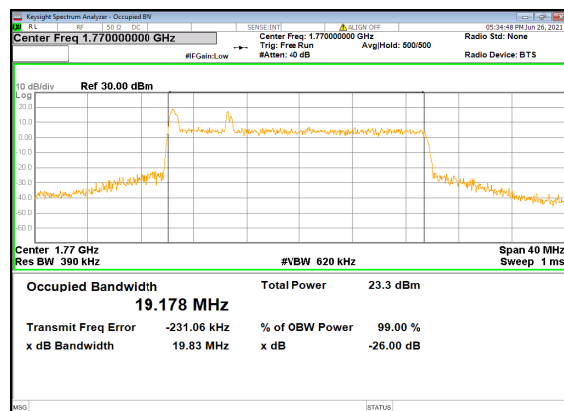
N66(20M)_DFT-s-OFDM_QPSK_Outer_Full_
High CH



N66(20M)_DFT-s-OFDM_64
QAM Outer Full High CH



N66(20M)_CP-OFDM_QPSK_Outer_Full_High CH



2.3. Frequency Stability

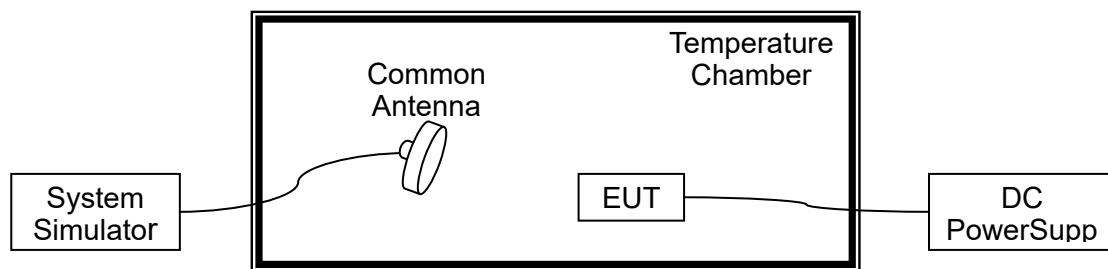
2.3.1. Requirement

According to FCC section 2.1055 & 27.54, the frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. According to FCC section 2.1055, the test conditions are:

- (a) The temperature is varied from -30°C to $+50^{\circ}\text{C}$ at intervals of not more than 10°C .
- (b) For hand carried battery powered equipment, the primary supply voltage is reduced to the battery operating end point which shall be specified by the manufacture. The supply voltage shall be measured at the input to the cable normally provided with the equipment, or at the power supply terminals if cables are not normally provided.

Note: The operating temperature of EUT is from 0°C to 35°C , which are specified by the applicant.

2.3.2. Test Description



The EUT which is powered by the DC Power Supply directly, is located in the Temperature Chamber. The EUT is commanded by the System Simulator (SS) to operate at the maximum output power. A call is established between the EUT and the SS via a Common Antenna.

2.3.3. Test procedure

KDB 971168 D01v03 Section 9.0 and ANSI/TIA-603-E-2016.

2.3.4. Test Result

The nominal, highest and lowest extreme voltages are separately 8.9VDC, 7.74VDC and 6.8VDC, which are specified by the applicant; the normal temperature here used is 20°C .



NR n5, QPSK, Channel 167300, SCS 15kHz, Frequency 836.5MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	7.74	+20 (Ref)	-30	-0.036	PASS
100		0	-19	-0.023	
100		+10	-16	-0.019	
100		+20	-14	-0.017	
100		+30	36	0.043	
100		+35	29	0.035	
115	8.9	+20	-35	-0.042	
85	6.8	+20	-18	-0.022	

NR n7, QPSK, Channel 507000, SCS 15kHz, Frequency 2535MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	7.74	+20 (Ref)	28	0.012	PASS
100		0	23	0.006	
100		+10	-29	-0.006	
100		+20	-30	-0.007	
100		+30	14	0.011	
100		+35	-30	0.005	
115	8.9	+20	22	-0.006	
85	6.8	+20	26	0.006	



NR n38, QPSK, Channel 519000, SCS 30kHz, Frequency 2595MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	7.74	+20 (Ref)	18	0.007	PASS
100		0	34	0.013	
100		+10	16	0.006	
100		+20	-13	-0.005	
100		+30	-33	-0.013	
100		+35	19	0.007	
115	8.9	+20	29	0.011	
85	6.8	+20	-15	-0.006	

NR n41, QPSK, Channel 518598, SCS 30kHz, Frequency 2593MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	7.74	+20 (Ref)	22	0.008	PASS
100		0	28	0.011	
100		+10	-21	-0.008	
100		+20	26	0.010	
100		+30	-35	-0.013	
100		+35	-27	-0.010	
115	8.9	+20	22	0.008	
85	6.8	+20	33	0.013	





REPORT No.: SZ21040341W12

NR n66, QPSK, Channel 349000, SCS 15kHz, Frequency 1745MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	7.74	+20 (Ref)	20	0.011	PASS
100		0	23	0.013	
100		+10	15	0.009	
100		+20	-15	-0.009	
100		+30	15	0.009	
100		+35	32	0.018	
115	8.9	+20	24	0.014	
85	6.8	+20	28	0.016	



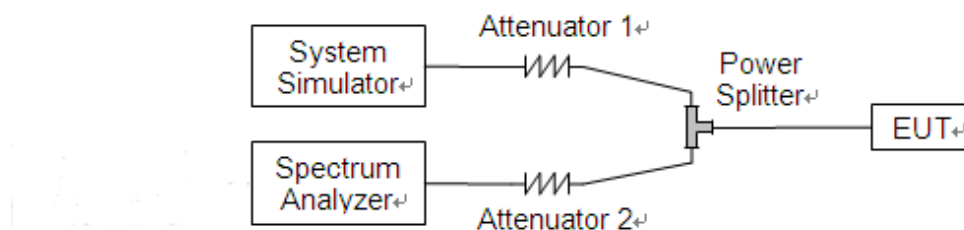
2.4. Peak to Average Radio

2.4.1. Requirement

According to FCC section 24.232(d), the peak to average ratio (PAR) of the transmission may not exceed 13dB.

2.4.2. Test Description

Test Set:



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

2.4.3. Test procedure

KDB 971168 D01v03 Section 5.7 and ANSI/TIA-603-E-2016.

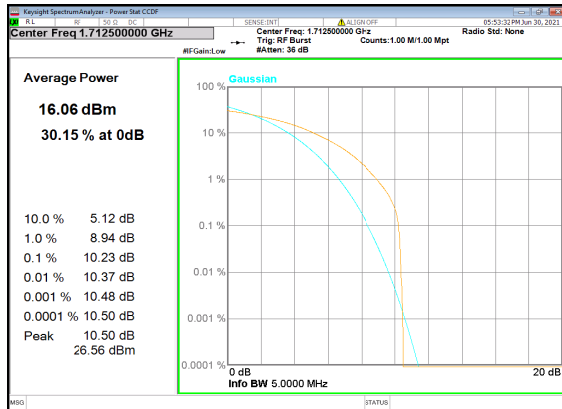
2.4.4. Test Result

Record the maximum PAPR level associated with a probability of 0.1%.

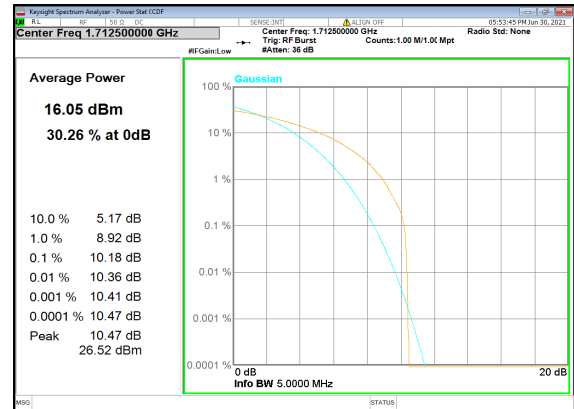


N66					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
5	Low	BPSK	10.23	<=13	PASS
5	Low	QPSK	10.18	<=13	PASS
5	Mid	BPSK	10.5	<=13	PASS
5	Mid	QPSK	10.46	<=13	PASS
5	High	BPSK	9.76	<=13	PASS
5	High	QPSK	9.7	<=13	PASS
10	Low	BPSK	10.22	<=13	PASS
10	Low	QPSK	10.35	<=13	PASS
10	Mid	BPSK	9.78	<=13	PASS
10	Mid	QPSK	9.86	<=13	PASS
10	High	BPSK	9.7	<=13	PASS
10	High	QPSK	8.16	<=13	PASS
15	Low	BPSK	10.26	<=13	PASS
15	Low	QPSK	10.23	<=13	PASS
15	Mid	BPSK	10.41	<=13	PASS
15	Mid	QPSK	10.52	<=13	PASS
15	High	BPSK	10.35	<=13	PASS
15	High	QPSK	10.33	<=13	PASS
20	Low	BPSK	10.17	<=13	PASS
20	Low	QPSK	10.19	<=13	PASS
20	Mid	BPSK	10.41	<=13	PASS
20	Mid	QPSK	10.47	<=13	PASS
20	High	BPSK	10.26	<=13	PASS
20	High	QPSK	10.27	<=13	PASS

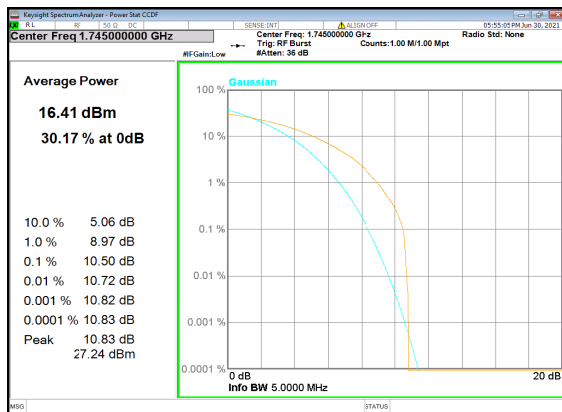
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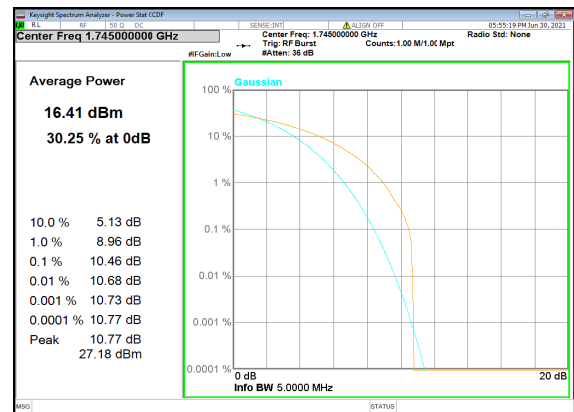
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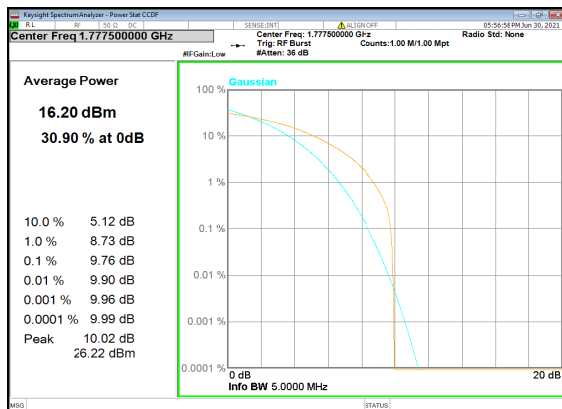
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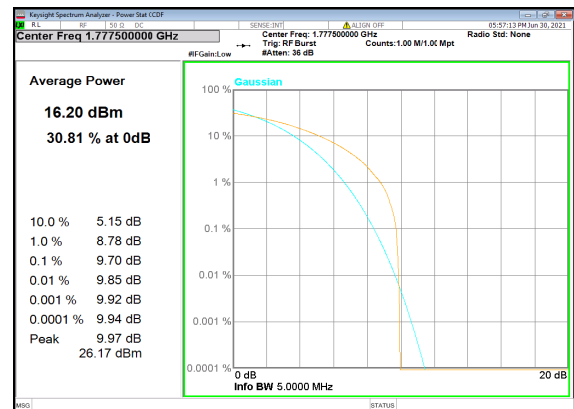
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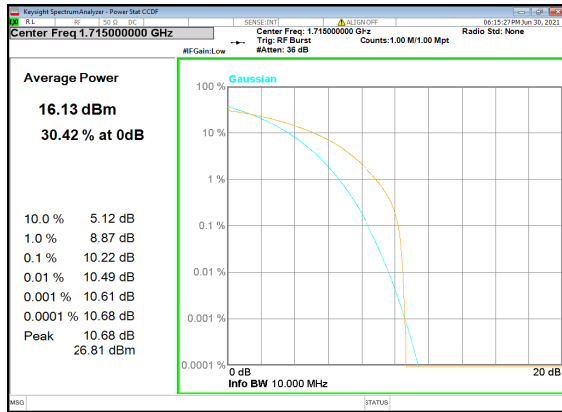
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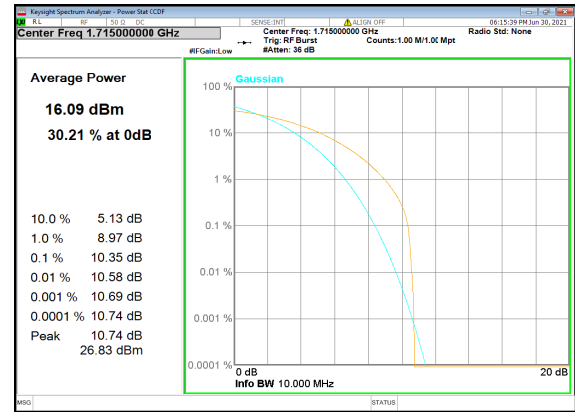
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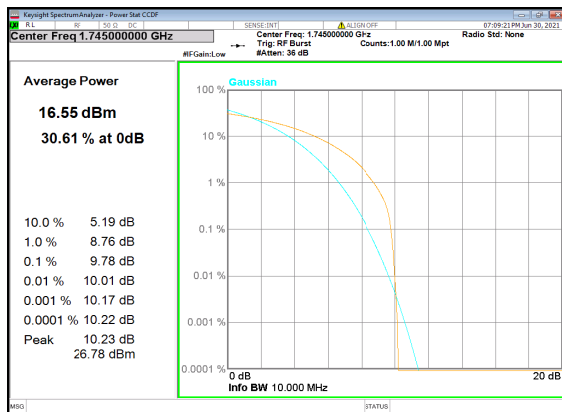
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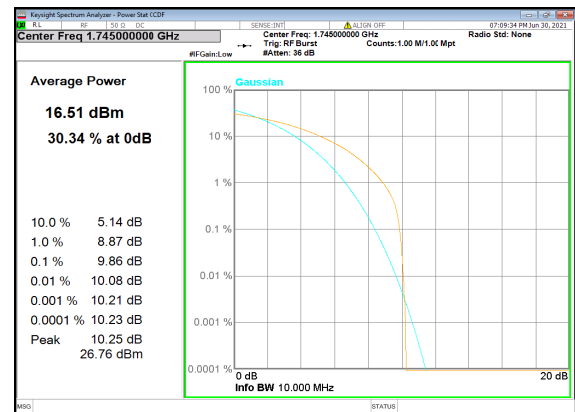
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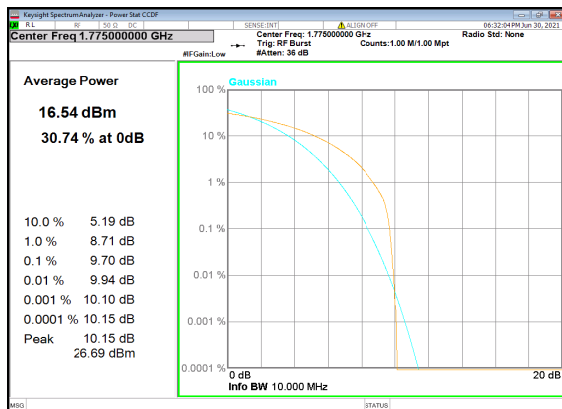
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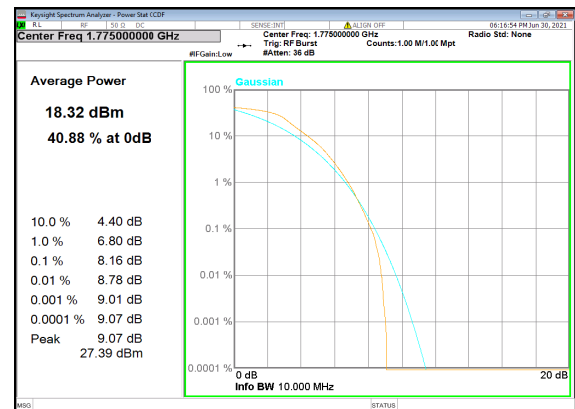
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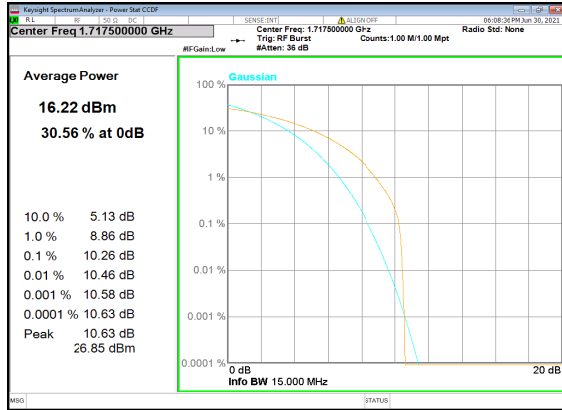
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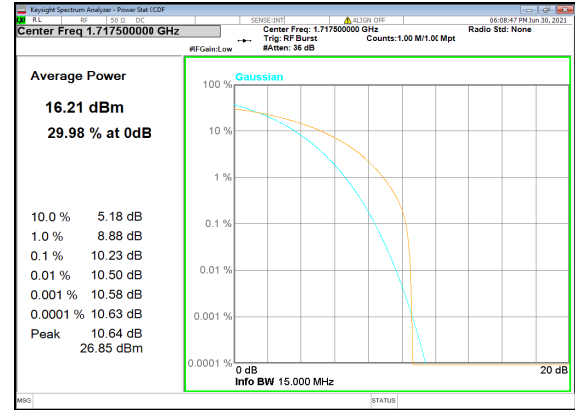
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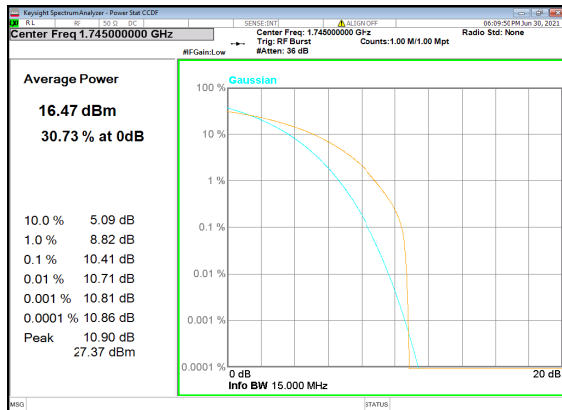
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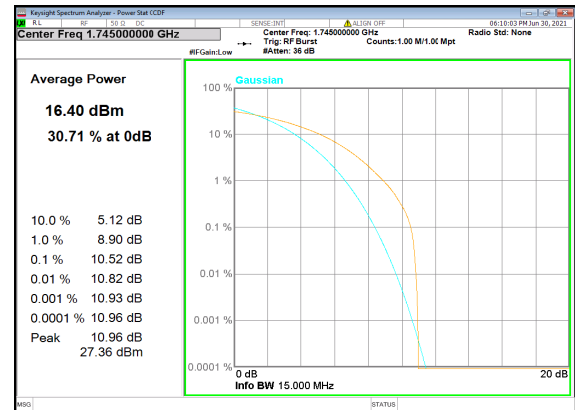
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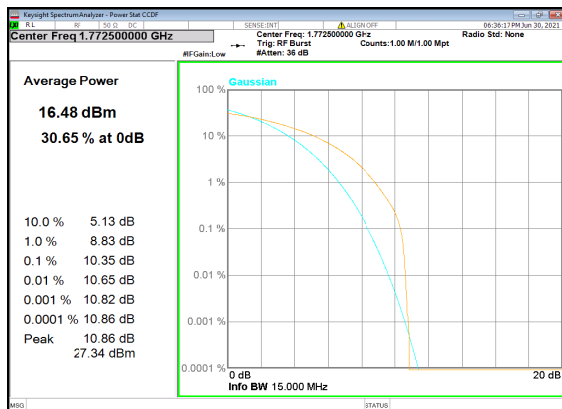
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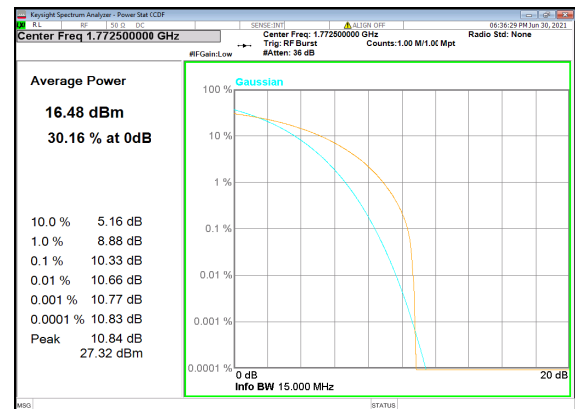
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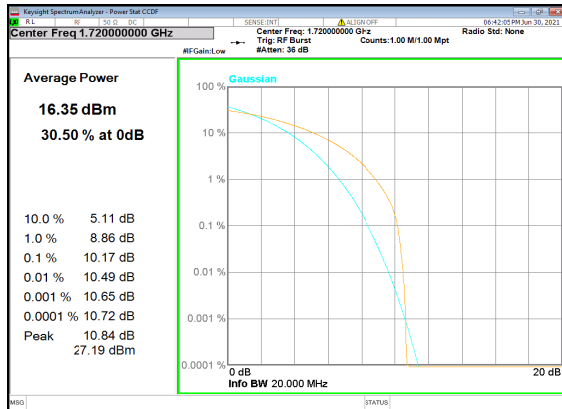
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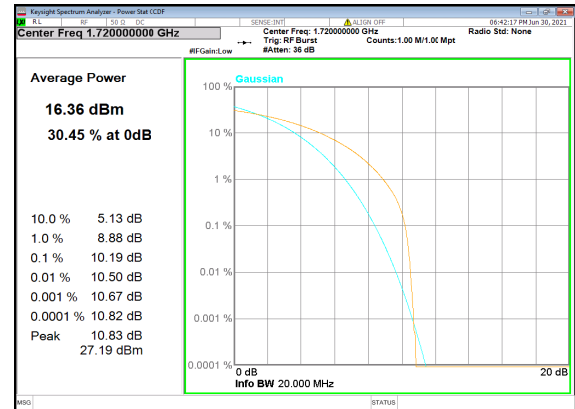
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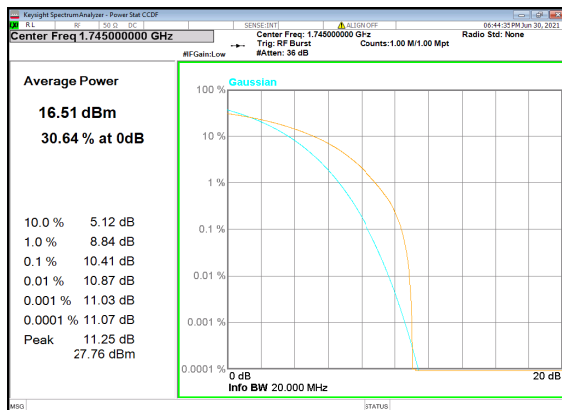
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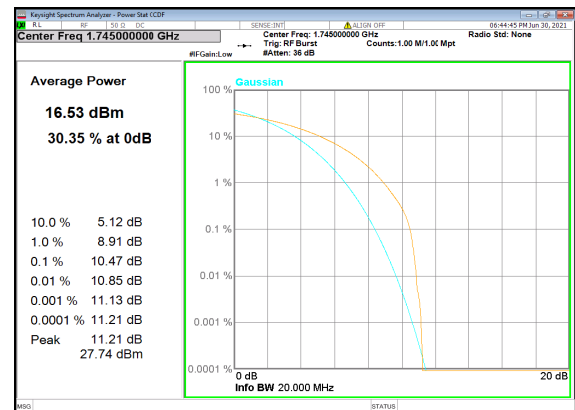
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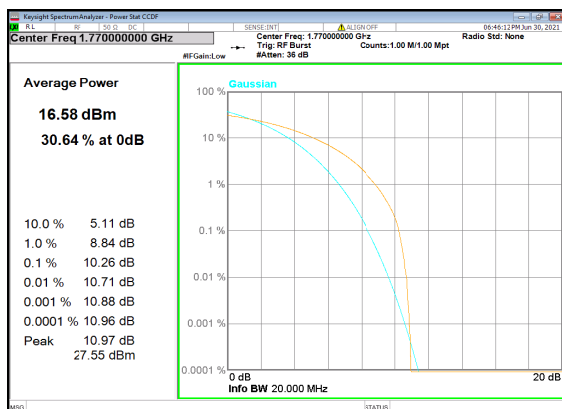
N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full _Mid_CH



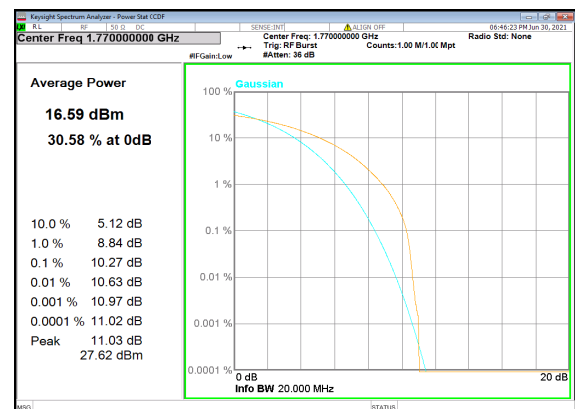
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N66(20M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full _High_CH



N66(20M)_DFT-s-OFDM_QPSK_Outer_Full High_CH



2.5. Conducted Spurious Emissions

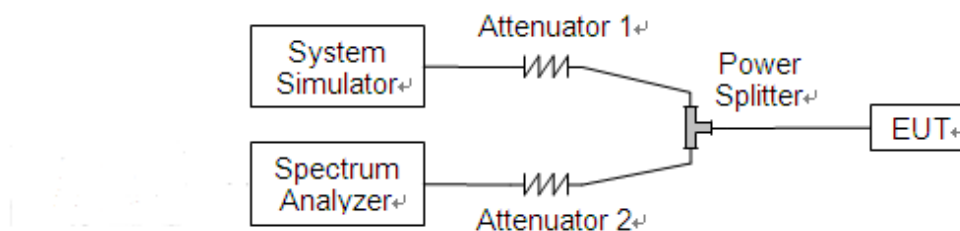
2.5.1. Requirement

According to FCC section 2.1051, the power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least $43 + 10 \log(P)$ dB. This calculated to be -13dBm.

Additional requirement for N7/N38/N41:

The power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least $55 + 10 \log(P)$ dB. This calculated to be -25dBm.

2.5.2. Test Description



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50 Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.



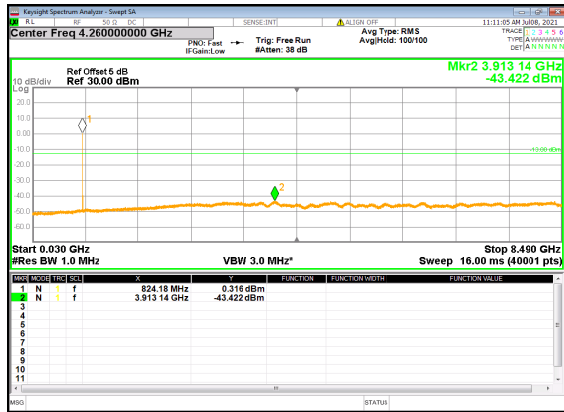
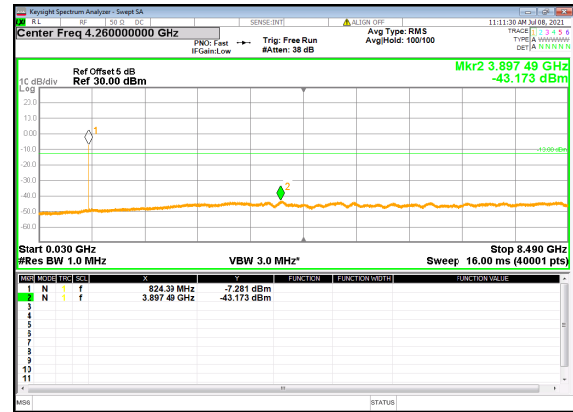
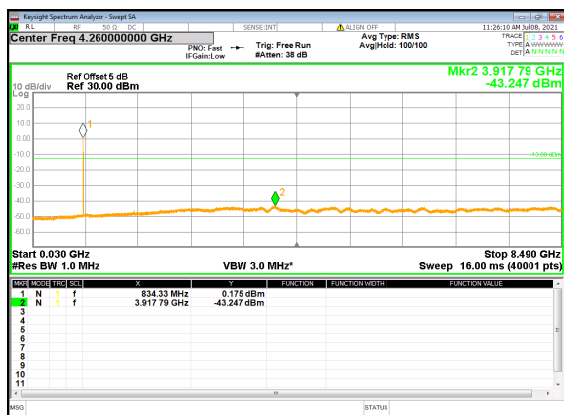
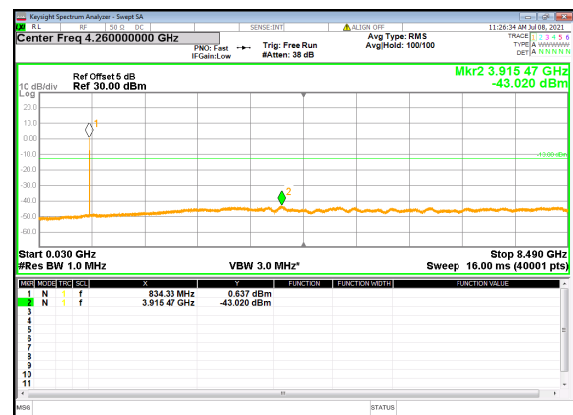
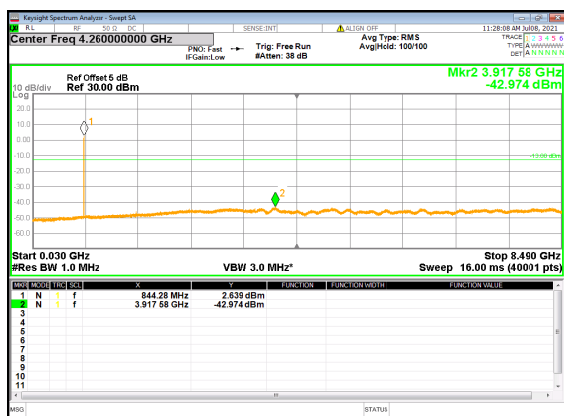
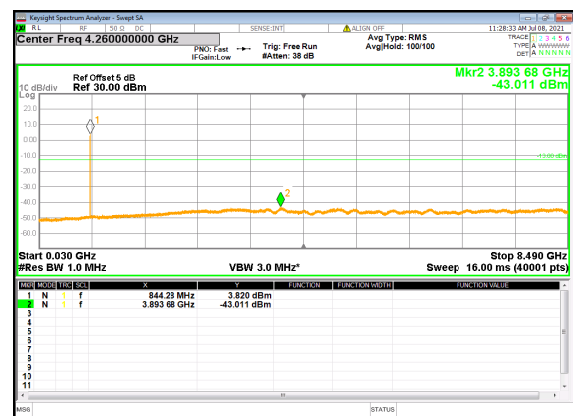
REPORT No.: SZ21040341W12

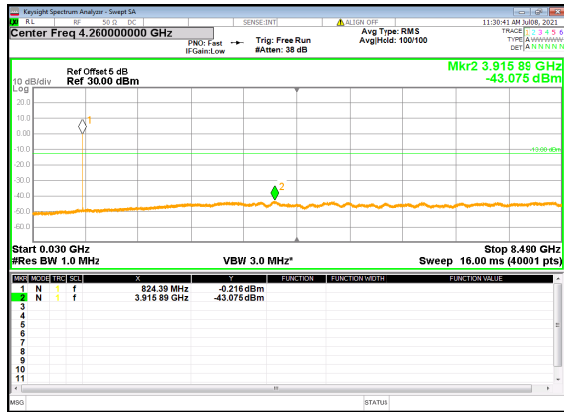
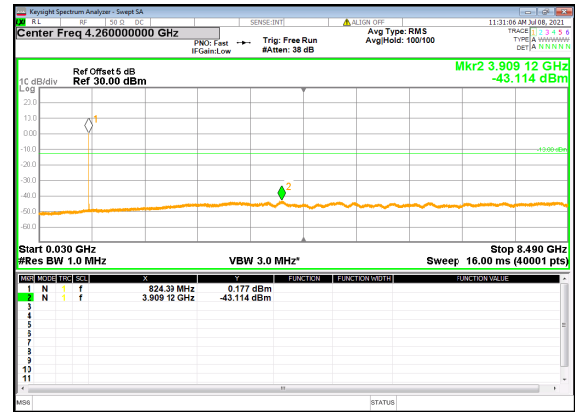
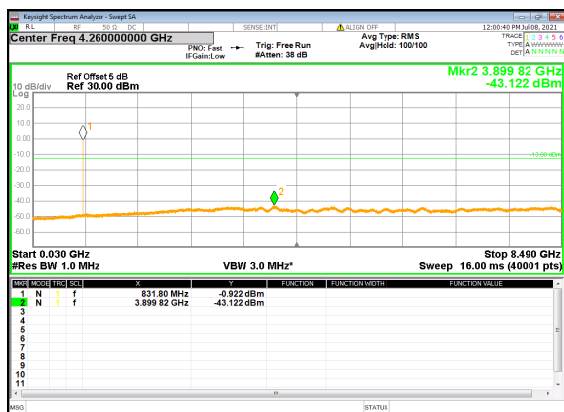
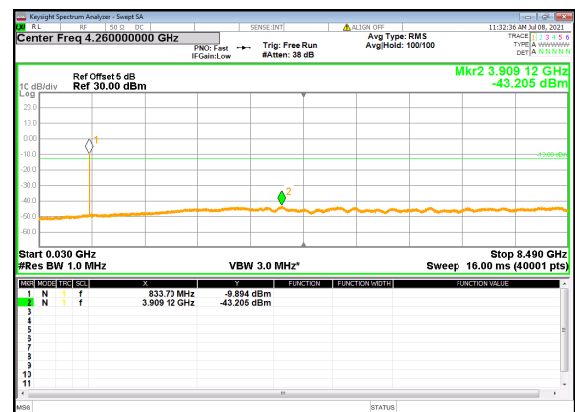
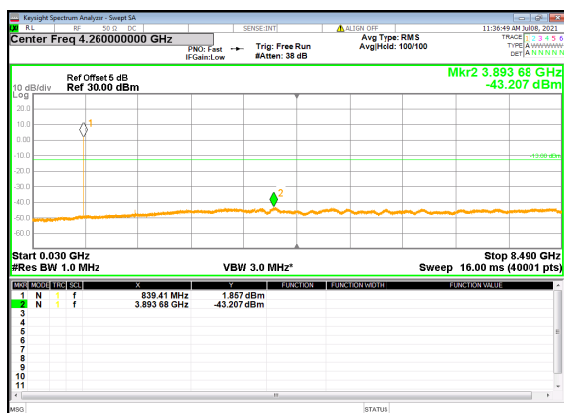
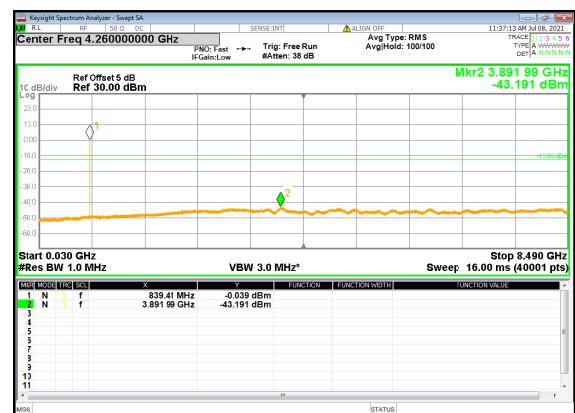
2.5.3. Test procedure

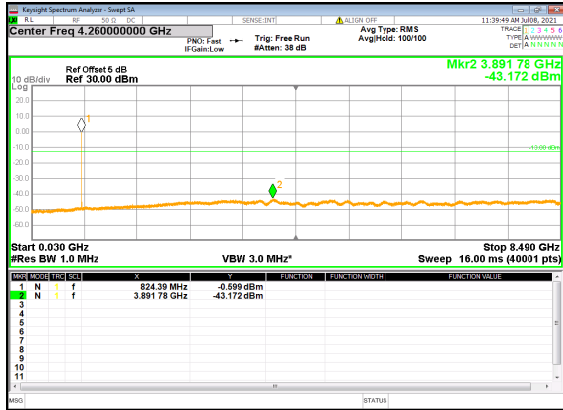
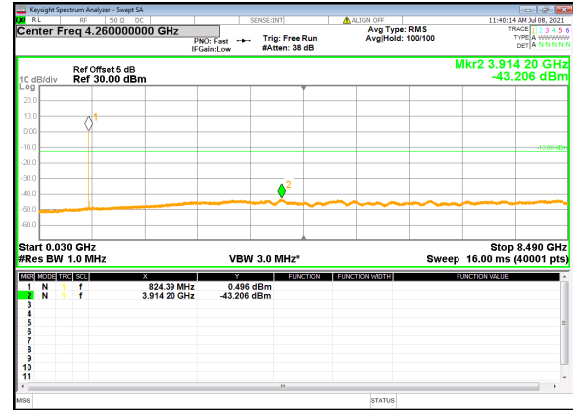
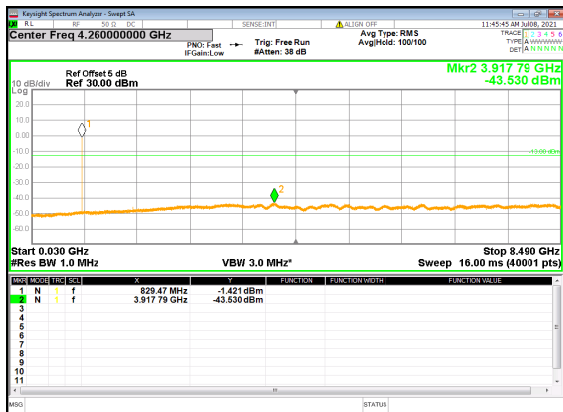
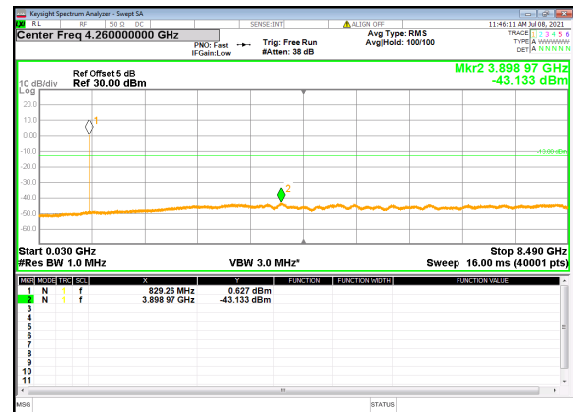
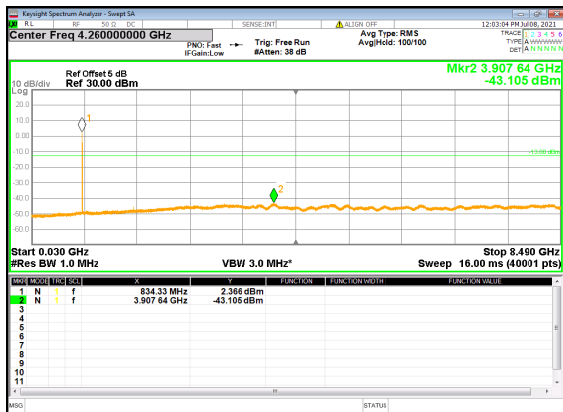
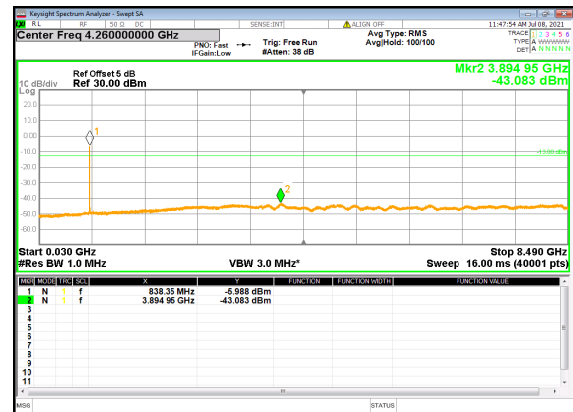
KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.

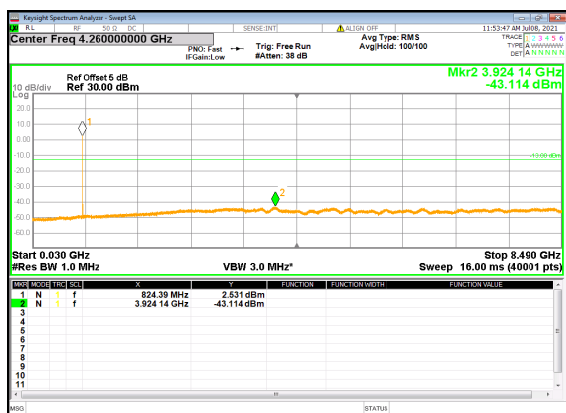
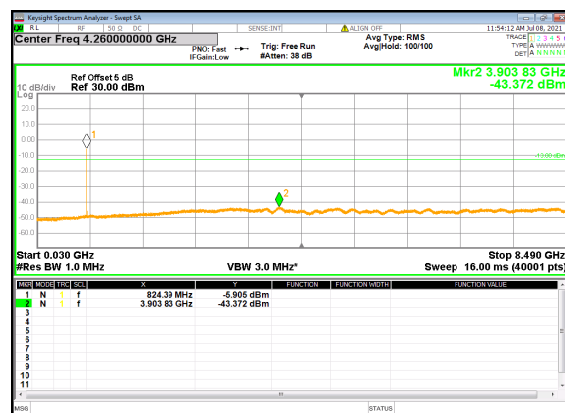
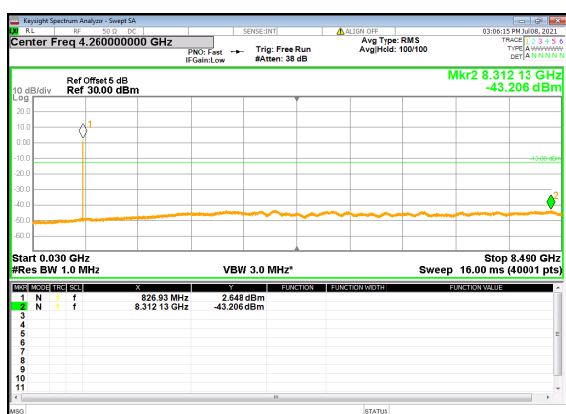
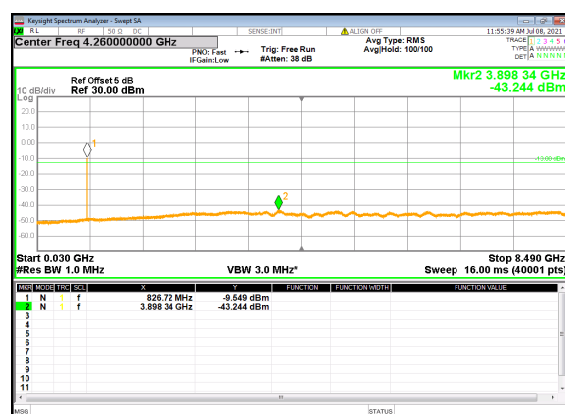
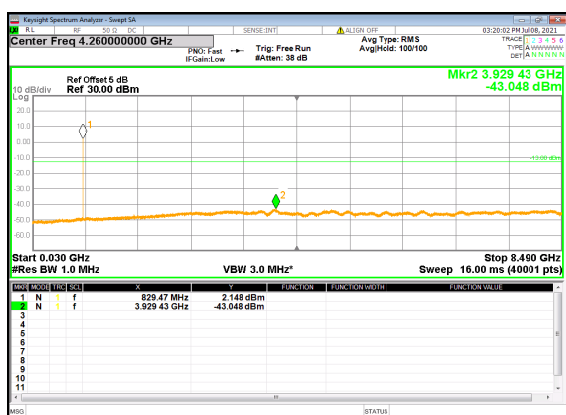
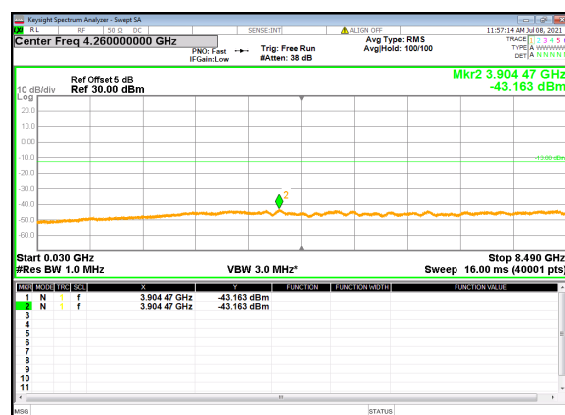
2.5.4. Test Result

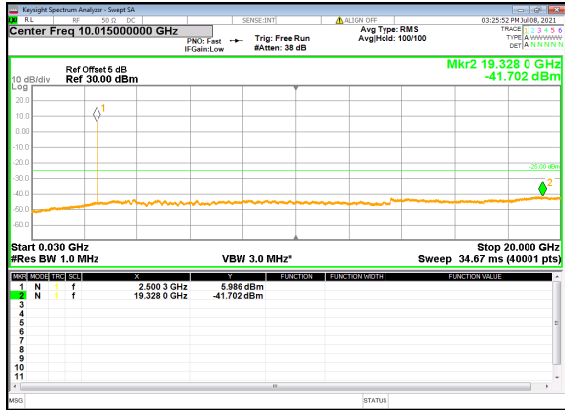
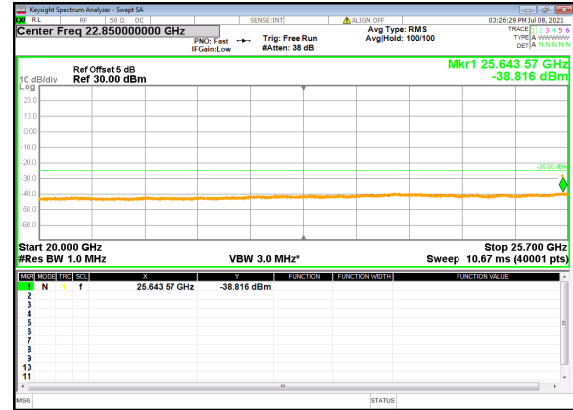
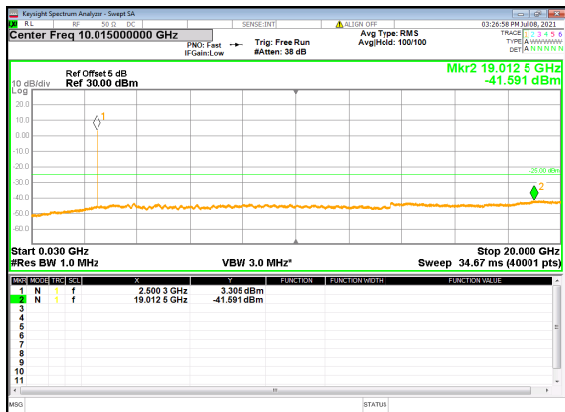
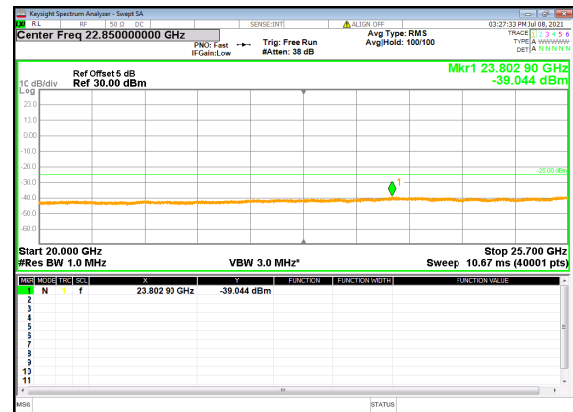
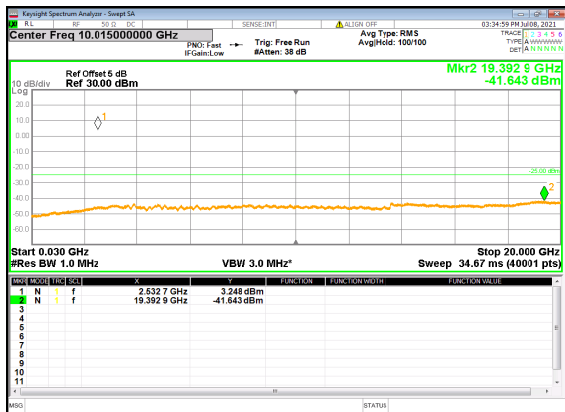
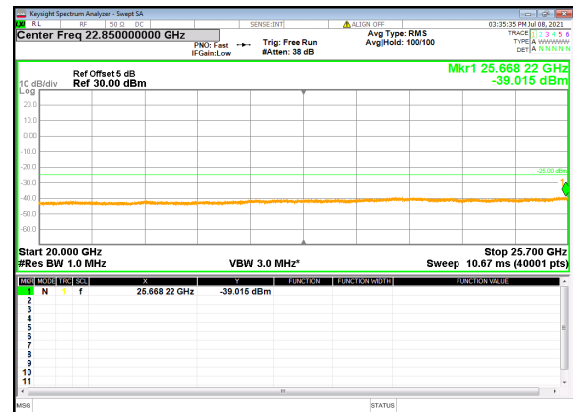


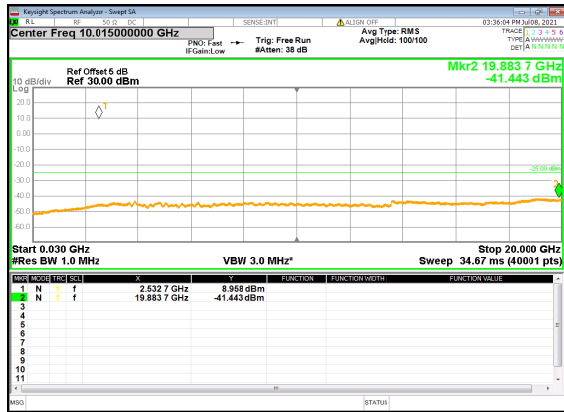
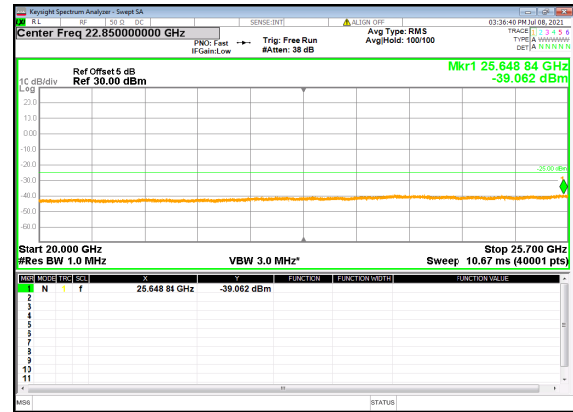
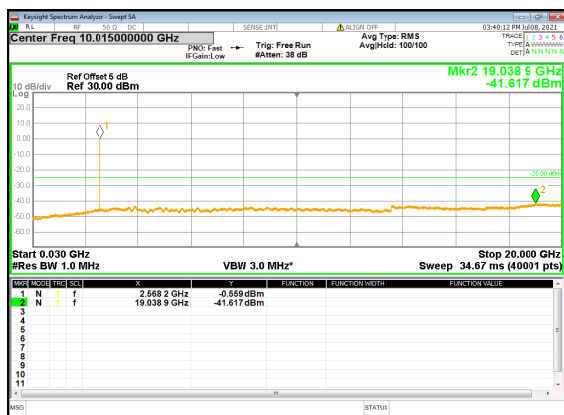
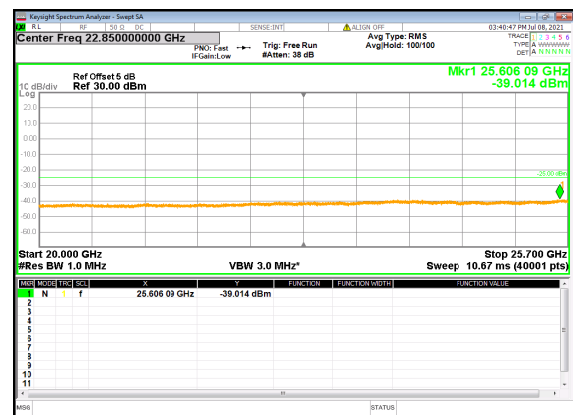
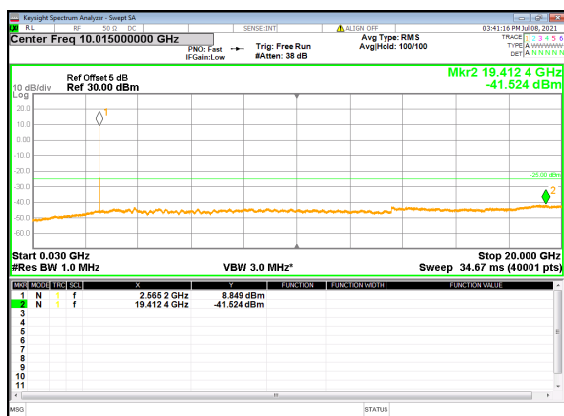
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Low_CHN5(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Lef
t_Mid_CHN5(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_
Mid_CHN5(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Lef
t_High_CHN5(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_
High_CH

N5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Low_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_Low_CHN5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Mid_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_Mid_CHN5(10M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_High_CHN5(10M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_High_CH

N5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Low_CHN5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_Low_CHN5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Mid_CHN5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_Mid_CHN5(15M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_High_CHN5(15M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_High_CH

N5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_L
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_Low_CHN5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_Mid_CHN5(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_Mid_CHN5(20M)_DFT-s-OFDM_BPSK_Edge_1RB_L
eft_High_CHN5(20M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
_High_CH

N7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Lef
t_Low_CHN7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Left
Low_CHN7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Lef
t_Low_CHN7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left
Low_CHN7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Lef
t_Mid_CHN7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Left
Mid_CH

N7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Lef
t_Mid_CHN7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_
Mid_CHN7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Lef
t_High_CHN7(5M)_DFT-s-OFDM_BPSK_Edge_1RB_Left_
High_CHN7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Lef
t_High_CHN7(5M)_DFT-s-OFDM_QPSK_Edge_1RB_Left_
High_CH