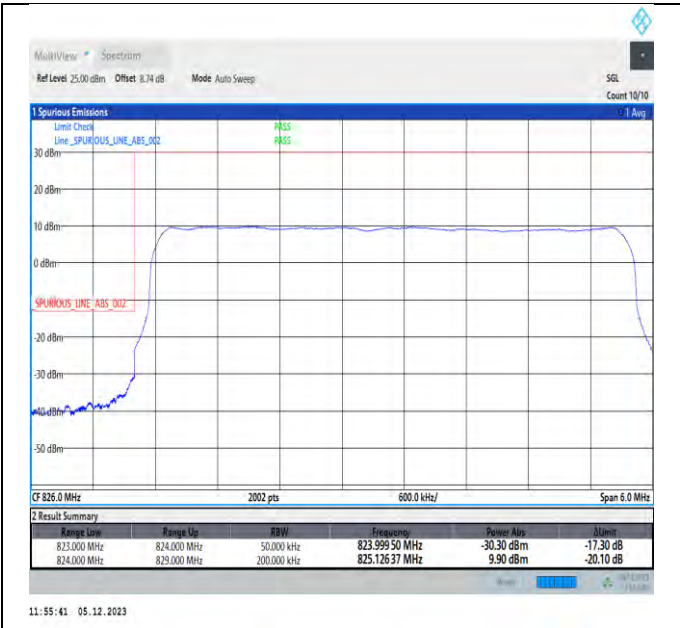


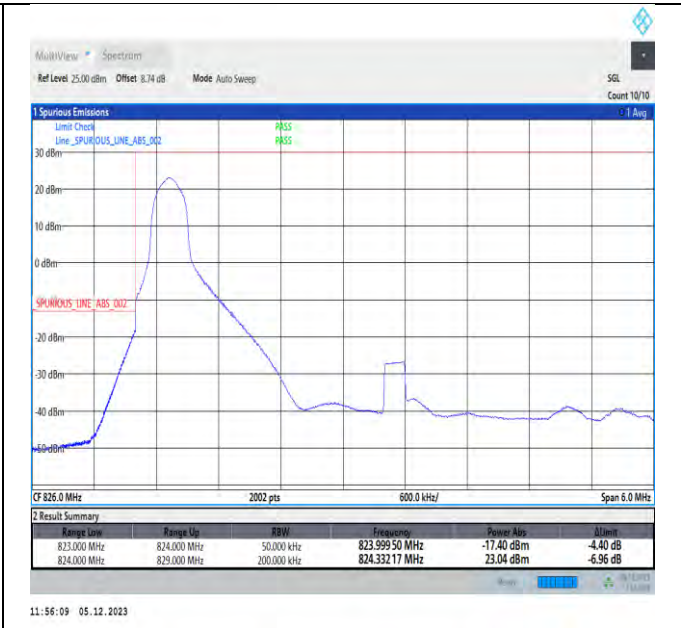
N41	30	80	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	80	CP-QPSK	L	Outer_Full	see graph	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	80	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	80	DFT-QPSK	H	Outer_Full	see graph	PASS
N41	30	80	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	80	CP-QPSK	H	Outer_Full	see graph	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	90	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	90	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	90	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	90	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	90	CP-QPSK	L	Outer_Full	see graph	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	90	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	90	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	90	DFT-QPSK	H	Outer_Full	see graph	PASS
N41	30	90	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	90	CP-QPSK	H	Outer_Full	see graph	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	100	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N41	30	100	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	100	DFT-QPSK	L	Outer_Full	see graph	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	100	CP-QPSK	L	Outer_Full	see graph	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N41	30	100	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N41	30	100	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	100	DFT-QPSK	H	Outer_Full	see graph	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N41	30	100	CP-QPSK	H	Outer_Full	see graph	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	5	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N66	15	5	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	5	DFT-QPSK	L	Outer_Full	see graph	PASS
N66	15	5	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	5	CP-QPSK	L	Outer_Full	see graph	PASS
N66	15	5	CP-QPSK	L	Edge_1RB_Left	see graph	PASS

N66	15	5	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N66	15	5	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	5	DFT-QPSK	H	Outer_Full	see graph	PASS
N66	15	5	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	5	CP-QPSK	H	Outer_Full	see graph	PASS
N66	15	5	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	10	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N66	15	10	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	10	DFT-QPSK	L	Outer_Full	see graph	PASS
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N66	15	10	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	10	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N66	15	10	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	10	DFT-QPSK	H	Outer_Full	see graph	PASS
N66	15	10	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	10	CP-QPSK	H	Outer_Full	see graph	PASS
N66	15	10	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	15	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
N66	15	15	DFT-PI2BPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	15	DFT-QPSK	L	Outer_Full	see graph	PASS
N66	15	15	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
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N66	15	15	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	15	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N66	15	15	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	15	DFT-QPSK	H	Outer_Full	see graph	PASS
N66	15	15	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	15	CP-QPSK	H	Outer_Full	see graph	PASS
N66	15	15	CP-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	20	DFT-PI2BPSK	L	Outer_Full	see graph	PASS
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N66	15	20	DFT-QPSK	L	Outer_Full	see graph	PASS
N66	15	20	DFT-QPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	20	CP-QPSK	L	Outer_Full	see graph	PASS
N66	15	20	CP-QPSK	L	Edge_1RB_Left	see graph	PASS
N66	15	20	DFT-PI2BPSK	H	Outer_Full	see graph	PASS
N66	15	20	DFT-PI2BPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	20	DFT-QPSK	H	Outer_Full	see graph	PASS
N66	15	20	DFT-QPSK	H	Edge_1RB_Right	see graph	PASS
N66	15	20	CP-QPSK	H	Outer_Full	see graph	PASS
N66	15	20	CP-QPSK	H	Edge_1RB_Right	see graph	PASS

Test Graphs



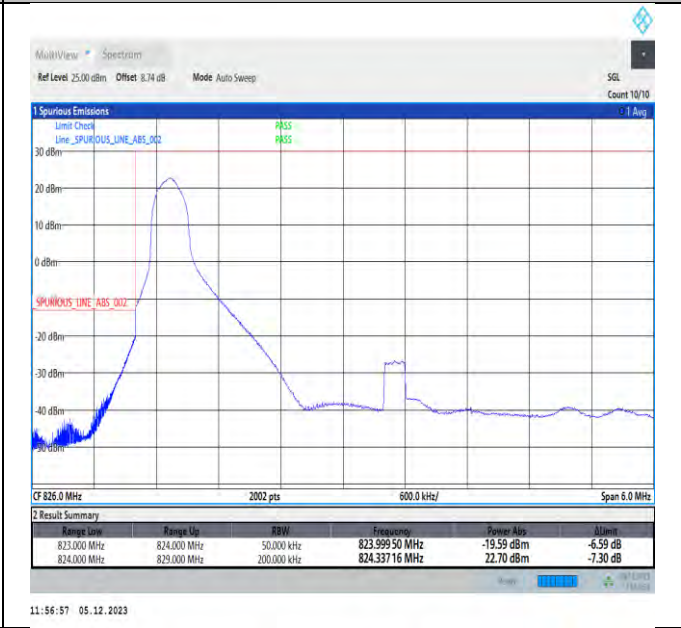
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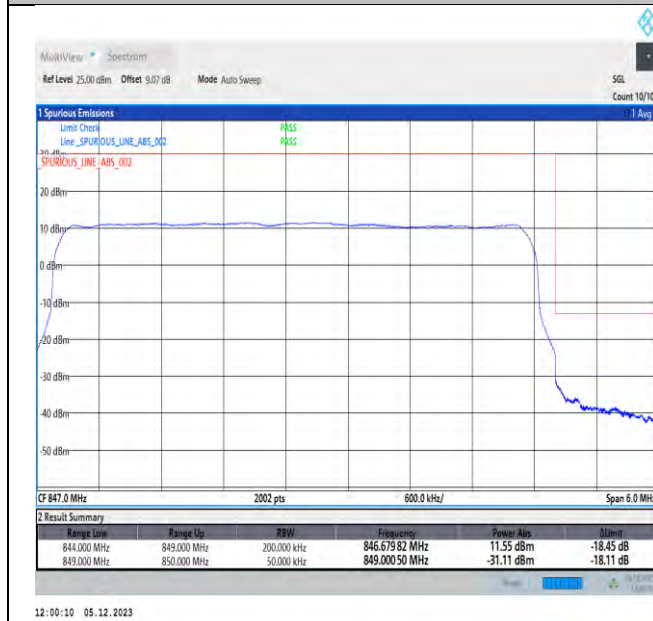
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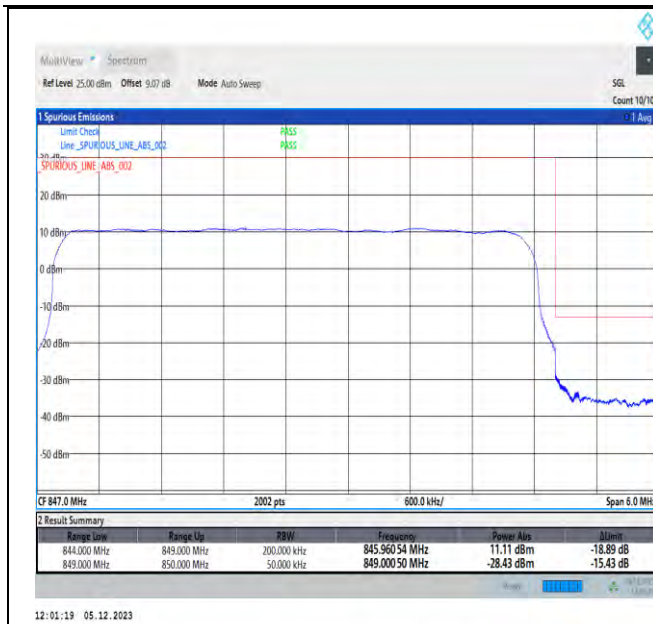
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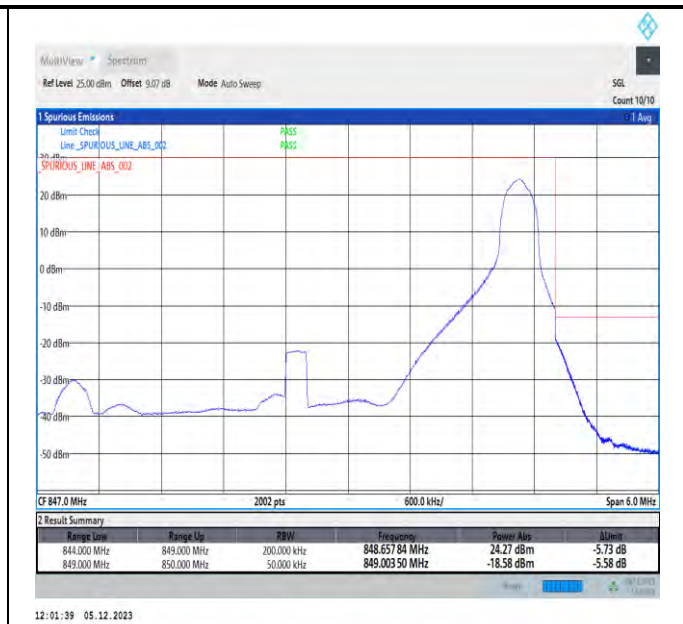
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NTNV_N5_PC3_15_5_H_TID16_RID1#1



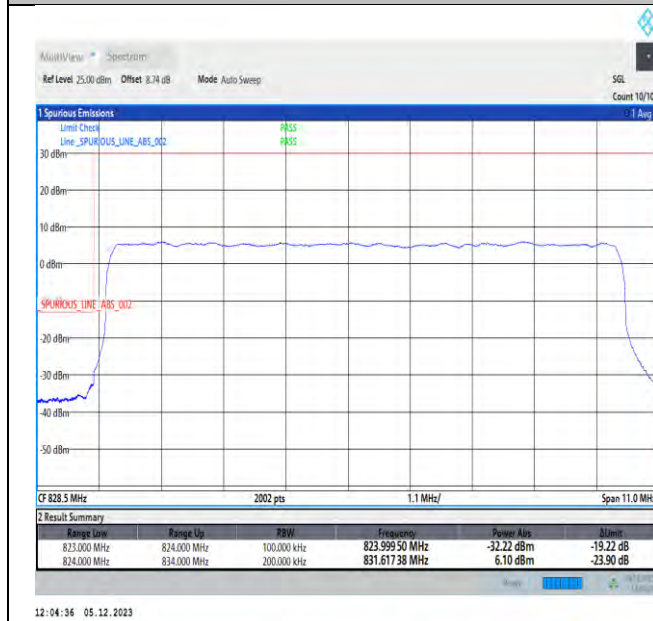
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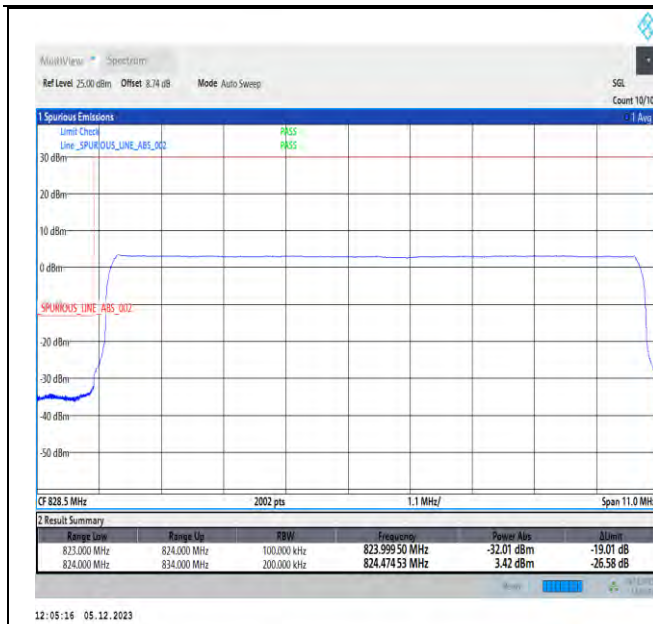
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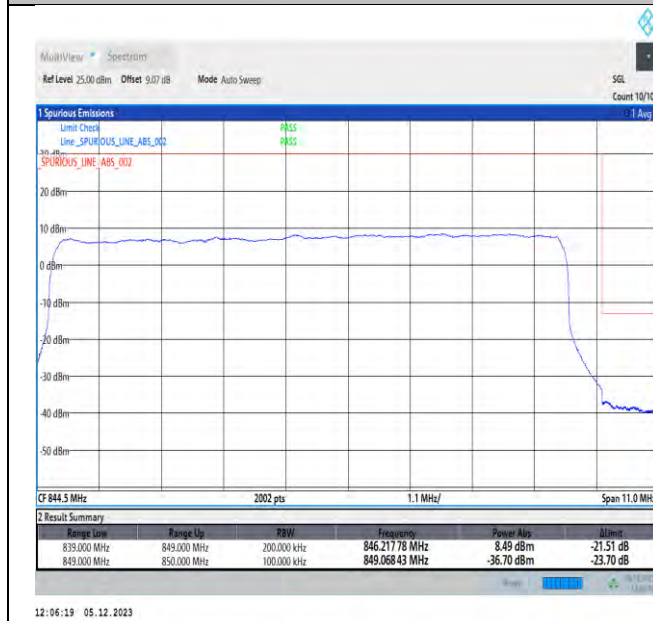
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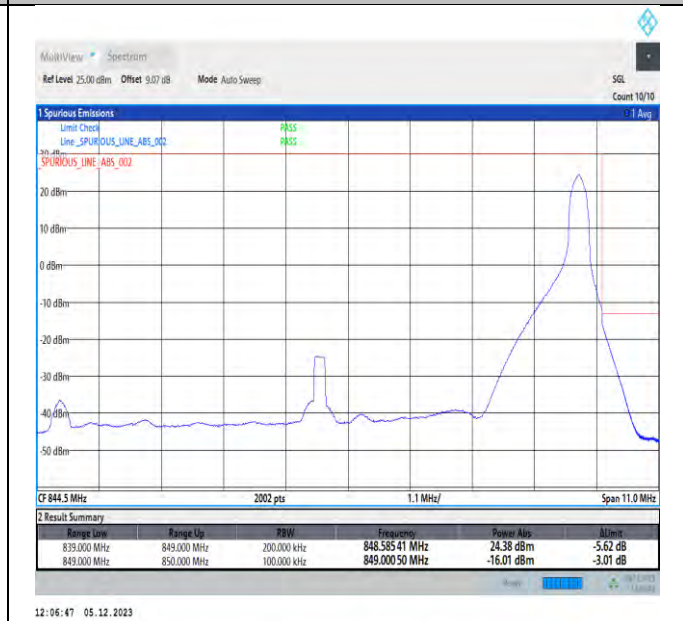
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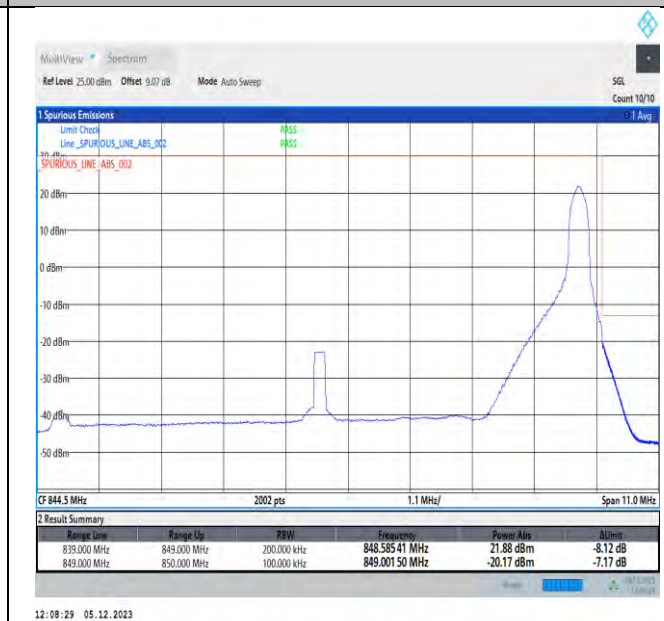
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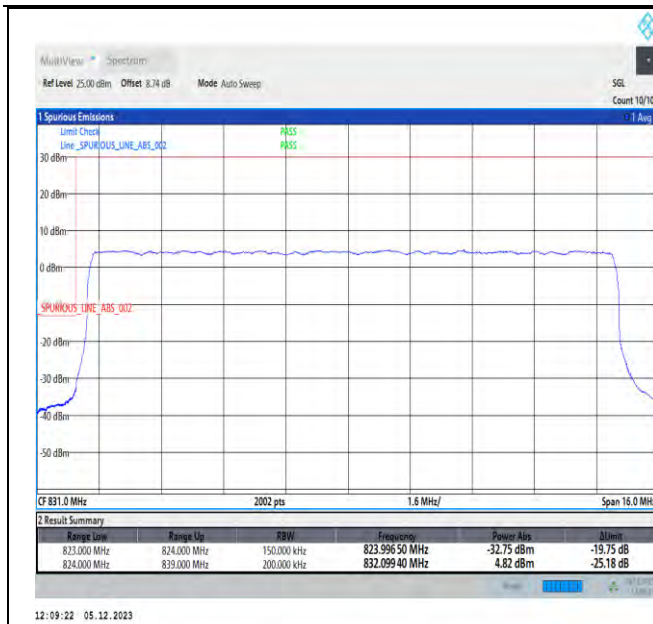
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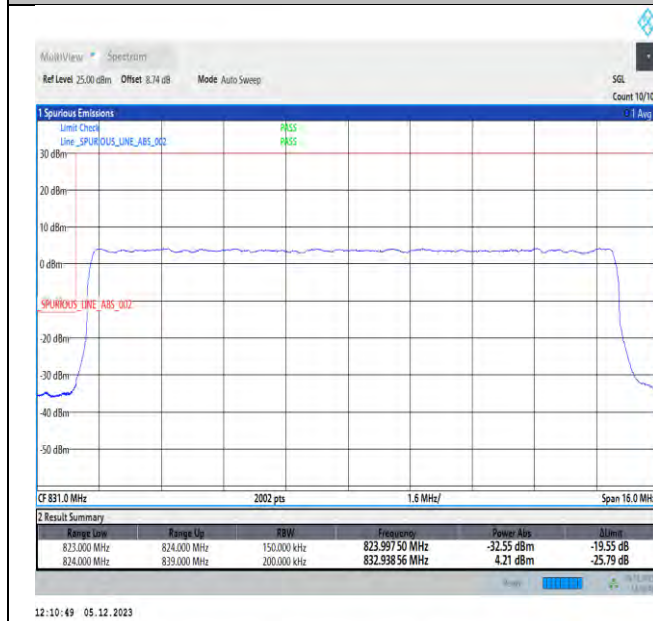
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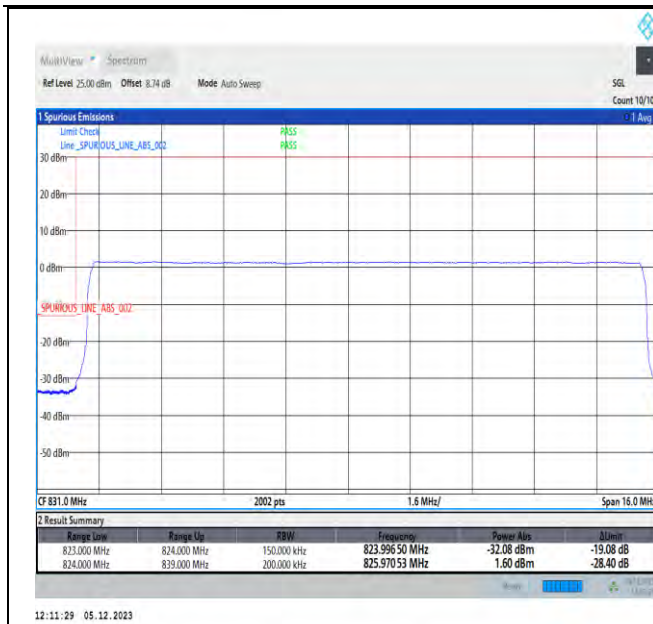
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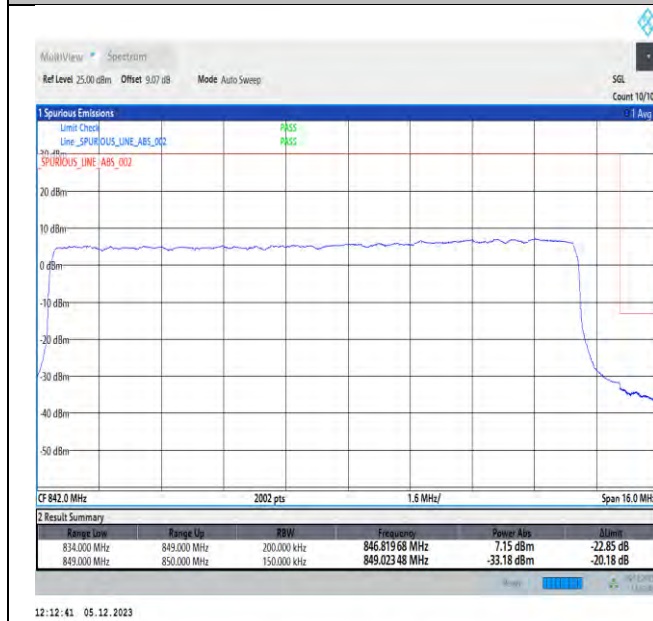
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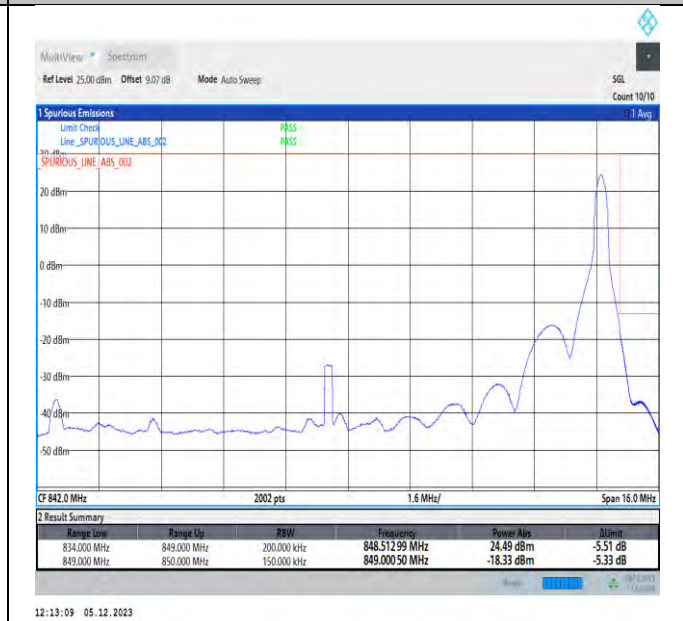
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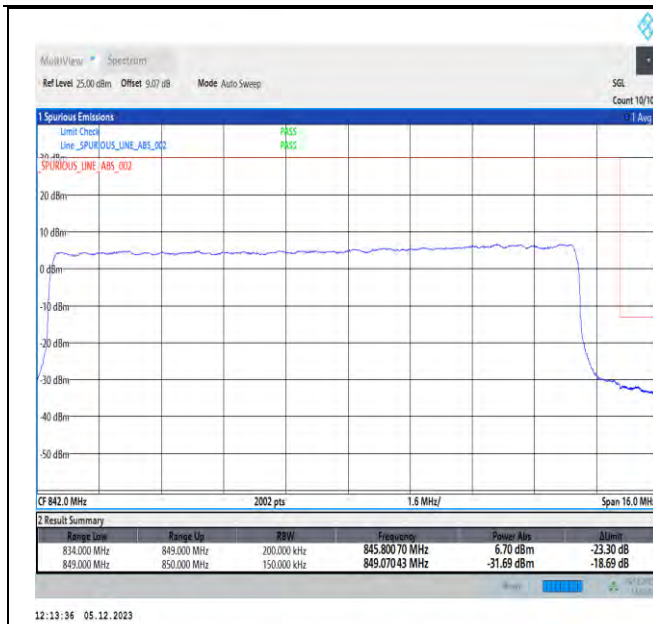
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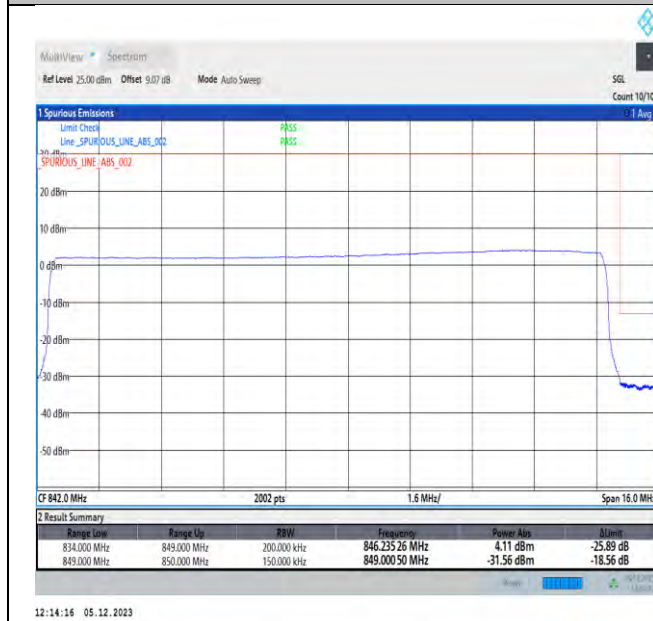
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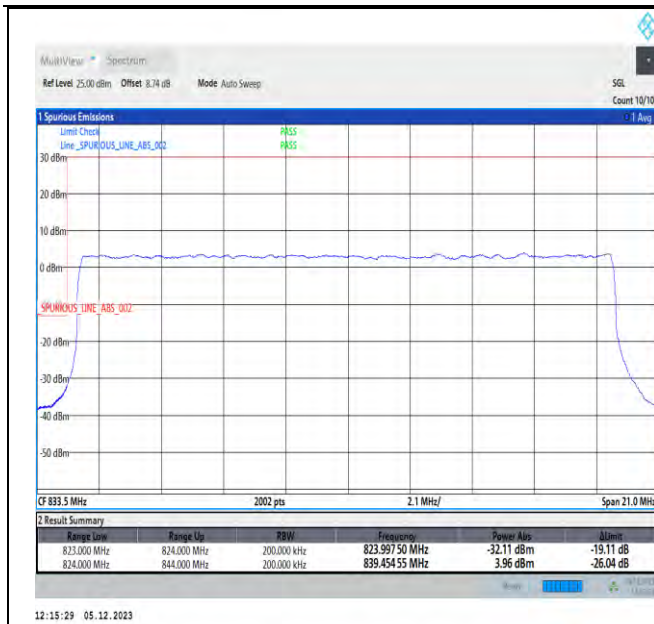
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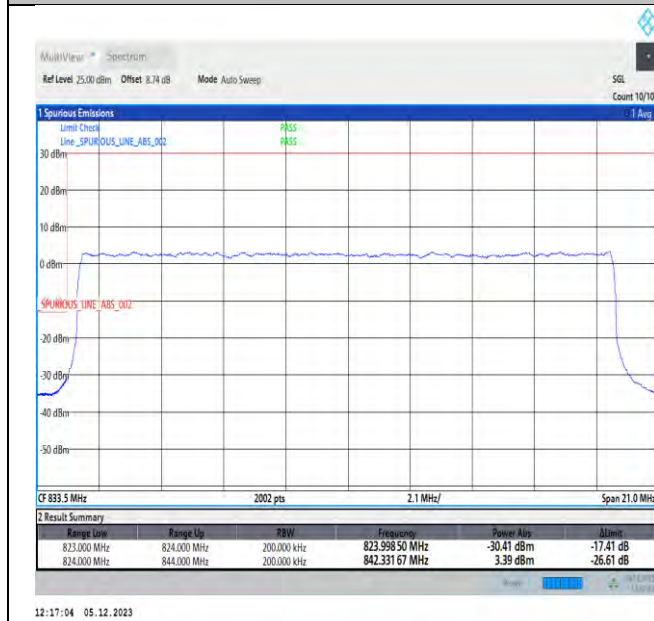
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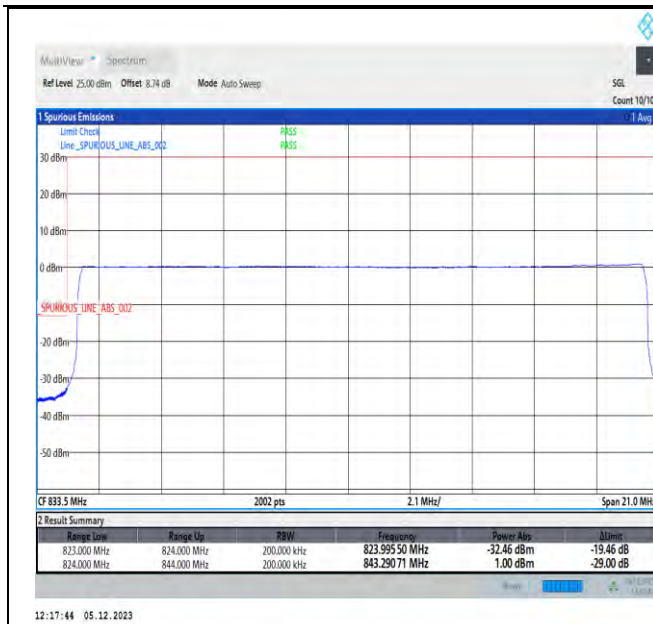
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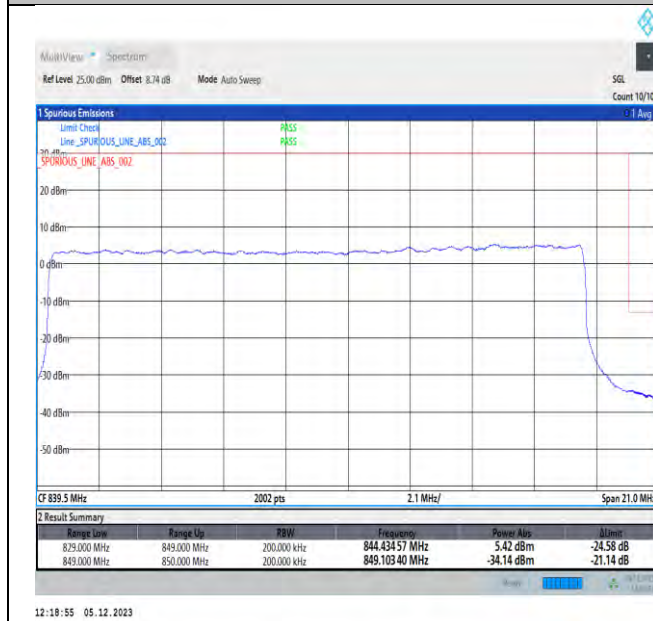
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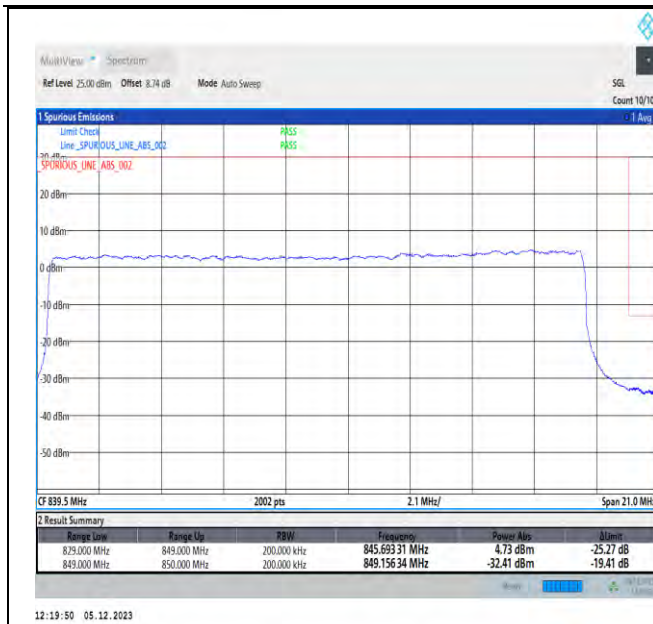
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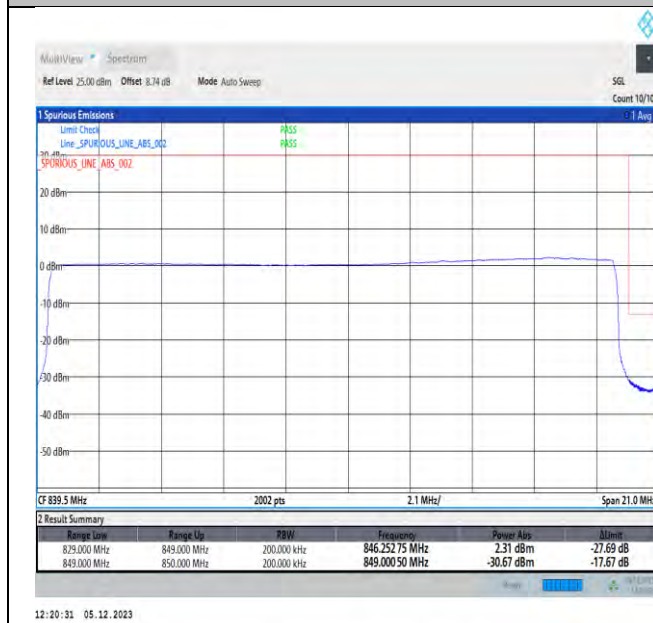
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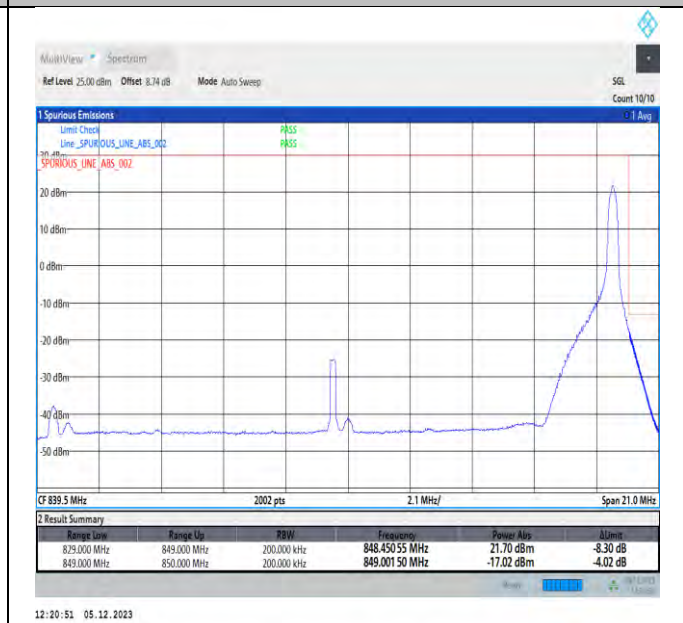
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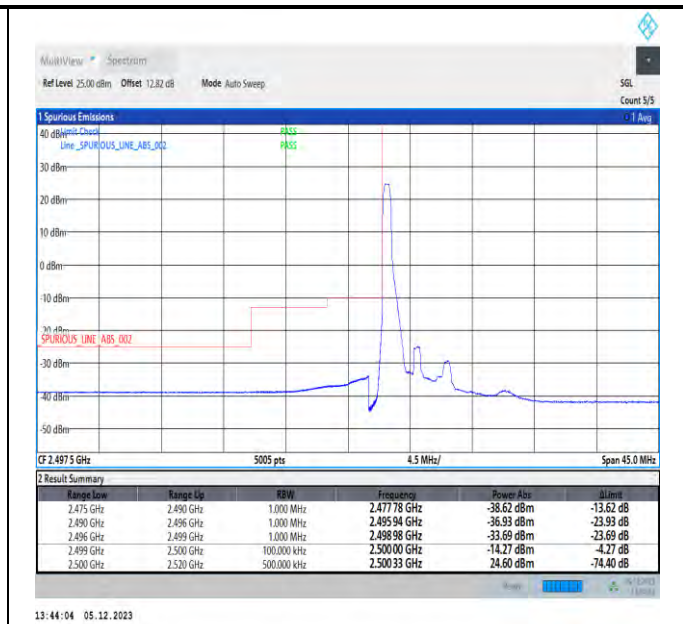
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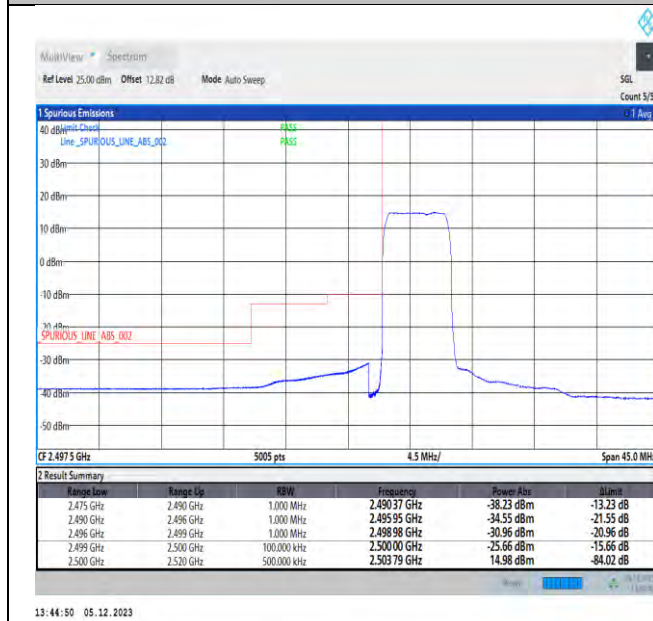
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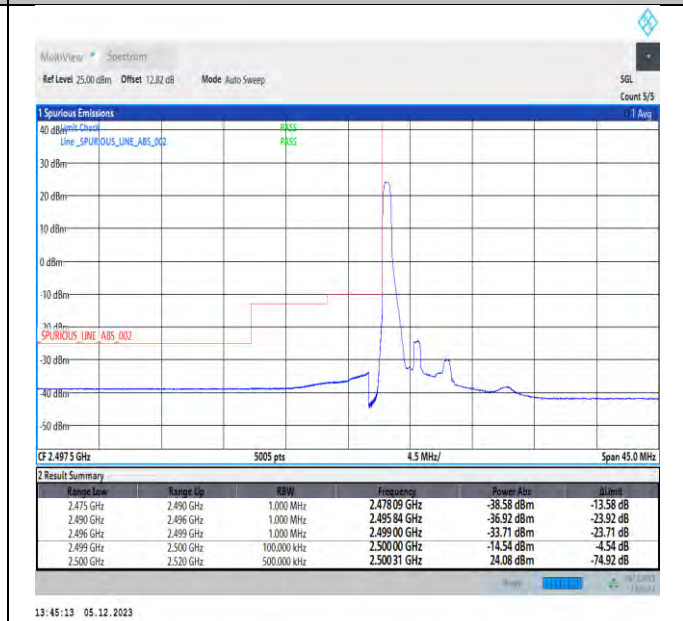
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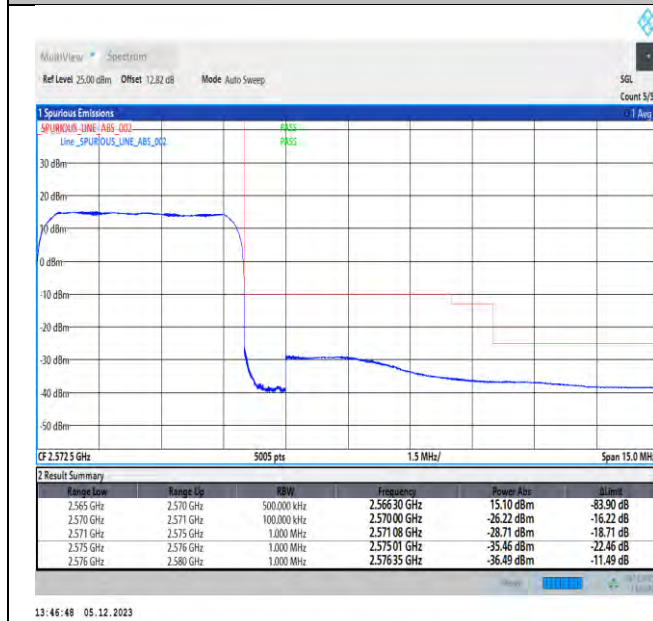
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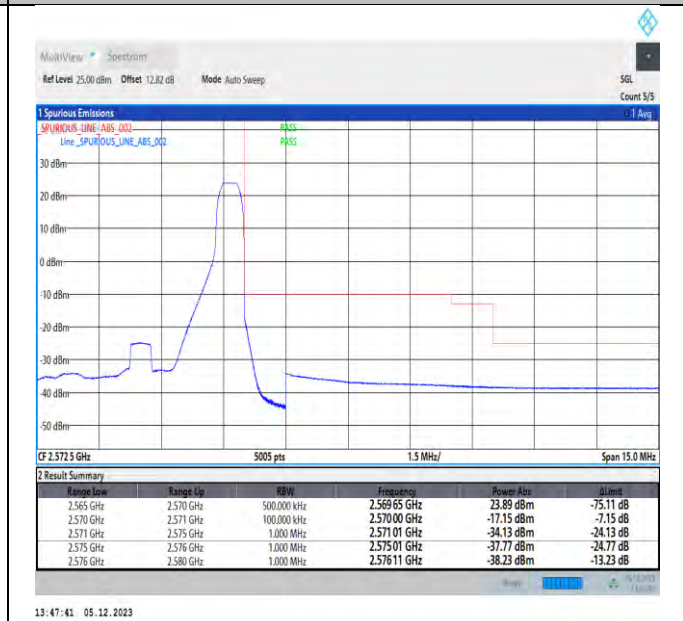
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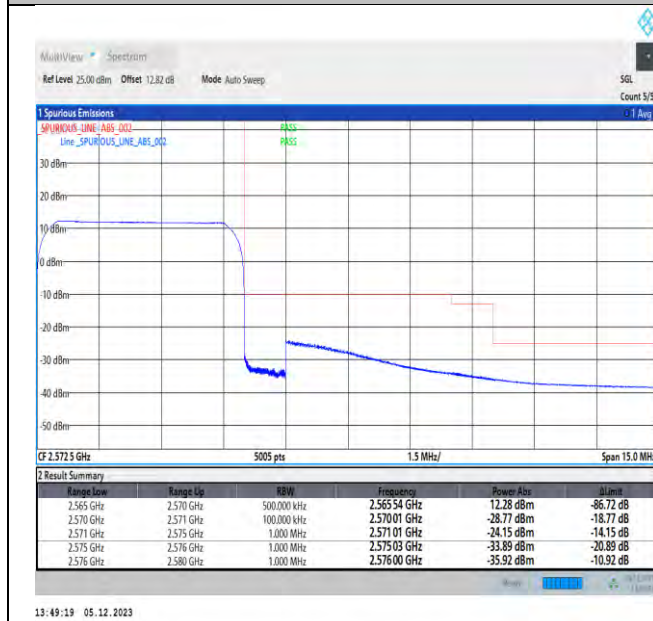
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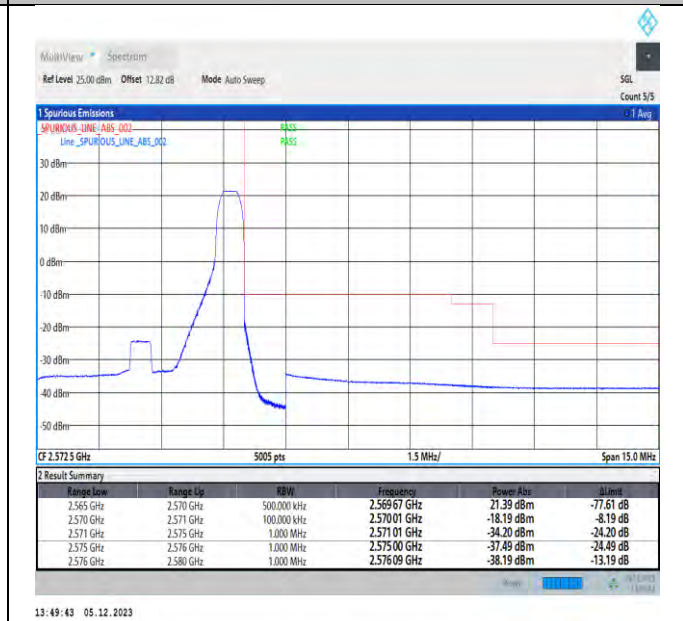
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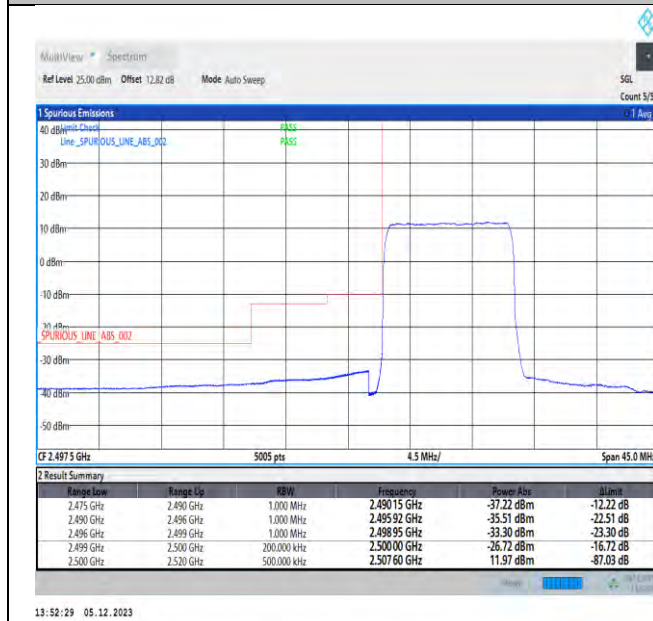
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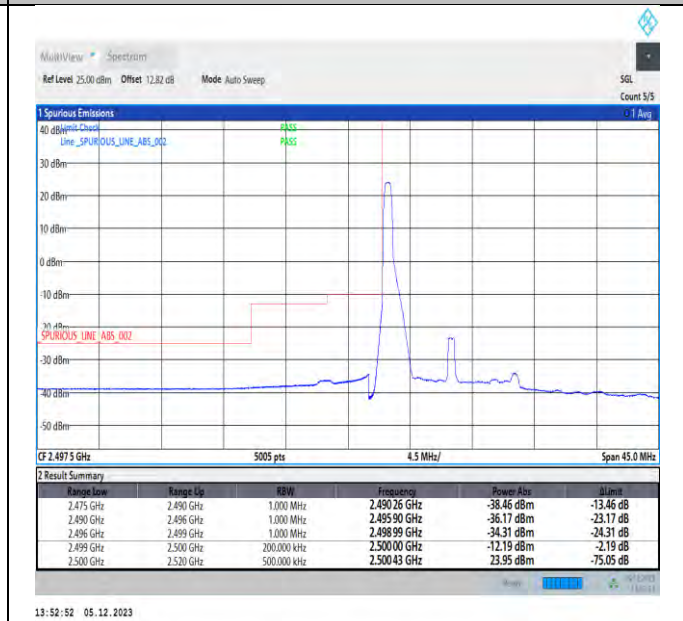
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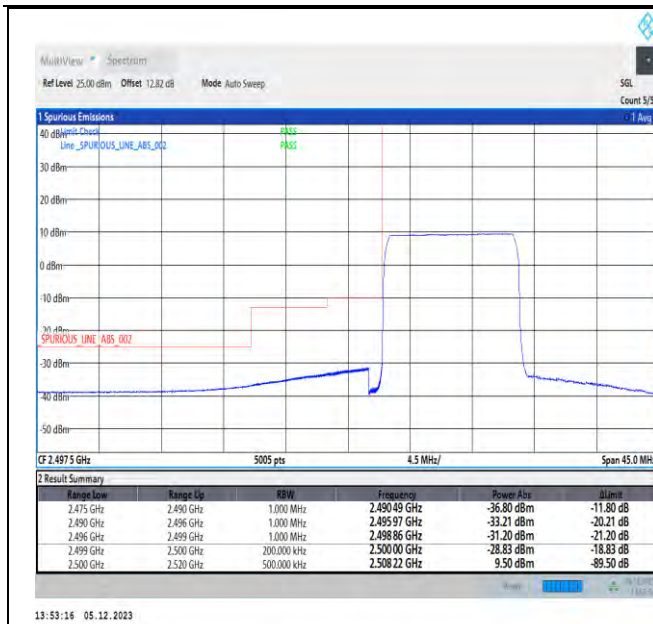
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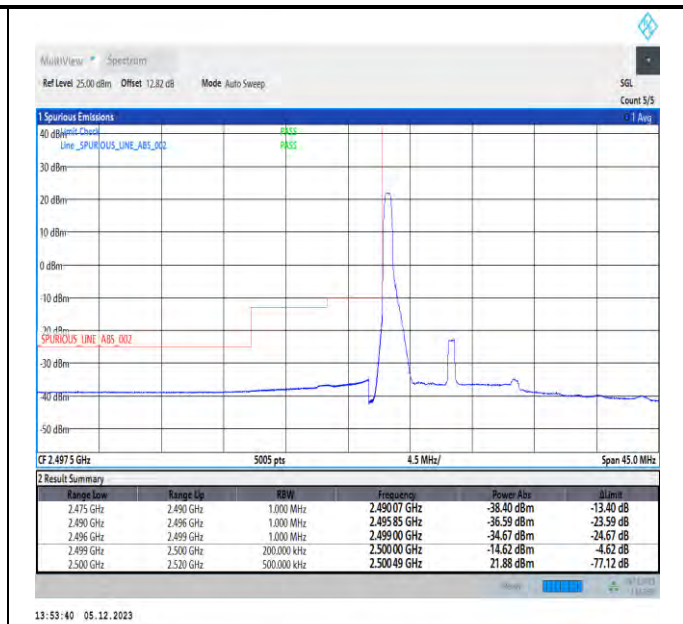
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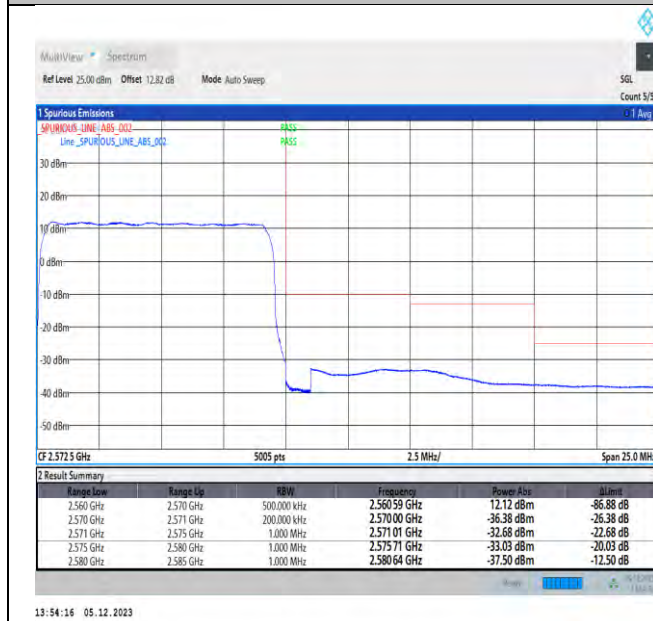
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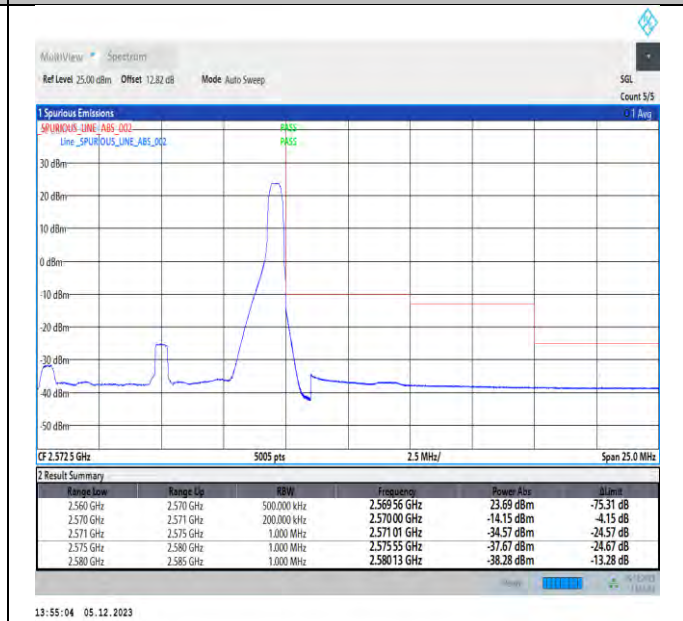
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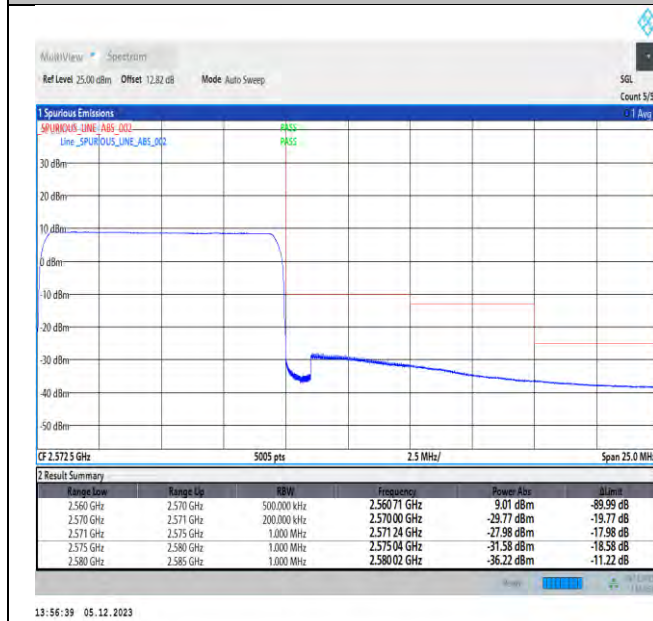
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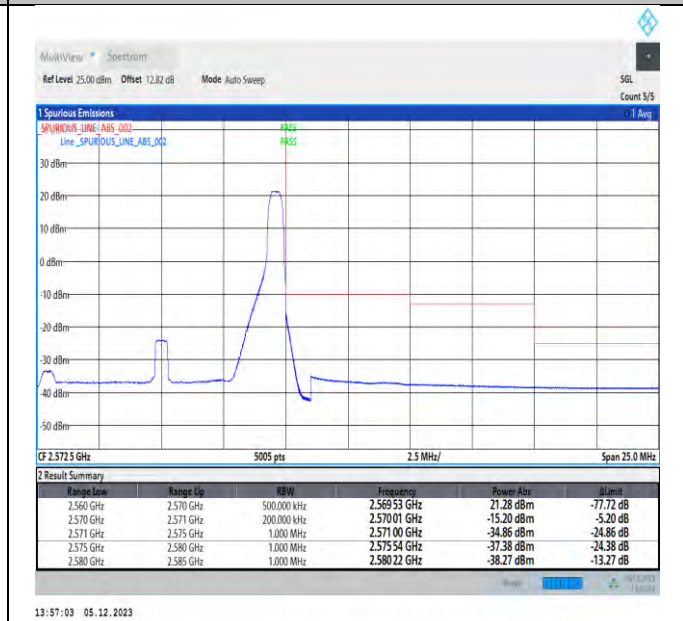
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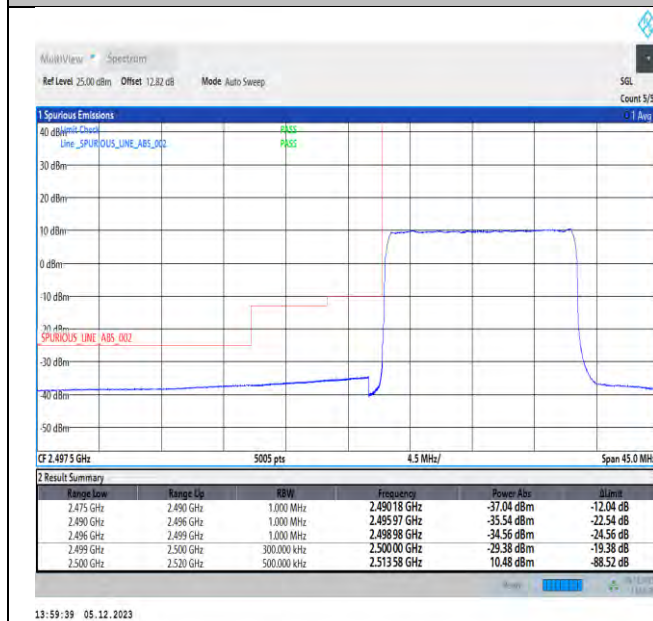
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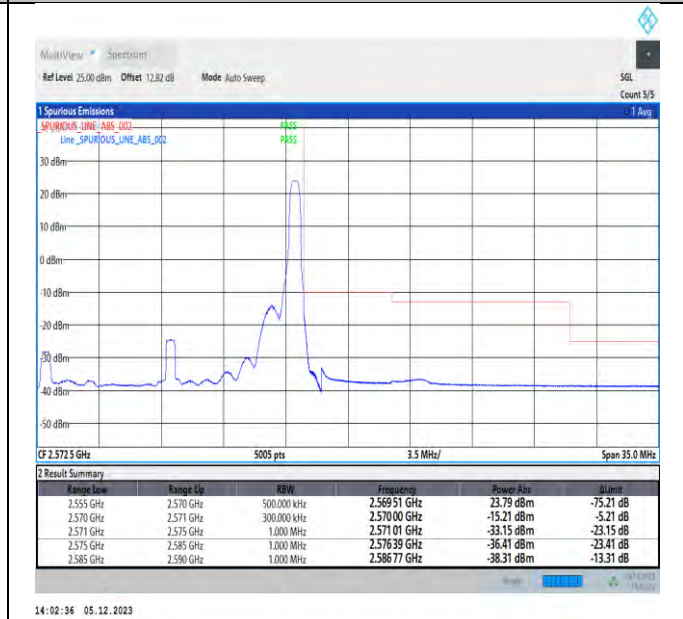
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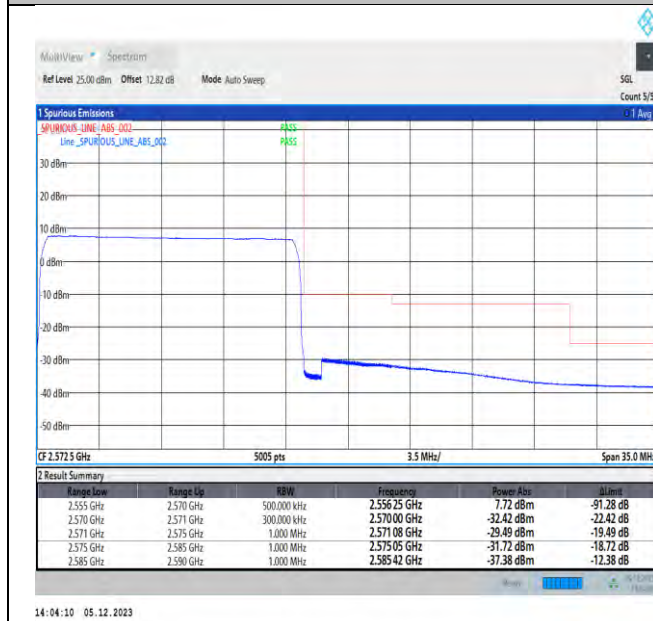
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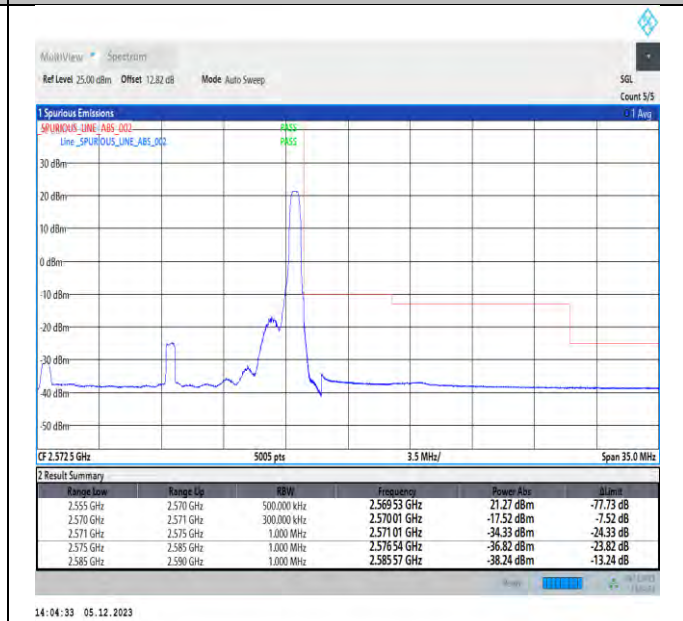
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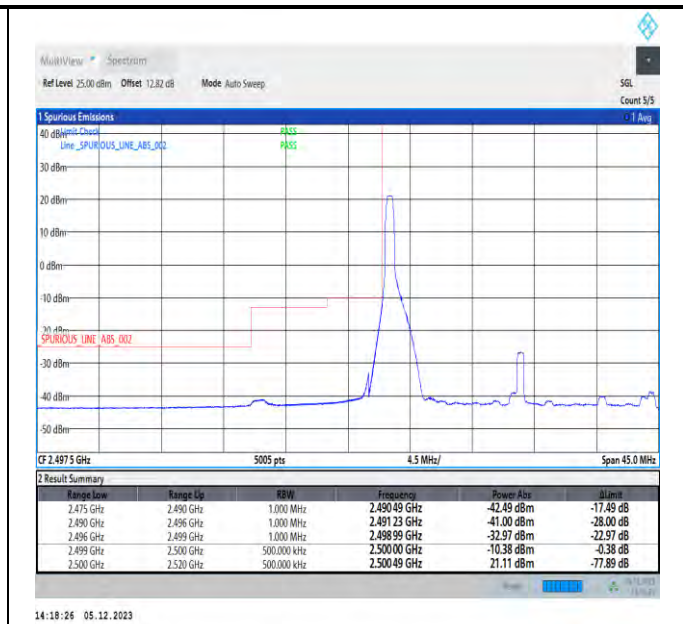
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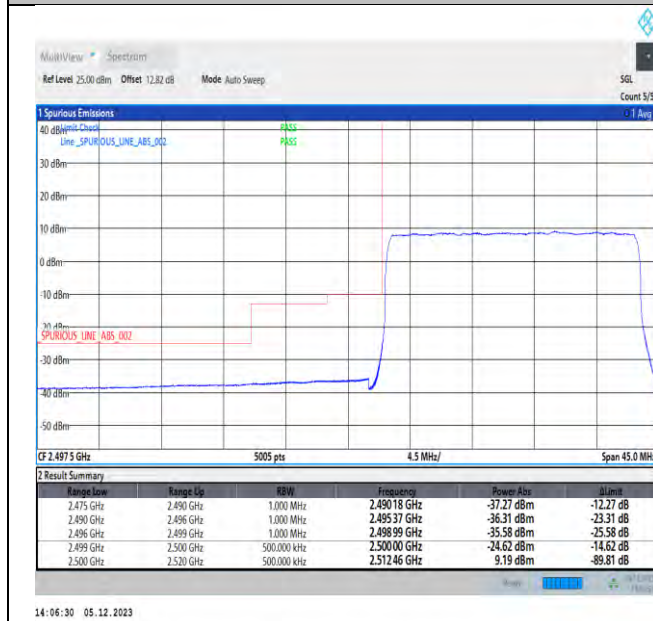
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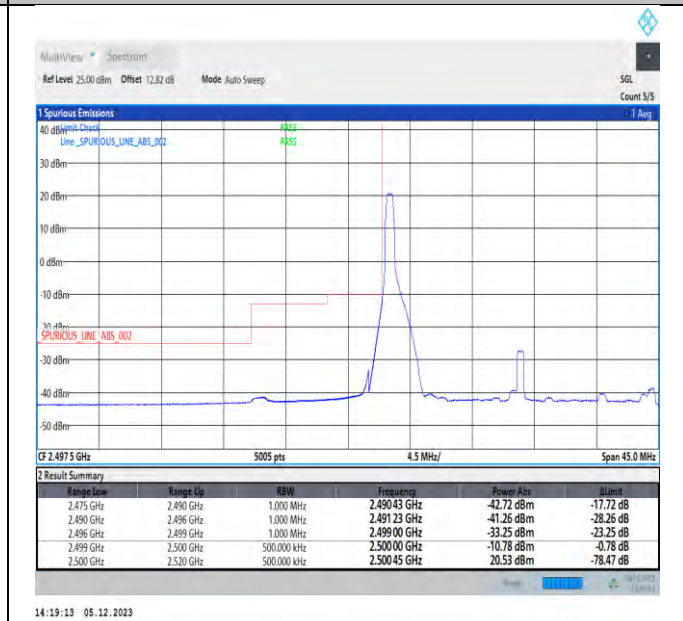
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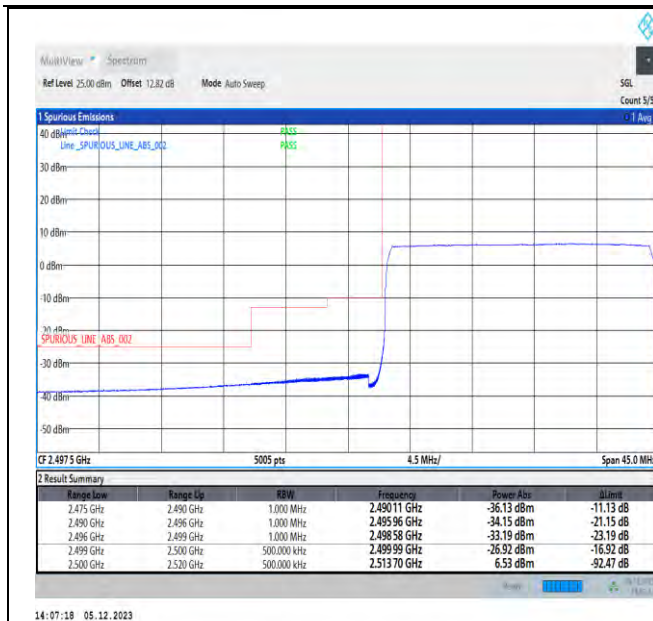
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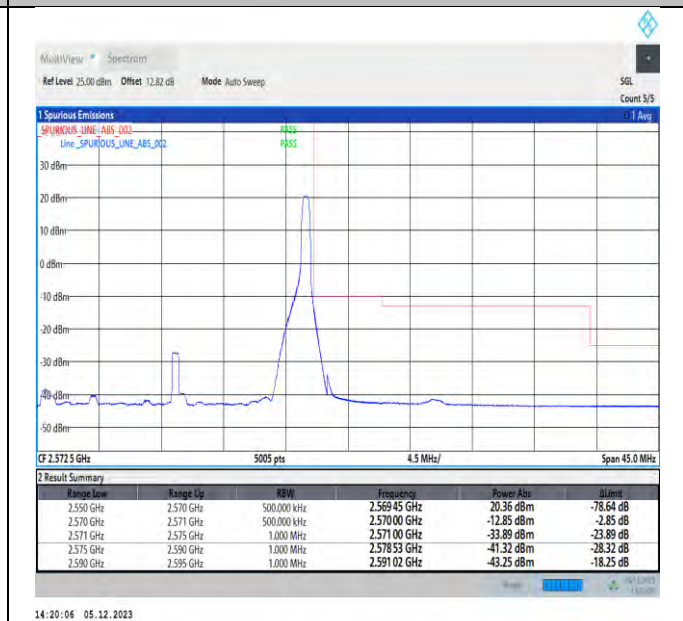
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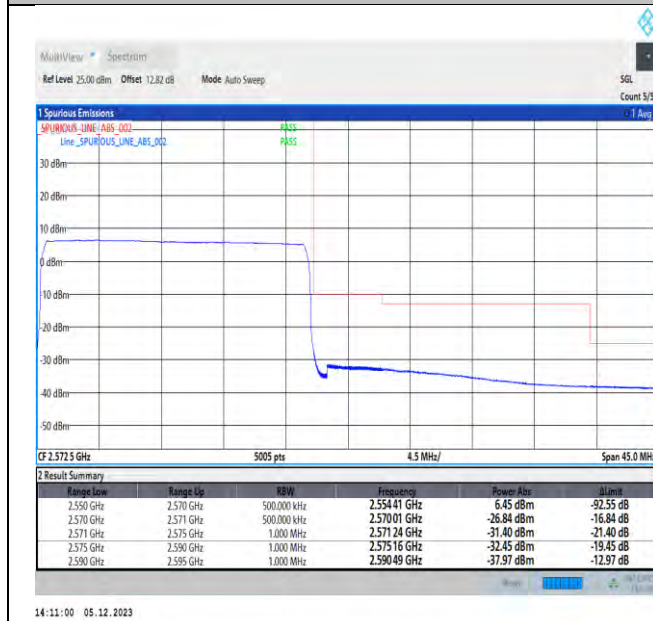
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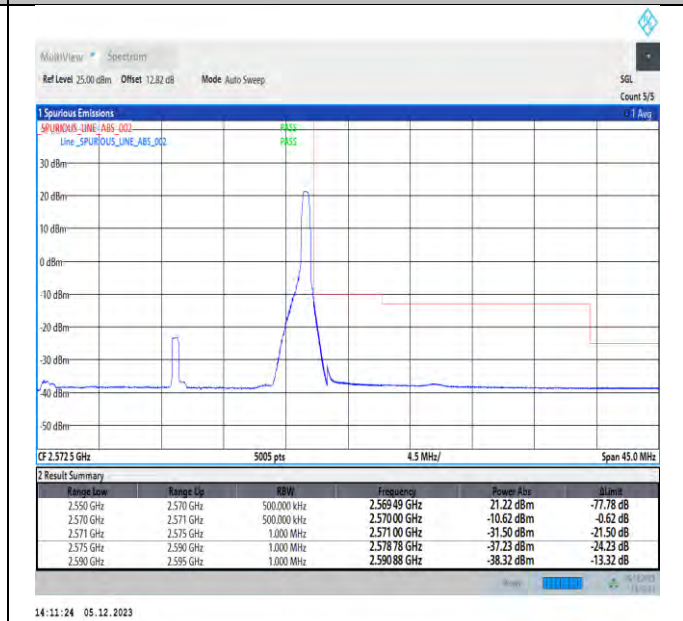
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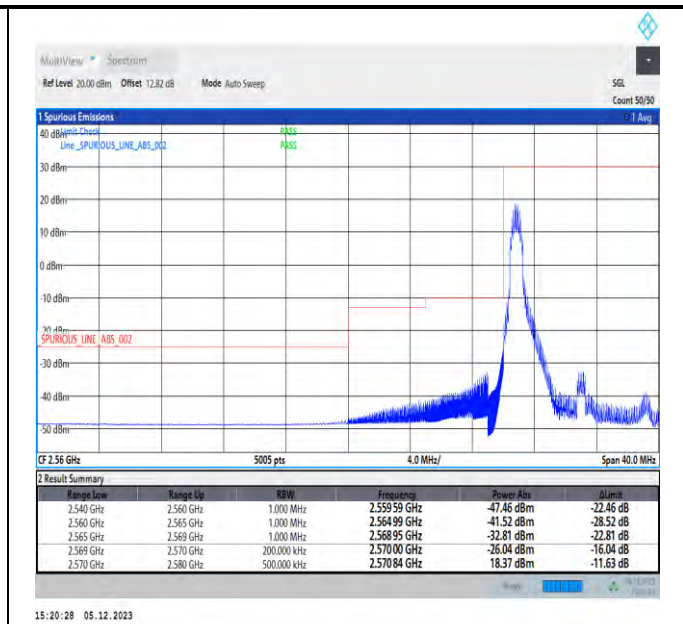
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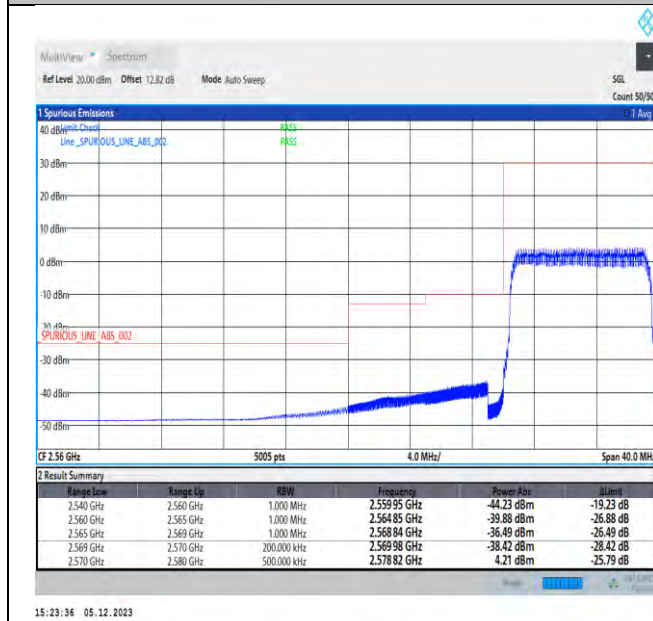
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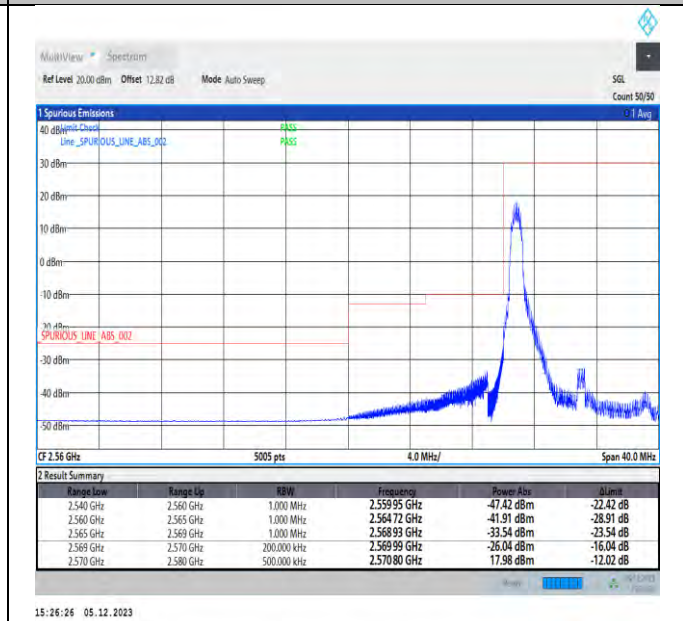
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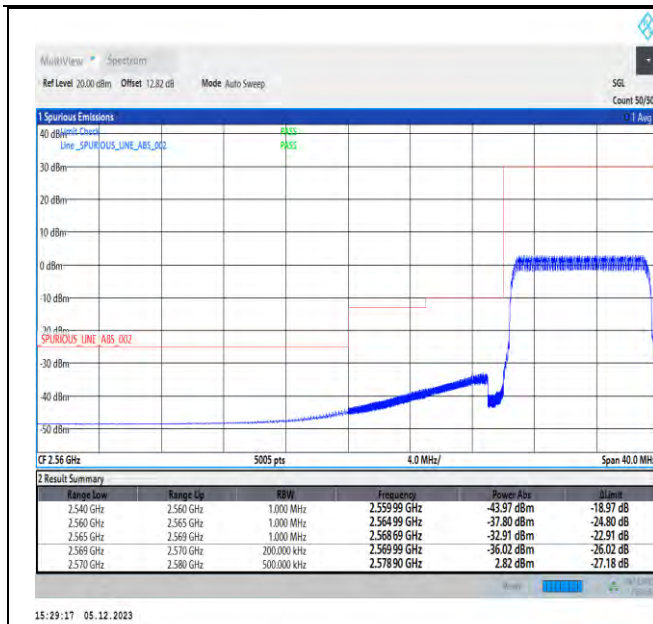
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NTNV_N38_PC3_30_10_L_TID16_RID1#1



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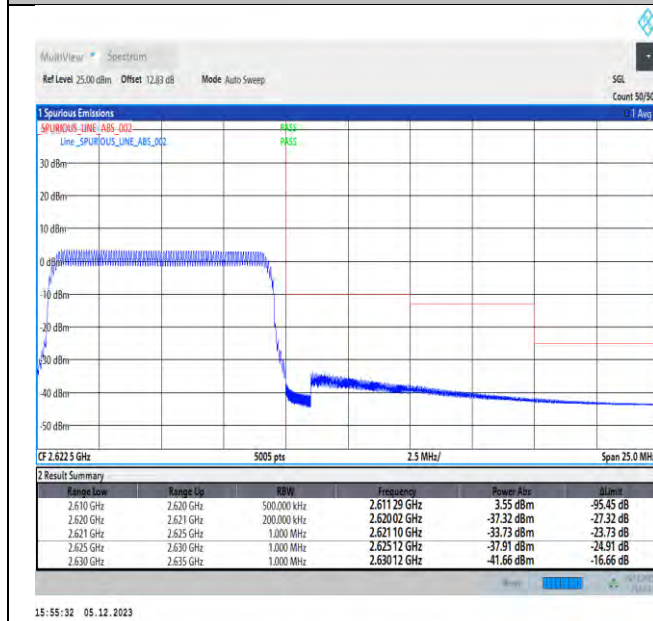
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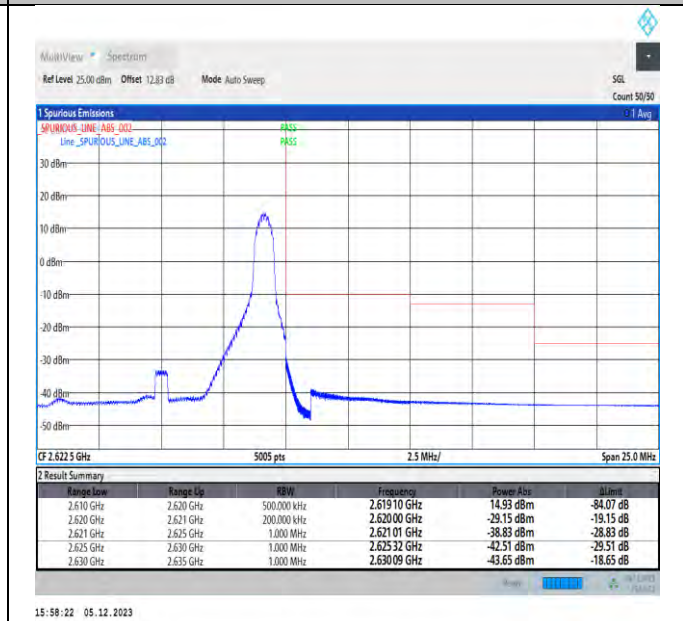
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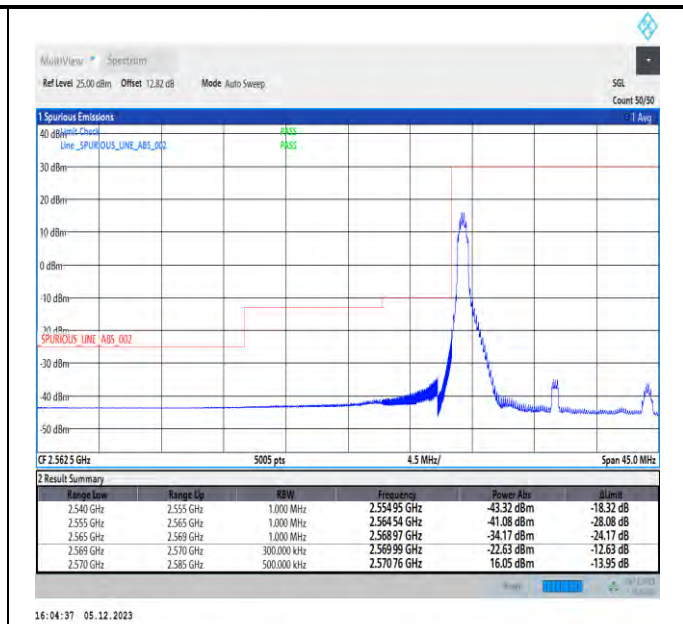
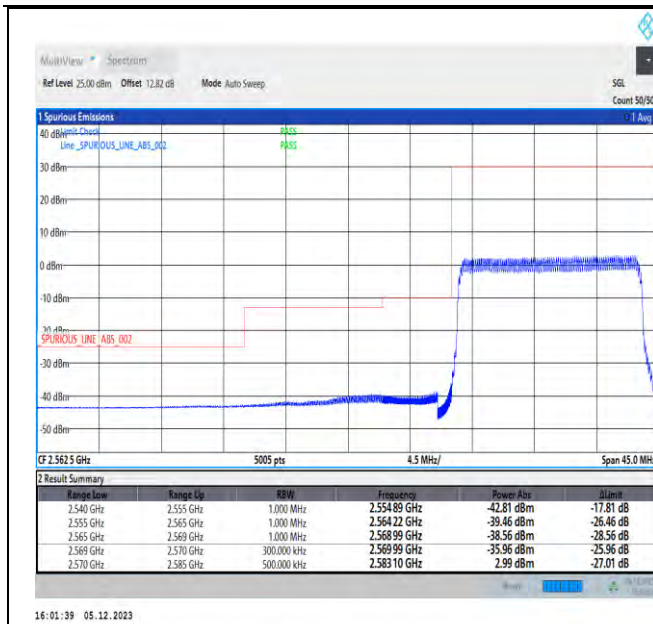
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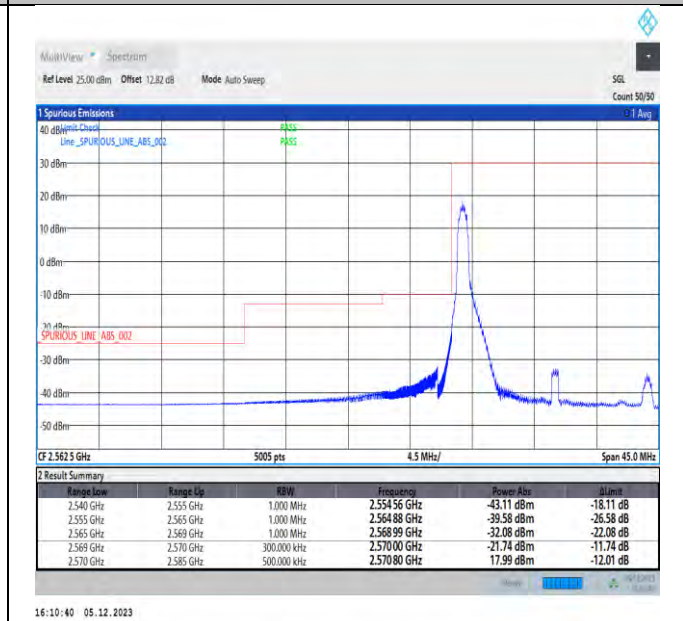
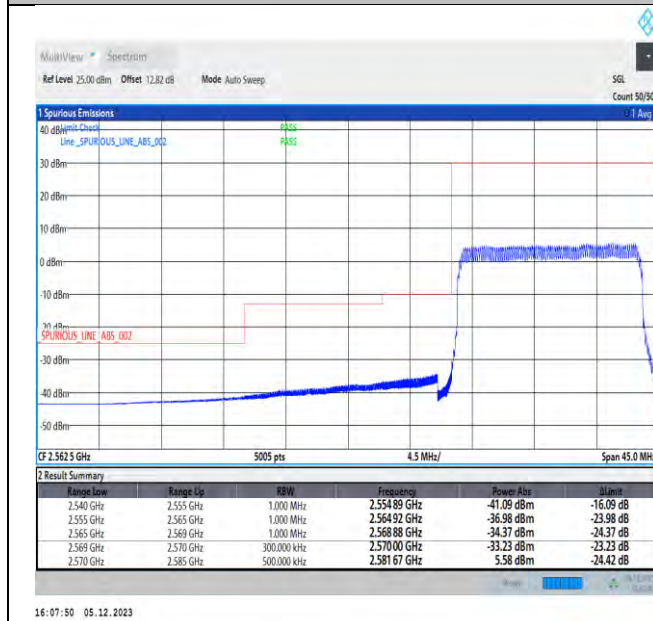


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NTNV_N38_PC3_30_15_L_TID4_RID1#1

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NTNV_N38_PC3_30_15_L_TID16_RID1#1



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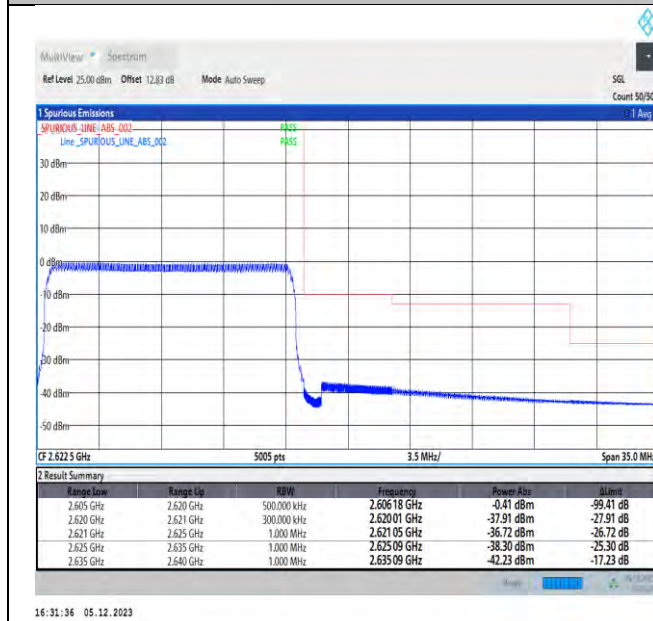
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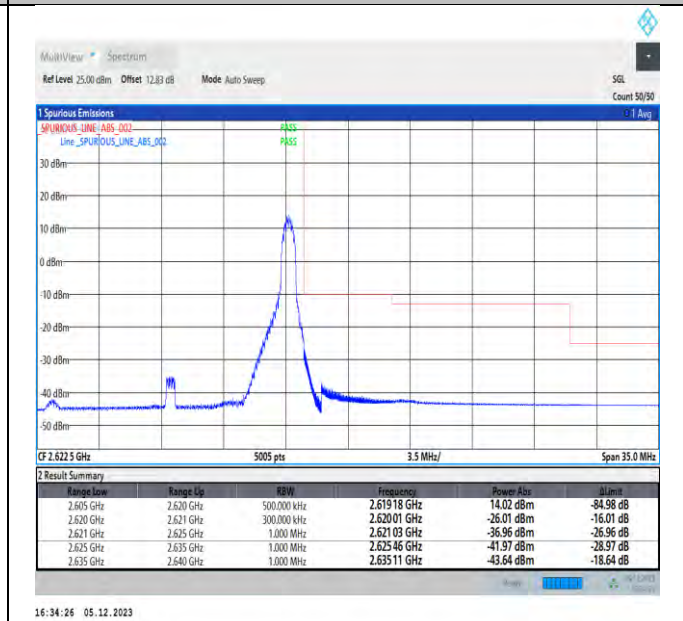
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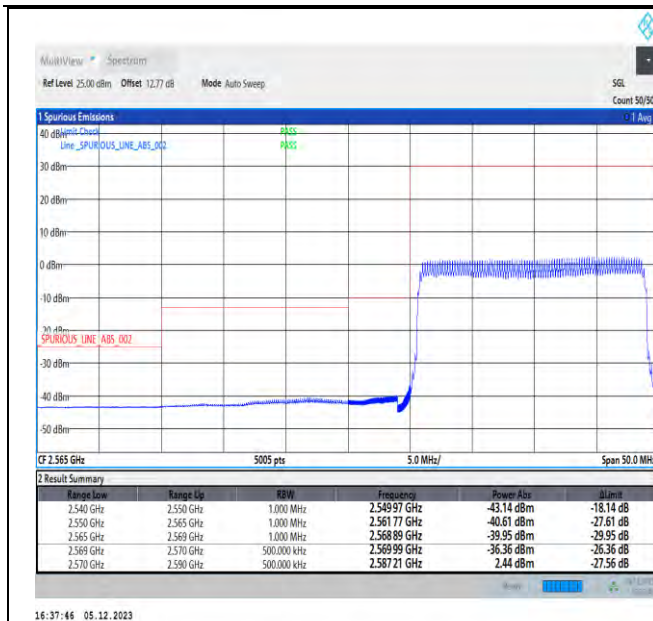
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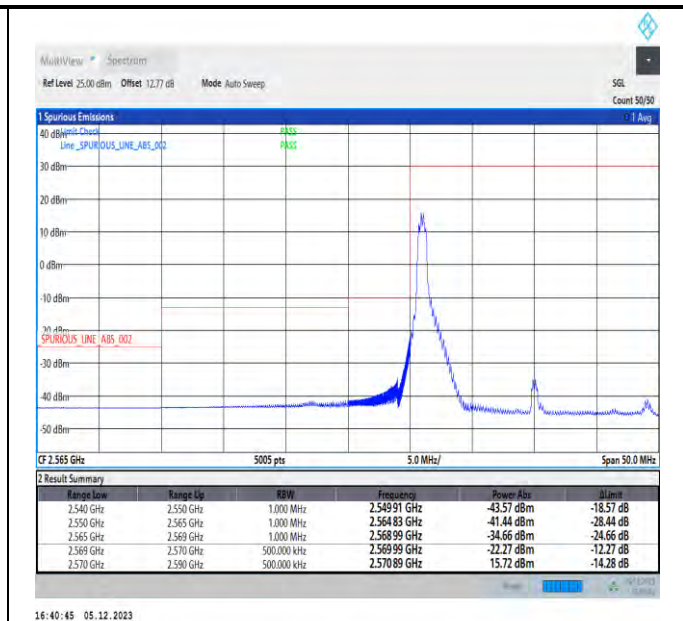
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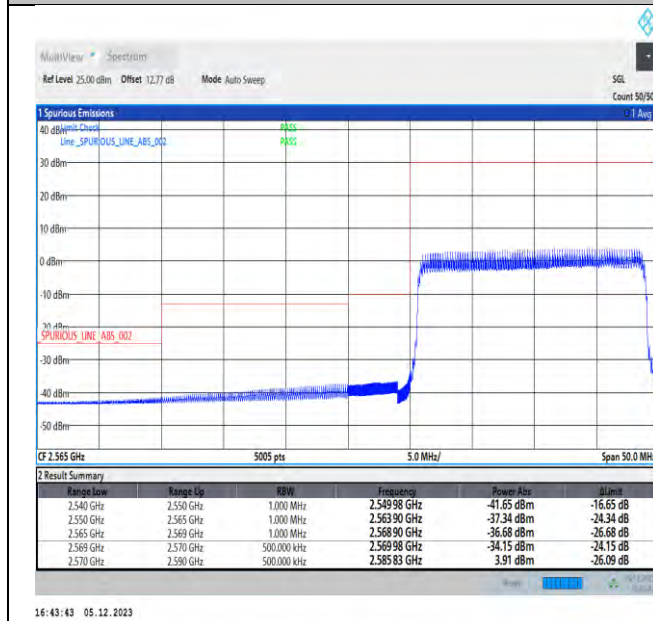
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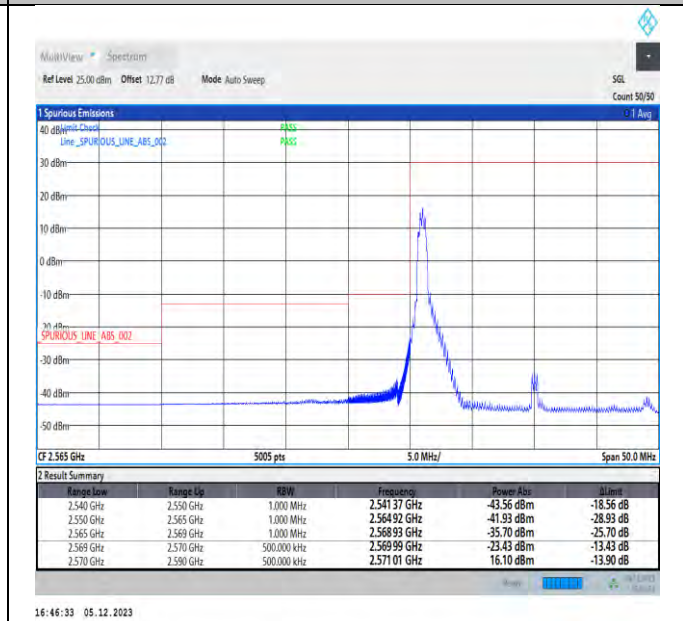
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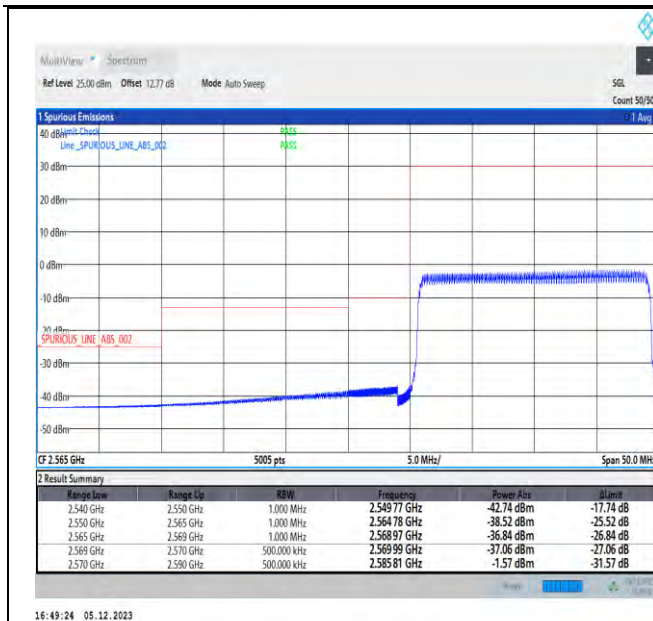
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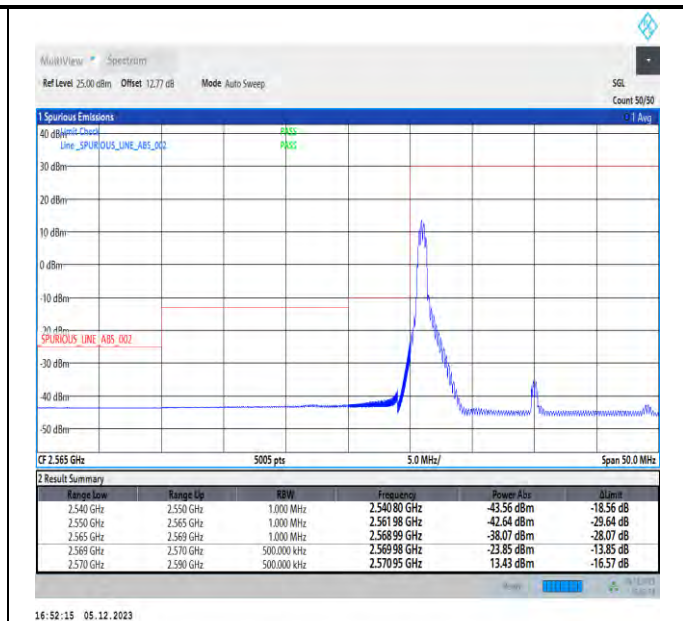
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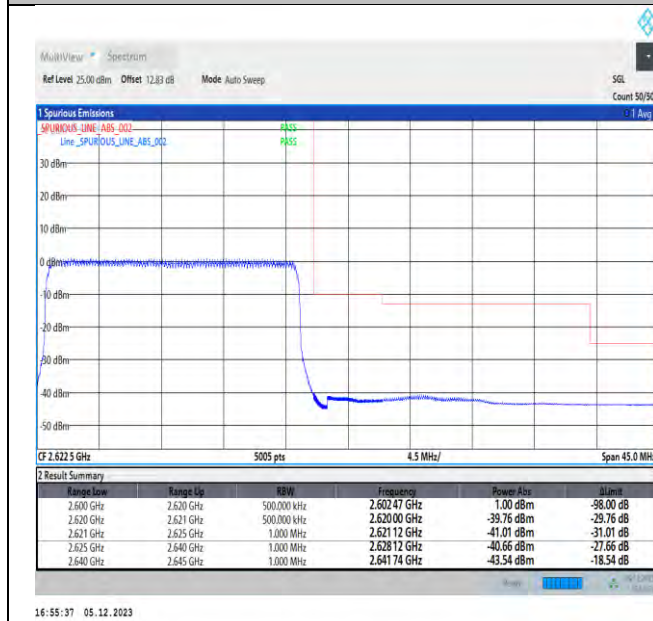
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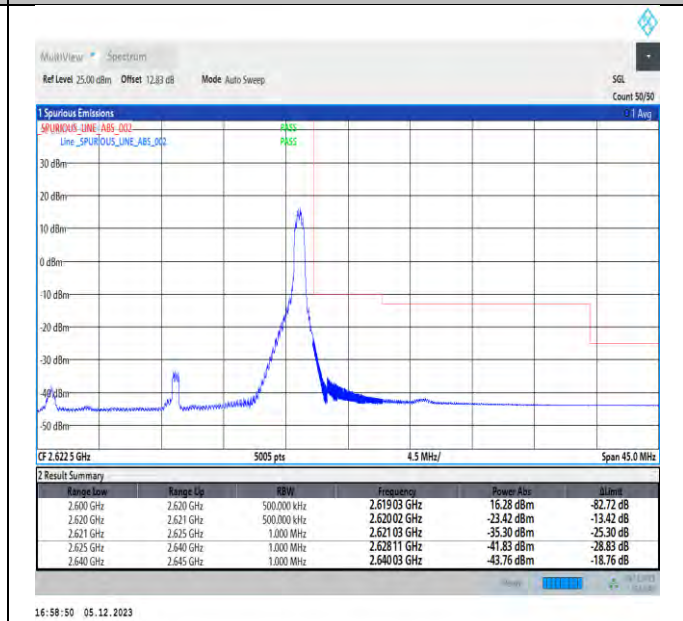
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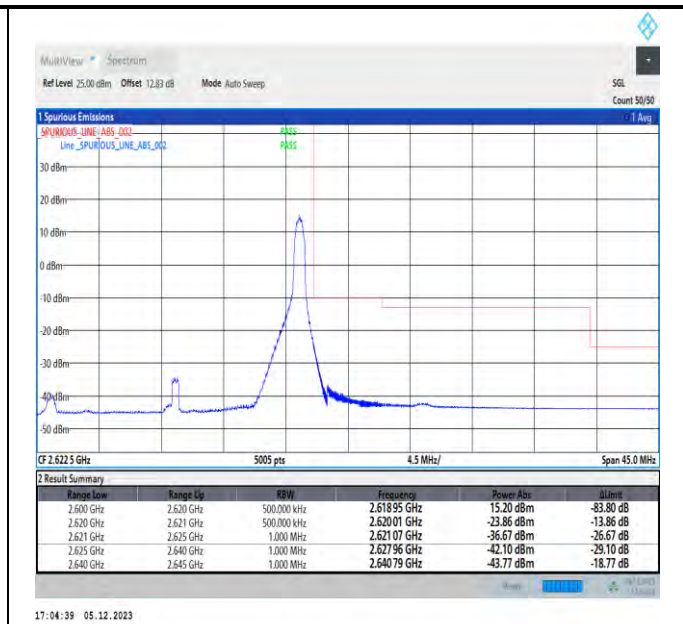
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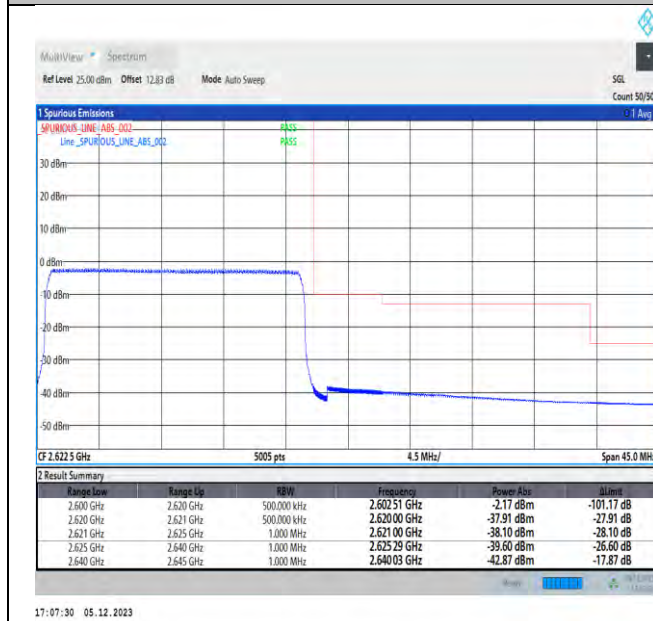
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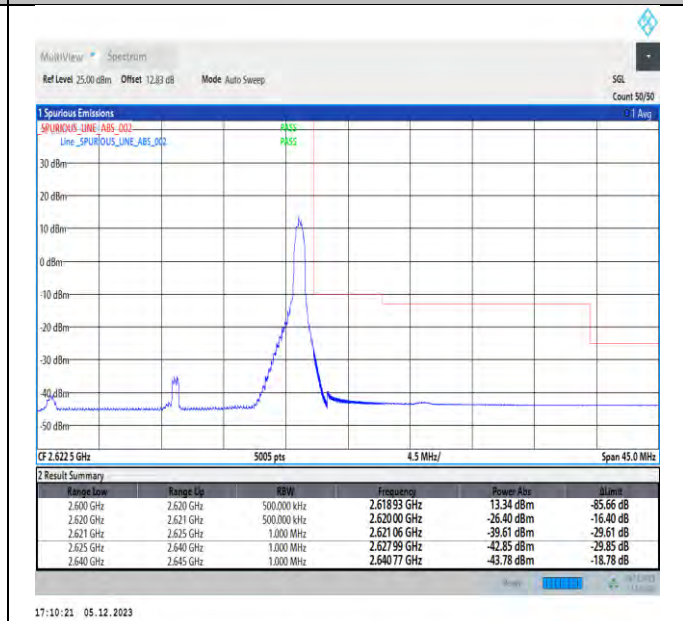
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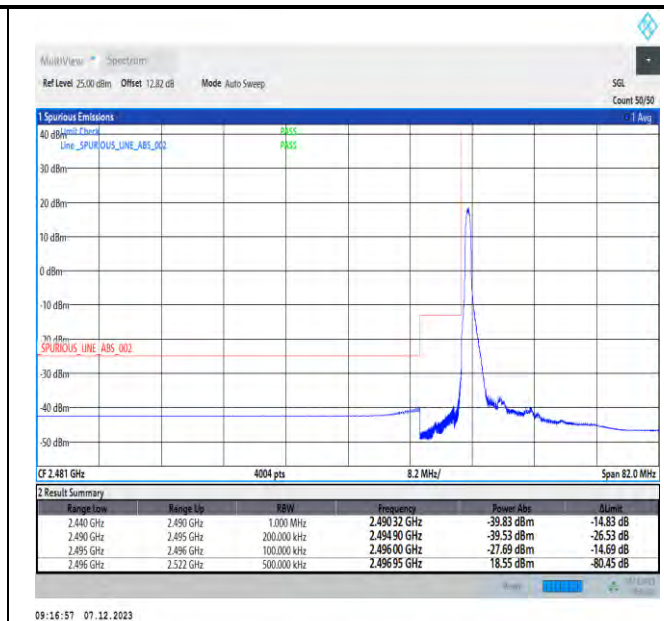
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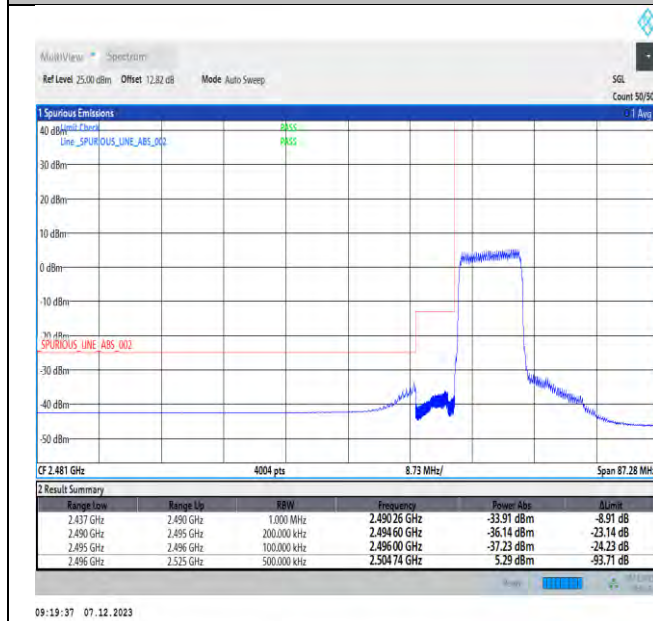
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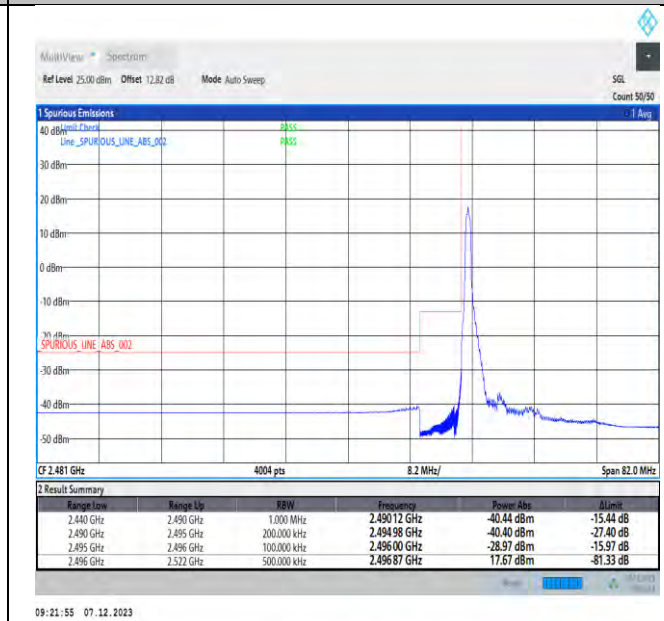
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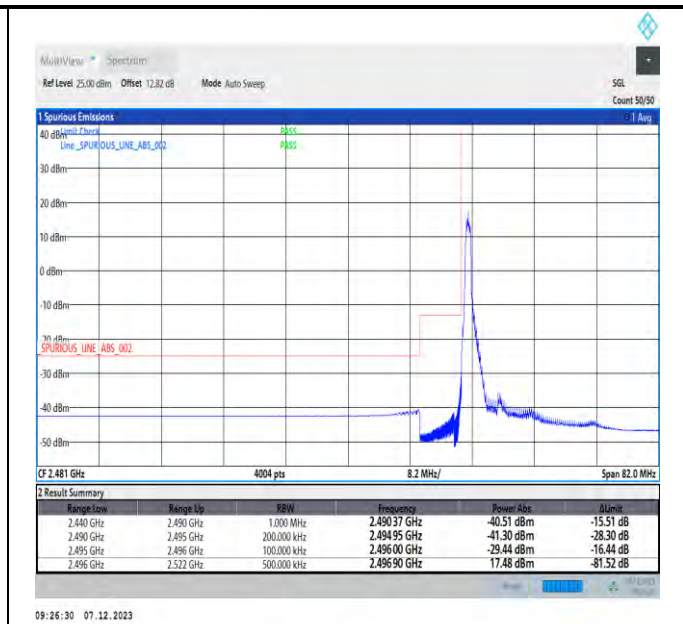
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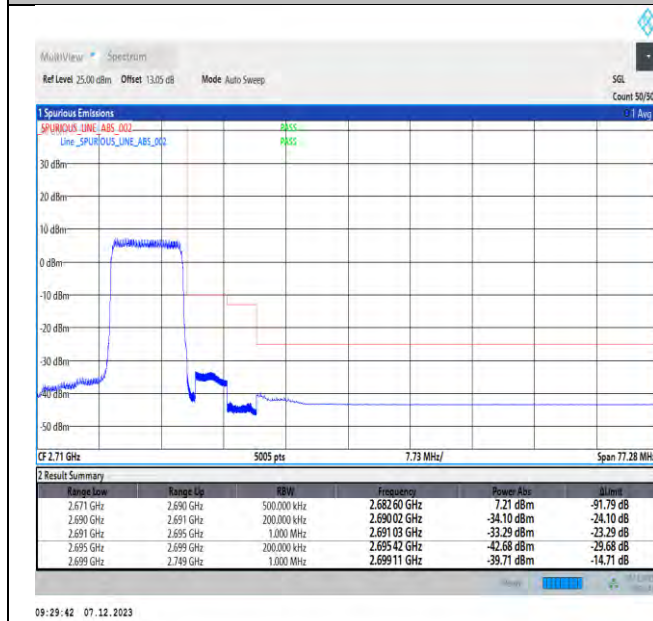
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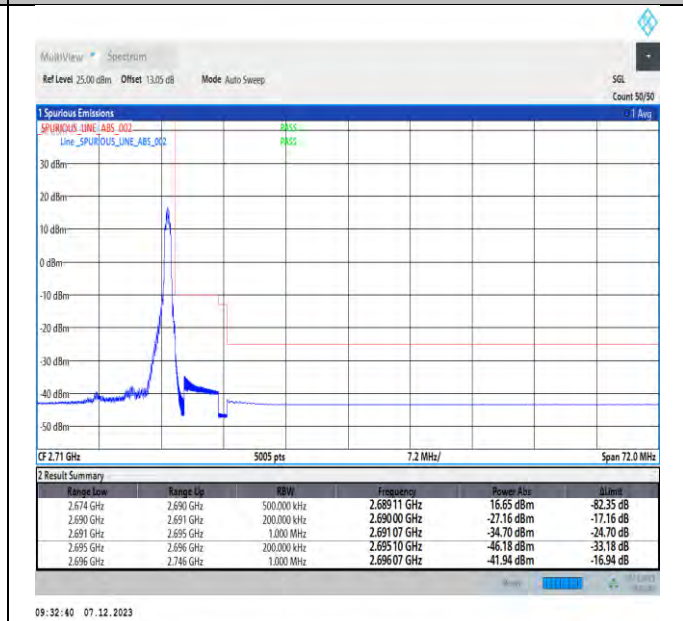
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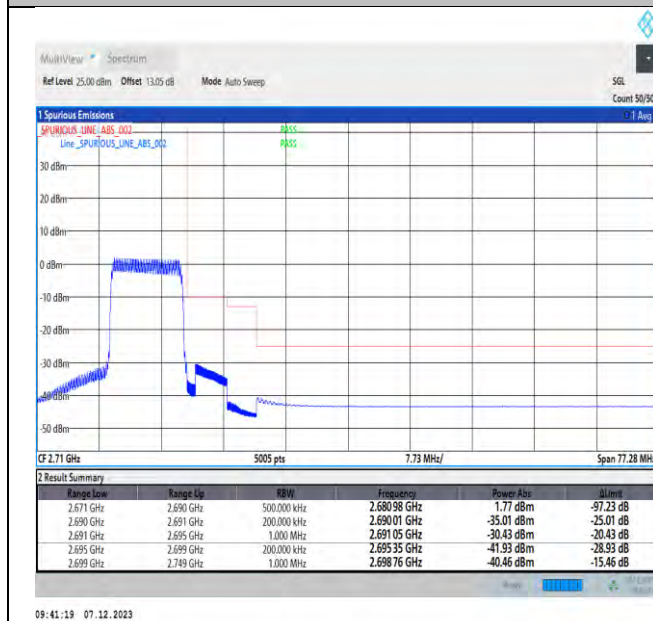
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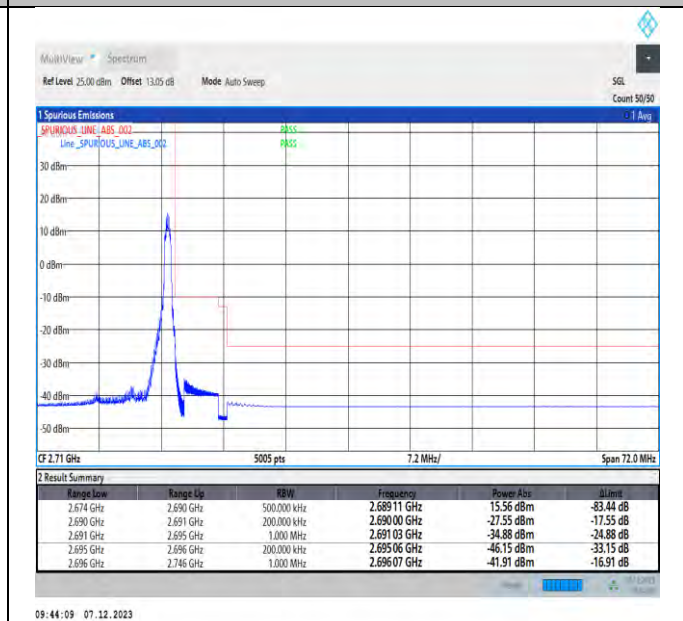
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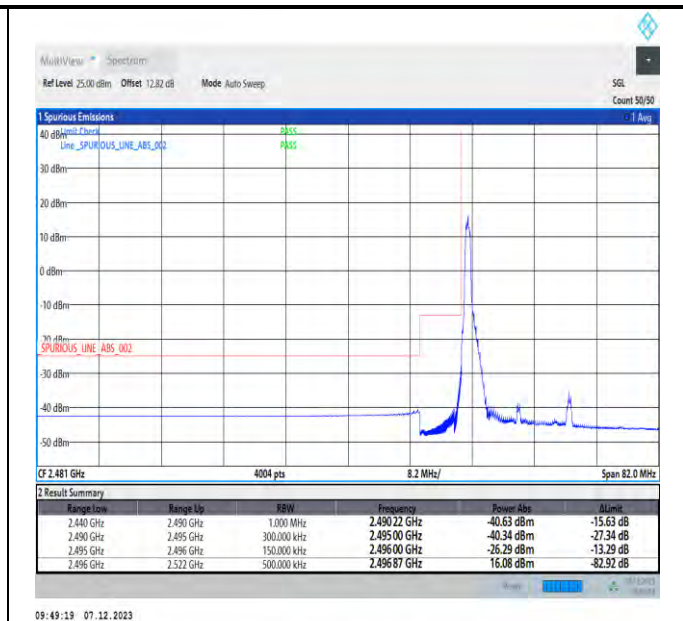
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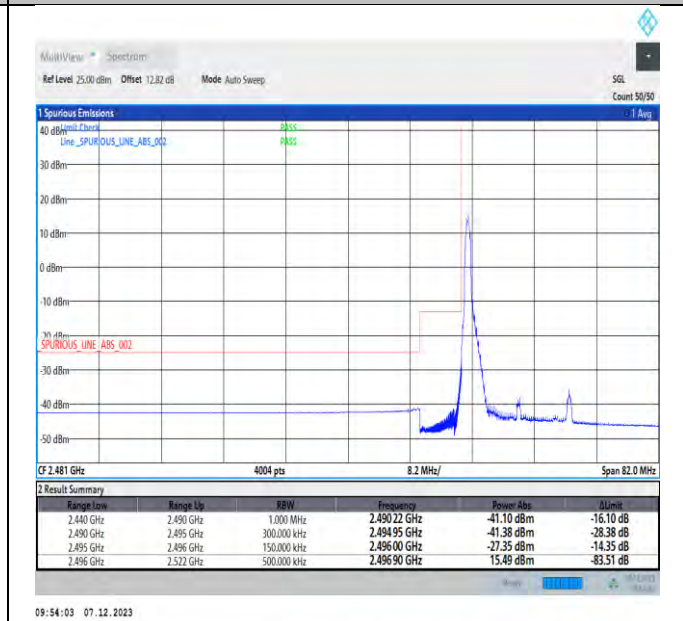
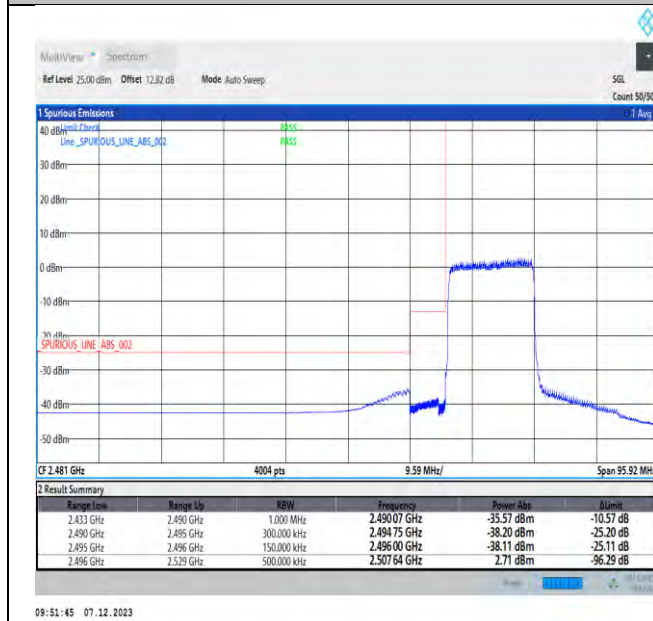


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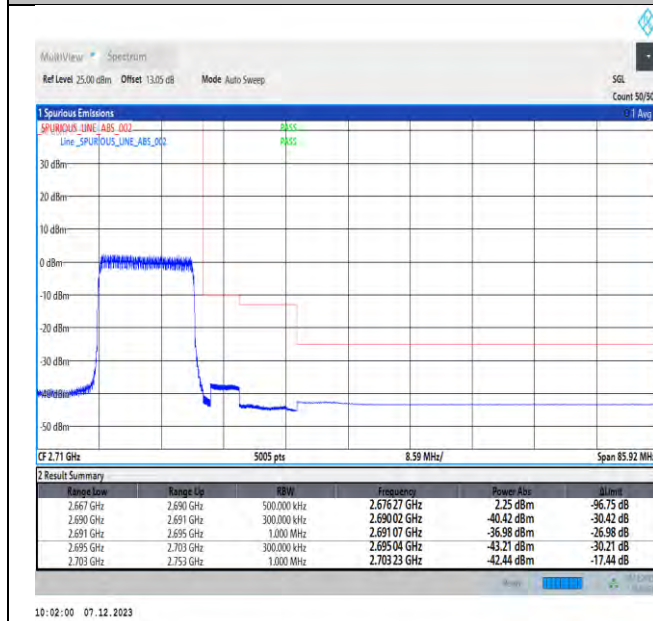
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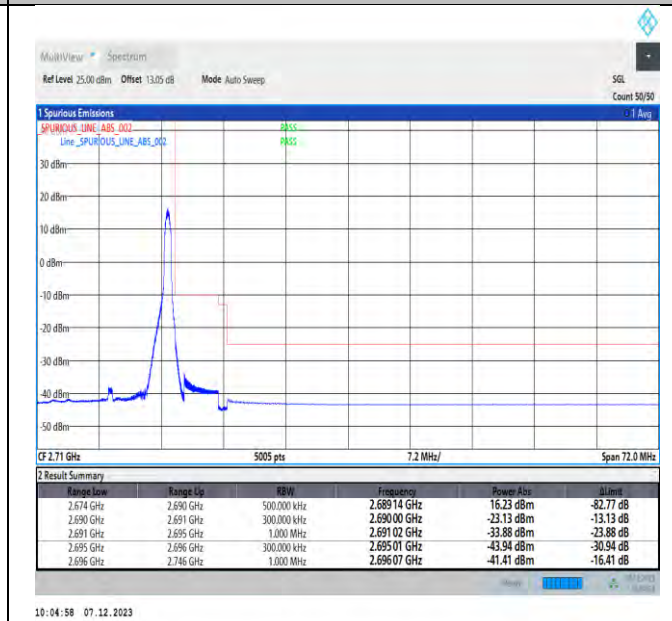
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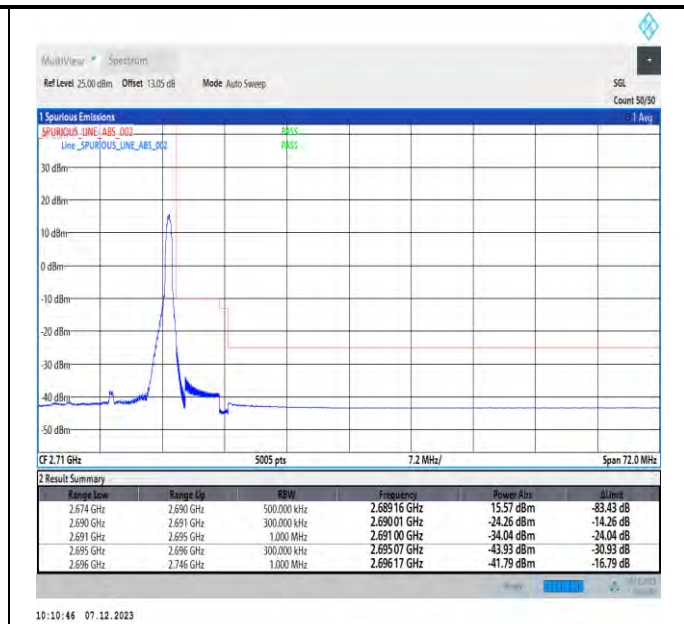
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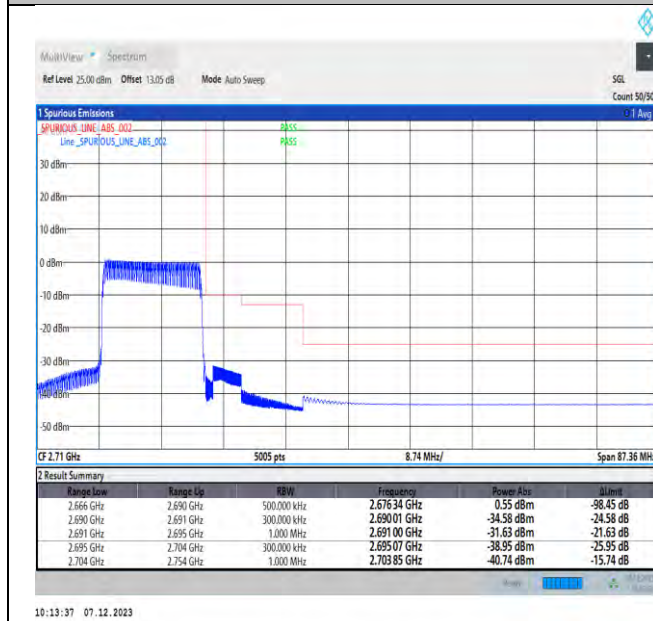
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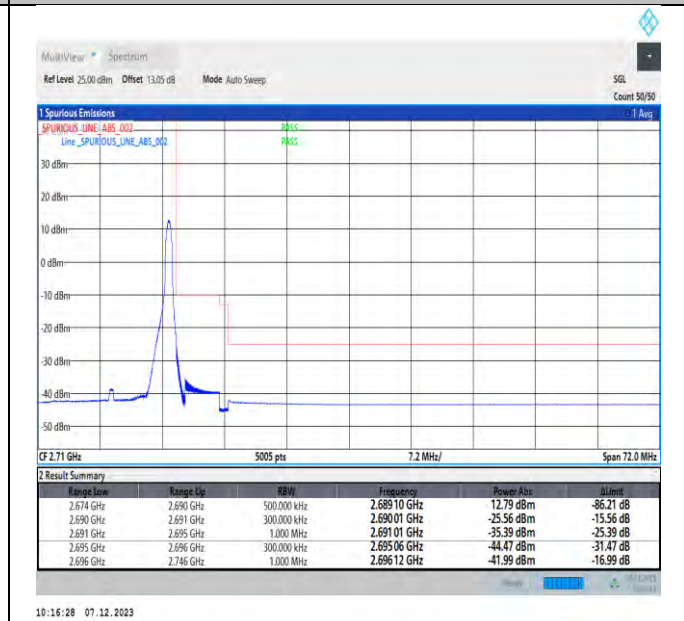
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