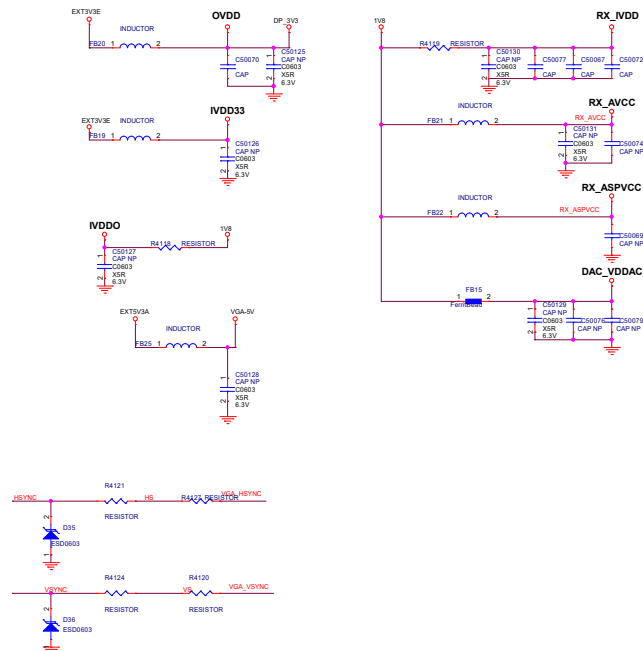
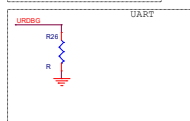
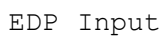
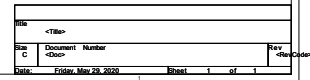
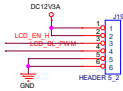


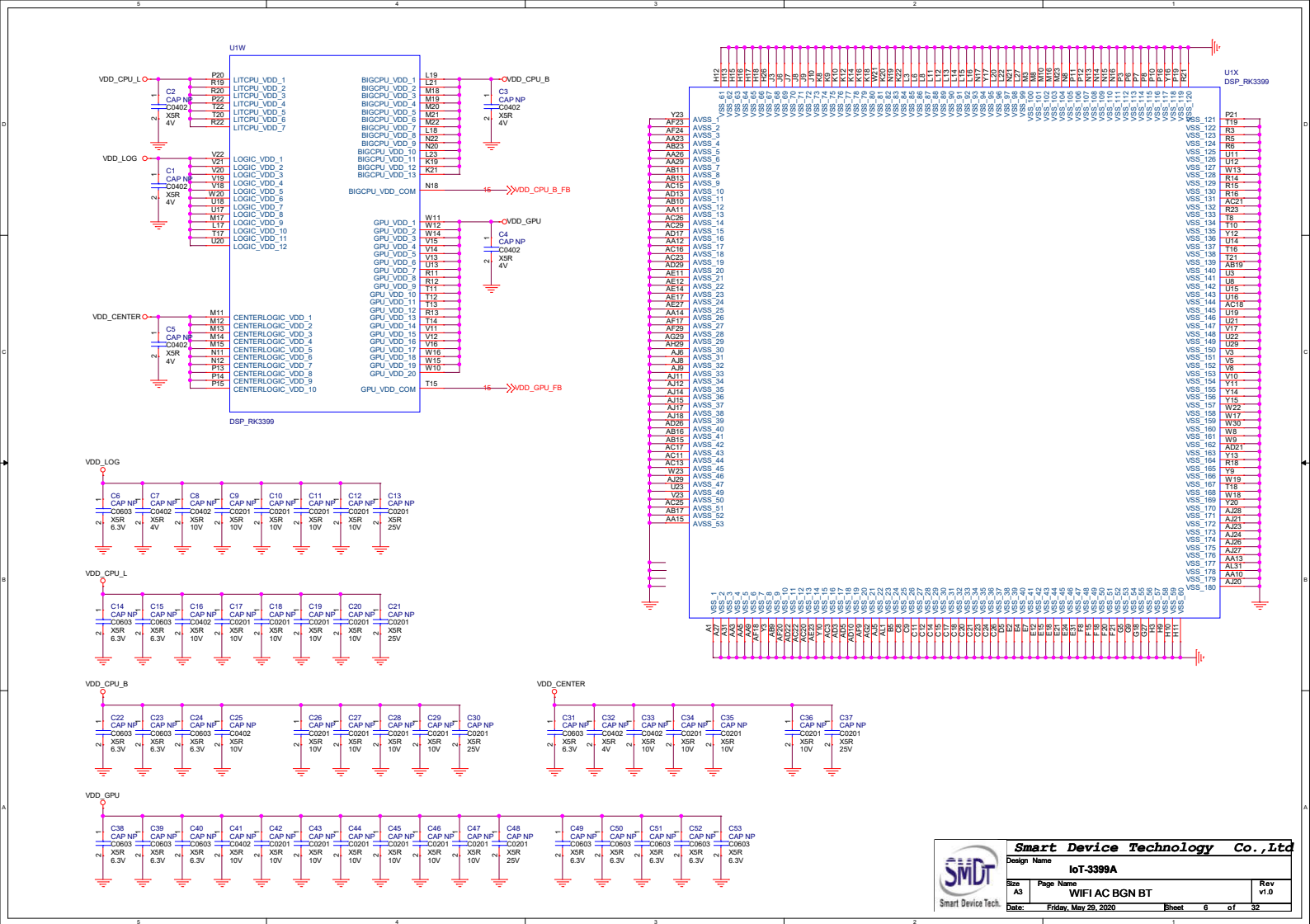
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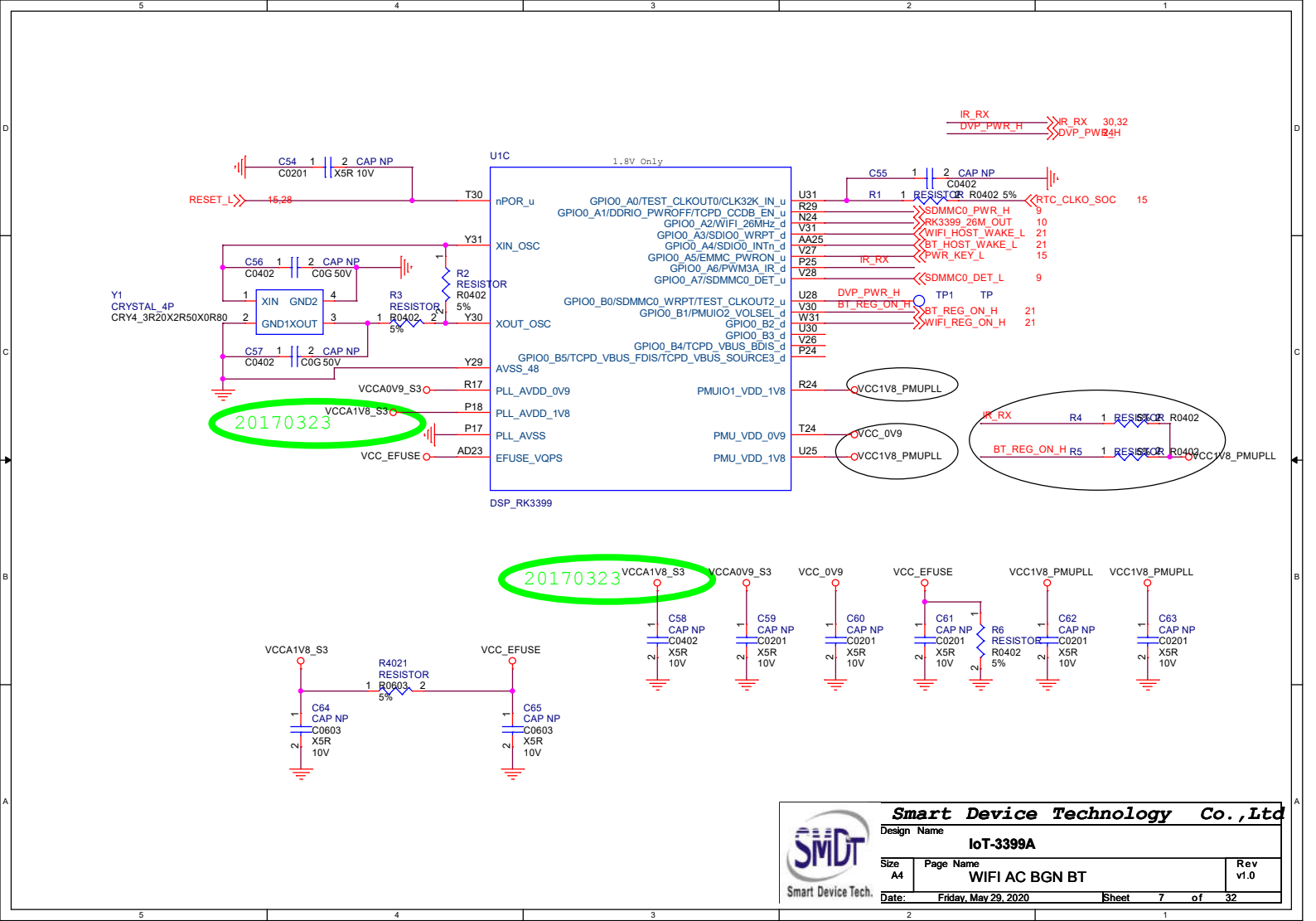


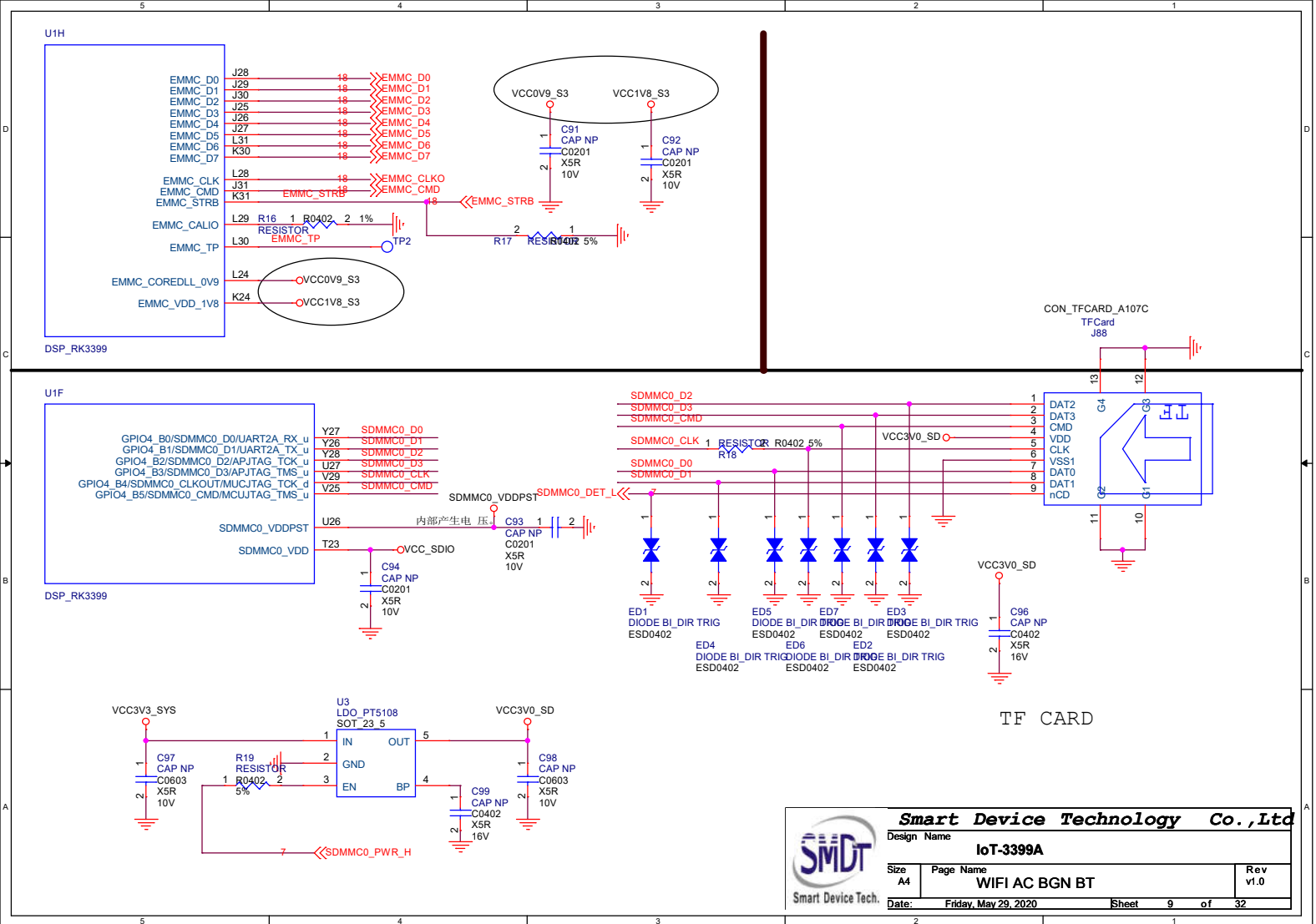


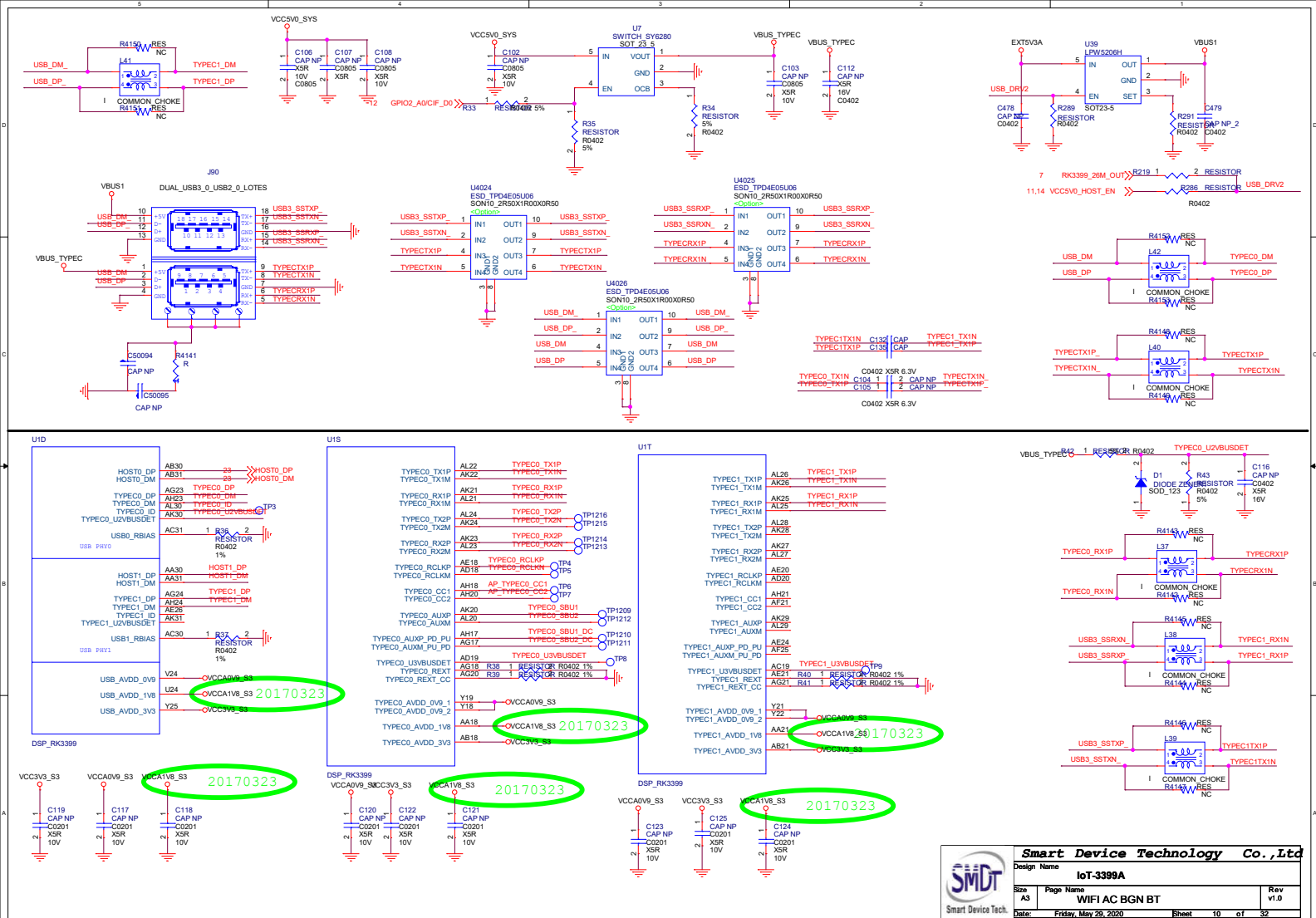
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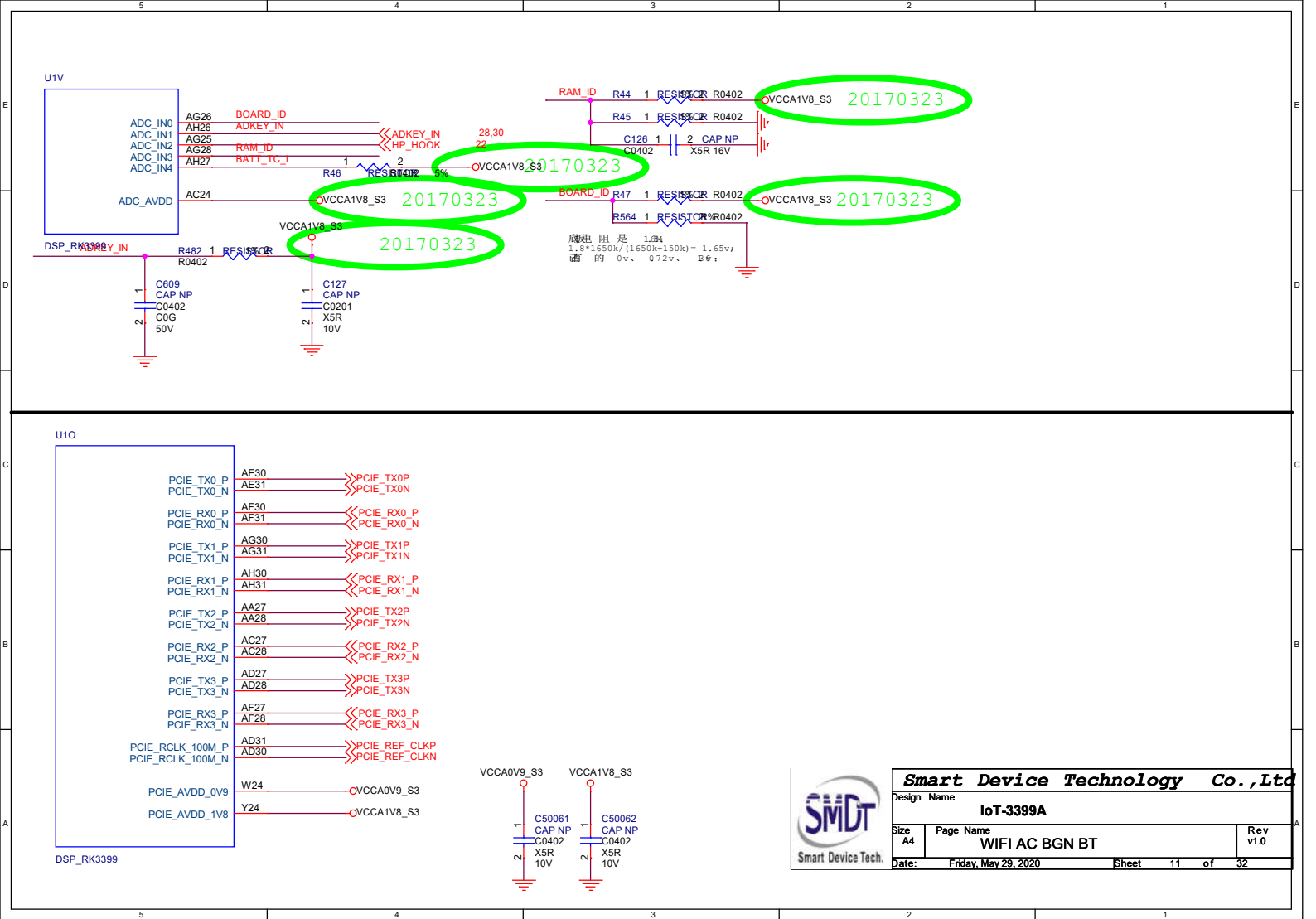
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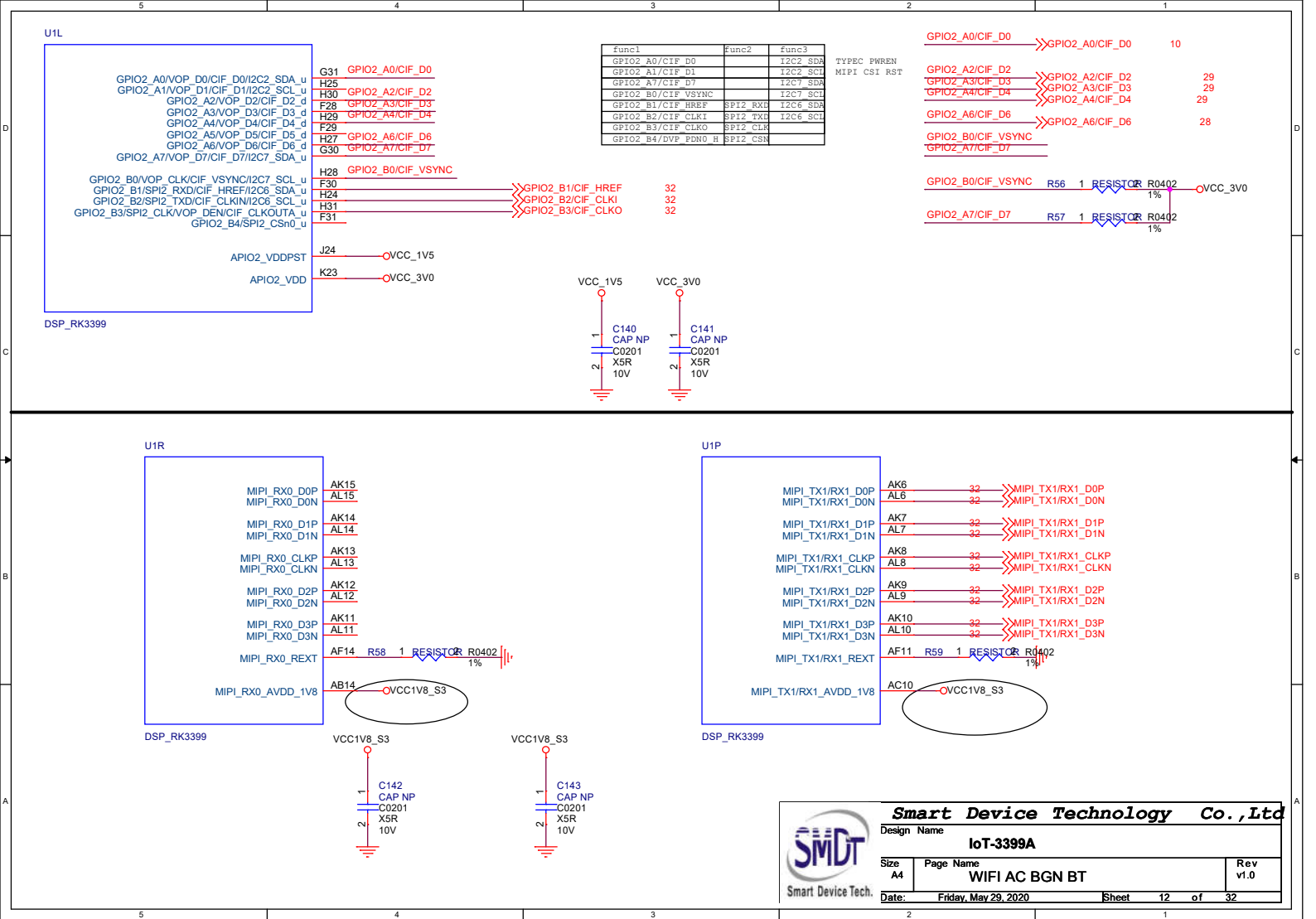


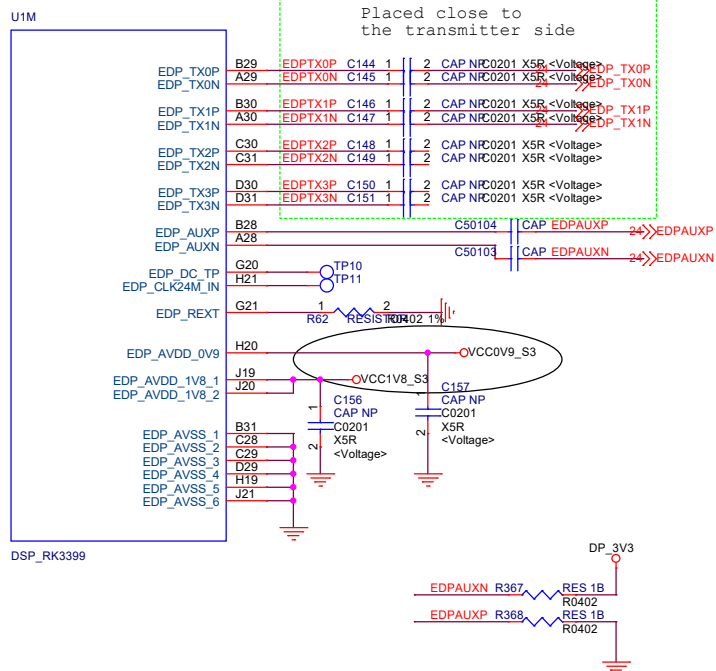
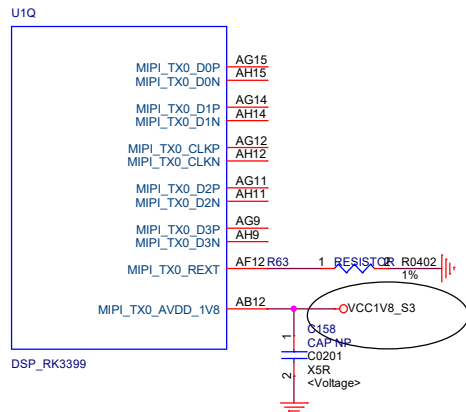
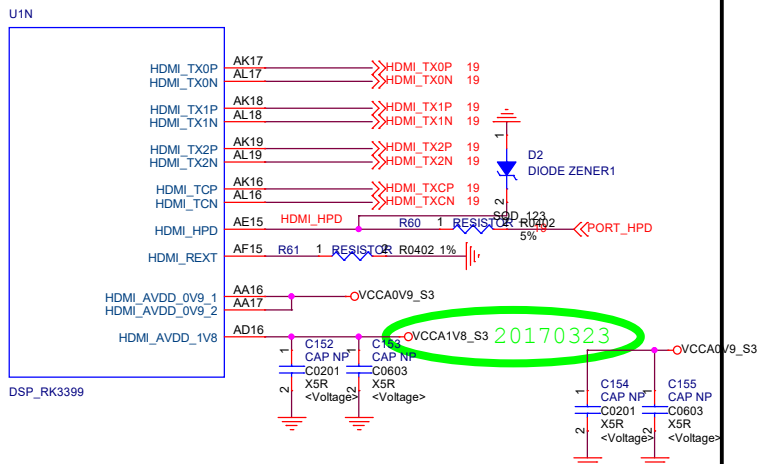


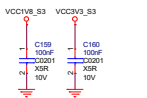








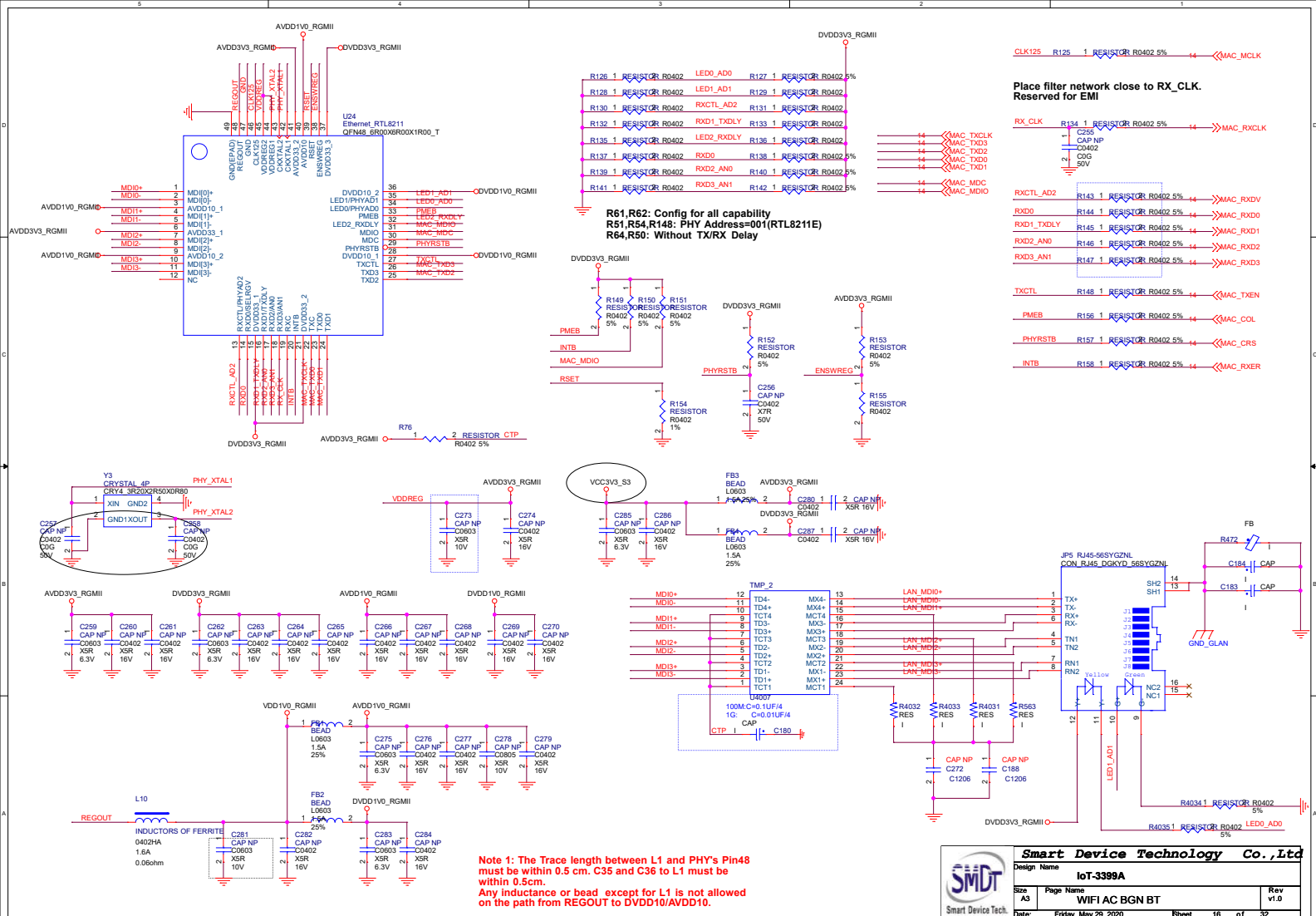




UART2DBG_RX
UART2DBG_TX

SP11_RXD
SP11_TXD





eMMC FLASH

VCC1V8_S3

R14 1 10K 2 R0402 EMMC_D0

R15 1 10K 2 R0402 EMMC_D1

R80 1 10K 2 R0402 EMMC_D2

R198 1 10K 2 R0402 EMMC_D3

R290 1 10K 2 R0402 EMMC_D4

R394 1 10K 2 R0402 EMMC_D5

R395 1 10K 2 R0402 EMMC_D6

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VCC1V8

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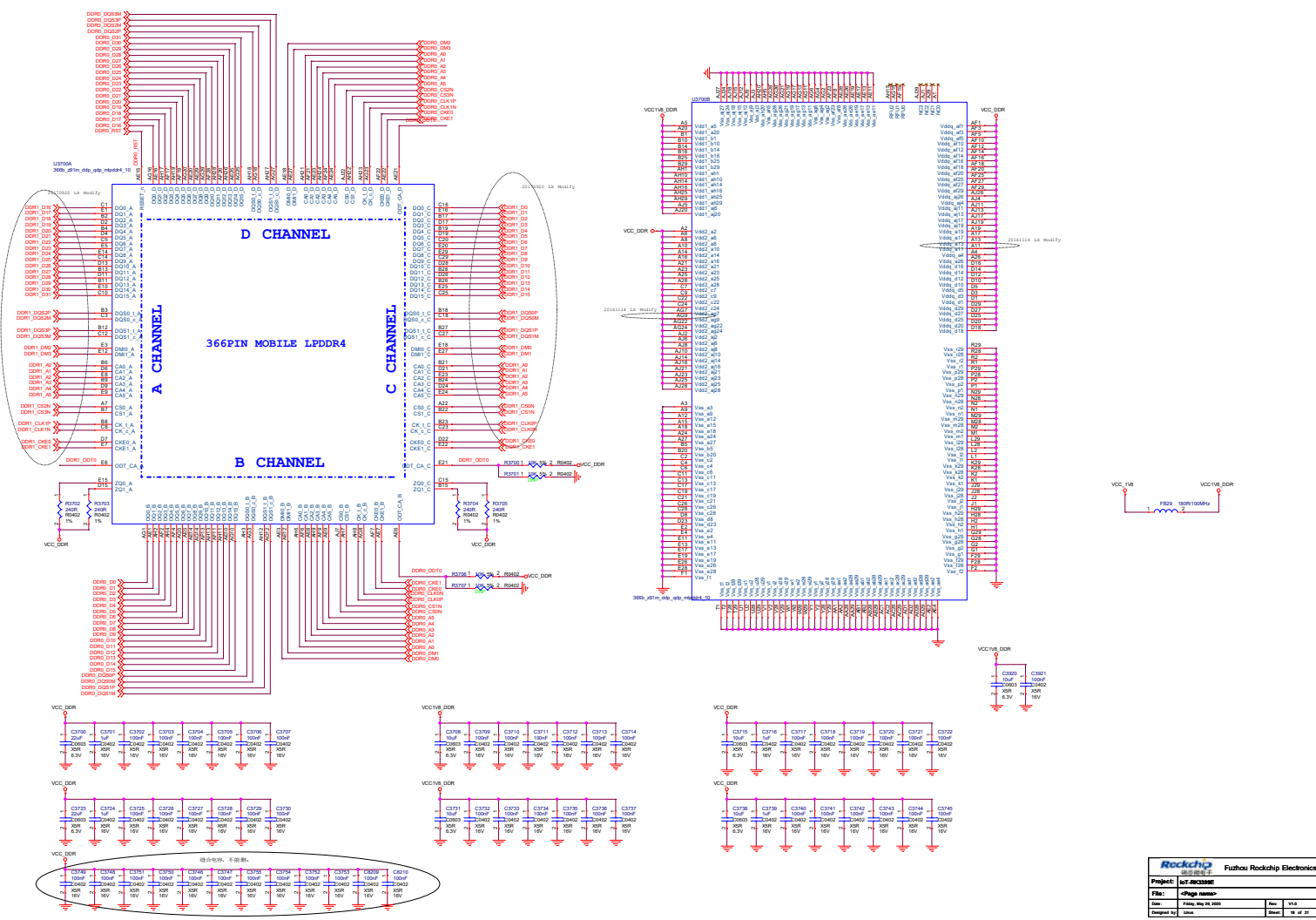
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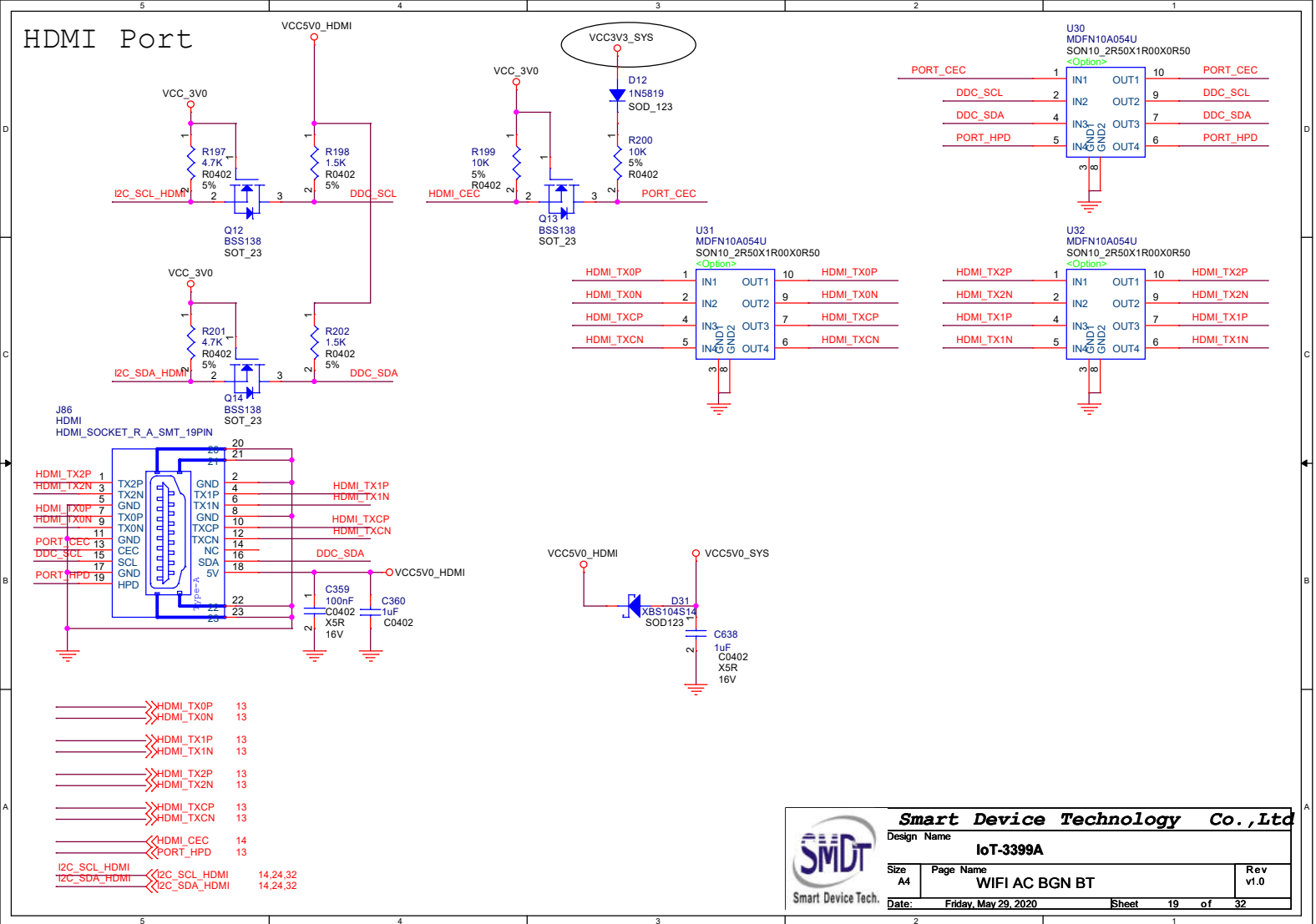
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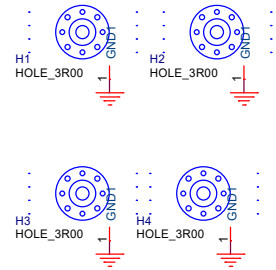
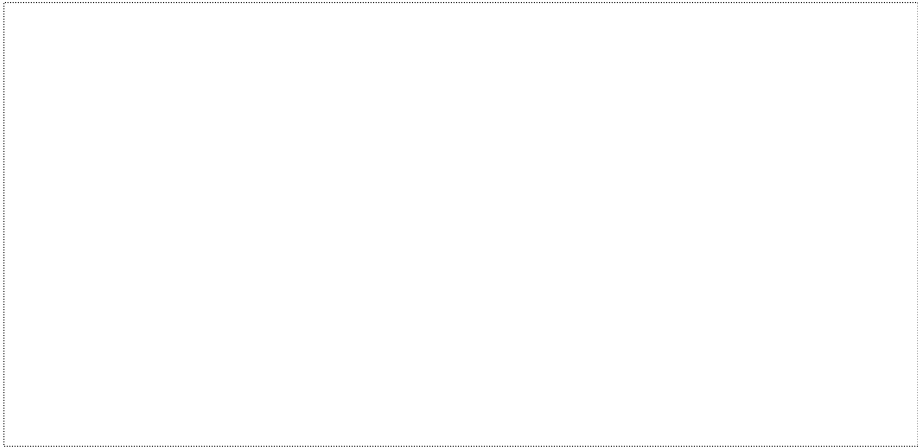


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IoT-3399A			
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HDMI Port





PAGE28 4CH*UART
PAGE29 MINI PCI-E
PAGE30 MCU
PAGE31 DCIN DCDC8.6V,DCDC5V DCDC3.3V
PAGE32 PCI-E (M2) (V00 NO OPTION)



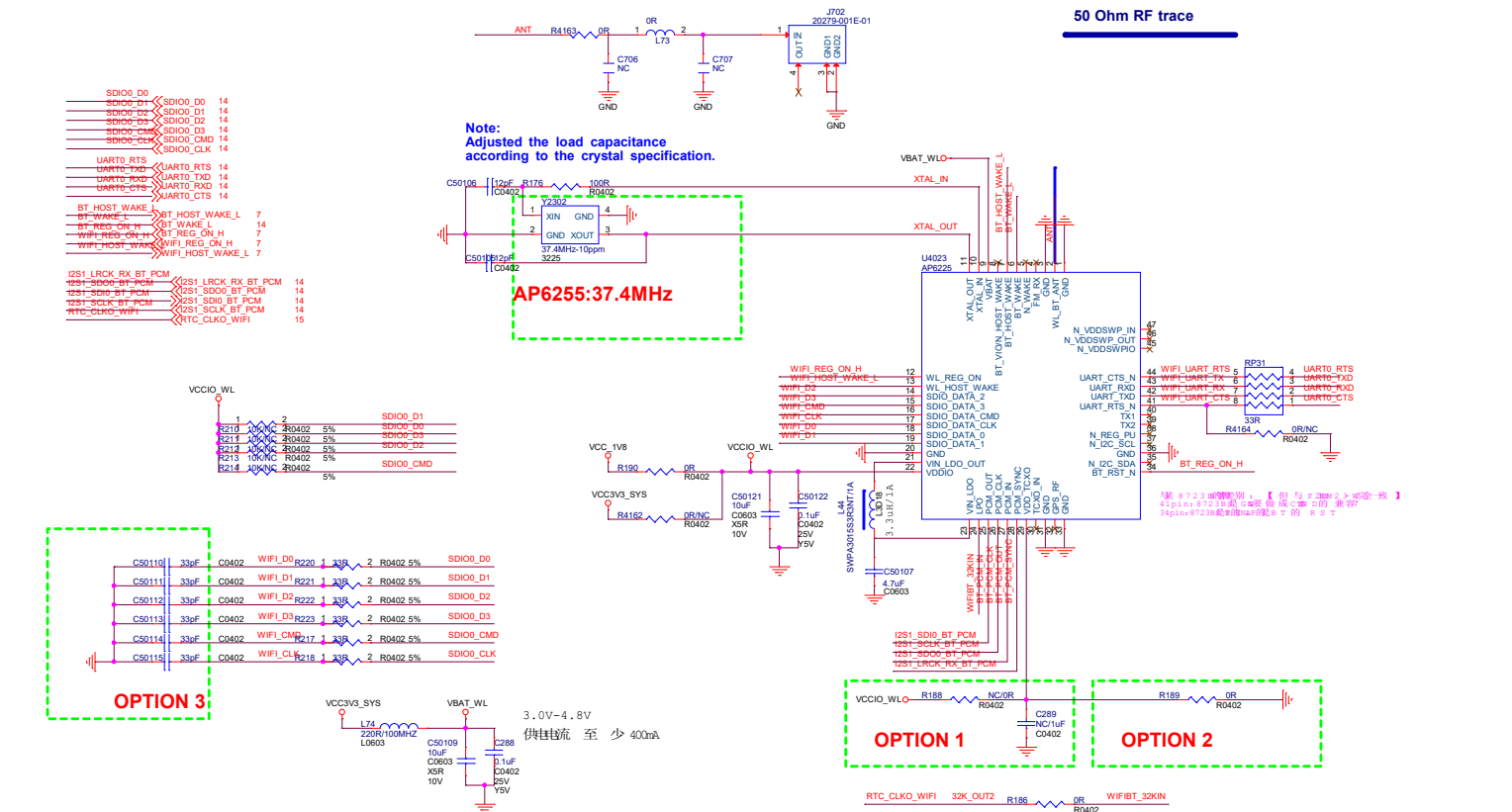
Smart Device Tech.

Smart Device Technology Co.,Ltd

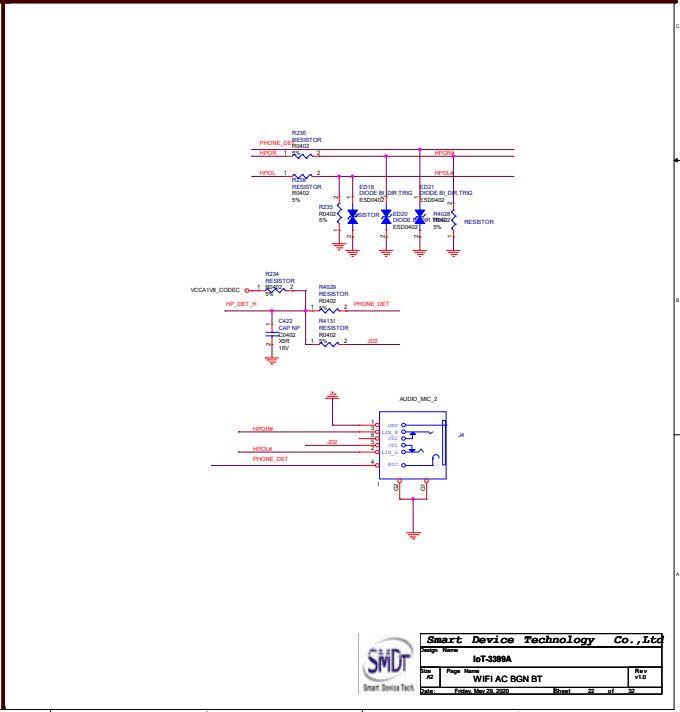
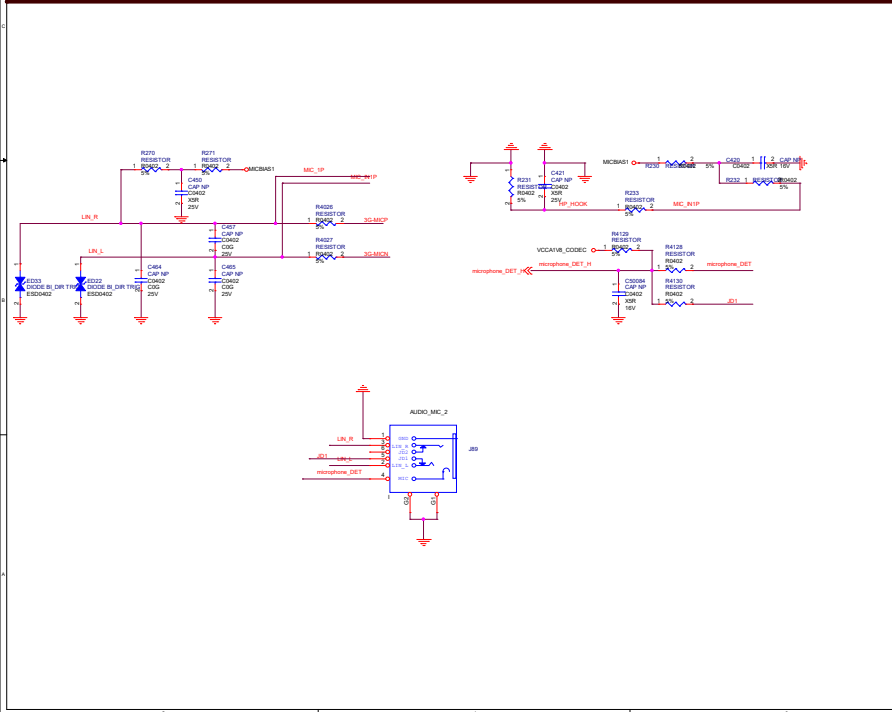
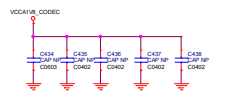
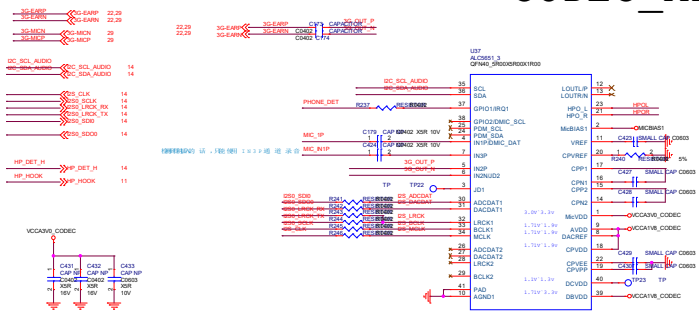
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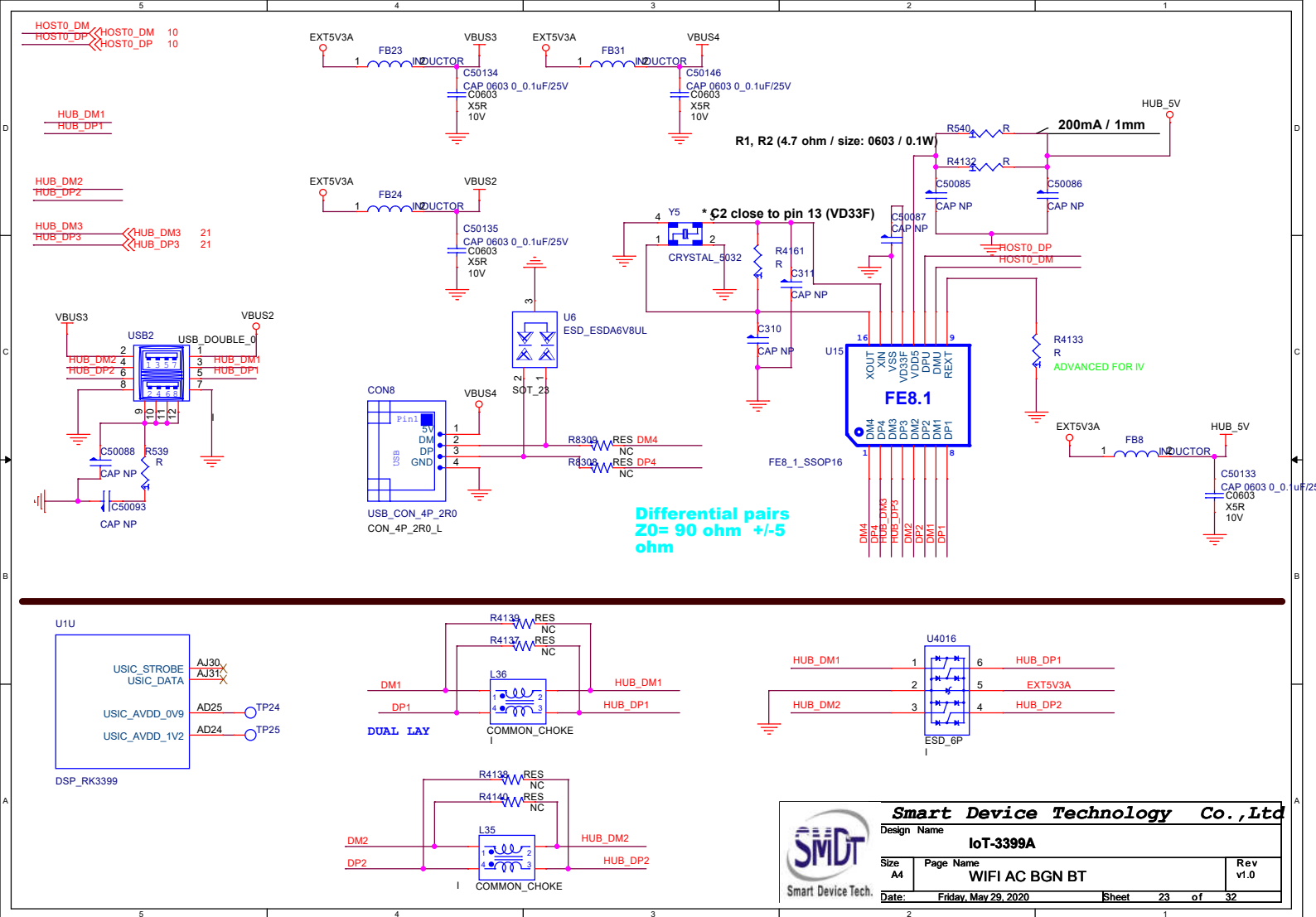
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Date: Friday, May 29, 2020		Sheet 20 of 32

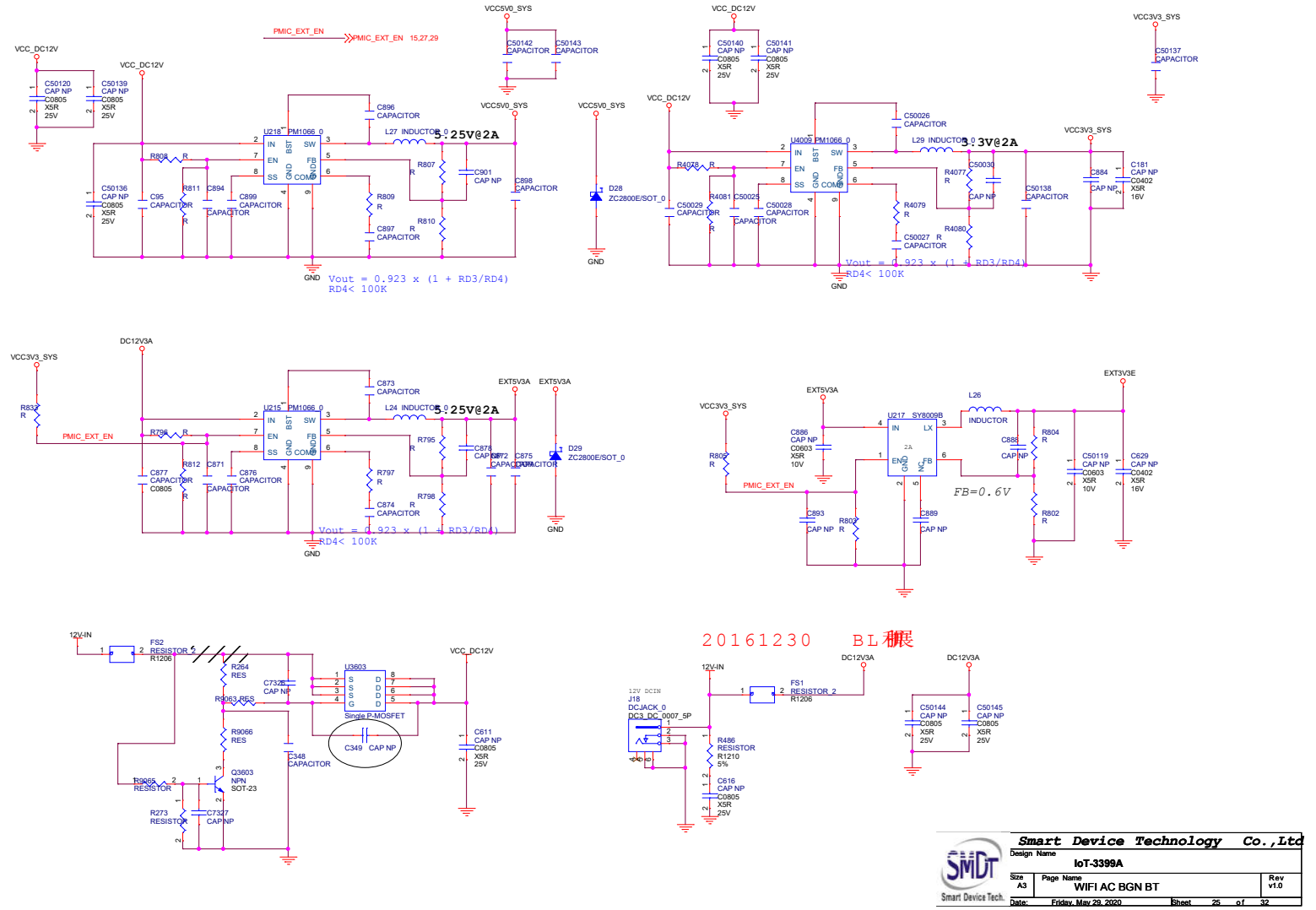
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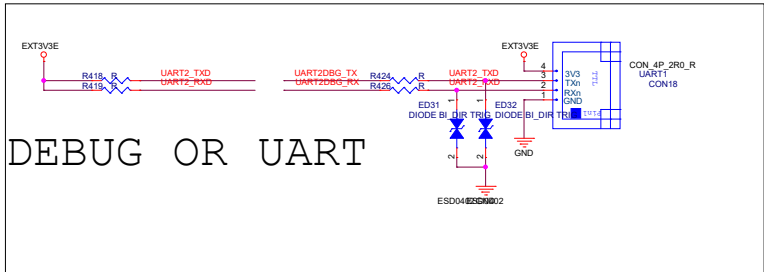


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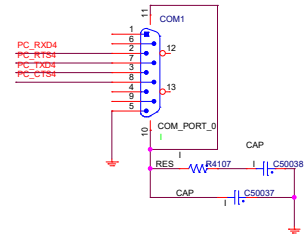
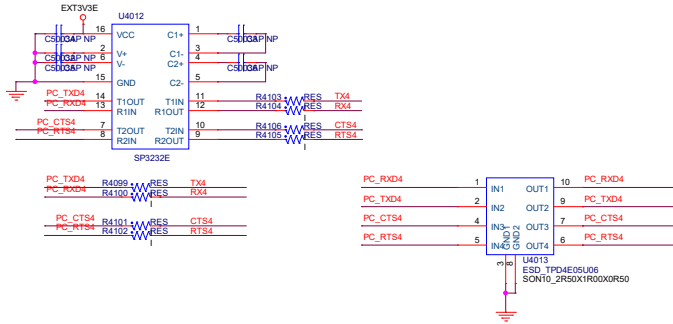
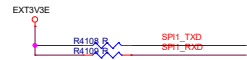


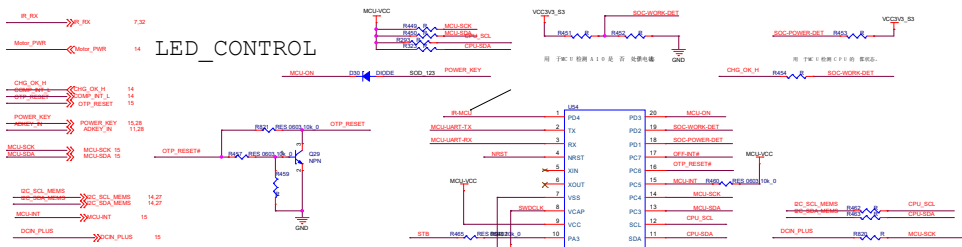




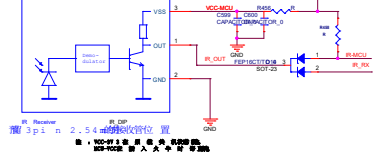


DEBUG OR UART

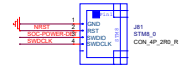
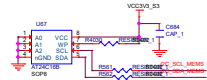
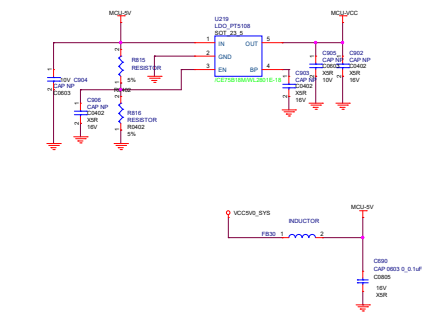
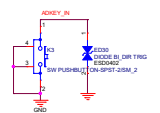
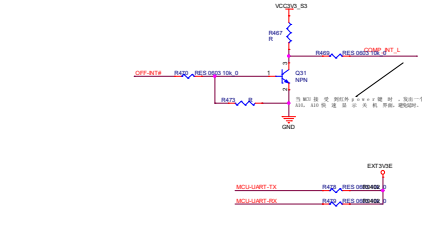
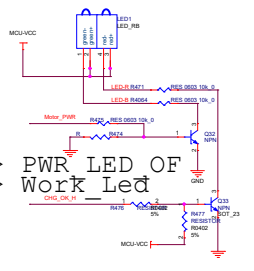




IR MODULE



Motor -> PWR
Motor -> Work Led





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Part Name

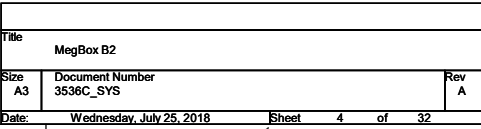
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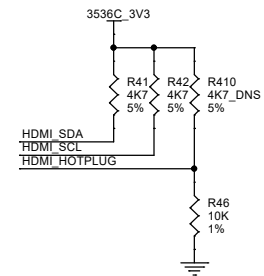
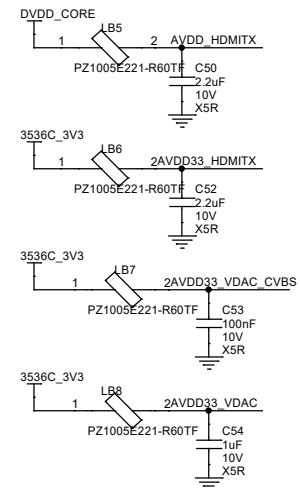
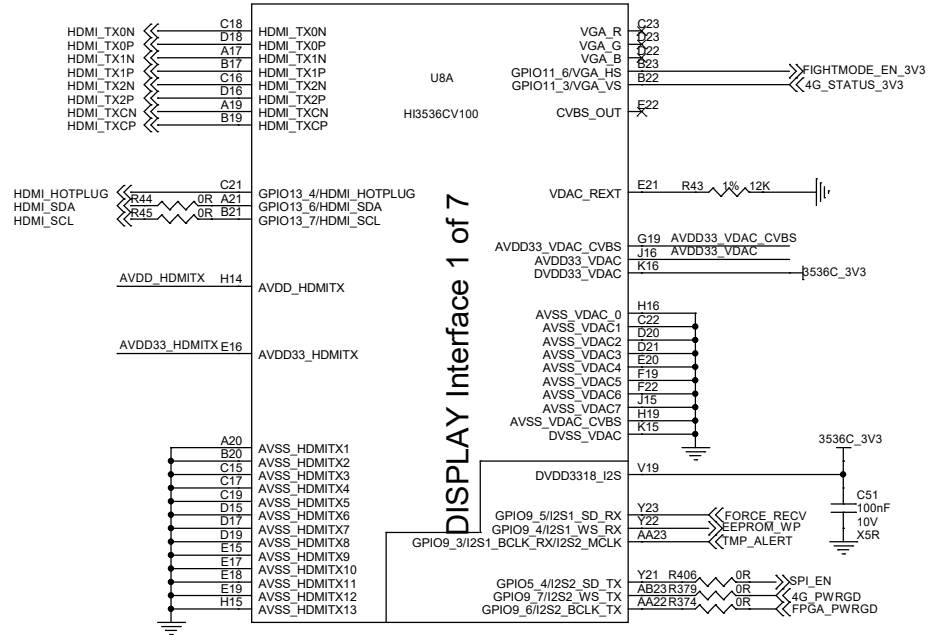
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WiFi AC BGN BT

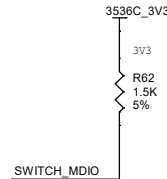
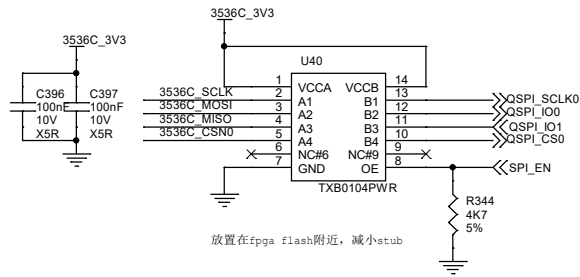
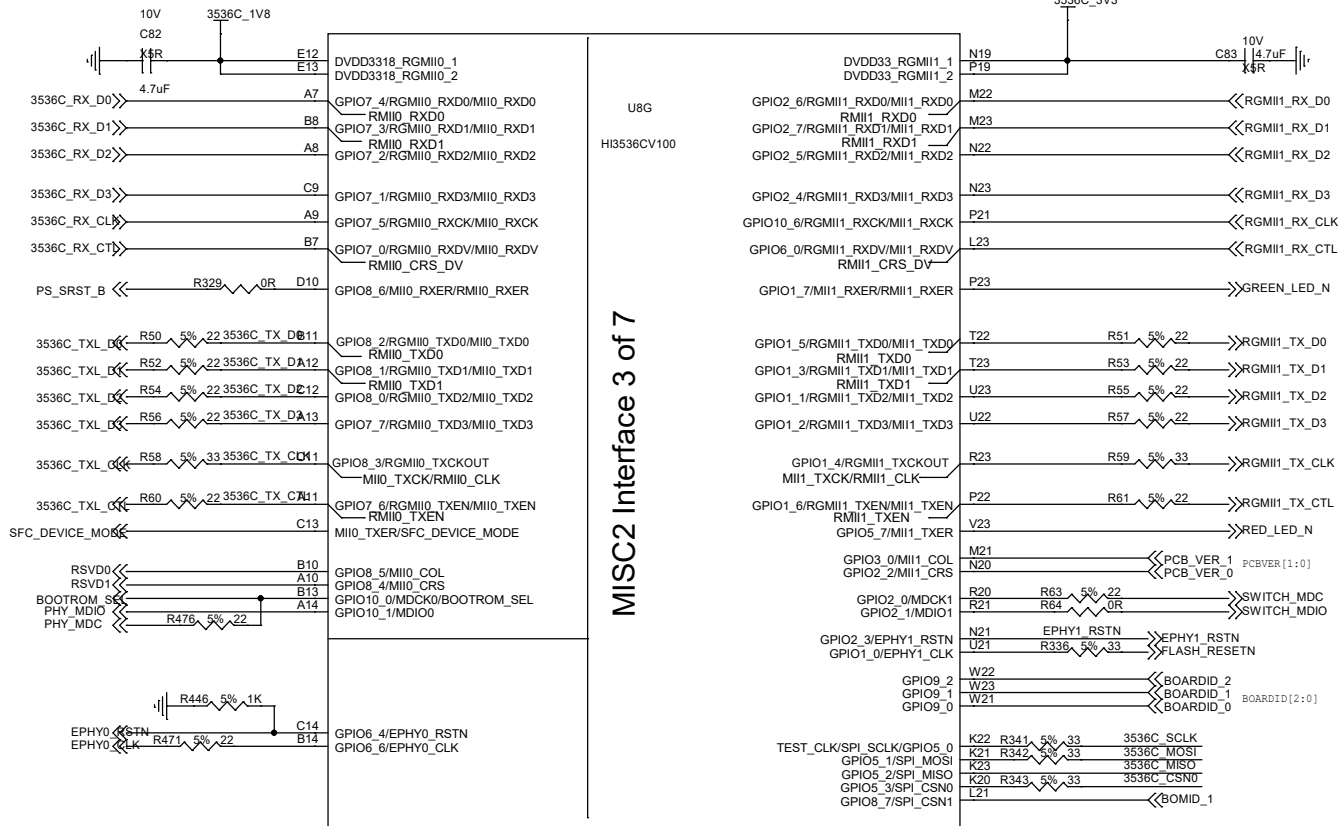
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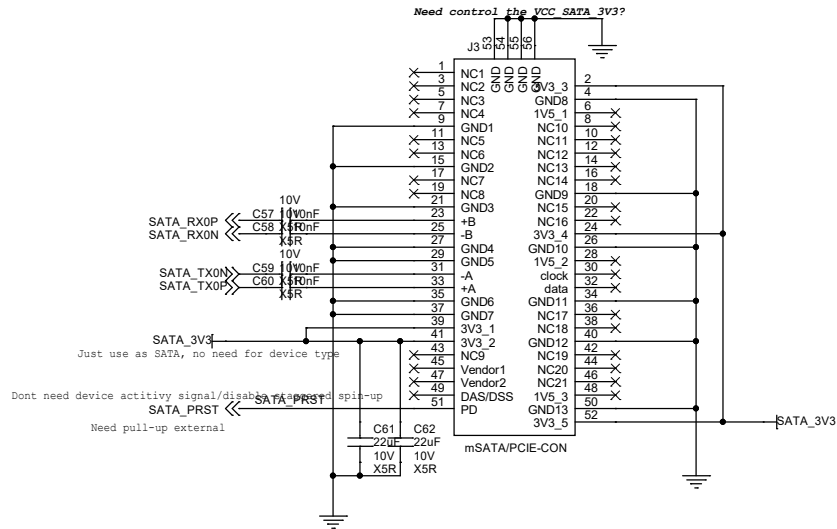
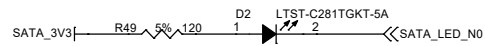
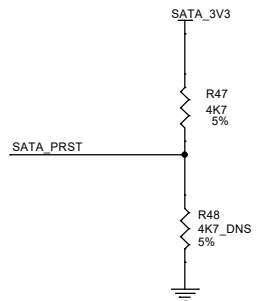
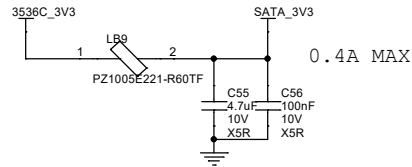
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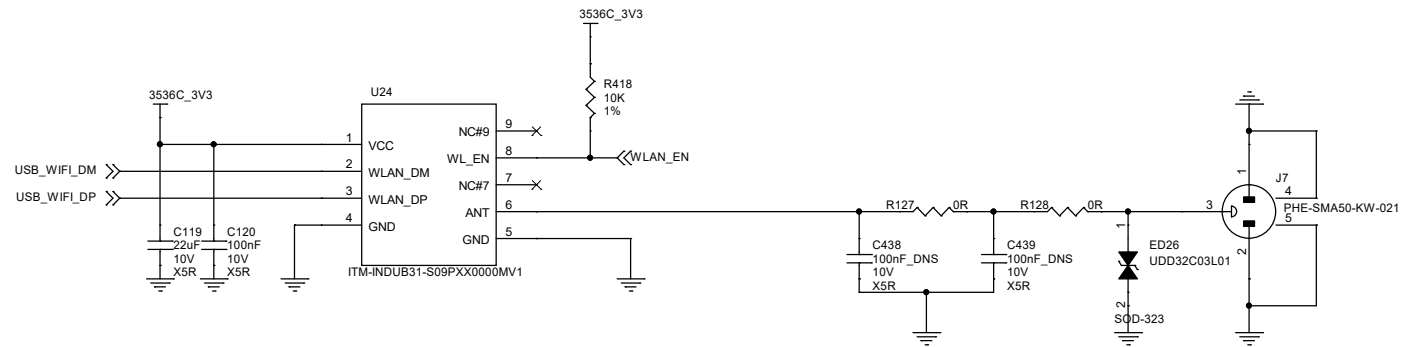




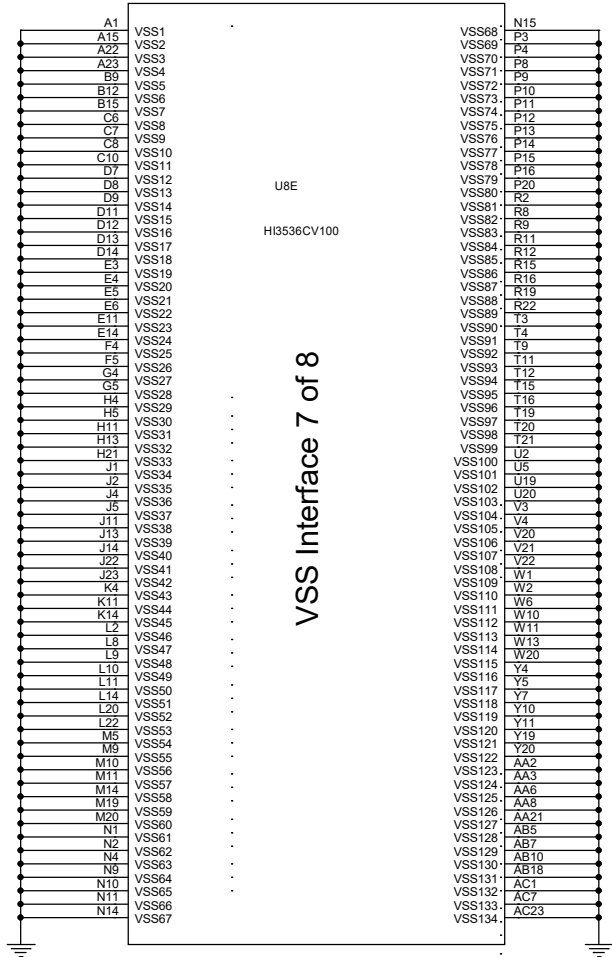
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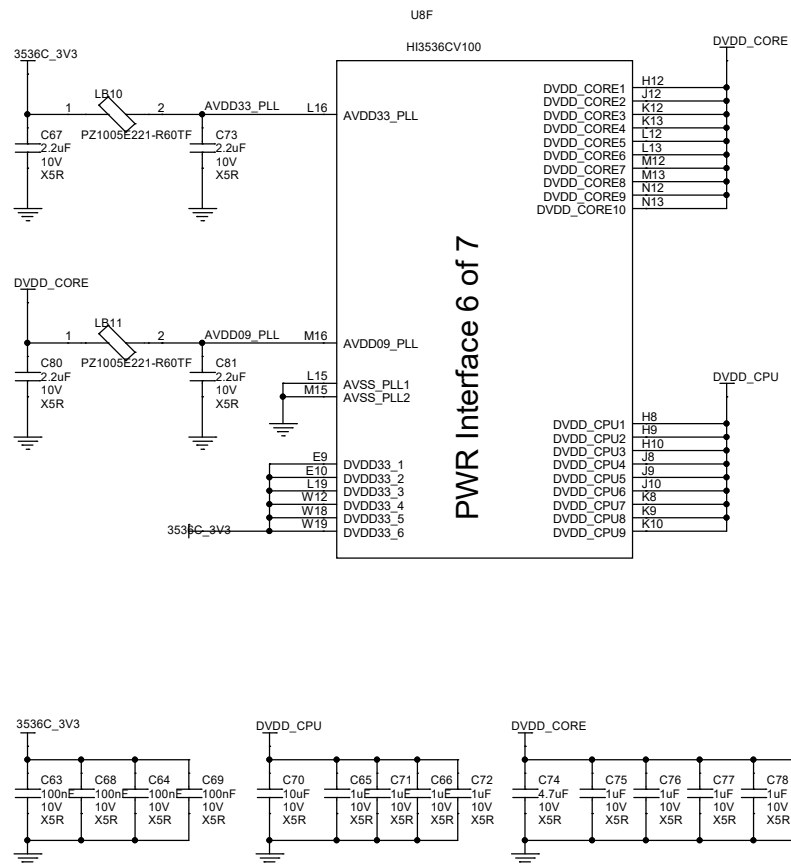




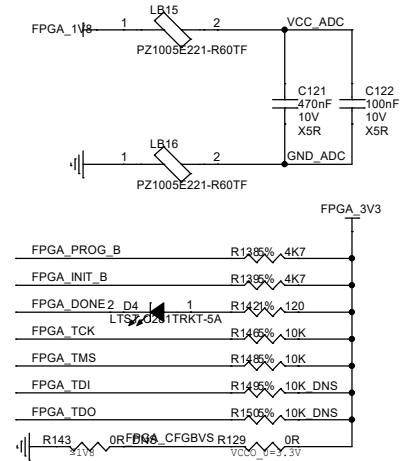
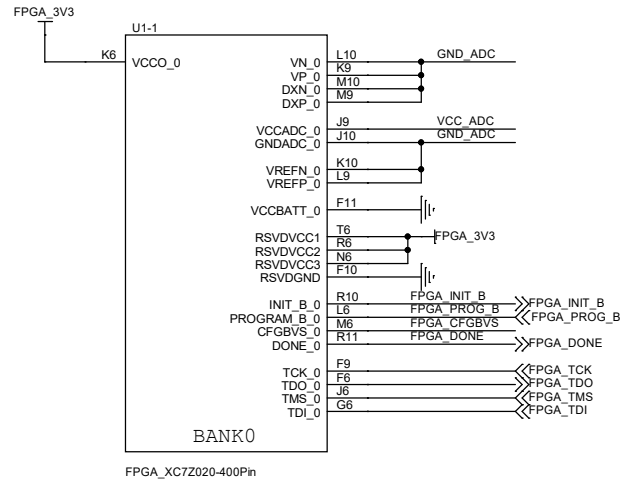
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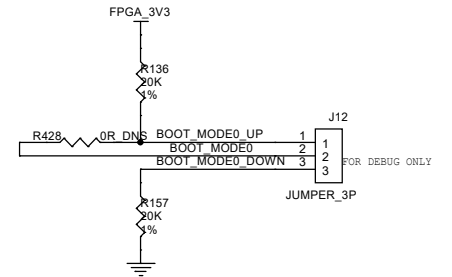
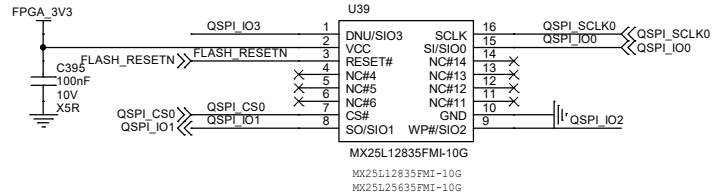
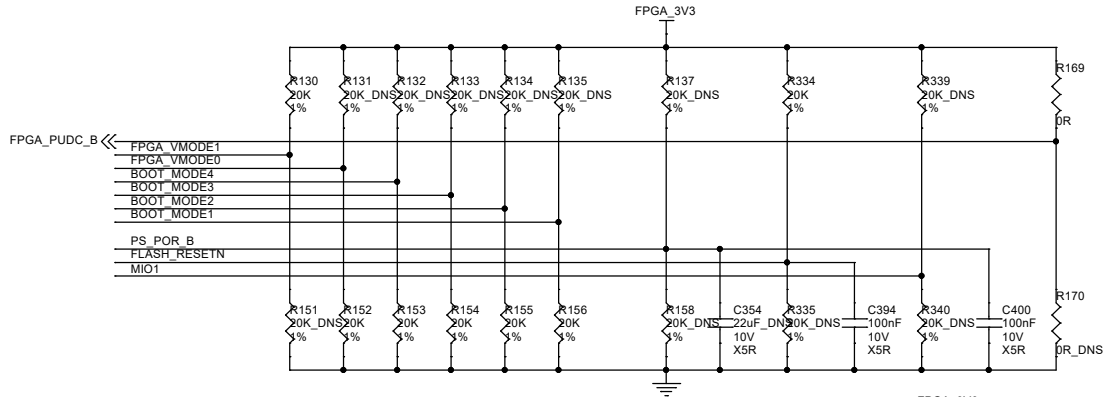
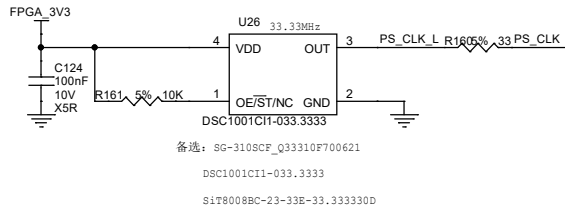
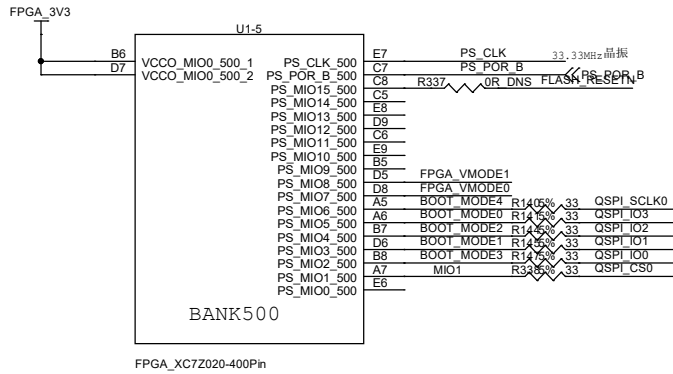


VSS Interface 7 of 8

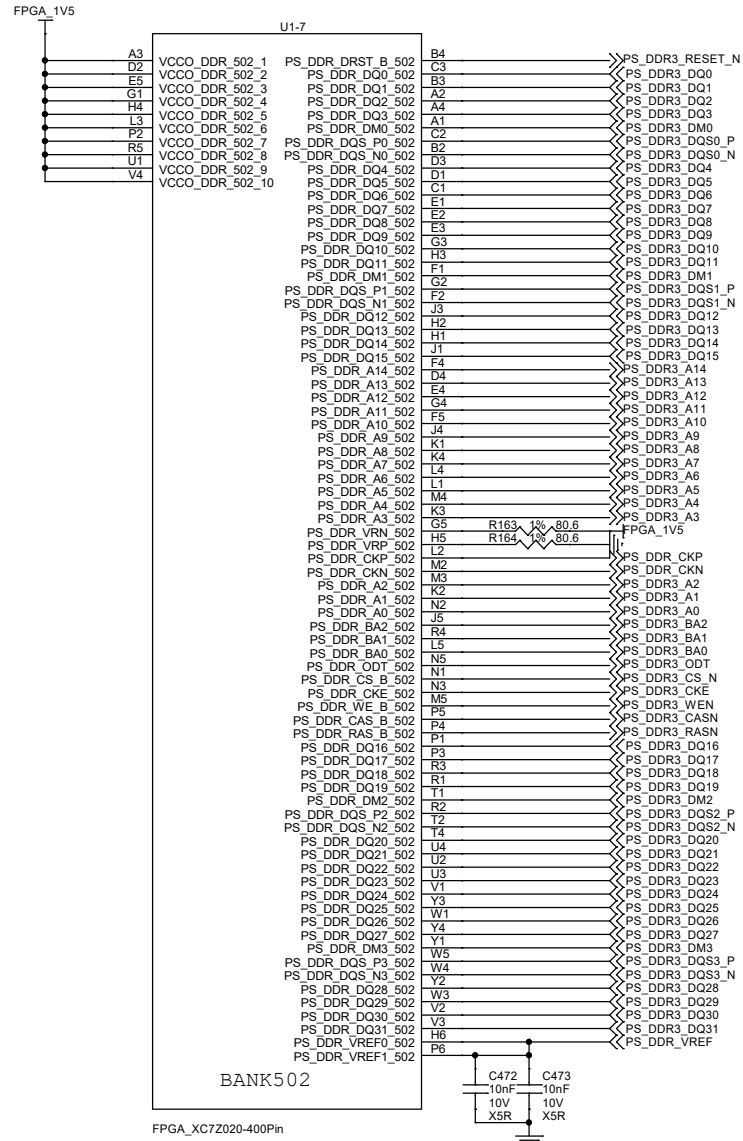


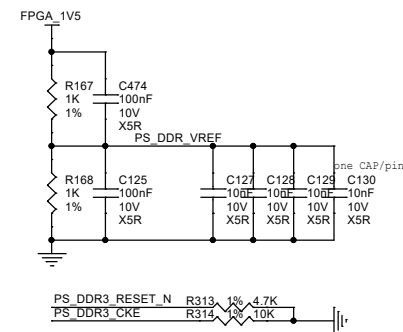
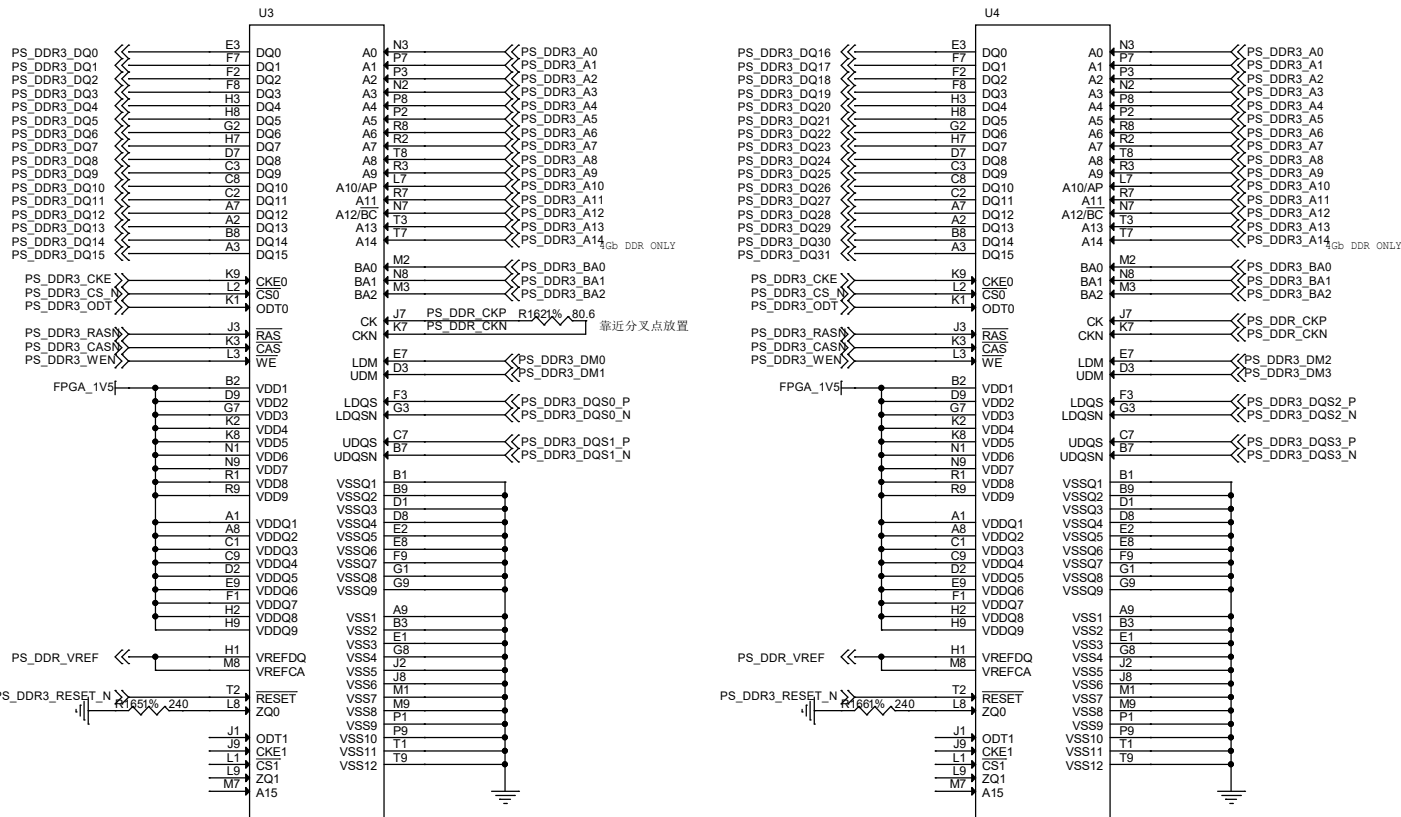
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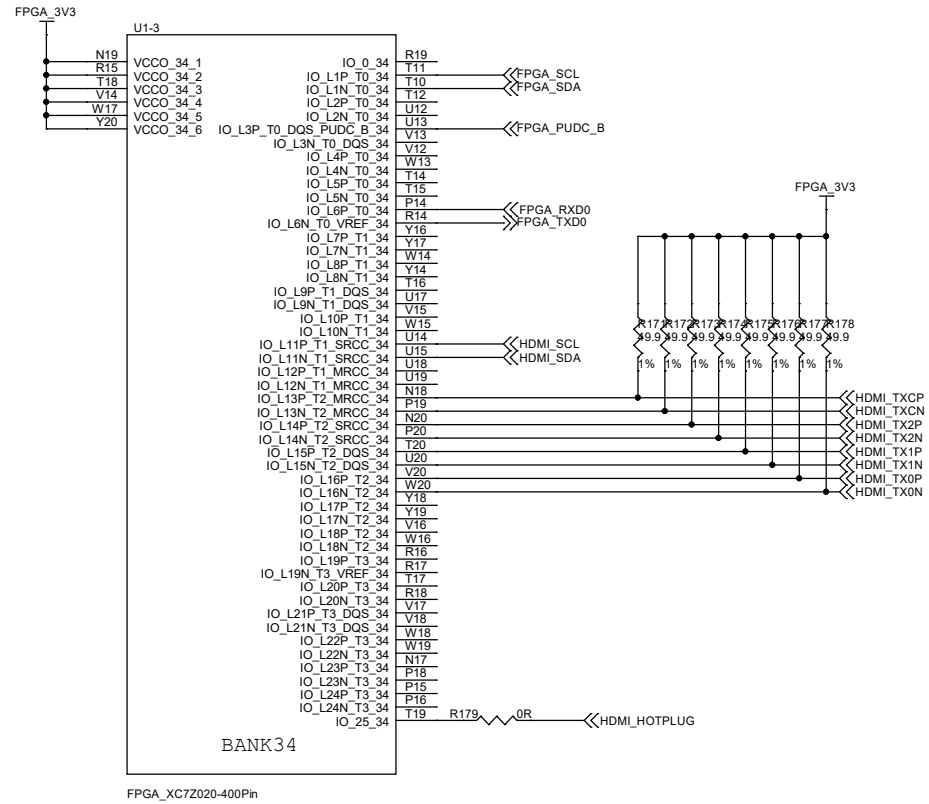


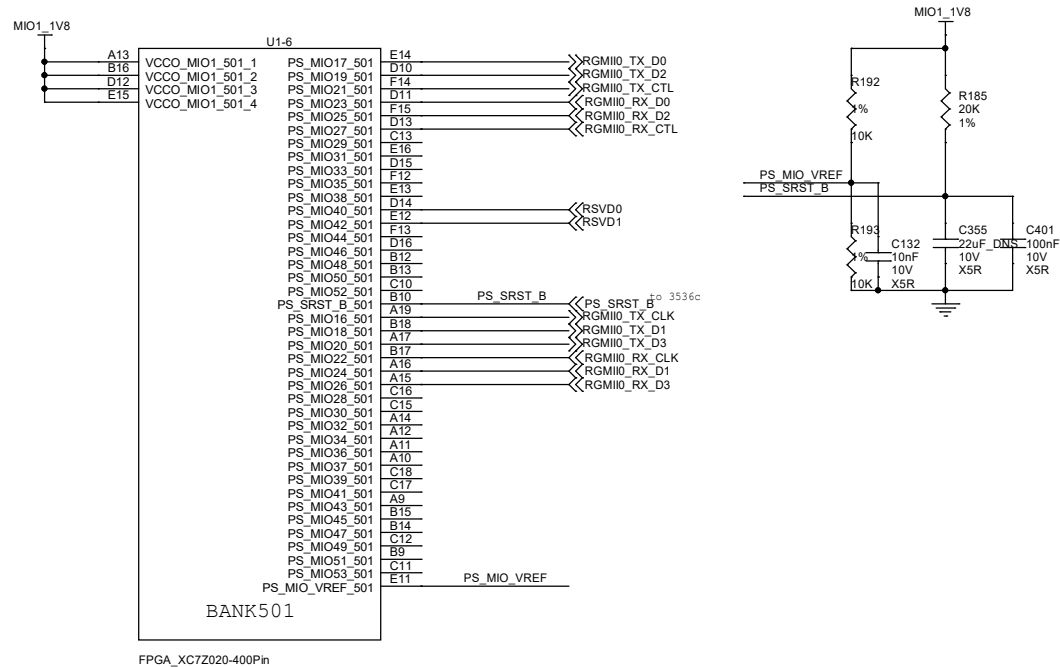


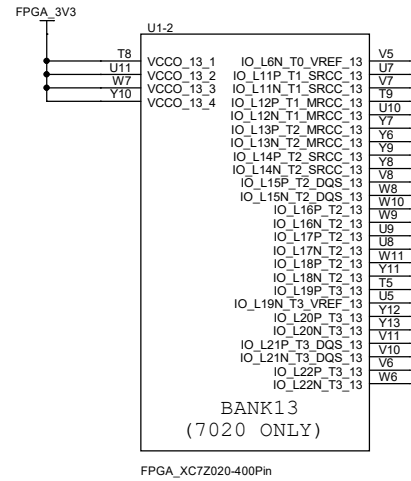
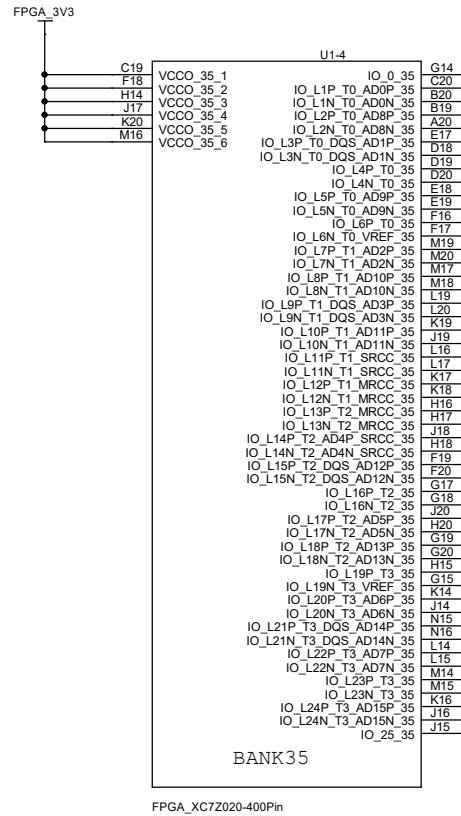
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	Bank 1	Bank 0	FLL Enabled 0
			FLL Bypassed 1
2.5V, 3.3V	0	0	BOOT_MODE3
1.8V	1	1	Cascade mode 0
			Independent mode 1
	BOOT_MODE2	BOOT_MODE1	BOOT_MODE0
ITAG Boot Mode	0	0	0
NOR Boot	0	1	0
NAND	1	0	0
QUAD-SPI	0	0	1
SD Card	1	0	1

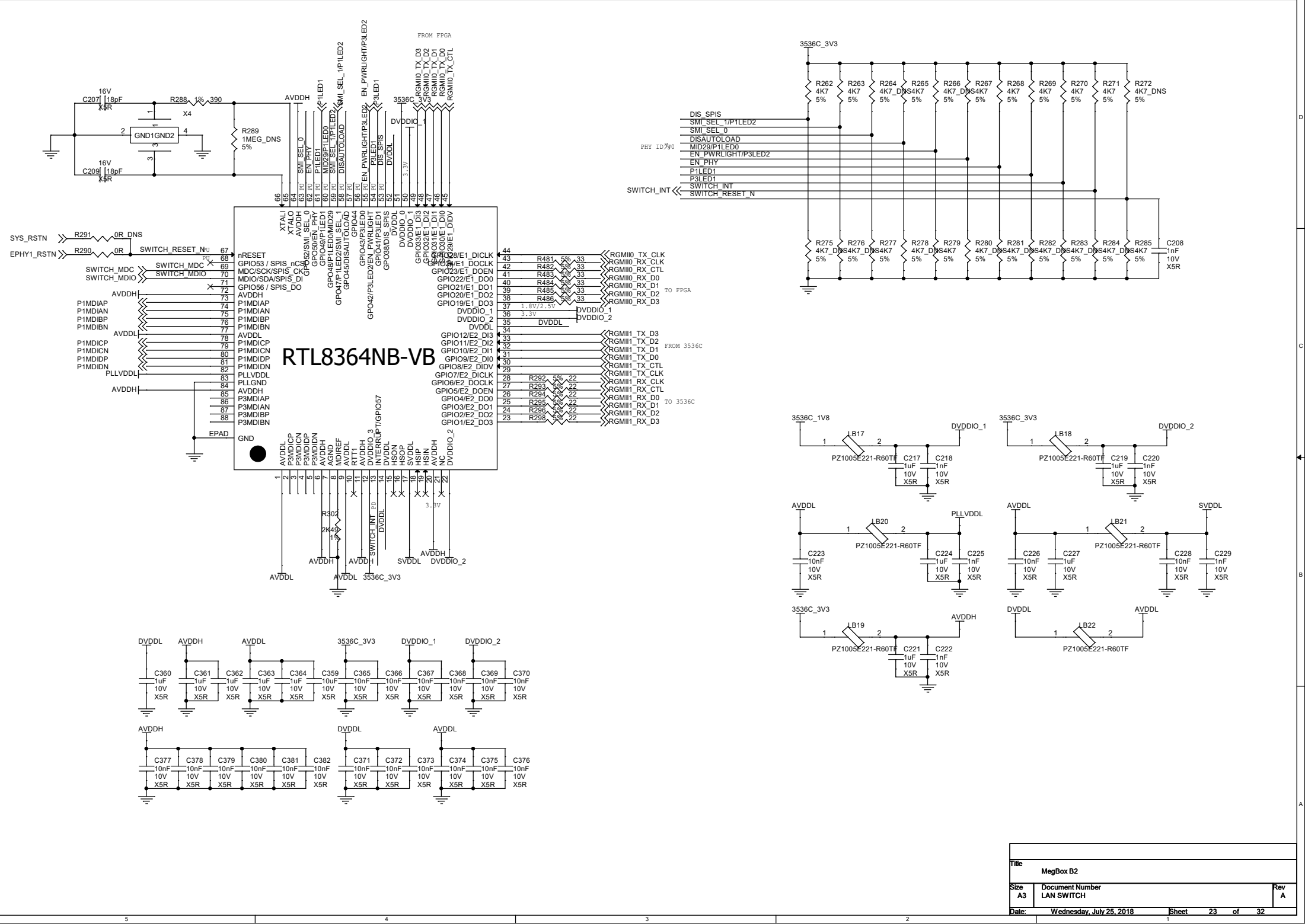


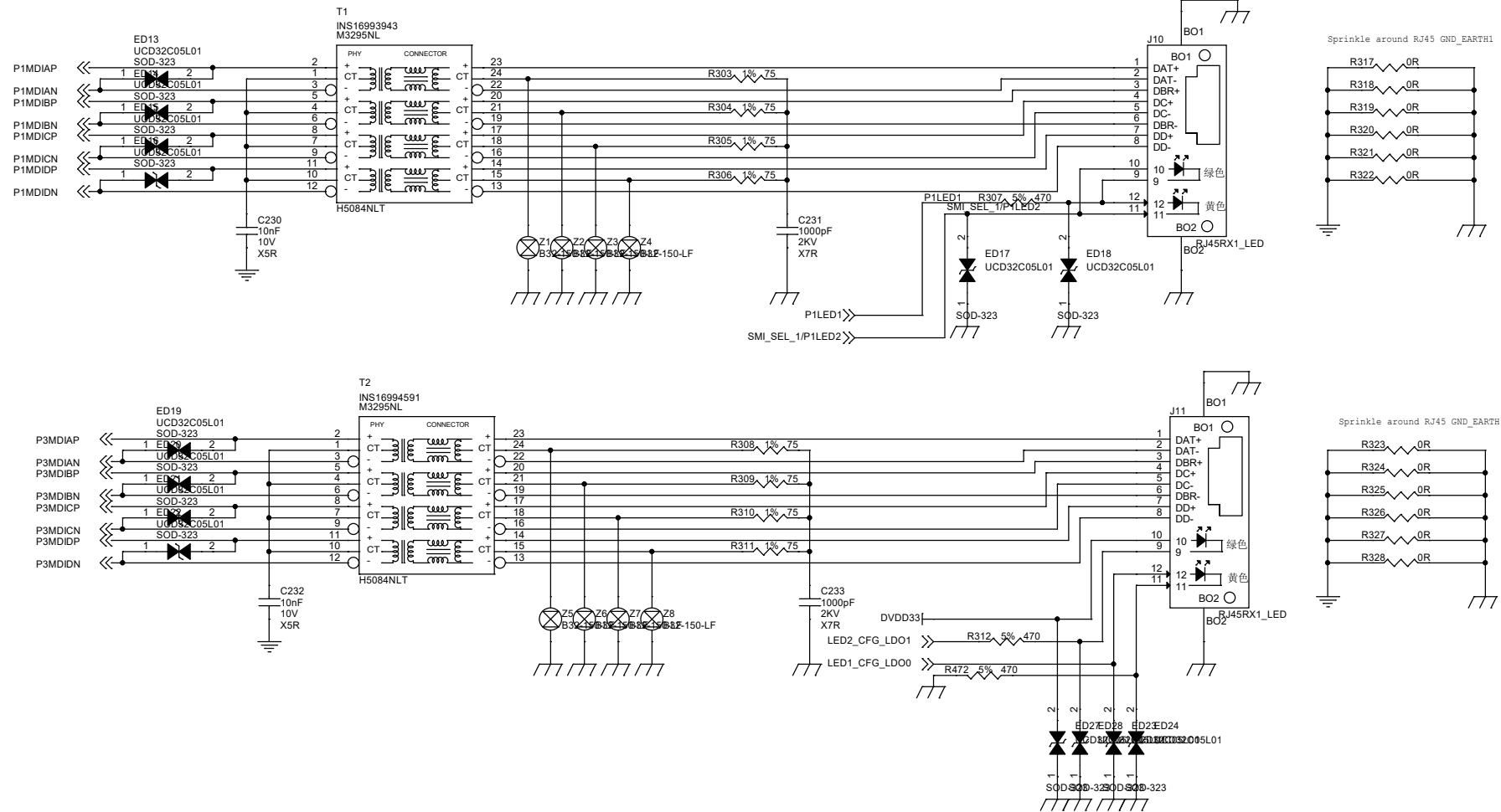


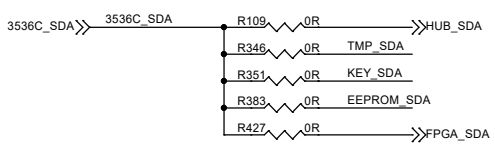
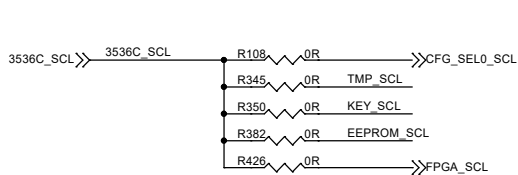




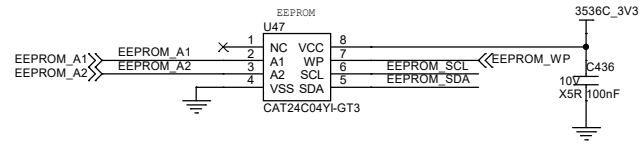
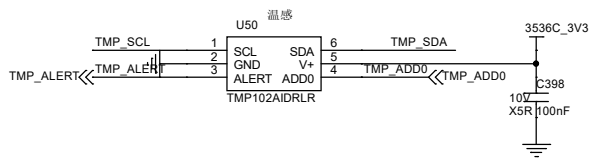
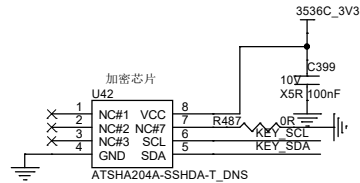


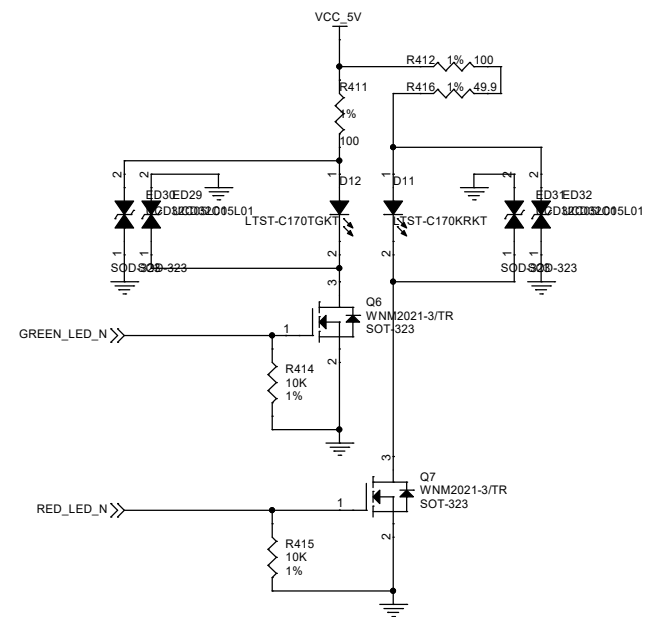
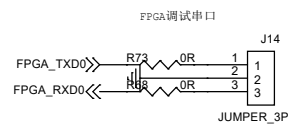
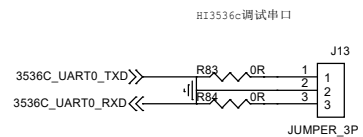
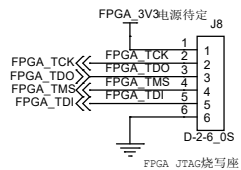






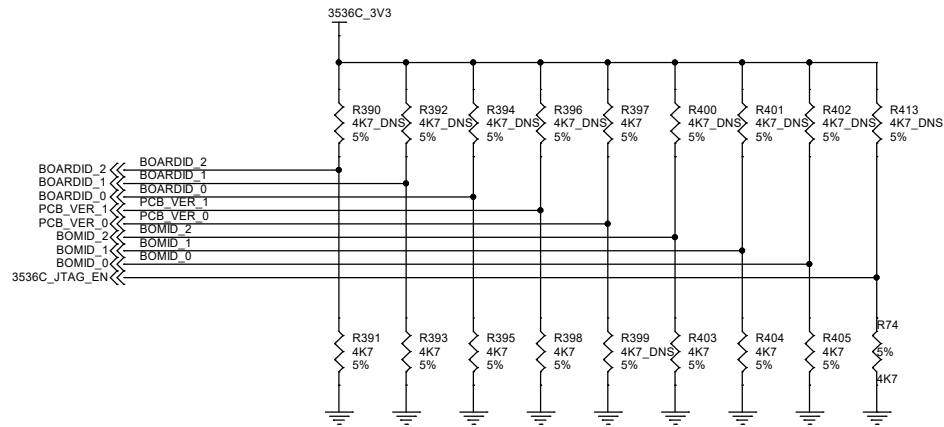
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温感	1001001b
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EEPROM	1010010b
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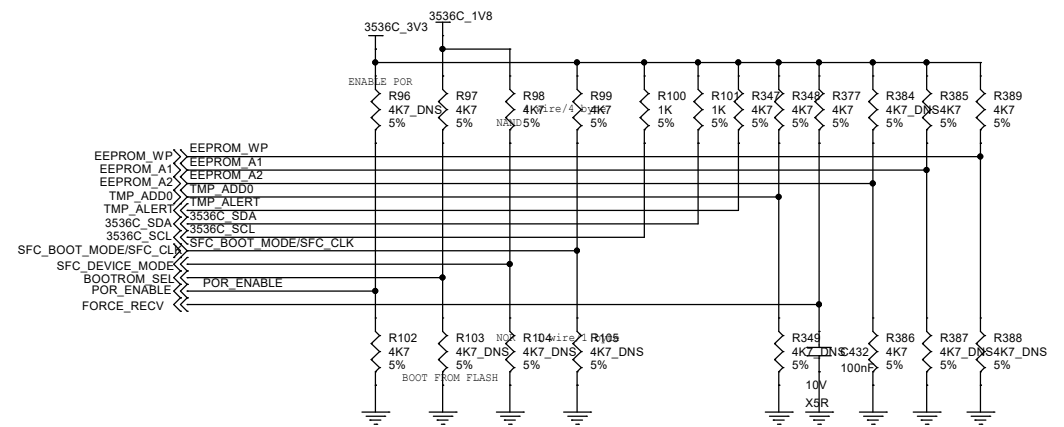


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BOARD STRAP PINS

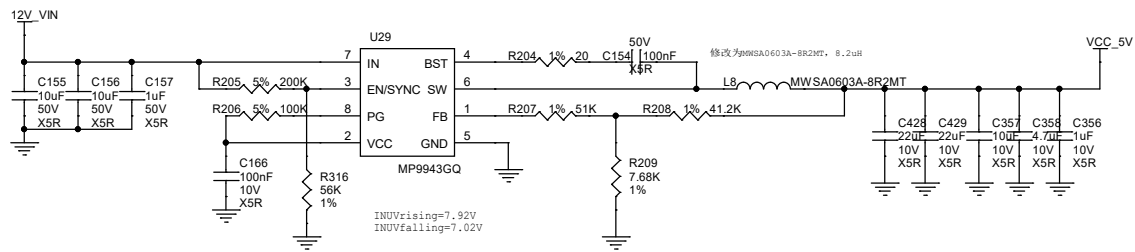
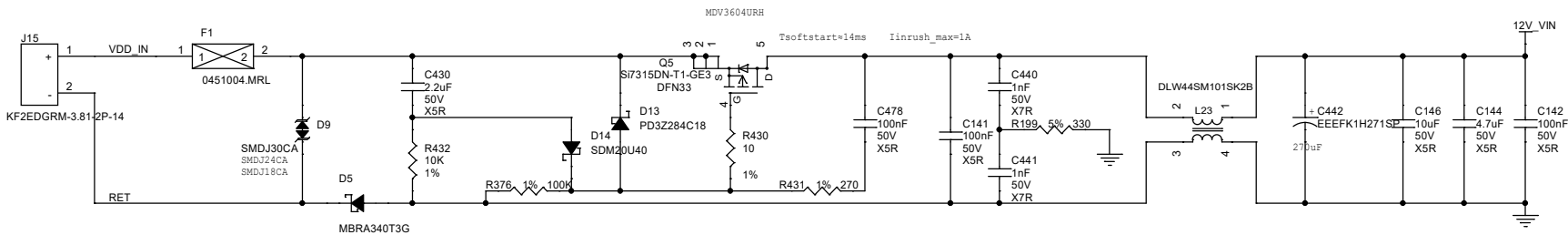


HI3536C STRAP PINS



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POWER INPUT RANGE: 8V~30V

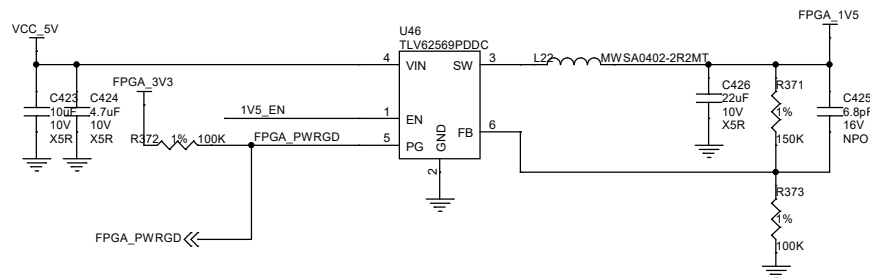
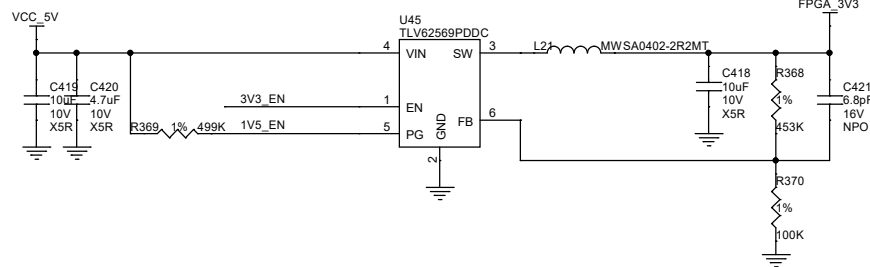
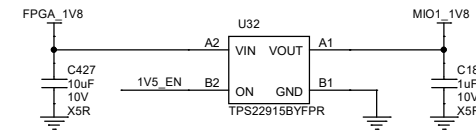
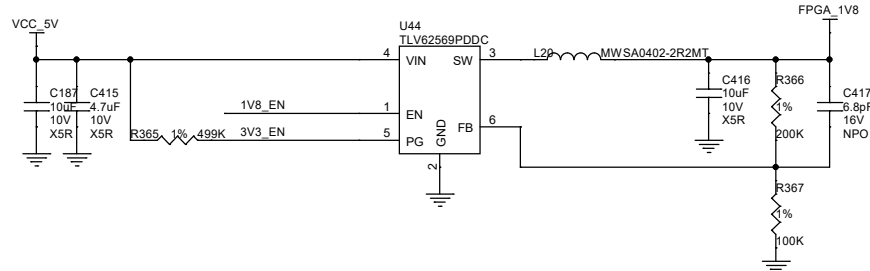
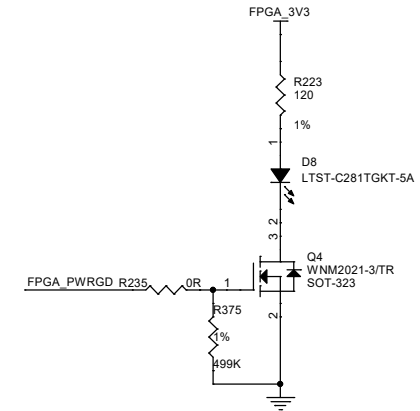
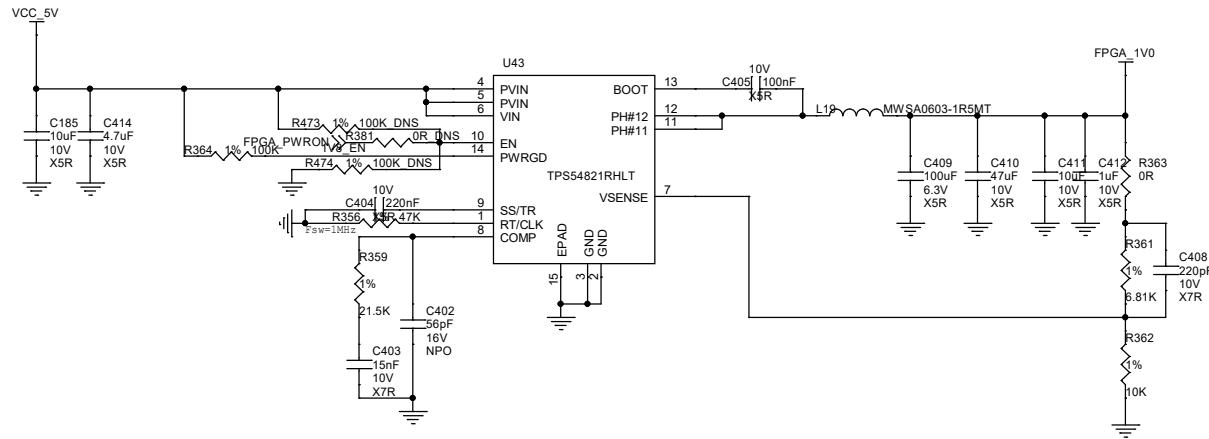


Hi3536C上电时序: 12V-->VCC_5V-->3536C_3V3/3536C_1V8-->DVDDL-->3536C_1V5-->DVDD_CORE/DVDD_CPU
Hi3536C下电时序: 12V-->VCC_5V-->3536C_3V3/3536C_1V8-->3536C_1V5/DVDD_CORE/DVDD_CPU

FPGA上电时序: FPGA_1V0-->FPGA_1V8-->FPGA_3V3-->FPGA_1V5/MIO1_1V8-->PS_POR_B
FPGA下电时序: 12V-->PS_POR_B-->.....

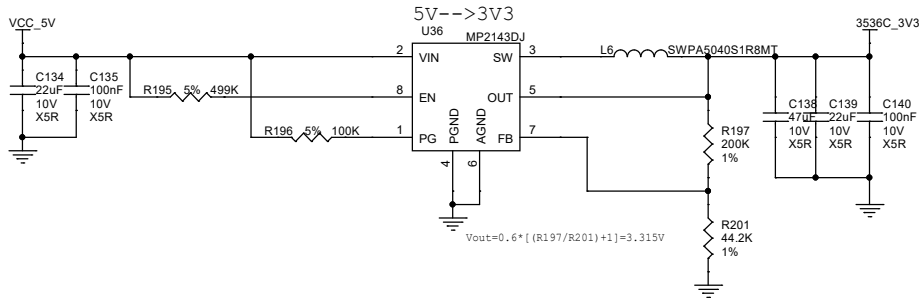
Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY INPUT	A
Date:	Friday, July 27, 2018	Sheet 28 of 32

FPGA POWER RAILS

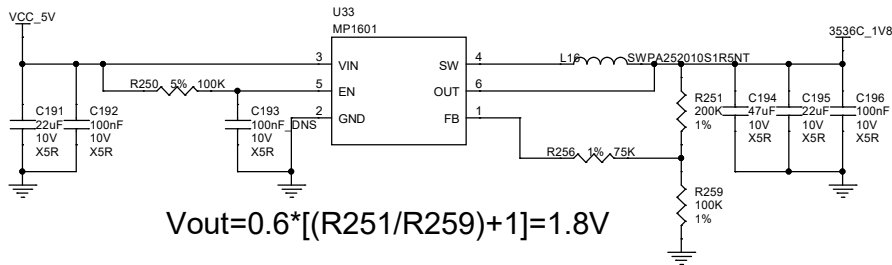


Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY: FPGA	A
Date:	Wednesday, July 25, 2018	Sheet 29 of 32

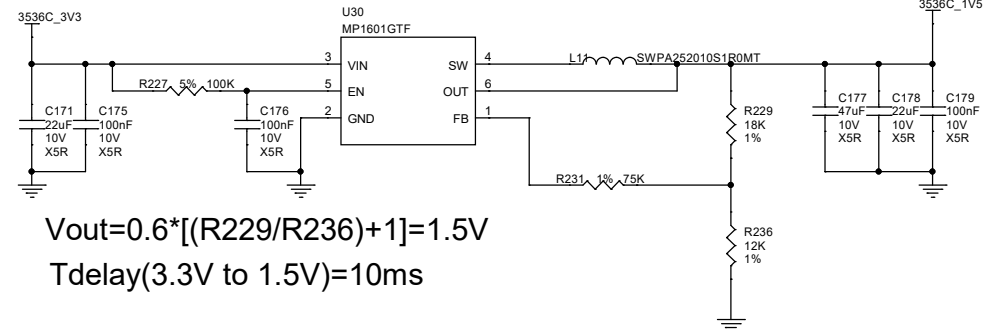
HI3536c POWER RAILS



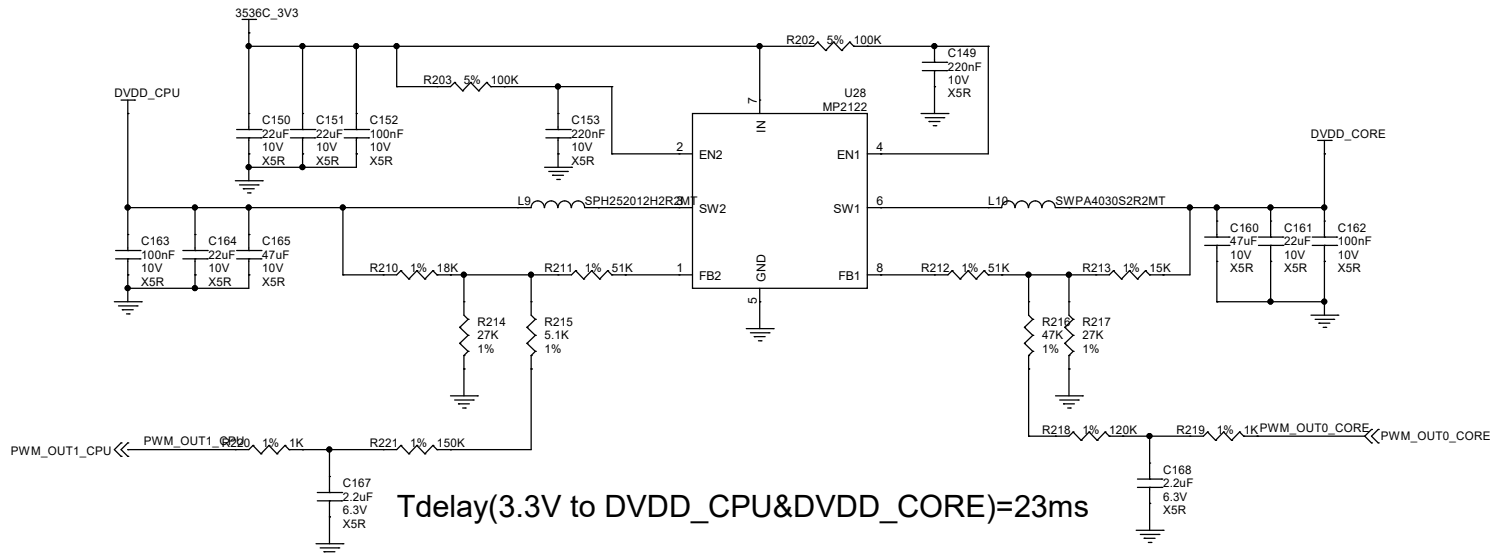
5V --> 1V8



3V3 --> 1V5

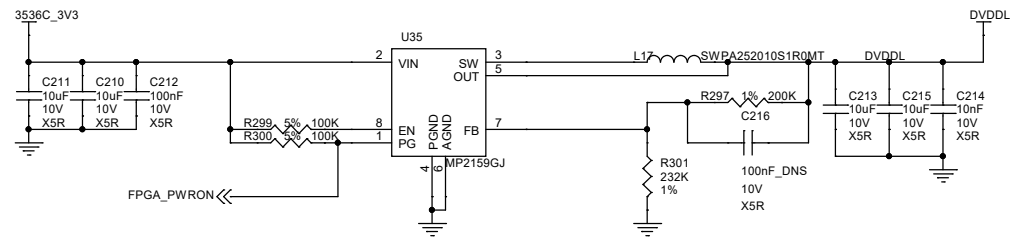
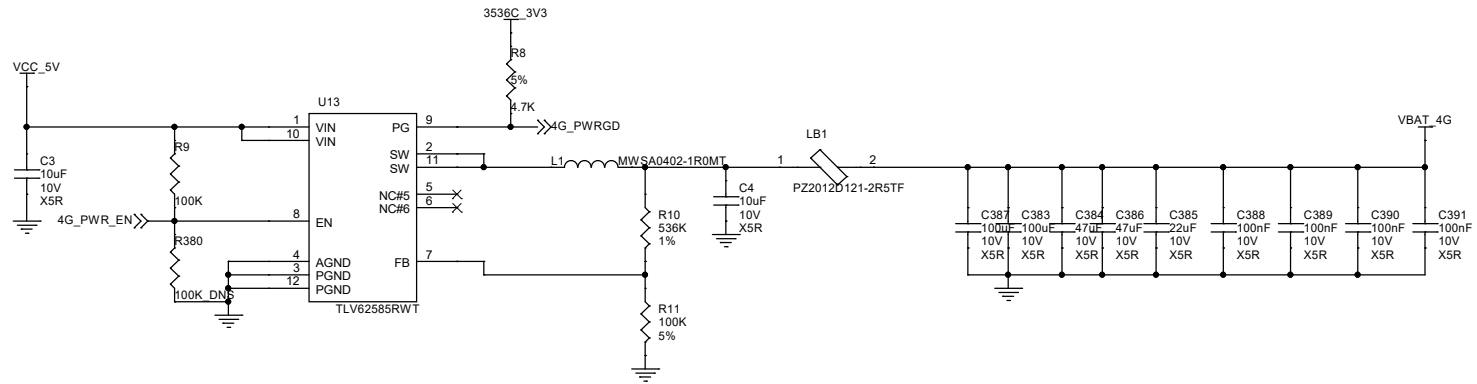


3V3 --> Hi3536C_Core/CPU



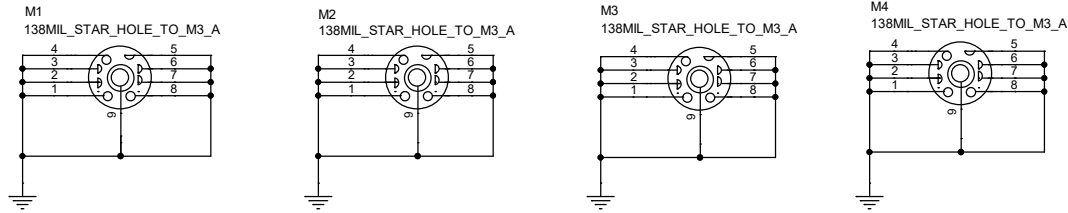
Title		
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Size	Document Number	Rev
A3	POWER SUPPLY: 3536C	A
Date:	Wednesday, July 25, 2018	Sheet 30 of 32

4G&SWITCH POWER RAILS

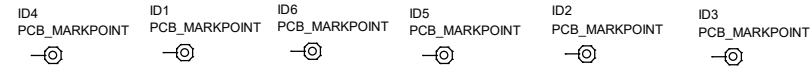


Title		
MegBox B2		
Size	Document Number	Rev
A3	POWER SUPPLY, etc.	A
Date:	Wednesday, July 25, 2018	Sheet 31 of 32

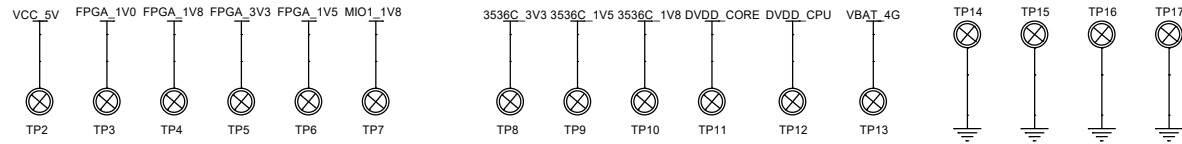
MECHANICAL



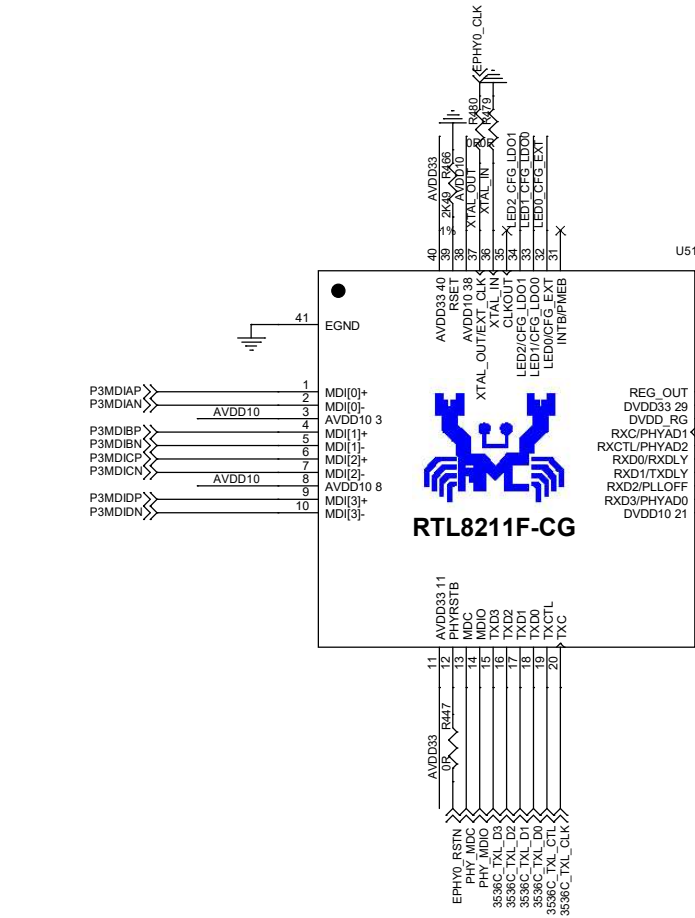
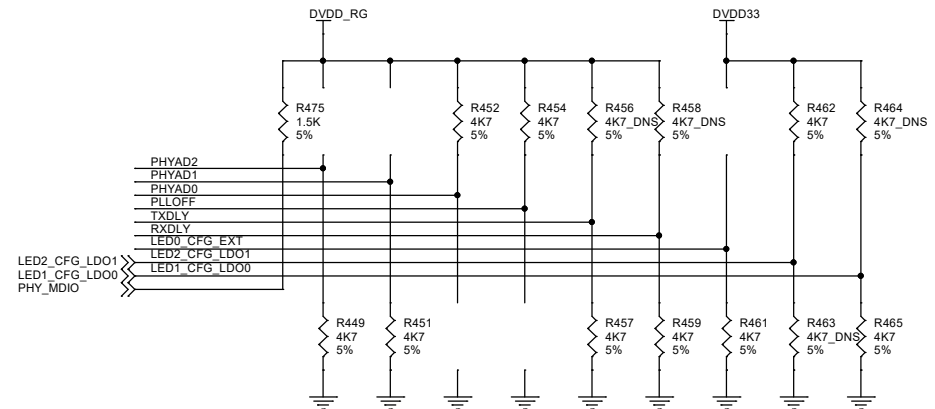
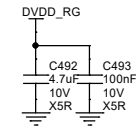
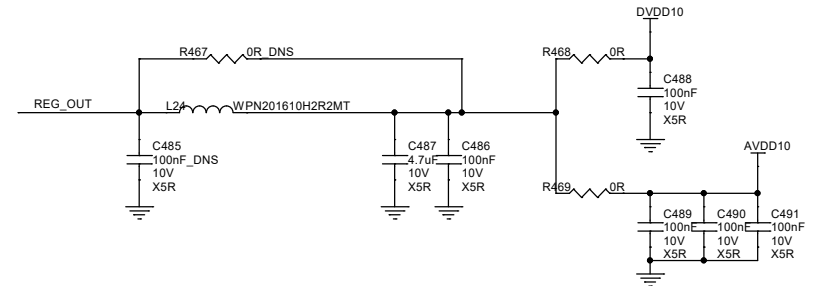
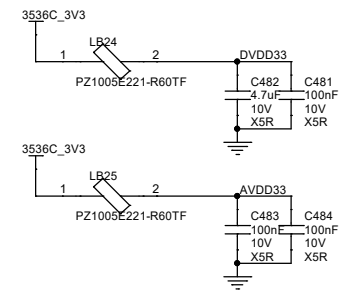
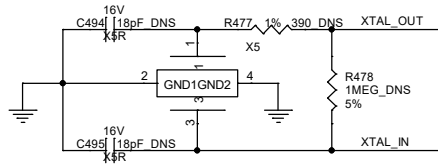
MARK POINTS



TEST POINTS

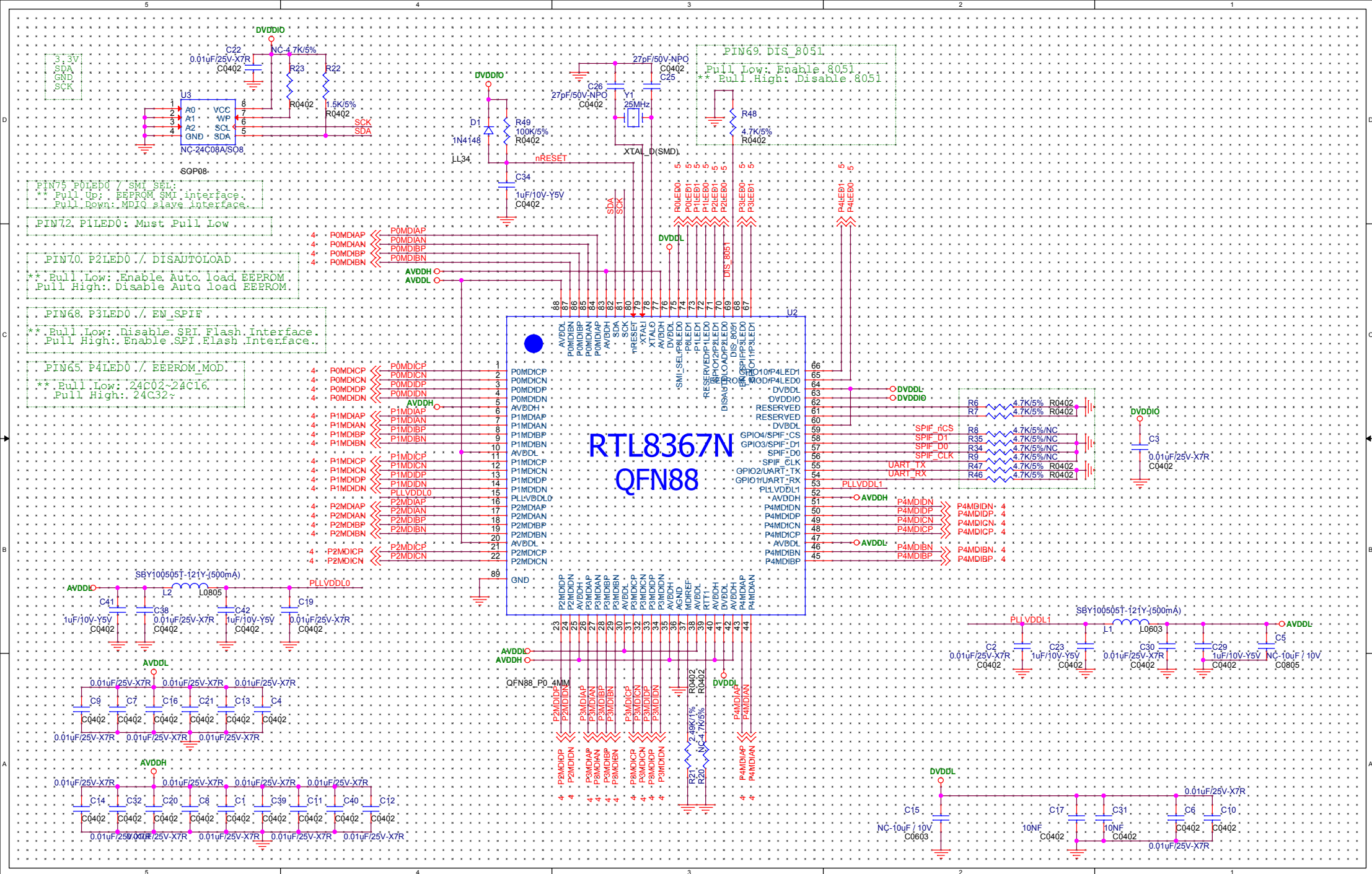


Title		
MegBox B2		
Size	Document Number	Rev
A3	MISC.	A
Date:	Wednesday, July 25, 2018	Sheet 32 of 32

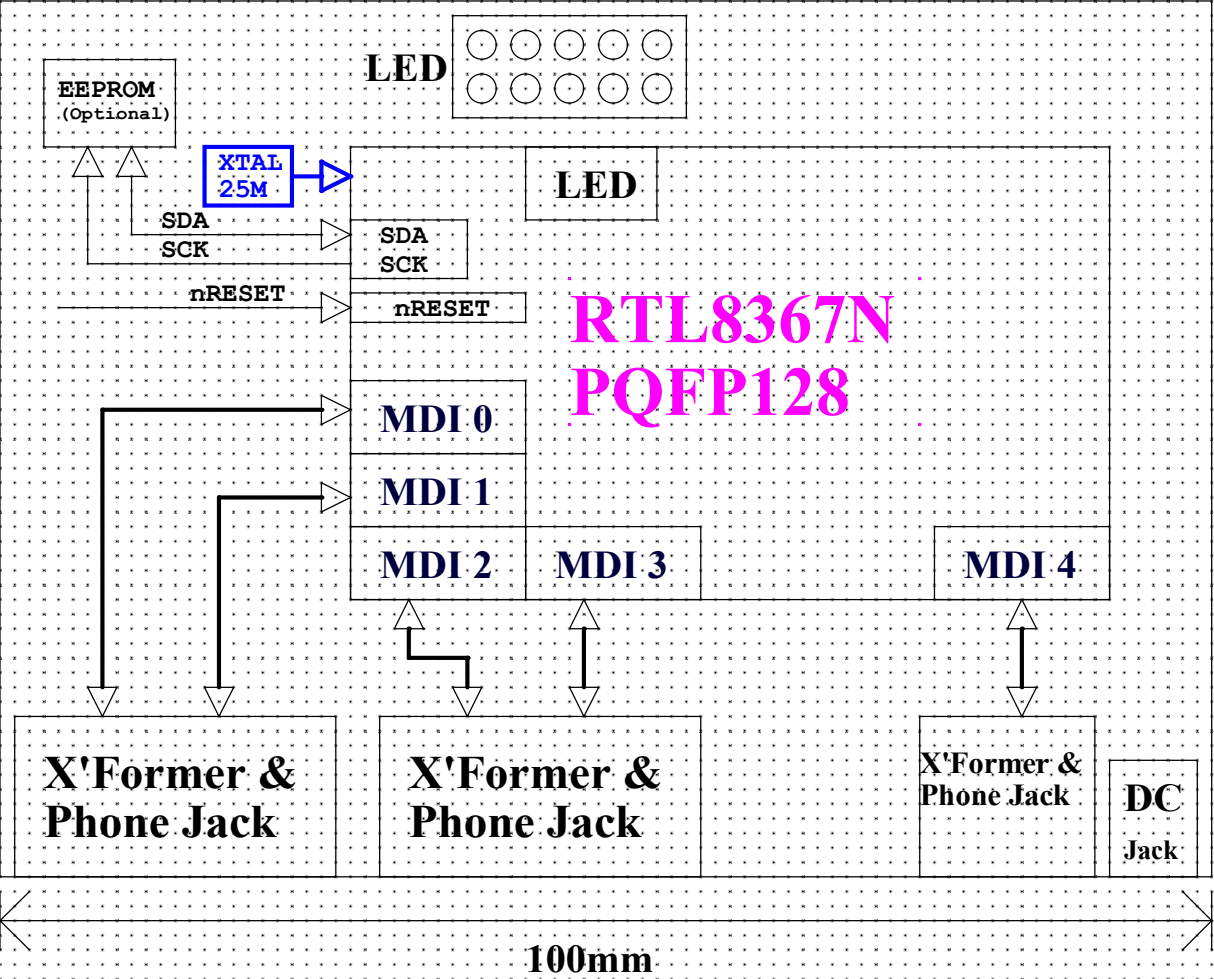


RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V	1'b1	2'b00
External 2.5V	1'b1	2'b01
External 1.8V	1'b1	2'b10
External 1.5V	1'b1	2'b11
Internal 2.5V	1'b0	2'b01
Internal 1.8V (default)	1'b0	2'b10
Internal 1.5V	1'b0	2'b11

PHY Address	PHYAD[2:0]
0	3'b000
1 (default)	3'b001
2	3'b010
3	3'b011
4	3'b100
5	3'b101
6	3'b110
7	3'b111



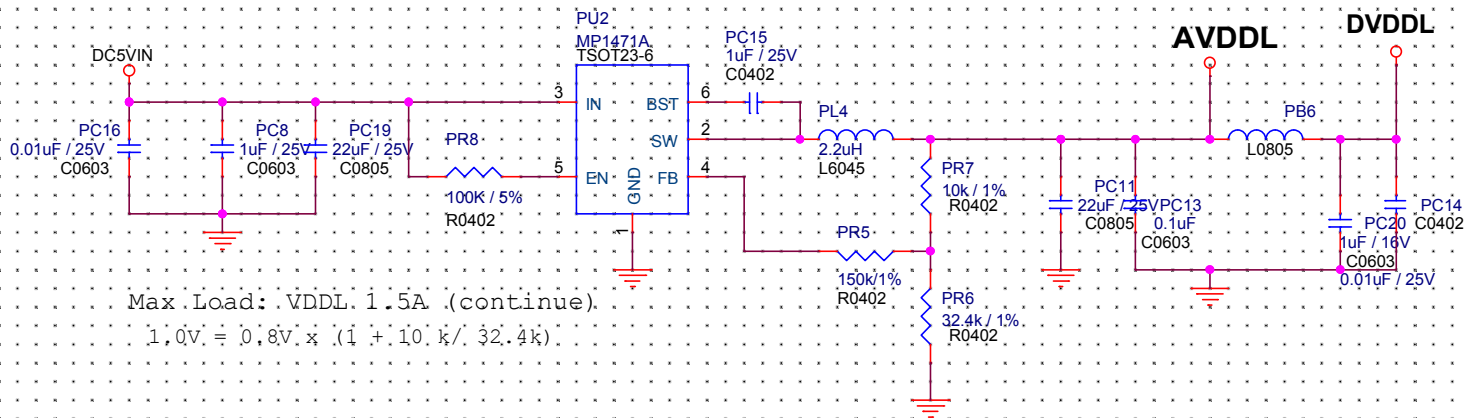
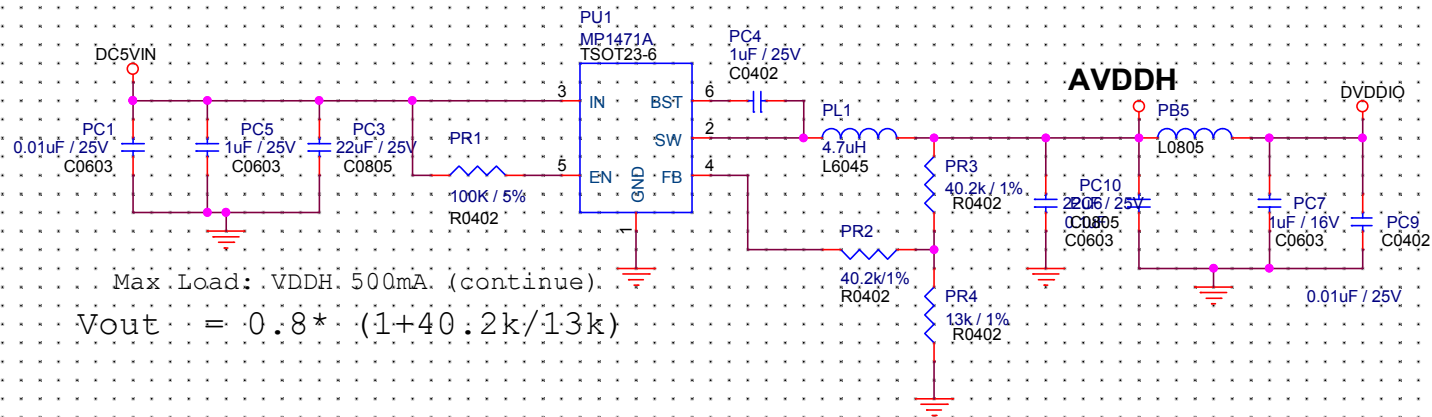
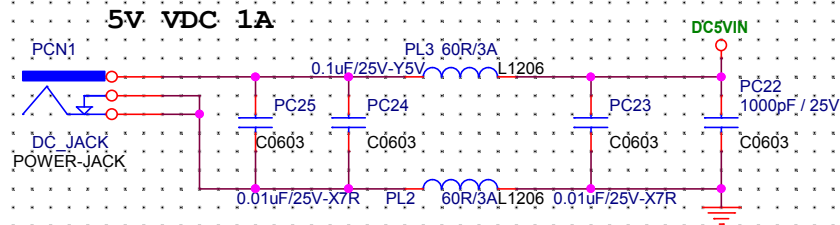
RTL8367 (PQFP128) Demo-Board Block Diagram



REVISION HISTORY

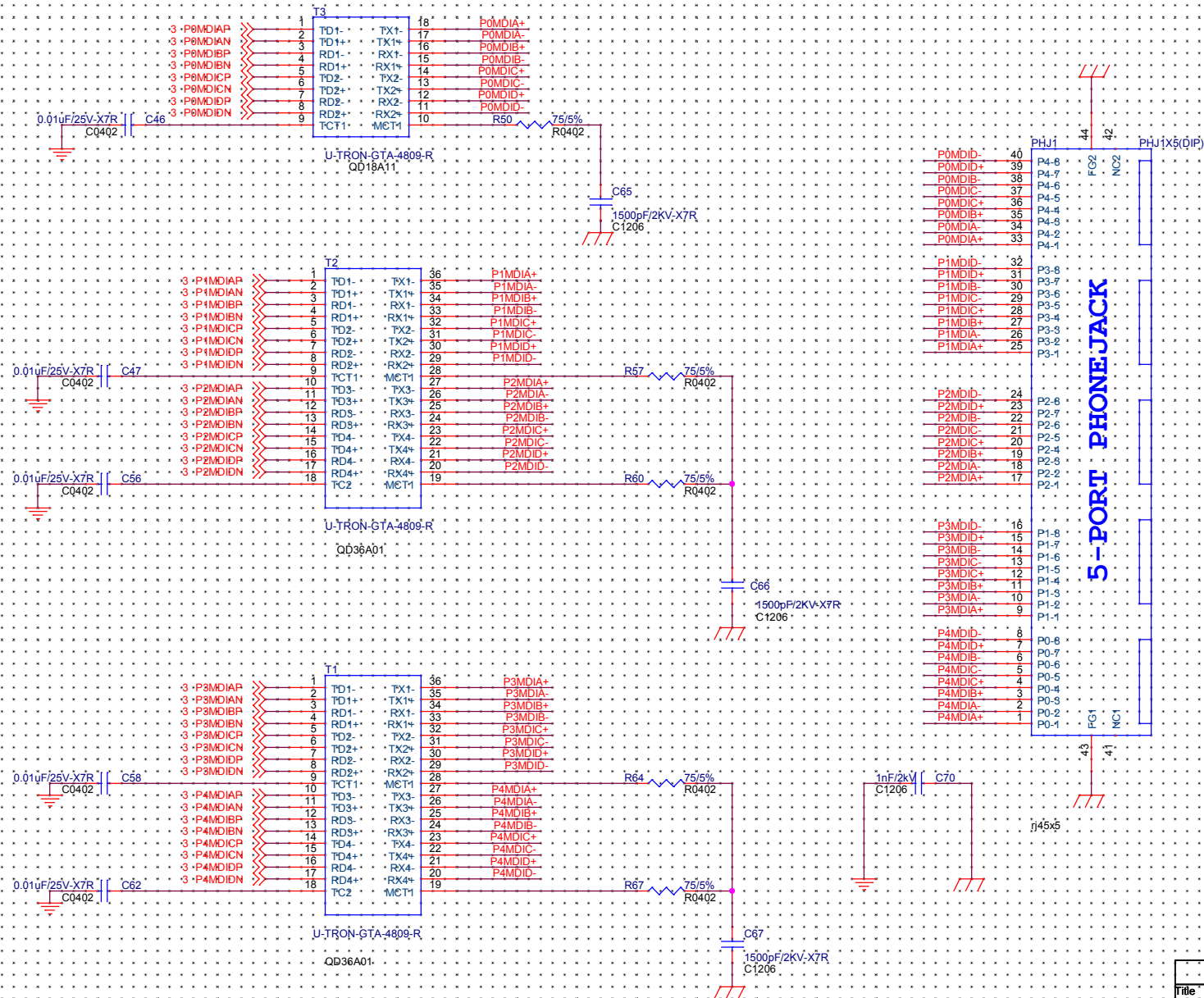
Revision	Release Date	Summary
v.3.0	2011/05/2	First release

Title		
RTL8367 Demo-Board Block Diagram		
Size	Document Number	Rev
B	<Doc>	v.3.0
Date:	Tuesday, March 28, 2017	Sheet 1 of 5



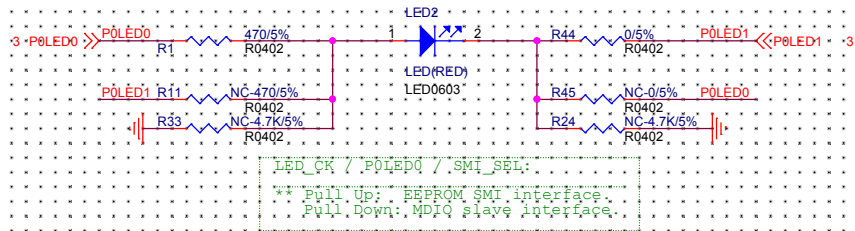
Title		RTL8367 Power Regulator	
Size	Document Number	Rev	
Custom	<Doc>	v.3.0	
Date:	Tuesday, March 28, 2017	Sheet	2 of 5

X'Formre Central Tap at Primary
Side must tie with each other.

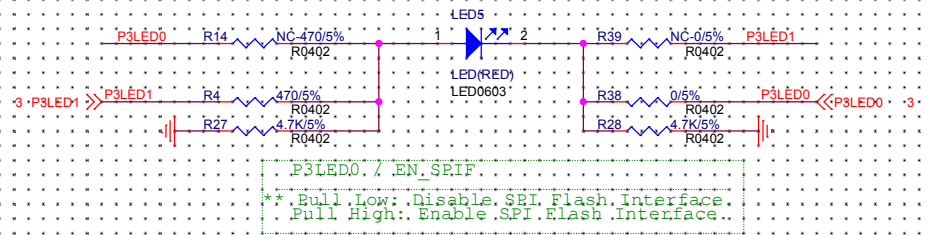


Title				
RTL8367 Transformier & Phonejack				
Size B	Document Number <Doc>			Rev v.3.0
Date:	Tuesday, March 28, 2017	Sheet	4	of 5

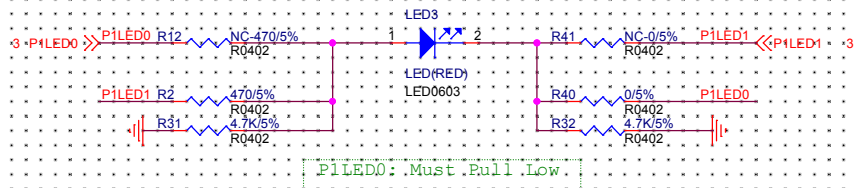
Port 0 LED



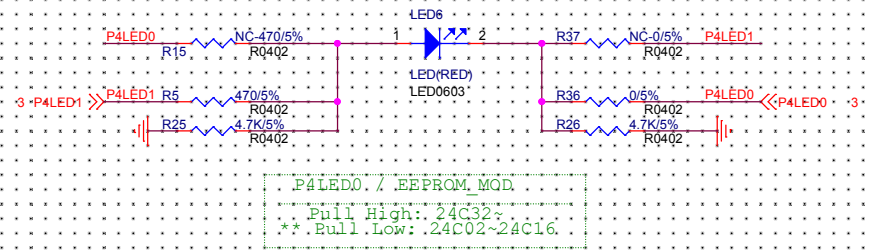
Port 3 LED



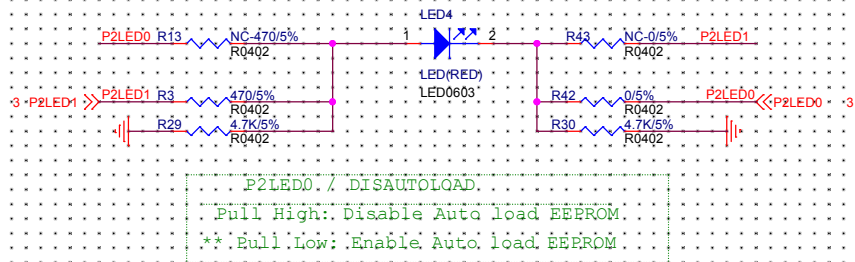
Port 1 LED



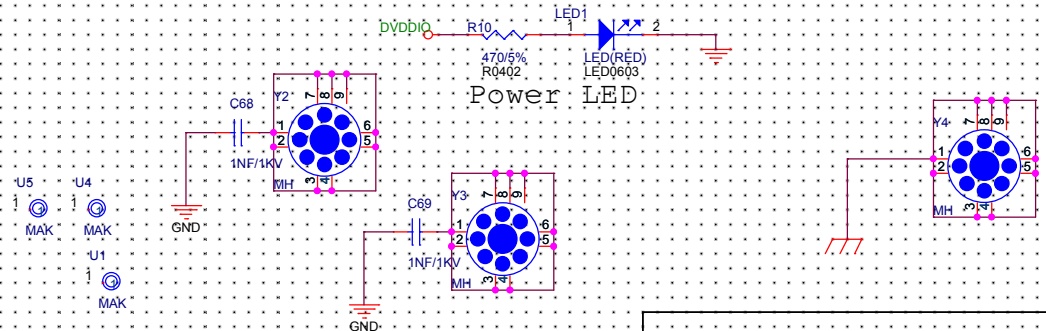
Port 4 LED



Port 2 LED



Power LED



Title: RTL8367 LED		
Size: B	Document Number: <Doc>	Rev: v3.0
Date: Tuesday, March 28, 2017	Sheet: 5	of 5