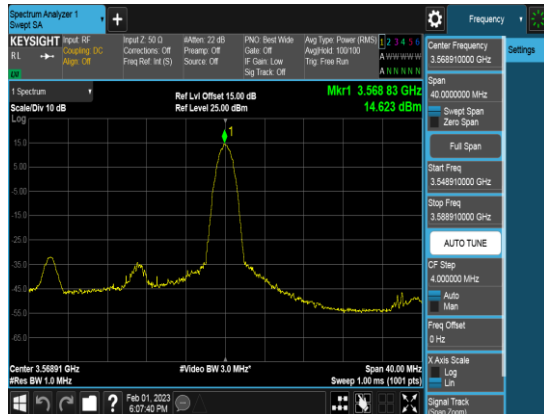
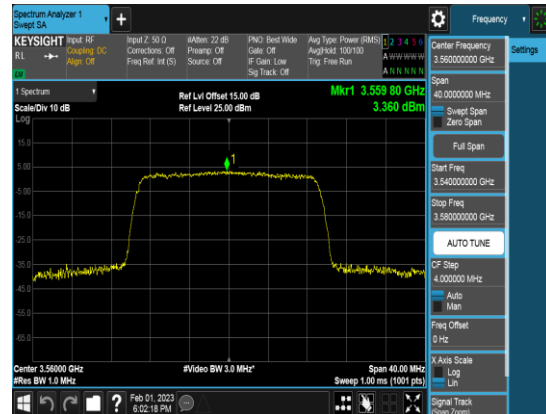


PCC 20MHz + SCC 10MHz

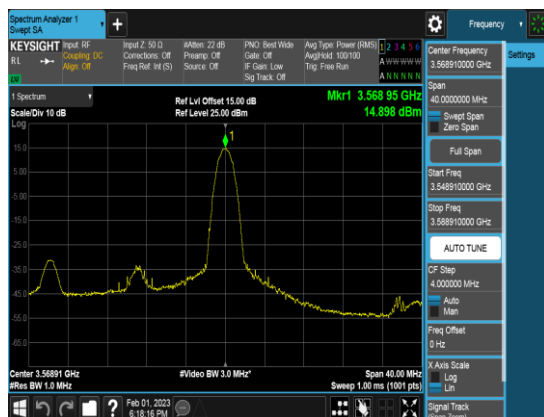
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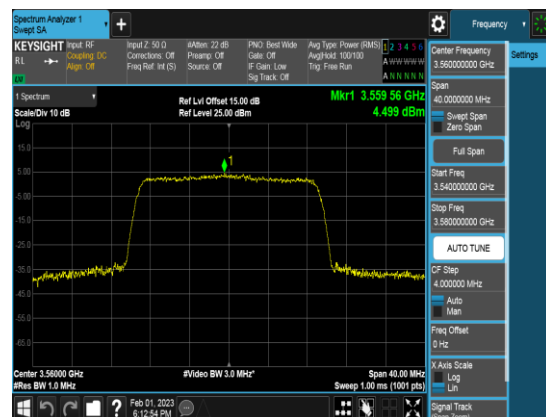
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PCC_20M_3560MHz_Q64_1RB0_PSD



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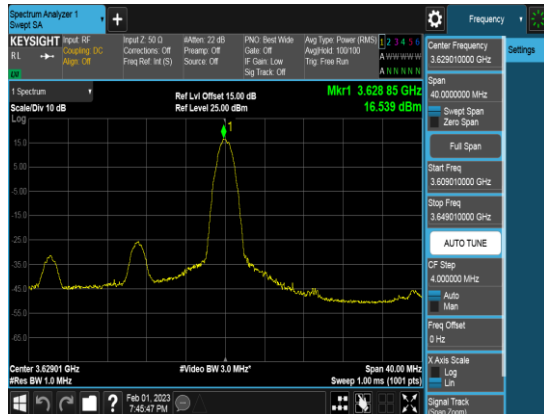


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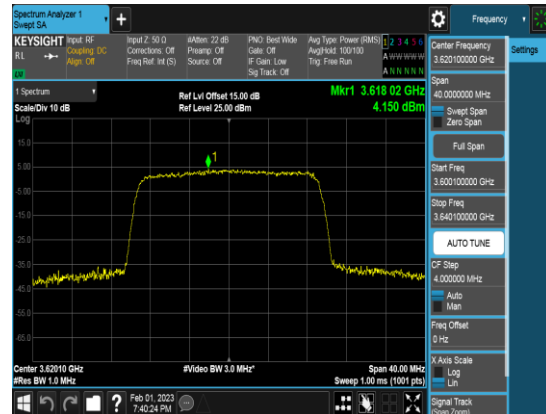
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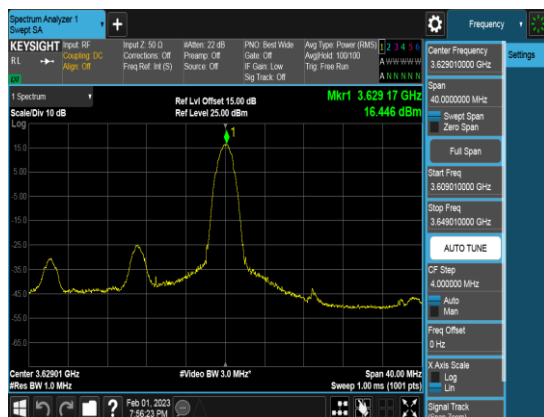
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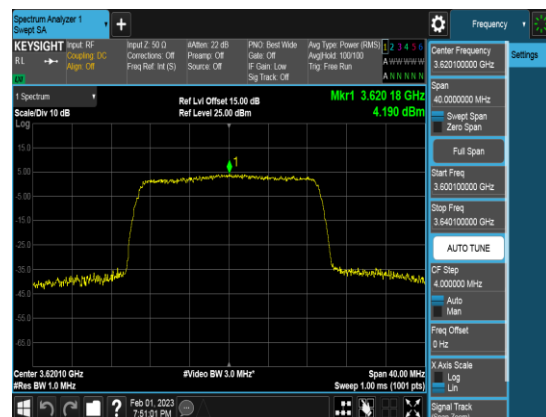
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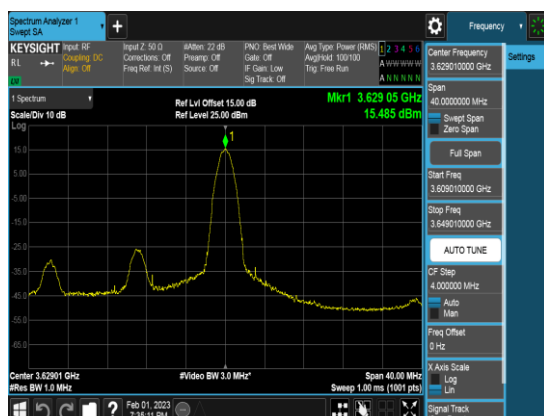
PCC_20M_3620.1MHz_Q64_1RB0_PSD



PCC_20M_3620.1MHz_Q64_FULLRB_PSD



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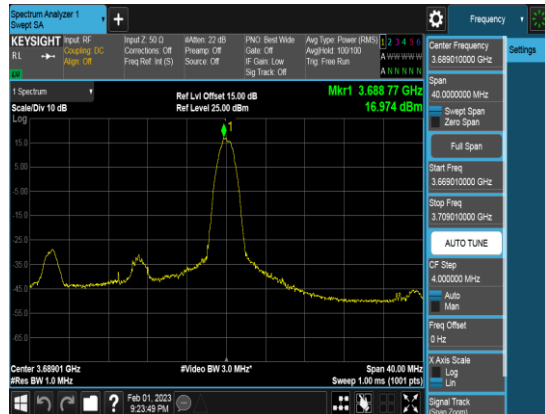


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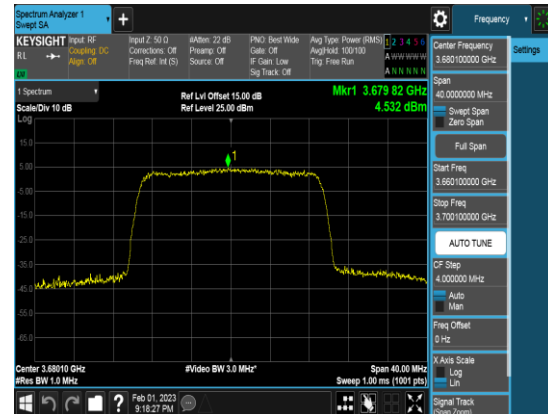
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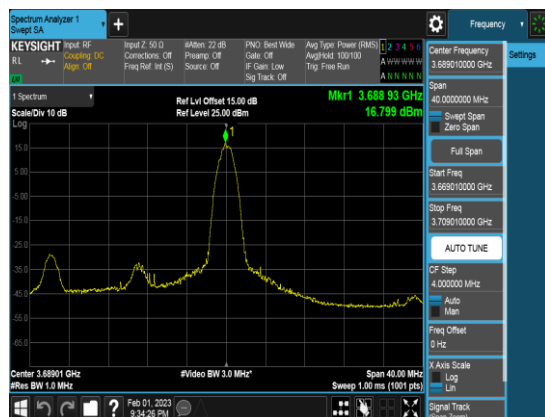
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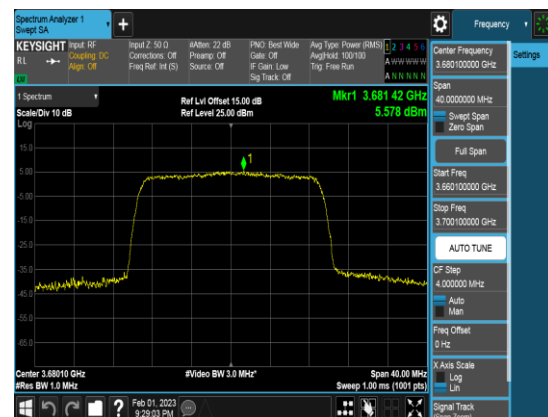
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PCC_20M_3680.1MHz_Q64_1RB0_PSD



PCC_20M_3680.1MHz_Q64_FULLRB_PSD



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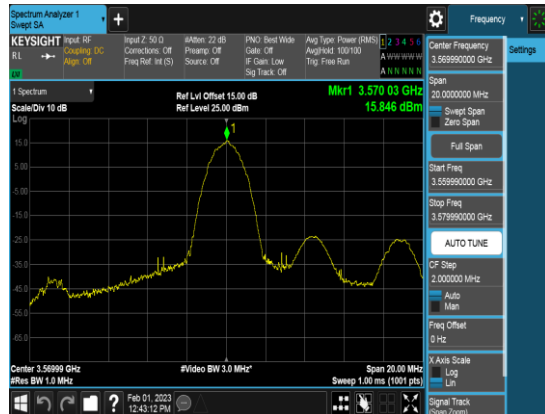


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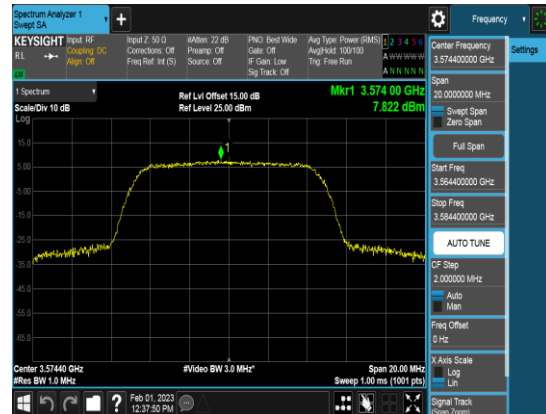
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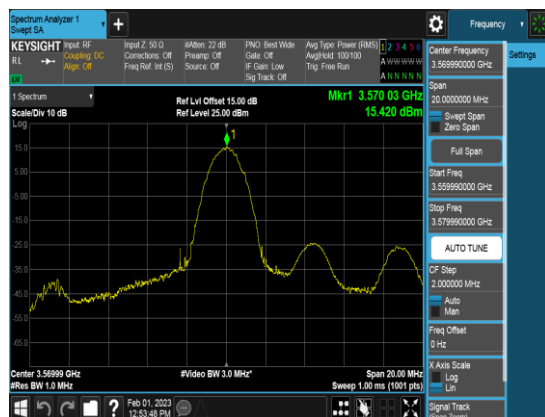
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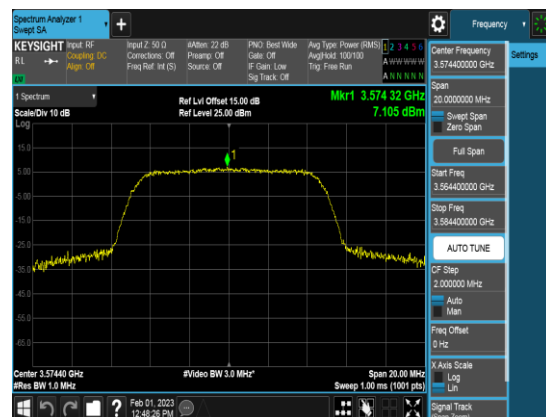
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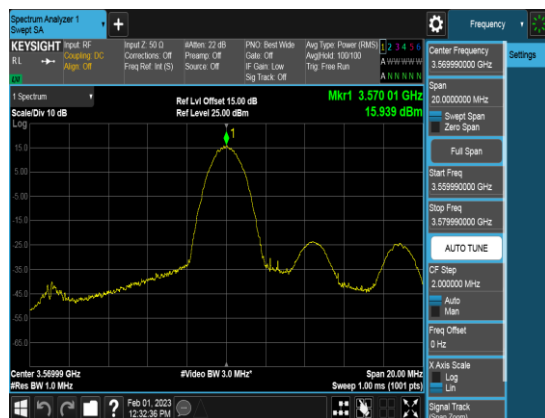
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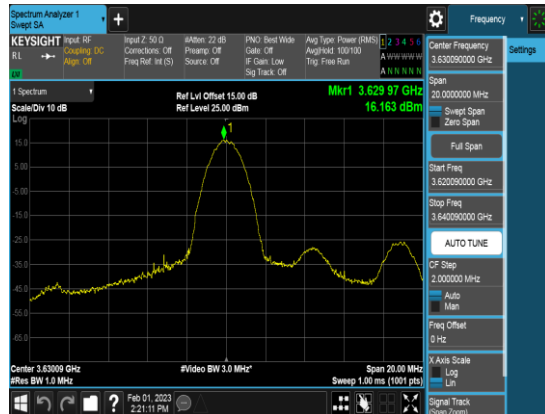


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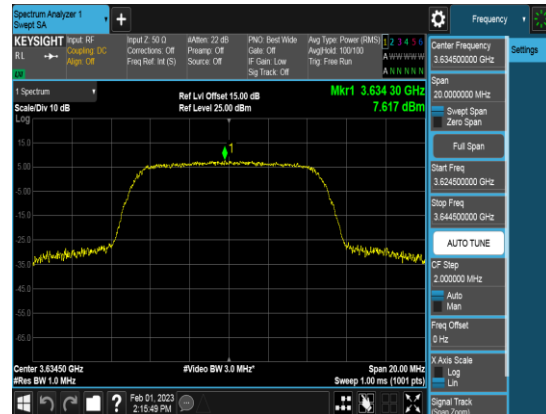
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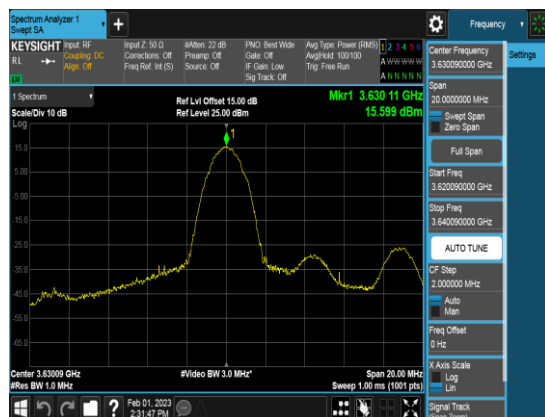
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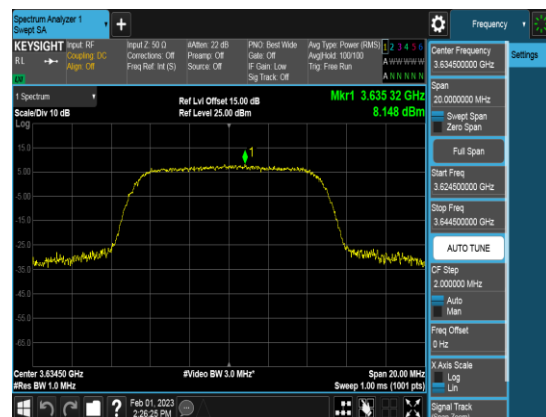
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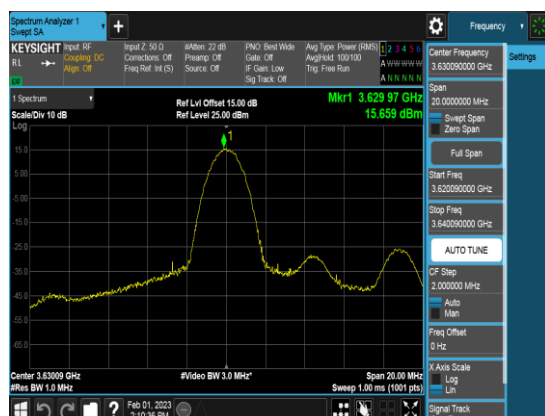
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SCC_10M_3634.5MHz_Q64_FULLRB_PSD



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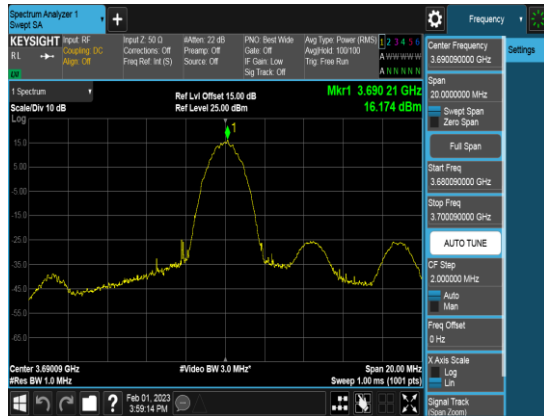


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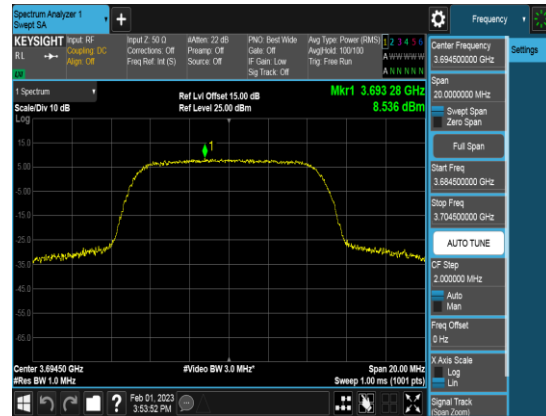
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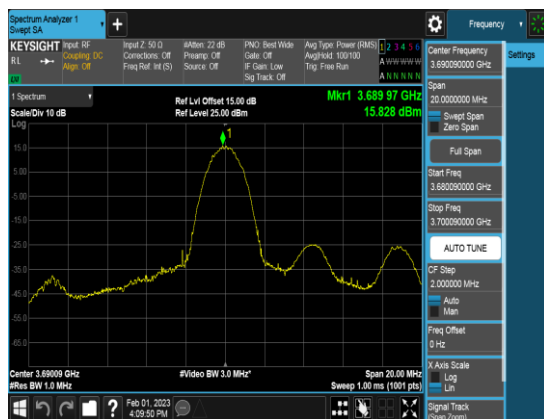
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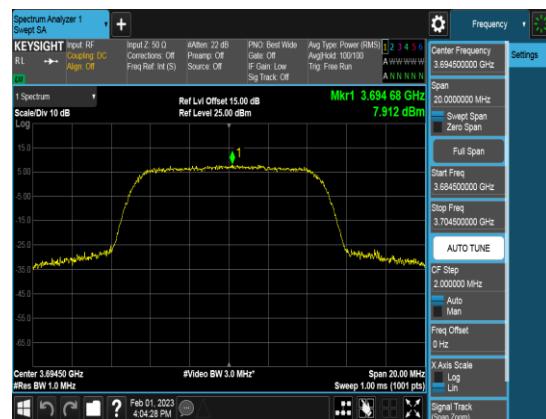
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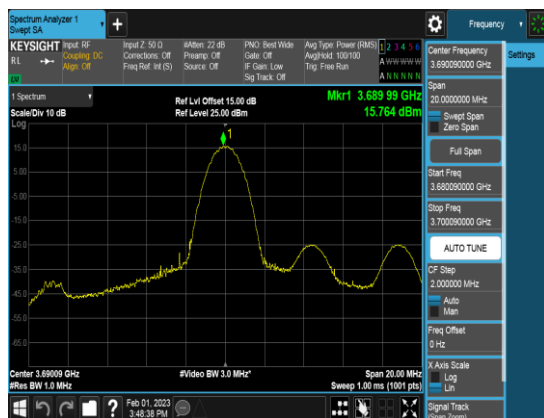
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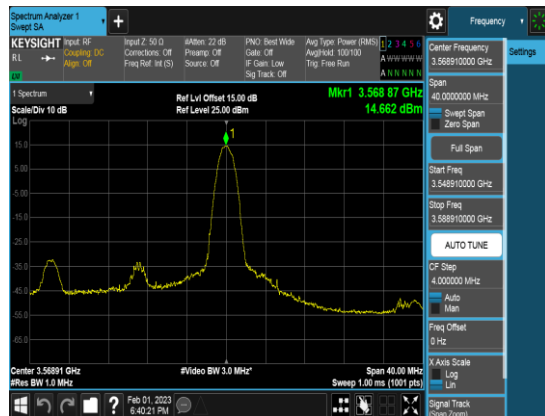
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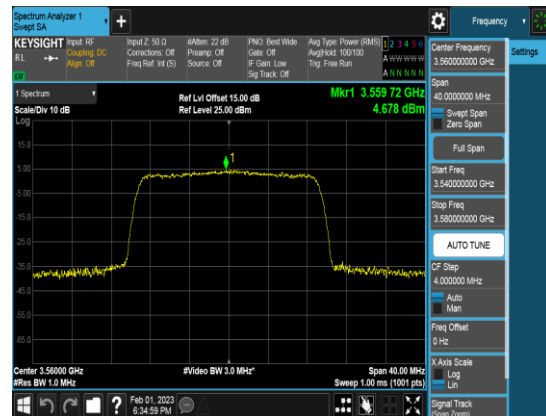
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PCC 20MHz + SCC 20MHz

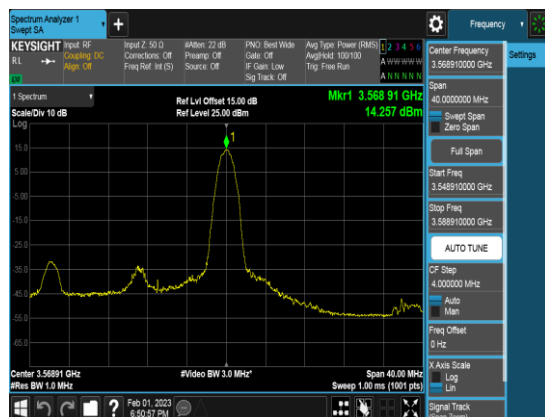
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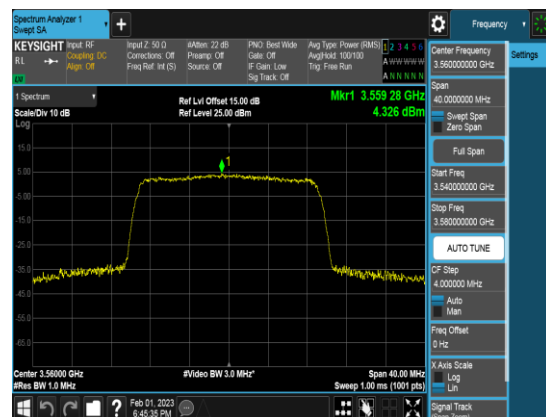
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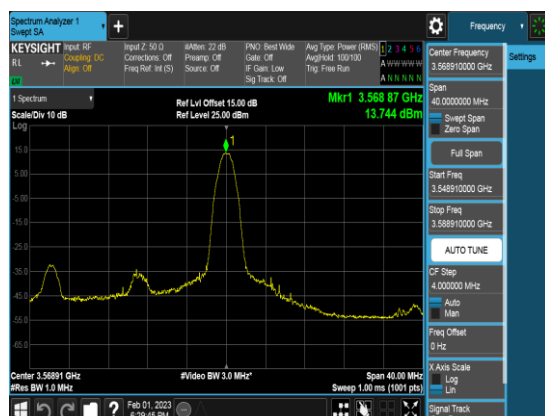
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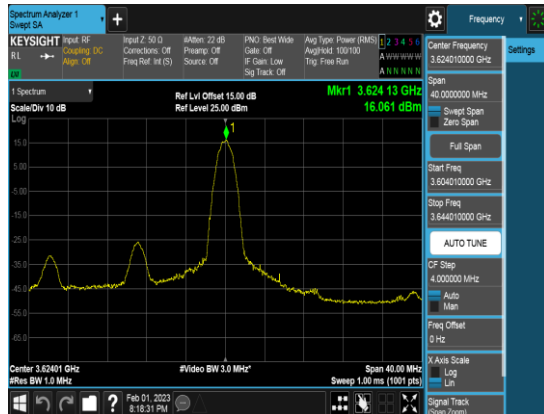


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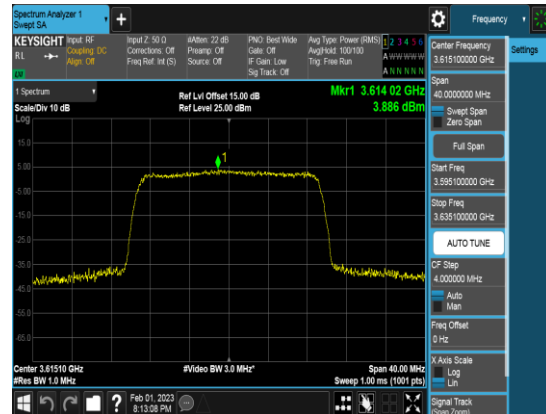
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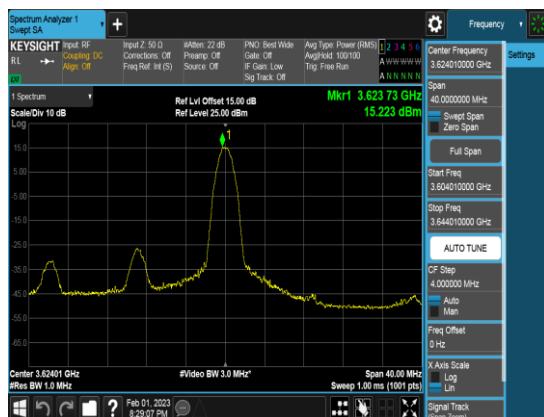
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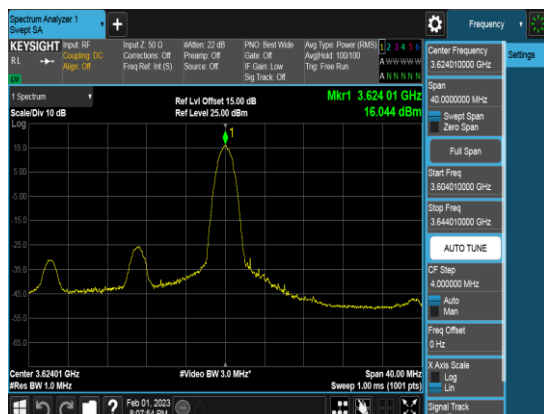
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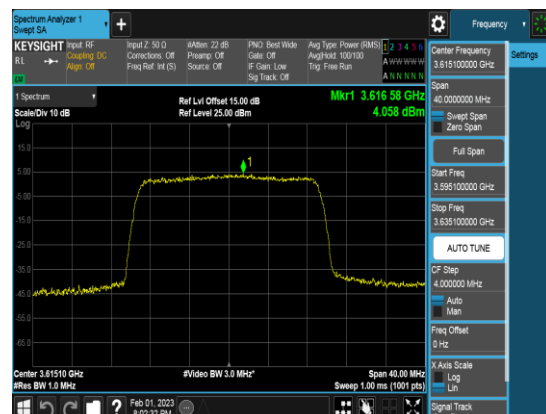
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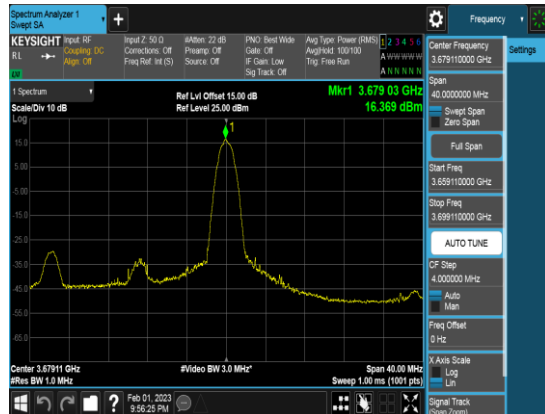


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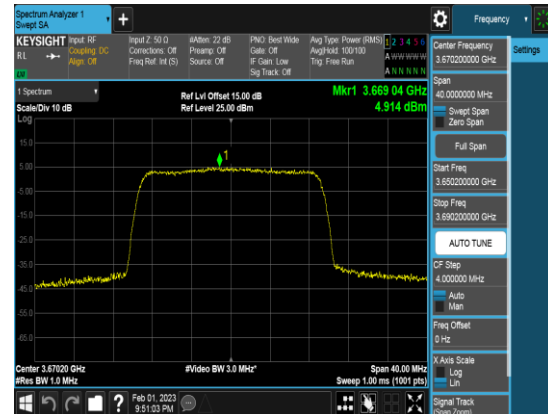
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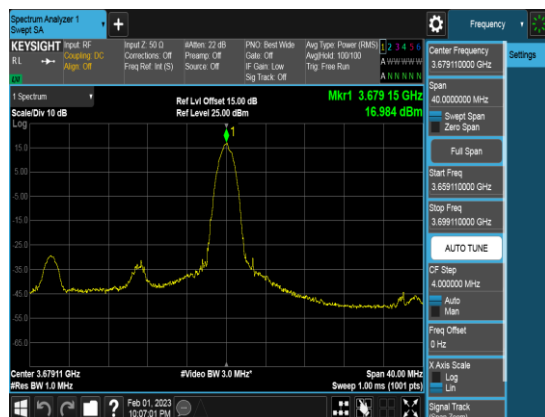
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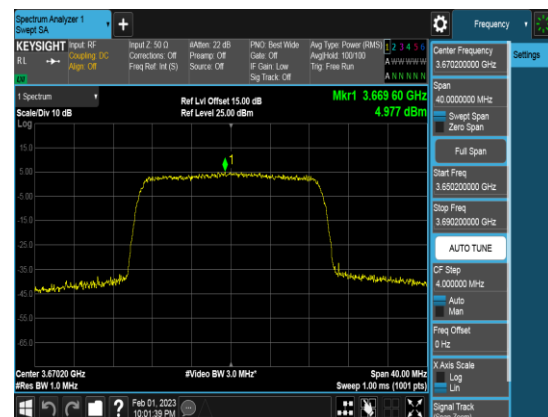
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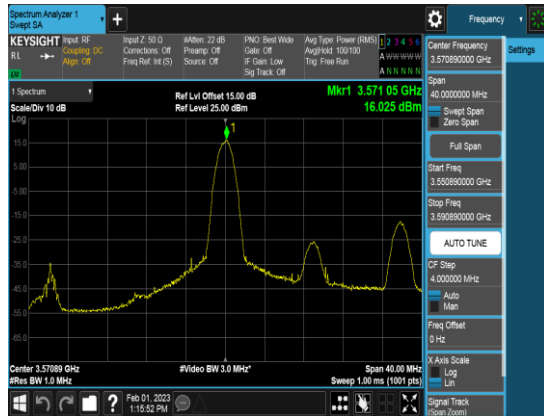


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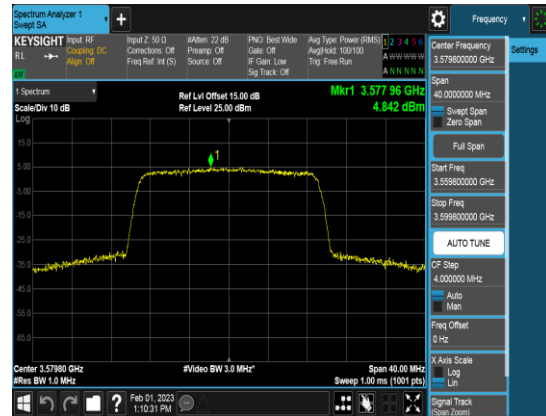
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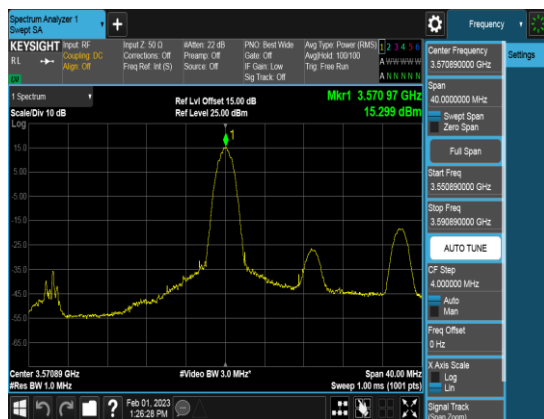
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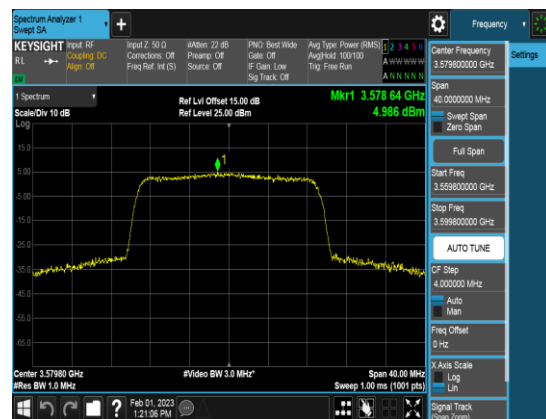
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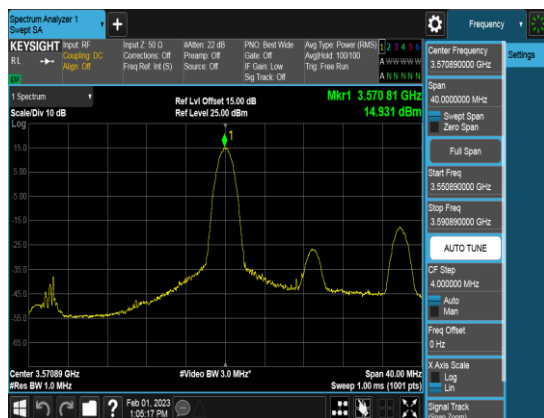
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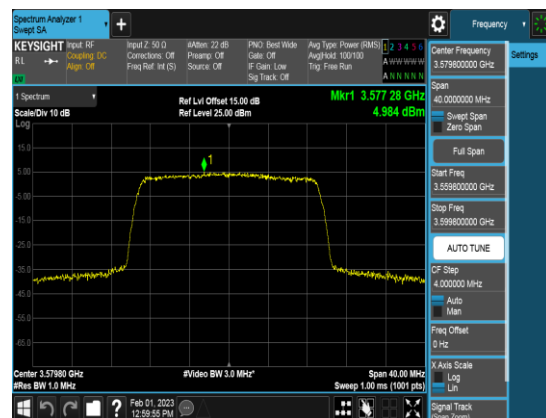
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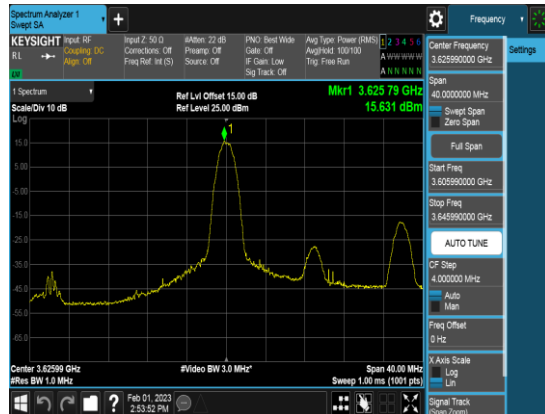


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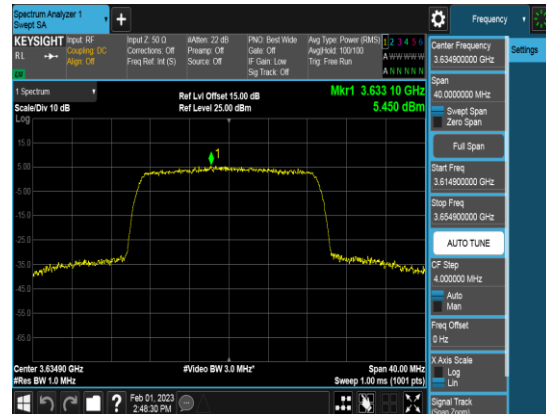
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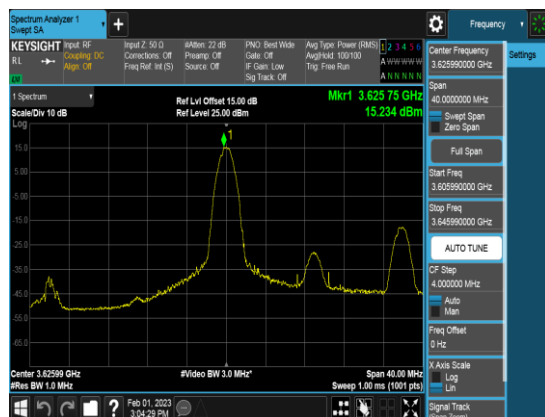
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SCC_20M_3634.9MHz_Q64_FULLRB_PSD



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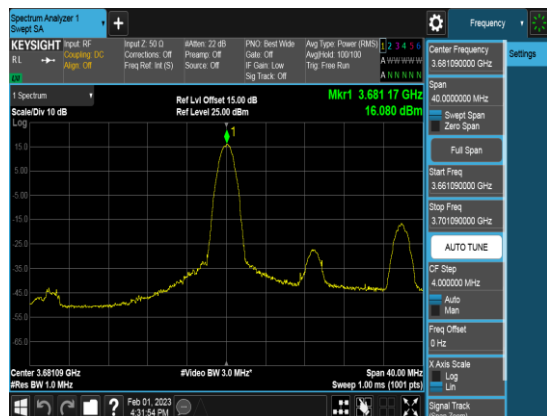


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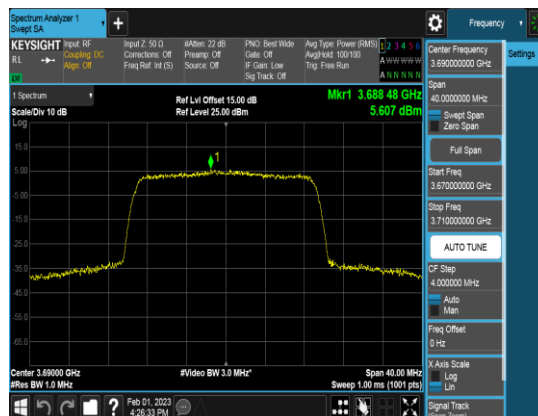
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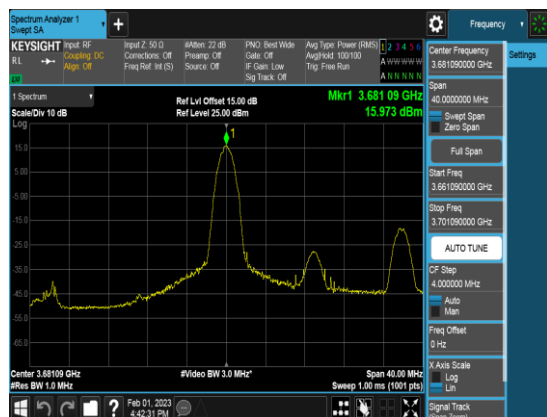
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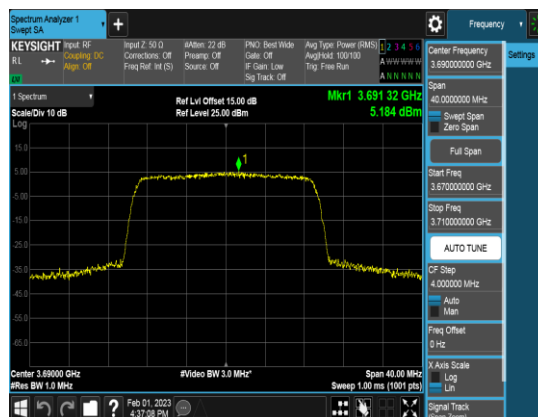
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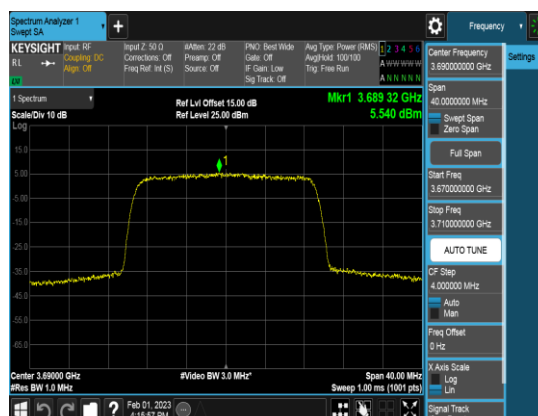
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SCC_20M_3690MHz_QPSK_1RB0_PSD



SCC_20M_3690MHz_QPSK_FULLRB_PSD



- End of the Appendix -



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