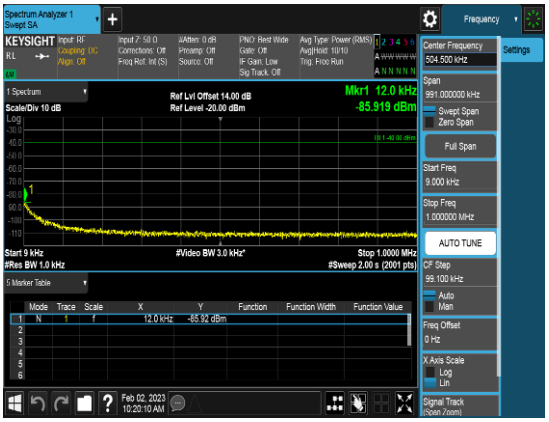
PCC_20M_3560MHz_Q64_FULLRB_CSE0a



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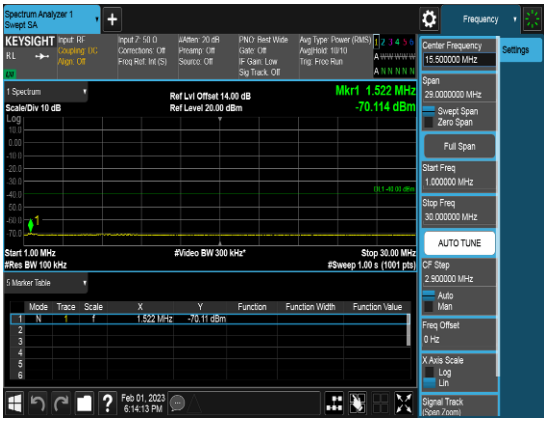
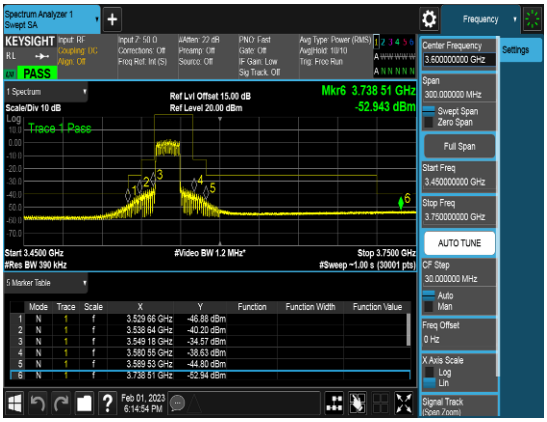


PCC_20M_3560MHz_Q64_FULLRB_CSE1

PCC_20M_3560MHz_Q64_FULLRB_CSE2

PCC_20M_3560MHz_Q64_FULLRB_CSE3

PCC_20M_3560MHz_Q64_FULLRB_CSEpart96



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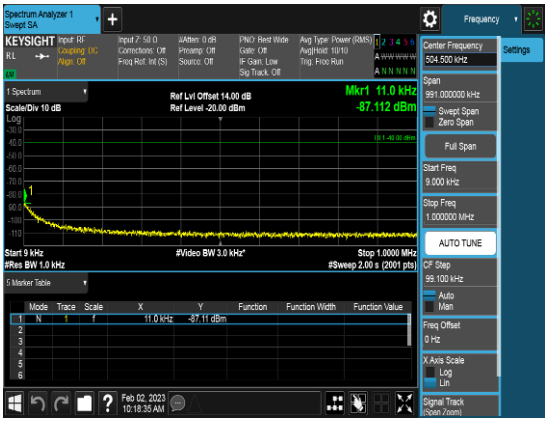
PCC_20M_3560MHz_QPSK_1RB_CSE0a



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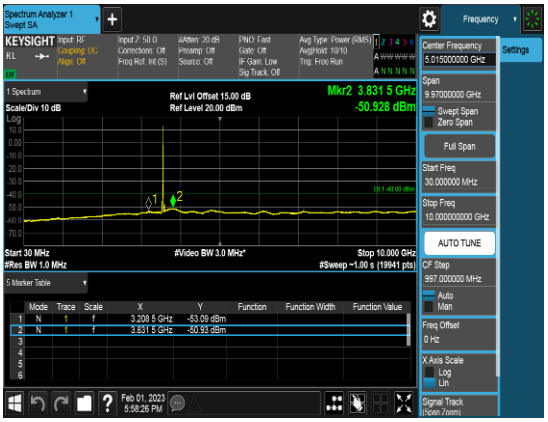

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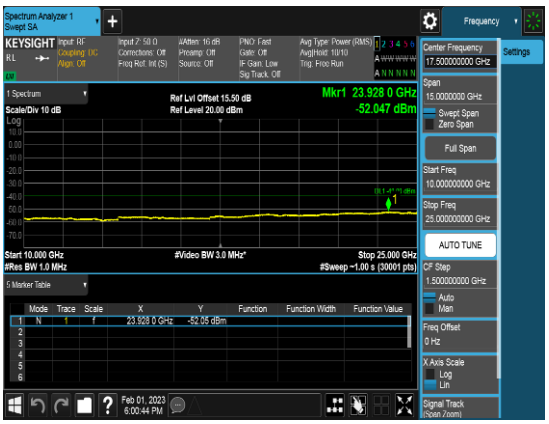
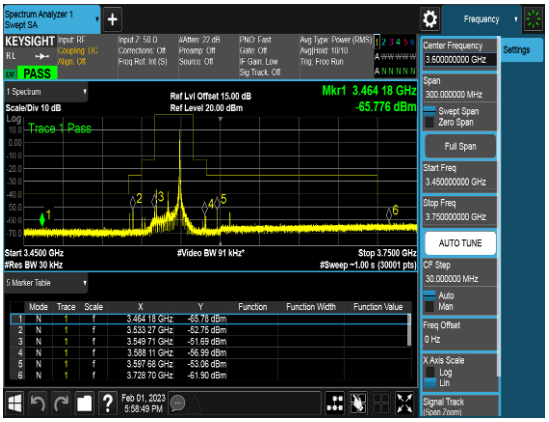


PCC_20M_3560MHz_QPSK_1RB_CSE3

PCC_20M_3560MHz_QPSK_1RB_CSEpart96



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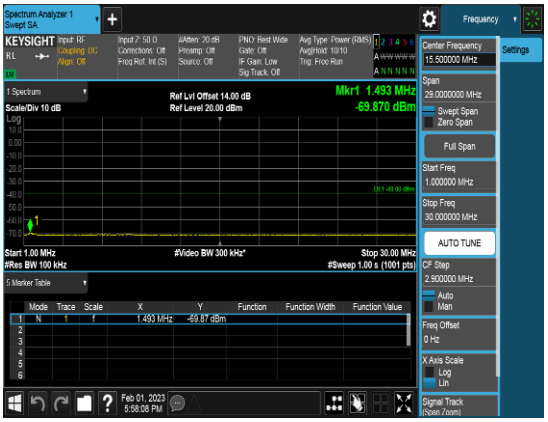

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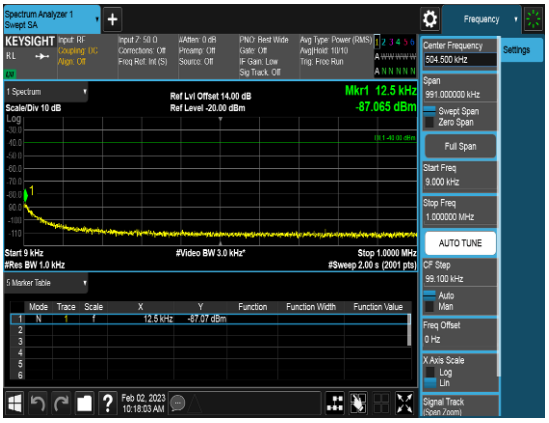
PCC_20M_3560MHz_QPSK_FULLRB_CSE0a



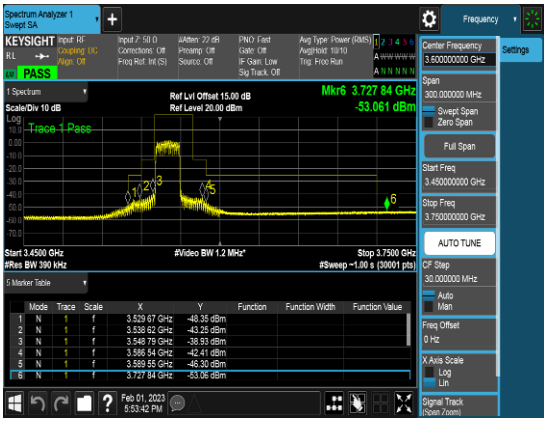
PCC_20M_3560MHz_QPSK_FULLRB_CSE0b

PCC_20M_3560MHz_QPSK_FULLRB_CSE1

PCC_20M_3560MHz_QPSK_FULLRB_CSE2

PCC_20M_3560MHz_QPSK_FULLRB_CSE3

PCC_20M_3560MHz_QPSK_FULLRB_CSEpart96



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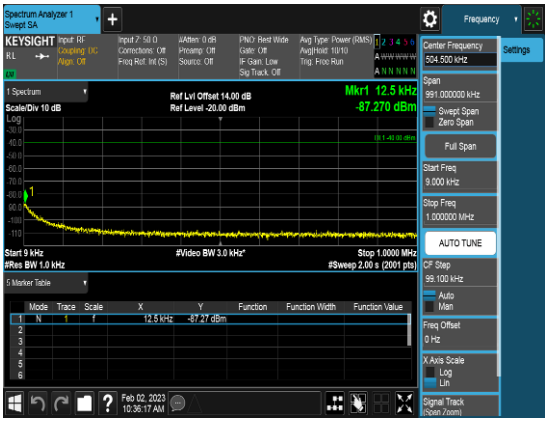
PCC_20M_3620.1MHz_Q16_1RB_CSE0a



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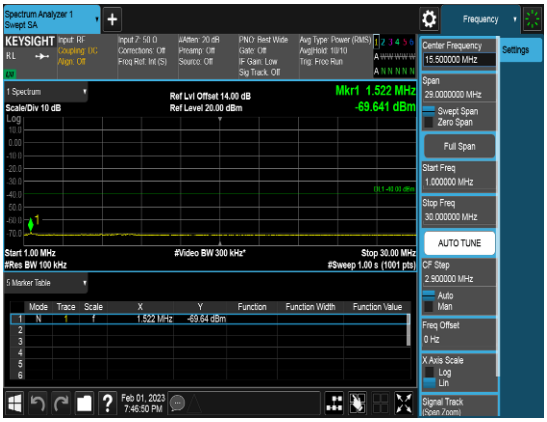
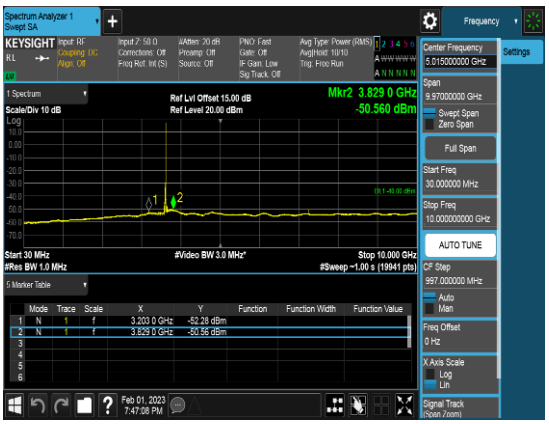


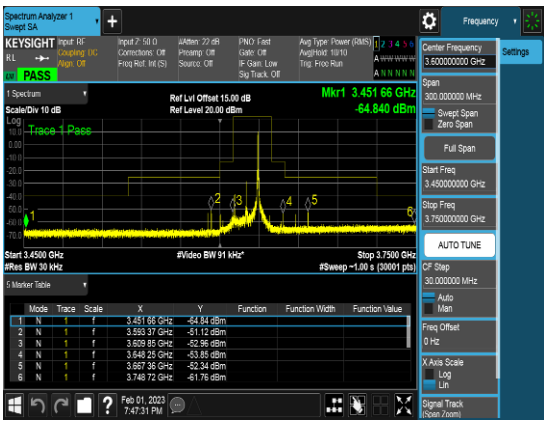
PCC_20M_3620.1MHz_Q16_1RB_CSE1



PCC_20M_3620.1MHz_Q16_1RB_CSE2

PCC_20M_3620.1MHz_Q16_1RB_CSE3

PCC_20M_3620.1MHz_Q16_1RB_CSEpart96



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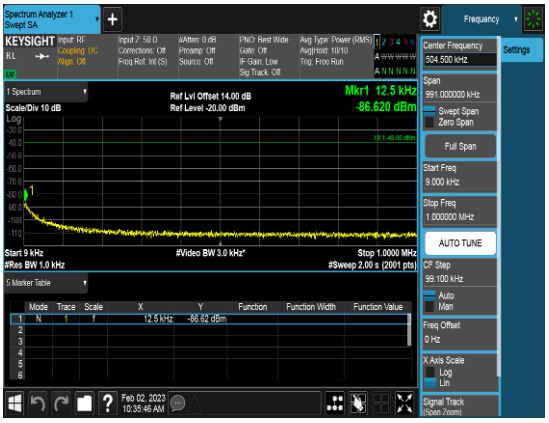
PCC_20M_3620.1MHz_Q16_FULLRB_CSE0a



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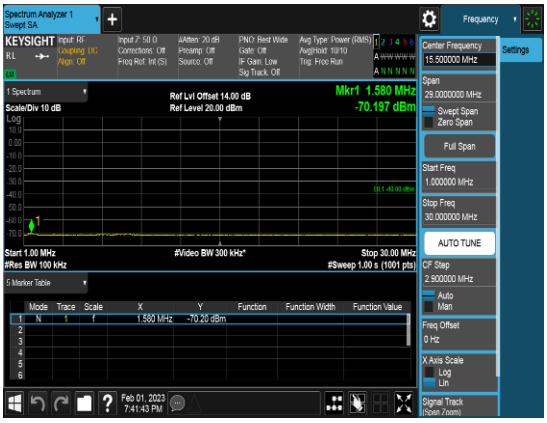
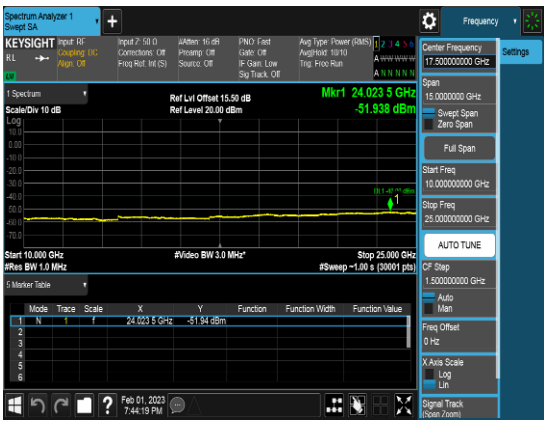


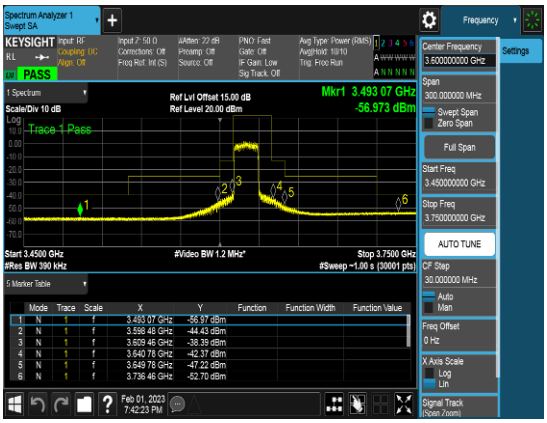
PCC_20M_3620.1MHz_Q16_FULLRB_CSE1

PCC_20M_3620.1MHz_Q16_FULLRB_CSE2



PCC_20M_3620.1MHz_Q16_FULLRB_CSE3

PCC_20M_3620.1MHz_Q16_FULLRB_CSEpart96



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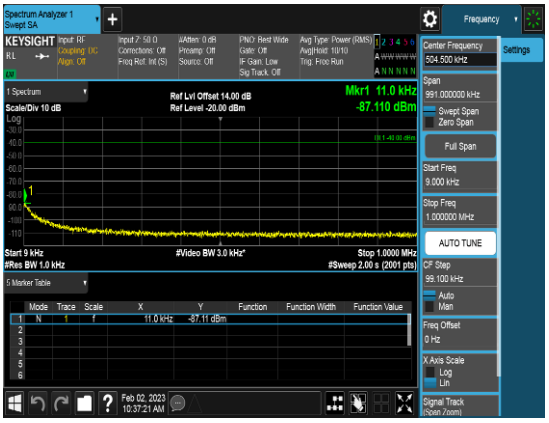
PCC_20M_3620.1MHz_Q64_1RB_CSE0a



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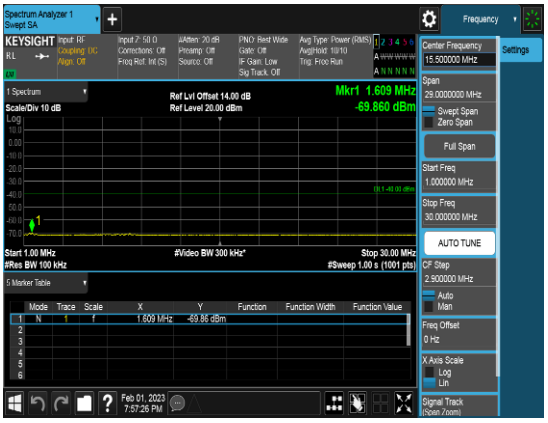
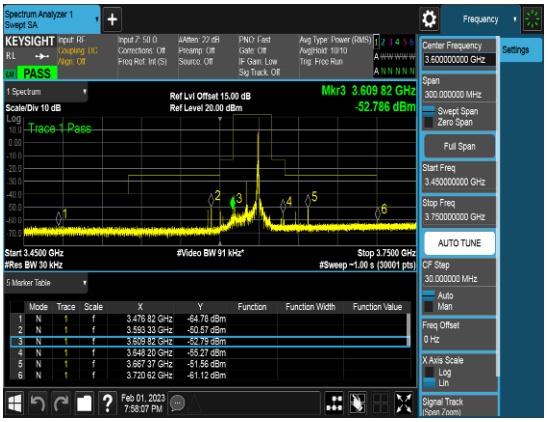


PCC_20M_3620.1MHz_Q64_1RB_CSE1

PCC_20M_3620.1MHz_Q64_1RB_CSE2

PCC_20M_3620.1MHz_Q64_1RB_CSE3

PCC_20M_3620.1MHz_Q64_1RB_CSEpart96



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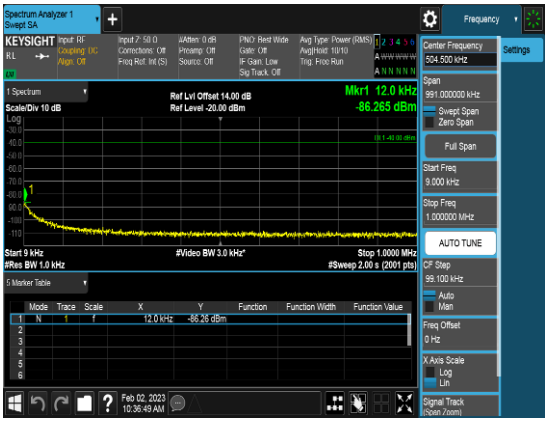
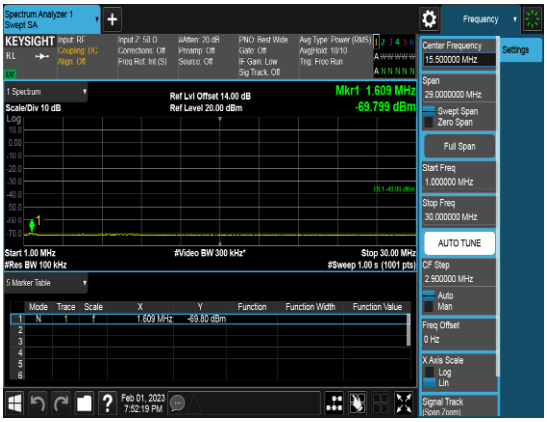
PCC_20M_3620.1MHz_Q64_FULLRB_CSE0a



PCC_20M_3620.1MHz_Q64_FULLRB_CSE0b



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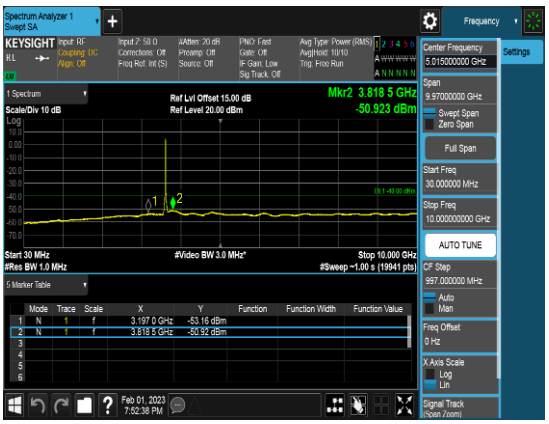
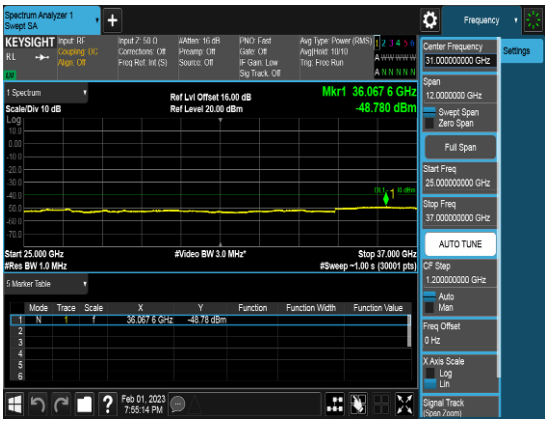


PCC_20M_3620.1MHz_Q64_FULLRB_CSE2

PCC_20M_3620.1MHz_Q64_FULLRB_CSE3



PCC_20M_3620.1MHz_Q64_FULLRB_CSEpart96

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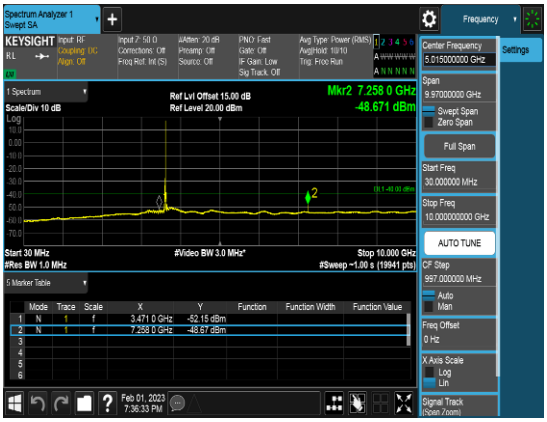
PCC_20M_3620.1MHz_QPSK_1RB_CSE0a

PCC_20M_3620.1MHz_QPSK_1RB_CSE1



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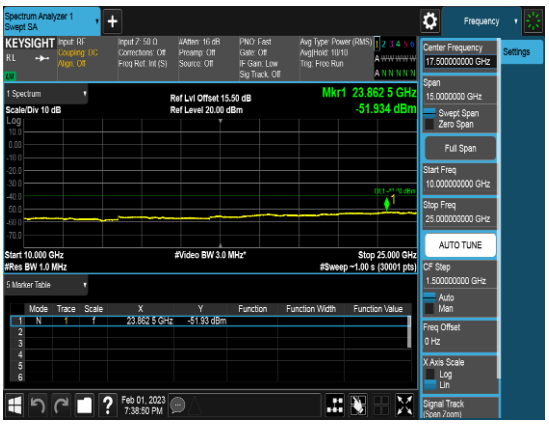
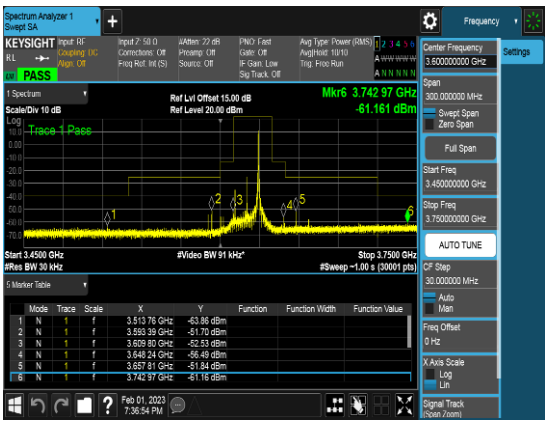


PCC_20M_3620.1MHz_QPSK_1RB_CSE3

PCC_20M_3620.1MHz_QPSK_1RB_CSEpart96



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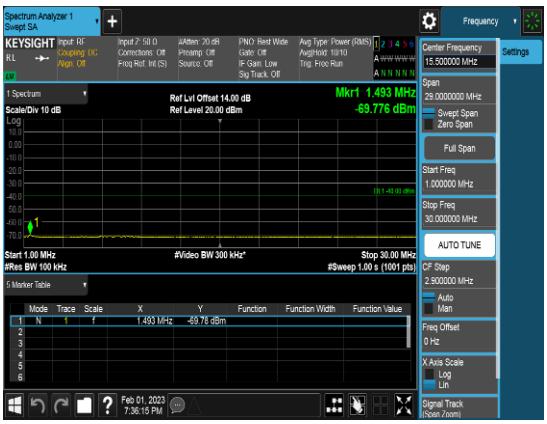

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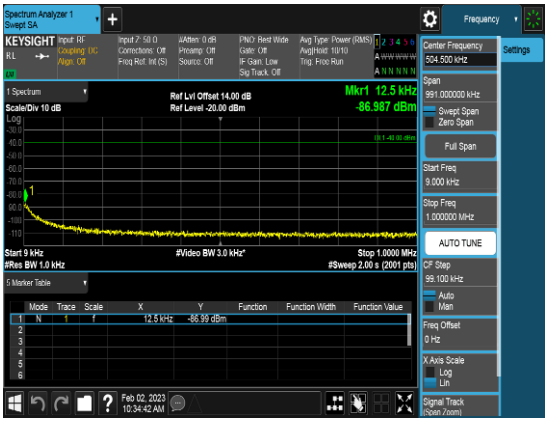
PCC_20M_3620.1MHz_QPSK_FULLRB_CSE0a



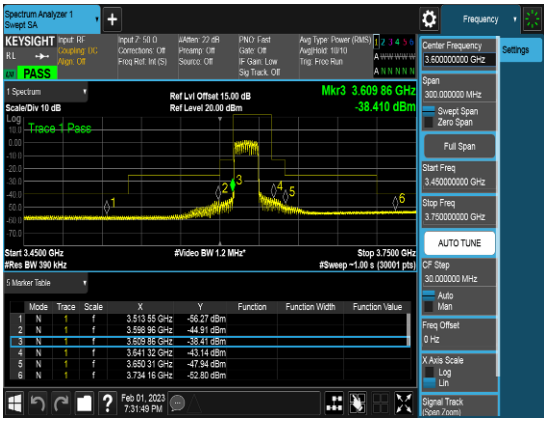
PCC_20M_3620.1MHz_QPSK_FULLRB_CSE0b

PCC_20M_3620.1MHz_QPSK_FULLRB_CSE1

PCC_20M_3620.1MHz_QPSK_FULLRB_CSE2

PCC_20M_3620.1MHz_QPSK_FULLRB_CSE3

PCC_20M_3620.1MHz_QPSK_FULLRB_CSEpart96



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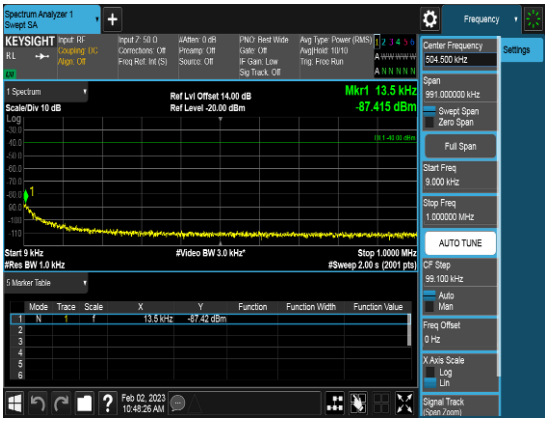
PCC_20M_3680.1MHz_Q16_1RB_CSE0a



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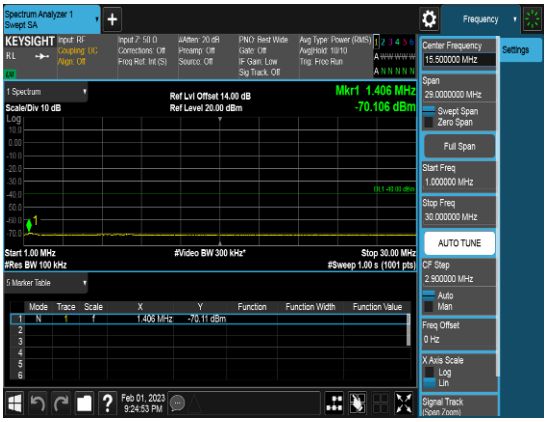

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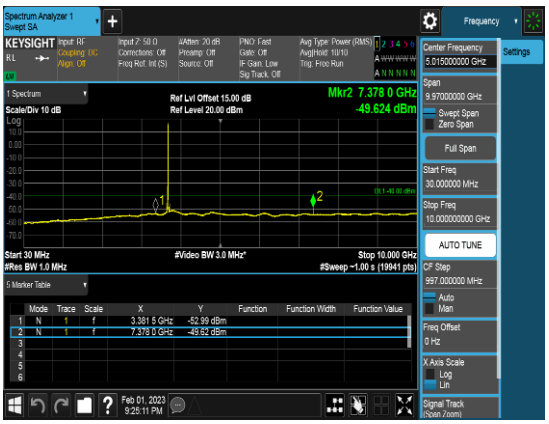

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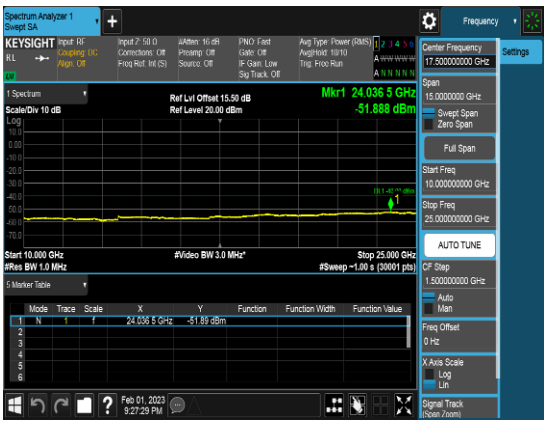

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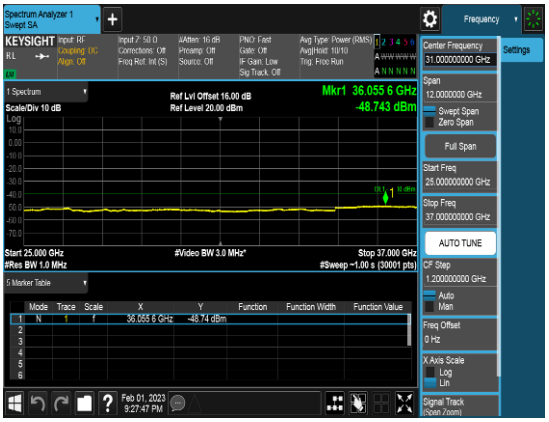
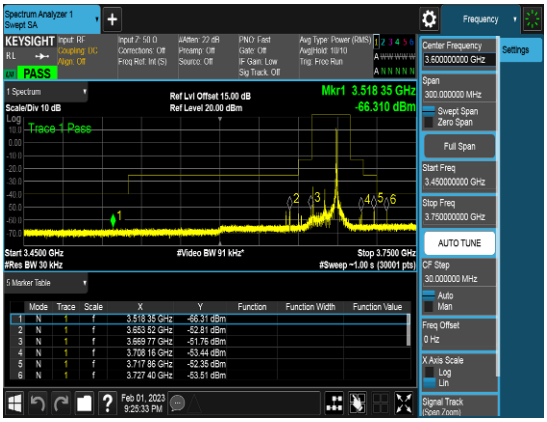


PCC_20M_3680.1MHz_Q16_1RB_CSEpart96



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