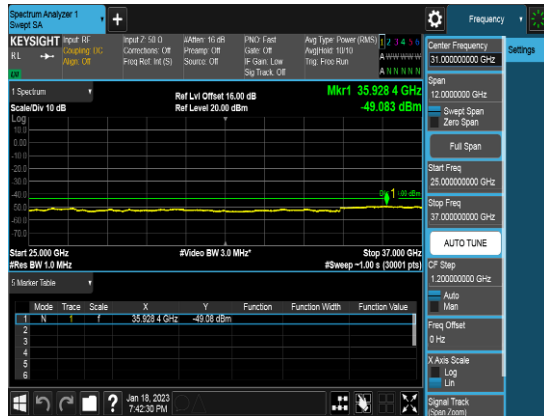


Ant 2_10M_Q64_1RB0_CSE3_High channel



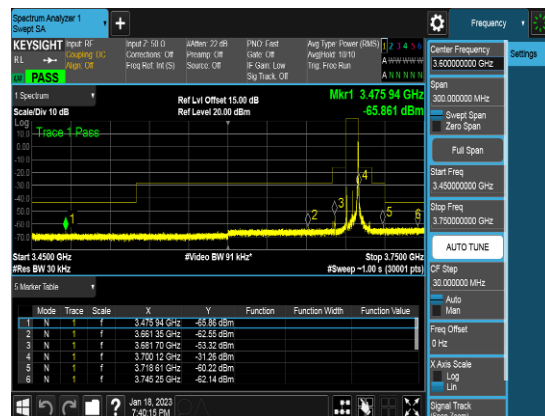
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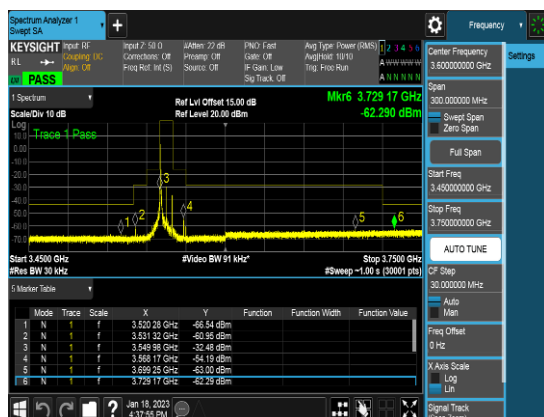
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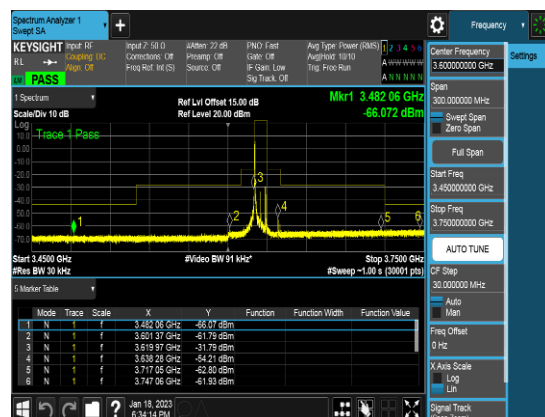
Ant 2_10M_Q64_1RB0_CSEpart96_High channel



Ant 2_10M_Q64_1RB0_CSEpart96_Low channel



Ant 2_10M_Q64_1RB0_CSEpart96_Middle channel



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Ant 2_10M_Q64_FULLRB_CSE0a_High channel



Ant 2_10M_Q64_FULLRB_CSE0a_Low channel



Ant 2_10M_Q64_FULLRB_CSE0a_Middle channel



Ant 2_10M_Q64_FULLRB_CSE0b_High channel



Ant 2_10M_Q64_FULLRB_CSE0b_Low channel



Ant 2_10M_Q64_FULLRB_CSE0b_Middle channel



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Ant 2_10M_Q64_FULLRB_CSE1_High channel



Ant 2_10M_Q64_FULLRB_CSE1_Low channel



Ant 2_10M_Q64_FULLRB_CSE1_Middle channel



Ant 2_10M_Q64_FULLRB_CSE2_High channel



Ant 2_10M_Q64_FULLRB_CSE2_Low channel



Ant 2_10M_Q64_FULLRB_CSE2_Middle channel

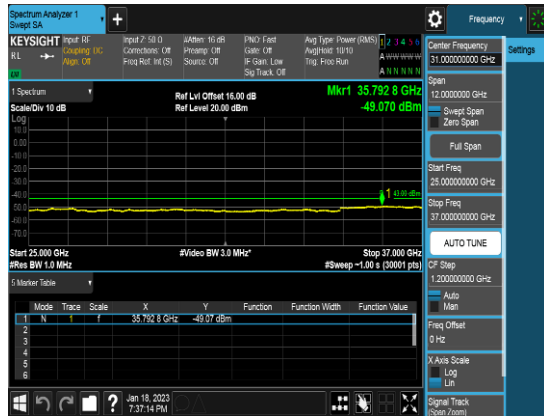


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Ant 2_10M_Q64_FULLRB_CSE3_High channel



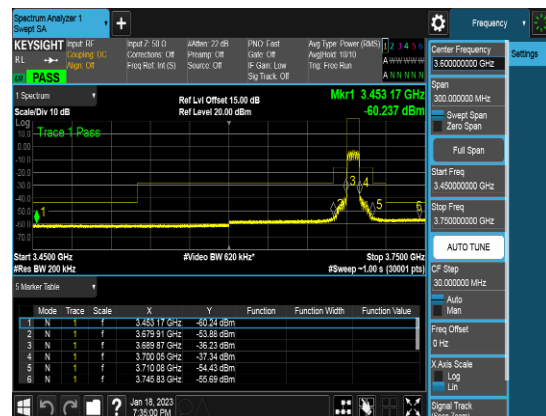
Ant 2_10M_Q64_FULLRB_CSE3_Low channel



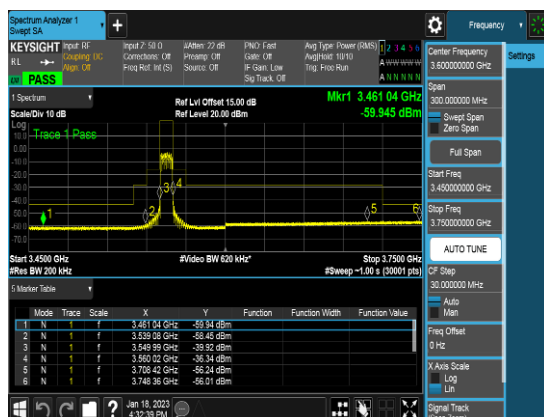
Ant 2_10M_Q64_FULLRB_CSE3_Middle channel



Ant 2_10M_Q64_FULLRB_CSEpart96_High channel



Ant 2_10M_Q64_FULLRB_CSEpart96_Low channel



Ant 2_10M_Q64_FULLRB_CSEpart96_Middle channel



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Ant 2_10M_QPSK_1RB0_CSE0a_High channel



Ant 2_10M_QPSK_1RB0_CSE0a_Low channel



Ant 2_10M_QPSK_1RB0_CSE0a_Middle channel



Ant 2_10M_QPSK_1RB0_CSE0b_High channel



Ant 2_10M_QPSK_1RB0_CSE0b_Low channel



Ant 2_10M_QPSK_1RB0_CSE0b_Middle channel



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Ant 2_10M_QPSK_1RB0_CSE1_High channel



Ant 2_10M_QPSK_1RB0_CSE1_Low channel



Ant 2_10M_QPSK_1RB0_CSE1_Middle channel



Ant 2_10M_QPSK_1RB0_CSE2_High channel



Ant 2_10M_QPSK_1RB0_CSE2_Low channel



Ant 2_10M_QPSK_1RB0_CSE2_Middle channel



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Ant 2_10M_QPSK_1RB0_CSE3_High channel



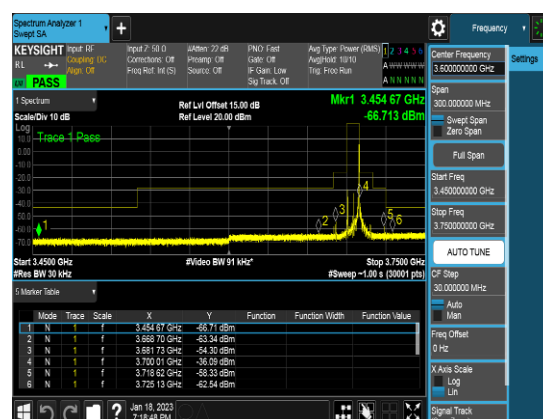
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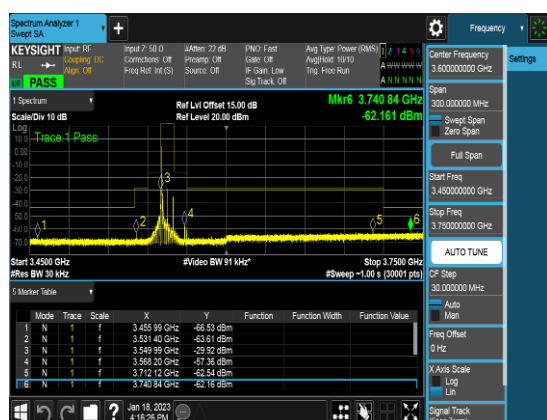
Ant 2_10M_QPSK_1RB0_CSE3_Middle channel



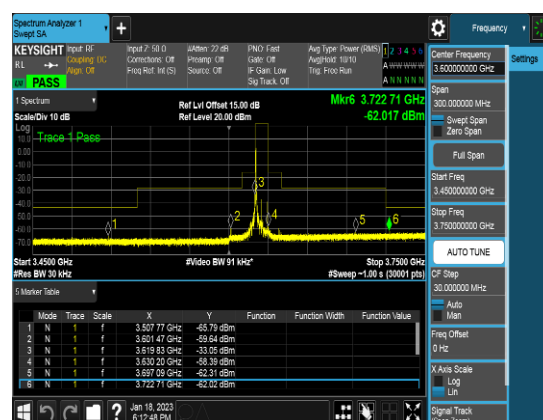
Ant 2_10M_QPSK_1RB0_CSEpart96_High channel



Ant 2_10M_QPSK_1RB0_CSEpart96_Low channel



Ant 2_10M_QPSK_1RB0_CSEpart96_Middle channel



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Ant 2_10M_QPSK_FULLRB_CSE0a_High channel



Ant 2_10M_QPSK_FULLRB_CSE0a_Low channel



Ant 2_10M_QPSK_FULLRB_CSE0a_Middle channel



Ant 2_10M_QPSK_FULLRB_CSE0b_High channel



Ant 2_10M_QPSK_FULLRB_CSE0b_Low channel



Ant 2_10M_QPSK_FULLRB_CSE0b_Middle channel



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Ant 2_10M_QPSK_FULLLRB_CSE1_High channel



Ant 2_10M_QPSK_FULLLRB_CSE1_Low channel



Ant 2_10M_QPSK_FULLLRB_CSE1_Middle channel



Ant 2_10M_QPSK_FULLLRB_CSE2_High channel



Ant 2_10M_QPSK_FULLLRB_CSE2_Low channel



Ant 2_10M_QPSK_FULLLRB_CSE2_Middle channel



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Ant 2_10M_QPSK_FULLRB_CSE3_High channel



Ant 2_10M_QPSK_FULLRB_CSE3_Low channel



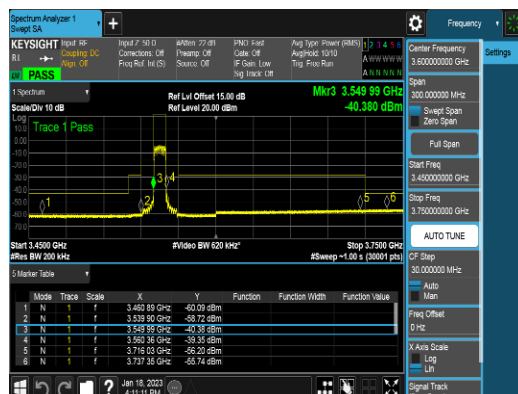
Ant 2_10M_QPSK_FULLRB_CSE3_Middle channel



Ant 2_10M_QPSK_FULLRB_CSEpart96_High channel



Ant 2_10M_QPSK_FULLRB_CSEpart96_Low channel



Ant 2_10M_QPSK_FULLRB_CSEpart96_Middle channel



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Ant 2_20M_Q16_1RB0_CSE0a_High channel



Ant 2_20M_Q16_1RB0_CSE0a_Low channel



Ant 2_20M_Q16_1RB0_CSE0a_Middle channel



Ant 2_20M_Q16_1RB0_CSE0b_High channel



Ant 2_20M_Q16_1RB0_CSE0b_Low channel



Ant 2_20M_Q16_1RB0_CSE0b_Middle channel



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