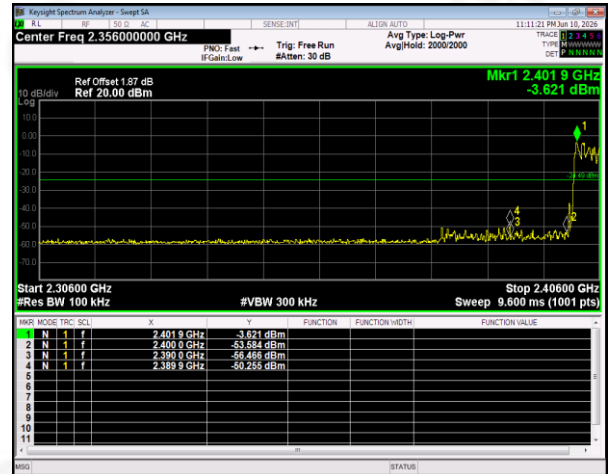
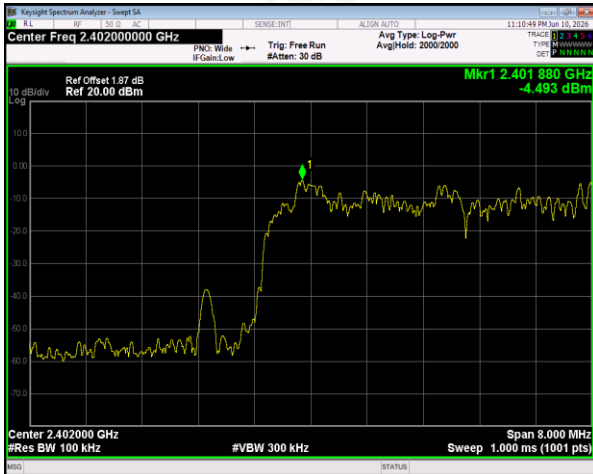


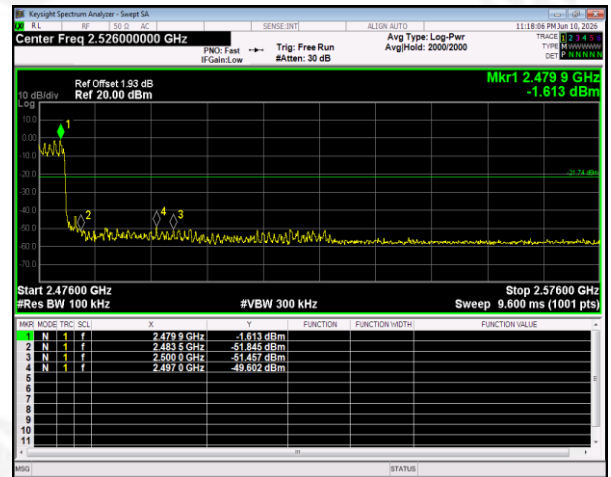


8-DPSK - 3-DH1

Hopping Band edge-left side



Hopping Band edge-right side

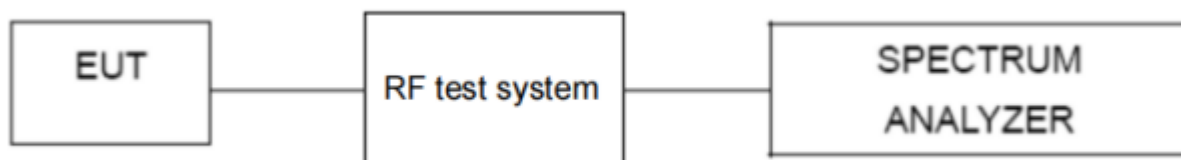




7. 20DB OCCUPIED BANDWIDTH

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)
Test Method:	ANSI C63.10-2020+Cor 1-2023

7.1 TEST SETUP



7.2 LIMIT

N/A

7.3 TEST PROCEDURE

1. Set RBW = 30 kHz.
2. Set the video bandwidth (VBW) $\geq 3 \times$ RBW.
3. Detector = Peak.
4. Trace mode = max hold.
5. Sweep = auto couple.
6. Allow the trace to stabilize.
7. Measure the maximum width of the emission that is constrained by the frequencies associated with the two outermost amplitude points (upper and lower frequencies) that are attenuated by 20 dB relative to the maximum level measured in the fundamental emission.

7.4 DEVIATION FROM STANDARD

No deviation.



7.5 TEST RESULT

Modulation	Packet	Test Channel	20dB Occupied Bandwidth (MHz)	Result
GFSK	1-DH1	Lowest	0.8758	Pass
		Middle	0.8702	
		Highest	0.8757	
$\pi/4$ -DQPSK	2-DH1	Lowest	1.259	Pass
		Middle	1.251	
		Highest	1.244	
8-DPSK	3-DH1	Lowest	1.241	Pass
		Middle	1.216	
		Highest	1.219	

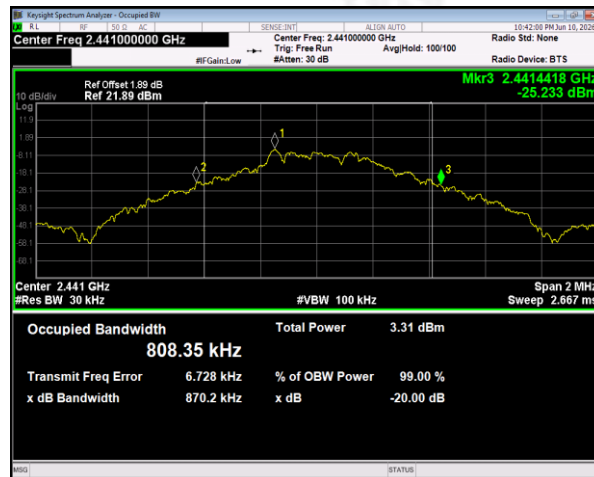


GFSK - 1-DH1 Test plots

Low Channel



Middle Channel



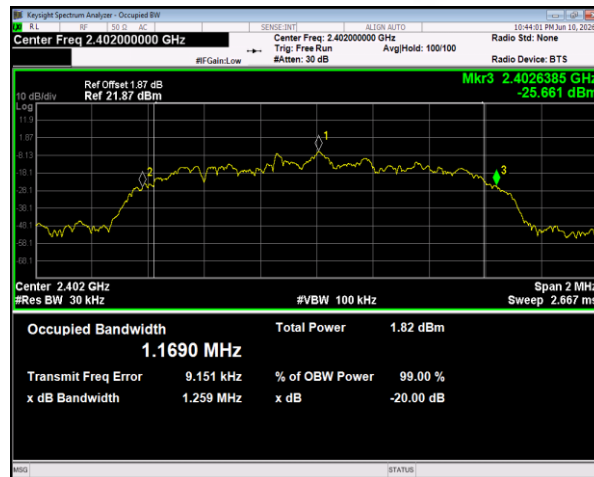
High Channel



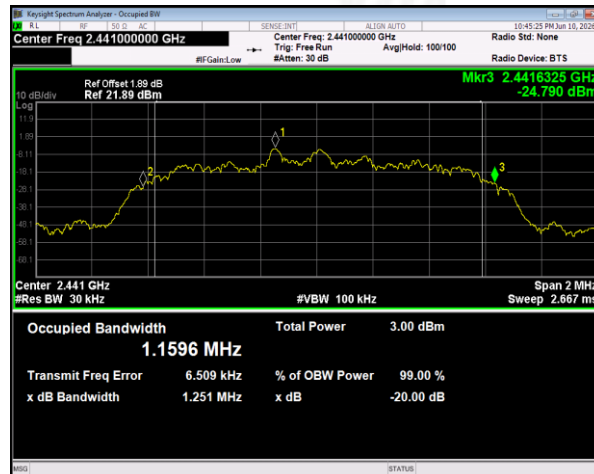


$\pi/4$ -DQPSK - 2-DH1 Test plots

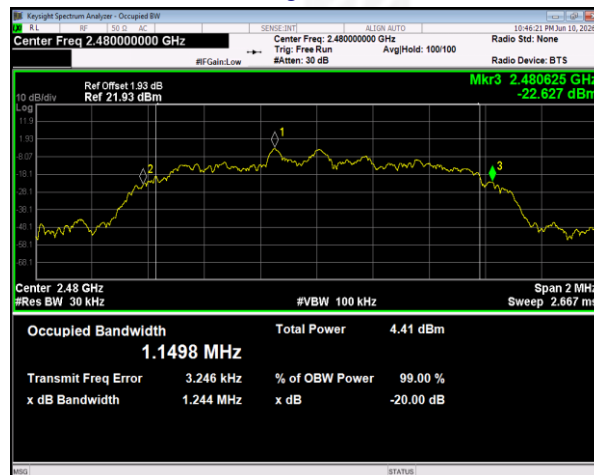
Low Channel



Middle Channel



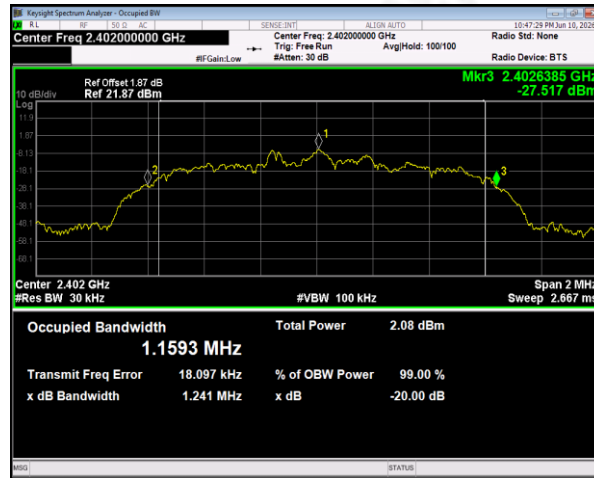
High Channel



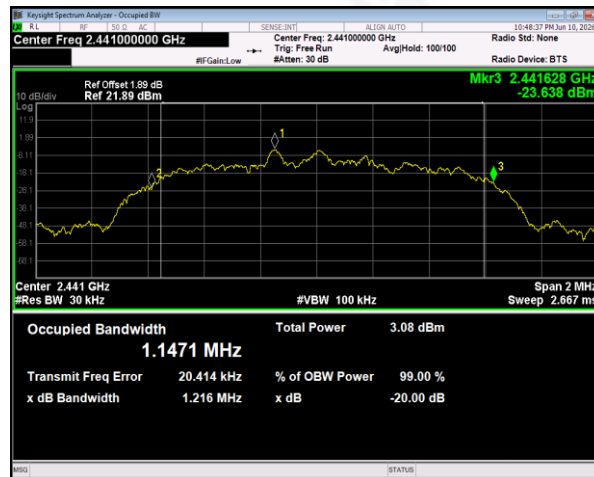


8-DPSK - 3-DH1 Test plots

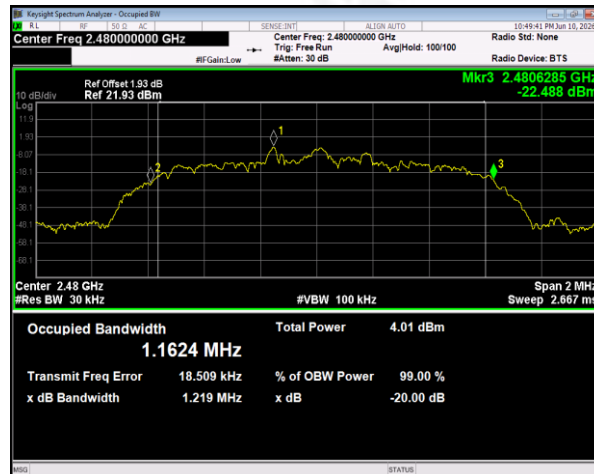
Low Channel



Middle Channel



High Channel

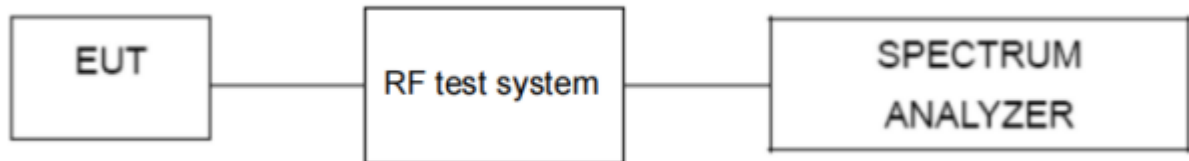




8. Maximum Peak Output Power

Test Requirement:	FCC Part15 C Section 15.247 (b)(1)
Test Method:	ANSI C63.10-2020+Cor 1-2023
Limit:	GFSK & $\pi/4$ -DQPSK & 8-DPSK: 20.97 dBm

8.1 BLOCK DIAGRAM OF TEST SETUP



8.2 LIMIT

For frequency hopping systems operating in the 2400-2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725-5850 MHz band: 1 watt.
For all other frequency hopping systems in the 2400-2483.5 MHz band: 0.125 watts.

For FHSs operating in the band 2400-2483.5 MHz, the maximum peak conducted output power shall not exceed 1.0 W if the hopset uses 75 or more hopping channels; the maximum peak conducted output power shall not exceed 0.125 W if the hopset uses less than 75 hopping channels.

8.3 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 2MHz. VBW =6MHz. Sweep = auto; Detector Function = Peak.
3. Keep the EUT in transmitting at lowest, medium and highest channel individually. Record the max value.

8.4 DEVIATION FROM STANDARD

No deviation.



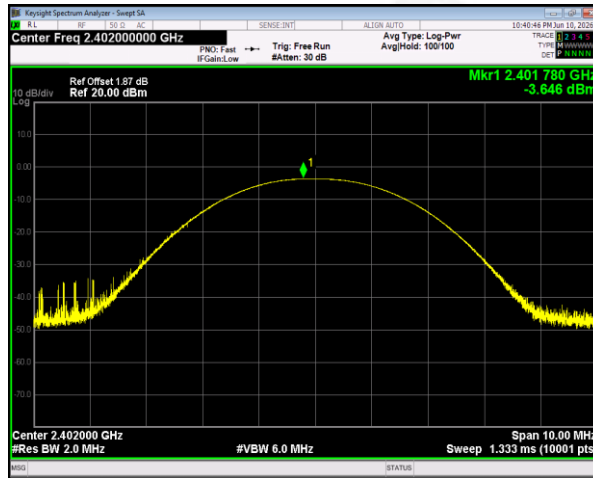
8.5 TEST RESULT

Modulation	Packet	Test Channel	Peak Output Power (dBm)	Limit (dBm)	Result
GFSK	1-DH1	Lowest	-3.65	20.97	Pass
		Middle	-2.84		
		Highest	-1.58		
$\pi/4$ -DQPSK	2-DH1	Lowest	-2.77	20.97	Pass
		Middle	-1.93		
		Highest	-0.71		
8-DPSK	3-DH1	Lowest	-2.34	20.97	Pass
		Middle	-1.37		
		Highest	-0.15		

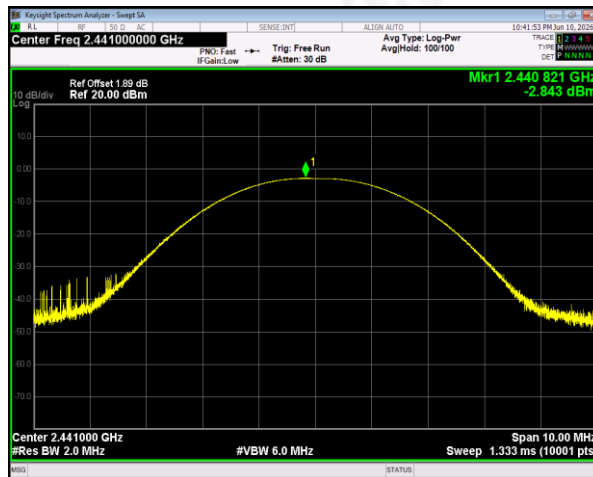


GFSK - 1-DH1 Test plots

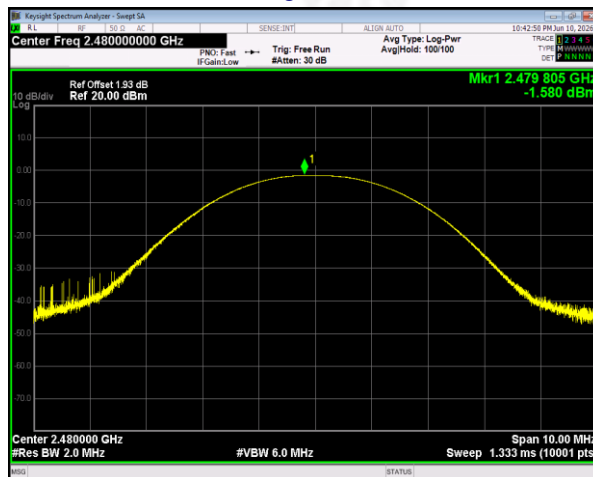
Low Channel



Middle Channel



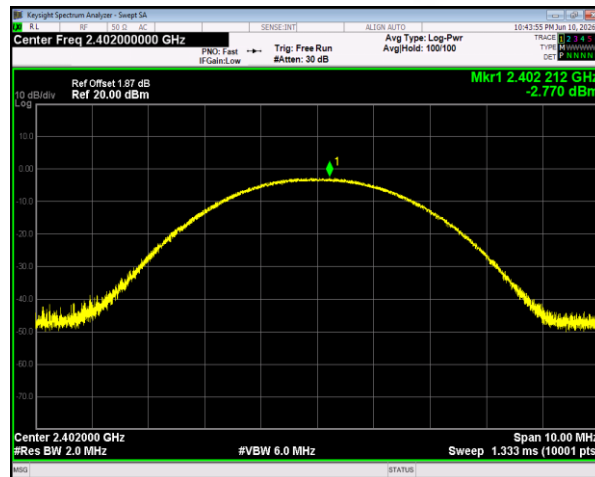
High Channel



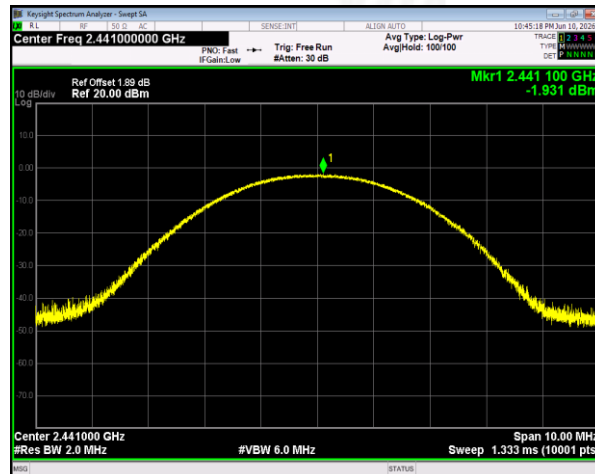


$\pi/4$ -DQPSK - 2-DH1 Test plots

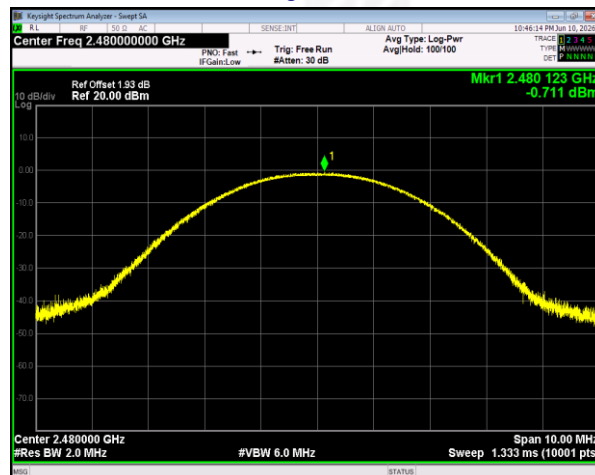
Low Channel



Middle Channel



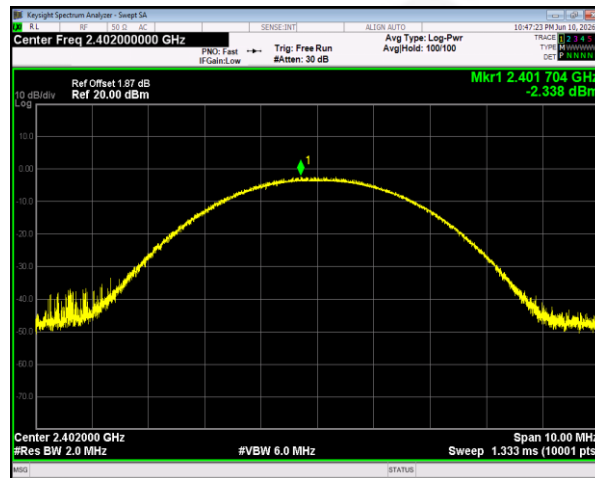
High Channel



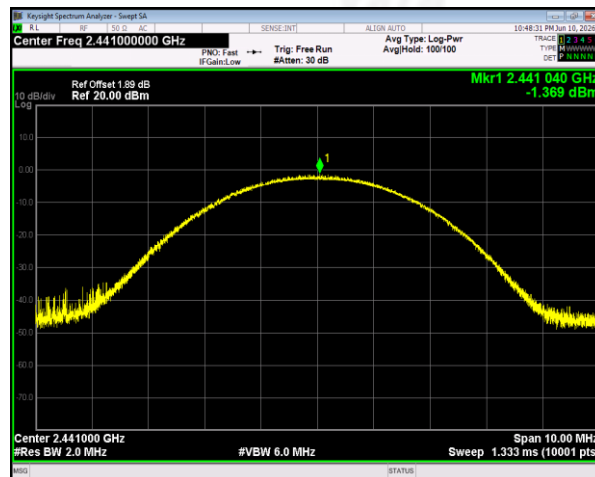


8-DPSK - 3-DH1 Test plots

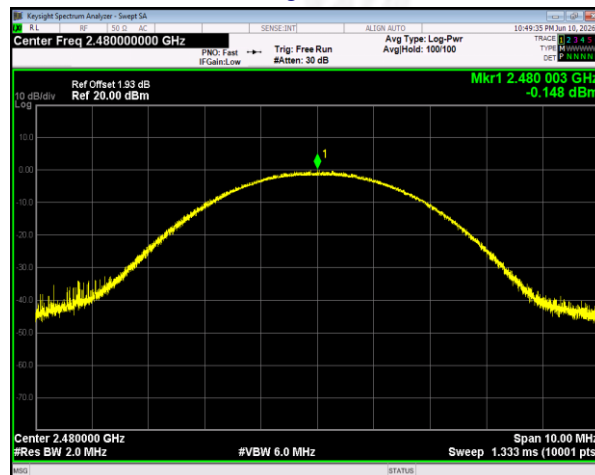
Low Channel



Middle Channel



High Channel

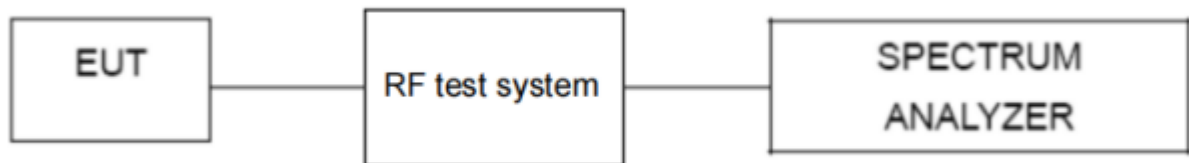




9. HOPPING CHANNEL SEPARATION

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)
Test Method:	ANSI C63.10-2020+Cor 1-2023
Receiver setup:	RBW=30KHz, VBW=100KHz, detector=Peak
Limit:	GFSK & $\pi/4$ -DQPSK & 8DPSK: 0.025MHz or 2/3 of the 20dB Bandwidth (whichever is greater)

9.1 TEST SETUP



9.2 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 30kHz. VBW = 100kHz , Span = 2.0MHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. Use the marker-delta function to determine the separation between the peaks of the adjacent channels. The limit is specified in one of the subparagraphs of this Section Submit this plot.

9.3 DEVIATION FROM STANDARD

No deviation.



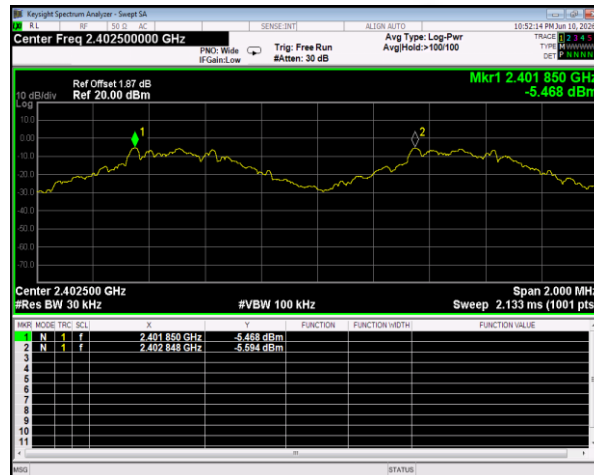
9.4 TEST RESULT

Modulation	Packet	Test Channel	Separation (MHz)	Limit (MHz)	Result
GFSK	1-DH1	Low	0.998	0.584	PASS
		Middle	1.000	0.58	PASS
		High	0.998	0.584	PASS
$\pi/4$ -DQPSK	2-DH1	Low	1.006	0.839	PASS
		Middle	1.000	0.834	PASS
		High	1.000	0.829	PASS
8-DPSK	3-DH1	Low	0.998	0.827	PASS
		Middle	1.000	0.811	PASS
		High	0.998	0.813	PASS

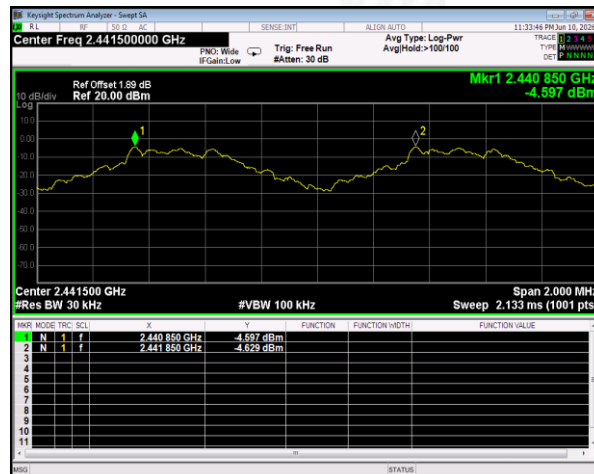


GFSK - 1-DH1 Test plots

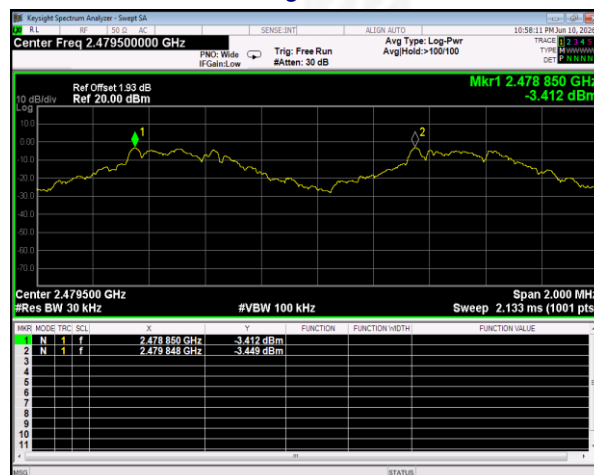
Low Channel



Middle Channel



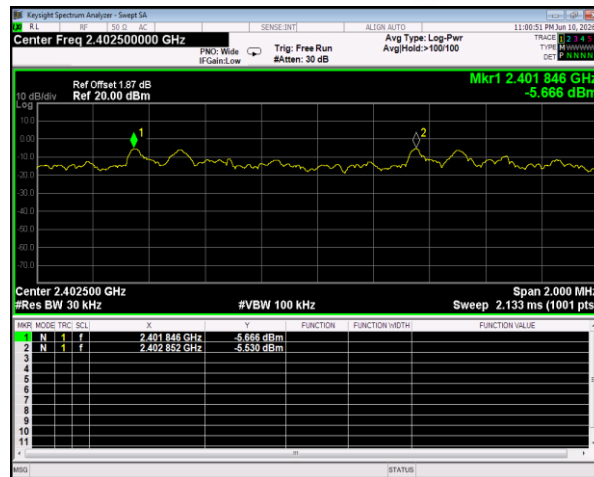
High Channel



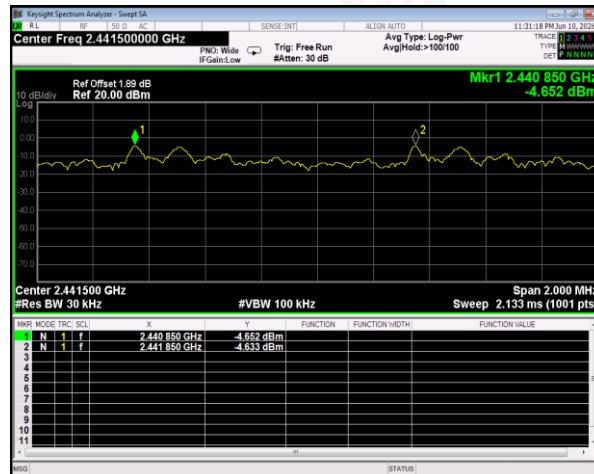


$\pi/4$ -DQPSK - 2-DH1 Test plots

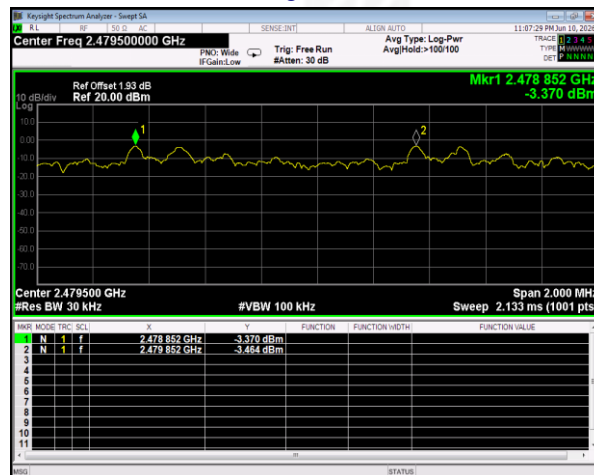
Low Channel



Middle Channel



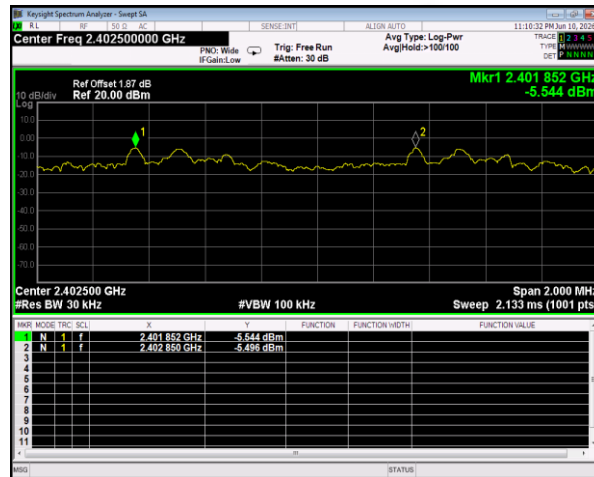
High Channel



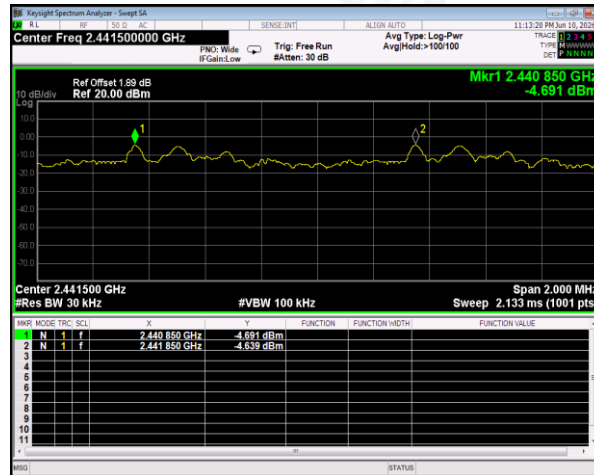


8-DPSK - 3-DH1 Test plots

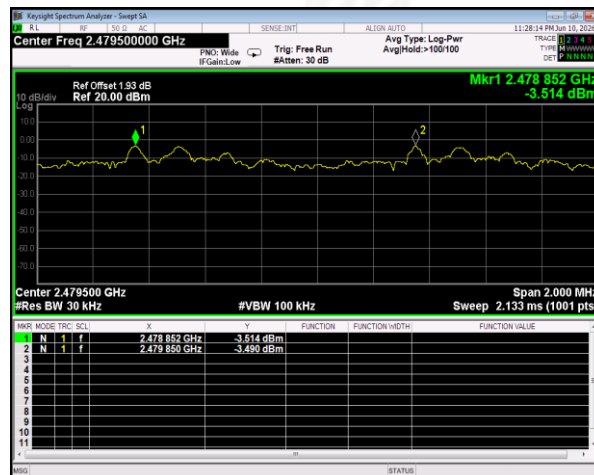
Low Channel



Middle Channel



High Channel

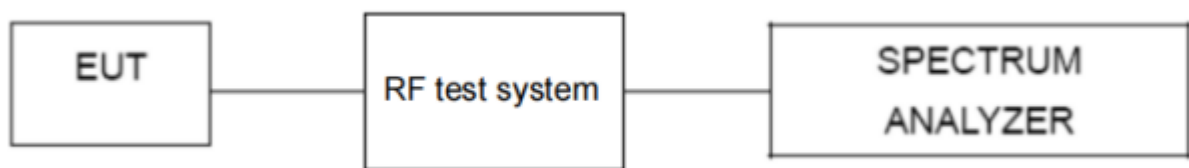




10.NUMBER OF HOPPING FREQUENCY

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10-2020+Cor 1-2023
Receiver setup:	RBW=100kHz, VBW=300kHz Frequency Range=2400MHz-2483.5MHz Detector=Peak
Limit:	$P_{\max-pk} \leq 1W$, $N_{ch} \geq 75$ Channels $P_{\max-pk} \leq 0.125W$, $N_{ch} \geq 15$ Channels

10.1 TEST SETUP



10.2 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 100kHz. VBW = 300kHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. It may prove necessary to break the span up to sections. in order to clearly show all of the hopping frequencies. The limit is specified in one of the subparagraphs of this Section.
4. Set the spectrum analyzer: Start Frequency = 2.4GHz, Stop Frequency = 2.4835GHz. Sweep=auto;

10.3 DEVIATION FROM STANDARD

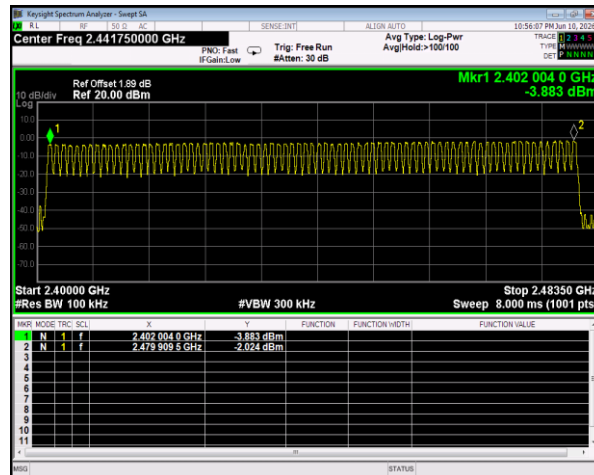
No deviation.



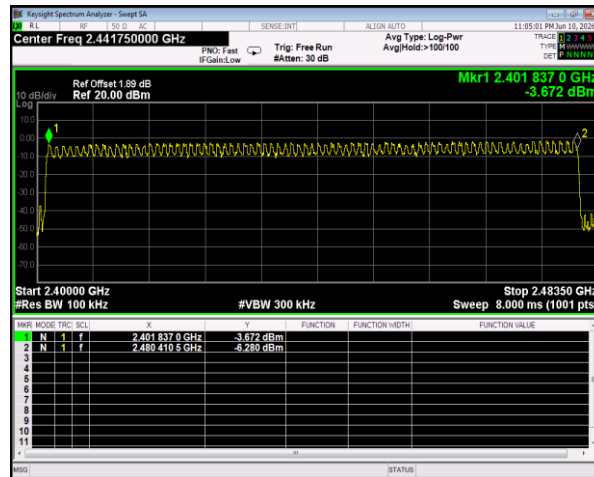
10.4 TEST RESULT

Modulation	Packet	Hopping Number	Limit	Result
GFSK	1-DH1	79	≥ 15	Pass
$\pi/4$ -DQPSK	2-DH1	79	≥ 15	Pass
8-DPSK	3-DH1	79	≥ 15	Pass

GFSK - 1-DH1 Test Plots

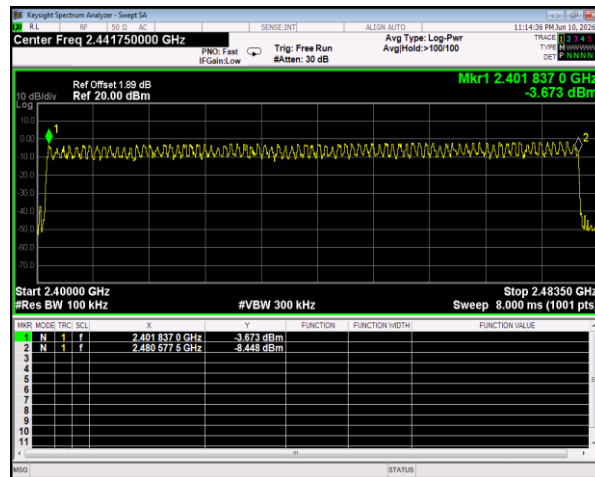


$\pi/4$ -DQPSK - 2-DH1 Test Plots





8-DPSK - 3-DH1 Test Plots

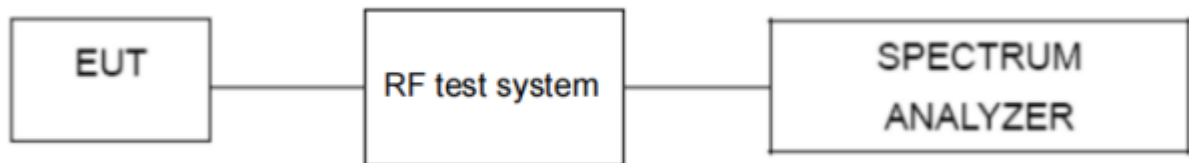




11. DWELL TIME

Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
Test Method:	ANSI C63.10-2020+Cor 1-2023
Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak
Limit:	0.4 Second

11.1 TEST SETUP



11.2 TEST PROCEDURE

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0Hz;
3. Set RBW = 1MHz and VBW = 3MHz. Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH1, DH3 DH5, 2DH1, 2DH3, 2DH5, 3DH1, 3DH3, 3DH5 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

11.3 DEVIATION FROM STANDARD

No deviation.



11.4 TEST RESULT

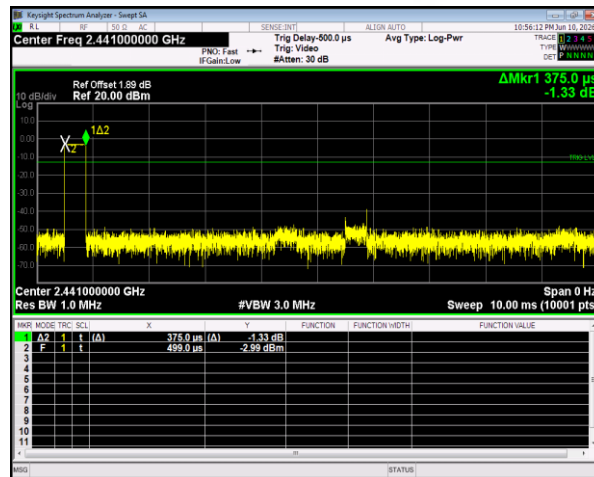
Mode	Frequency (MHz)	Pulse Time (ms)	Total Dwell Time (ms)	Burst Count	Period Time (ms)	Limit (ms)	Verdict
1-DH1	2441	0.375	118.5	316	31600	400	Pass
1-DH3	2441	1.631	272.377	167	31600	400	Pass
1-DH5	2441	2.879	351.238	122	31600	400	Pass
2-DH1	2441	0.379	119.385	315	31600	400	Pass
2-DH3	2441	1.622	262.764	162	31600	400	Pass
2-DH5	2441	2.879	282.142	98	31600	400	Pass
3-DH1	2441	0.372	117.552	316	31600	400	Pass
3-DH3	2441	1.636	260.124	159	31600	400	Pass
3-DH5	2441	2.887	311.796	108	31600	400	Pass

Note: Total Dwell Time (ms) = Pulse Time (ms)*Burst Count

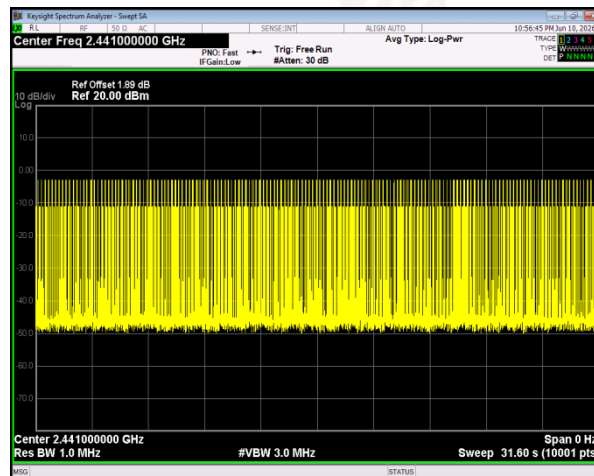


GFSK Test Plots

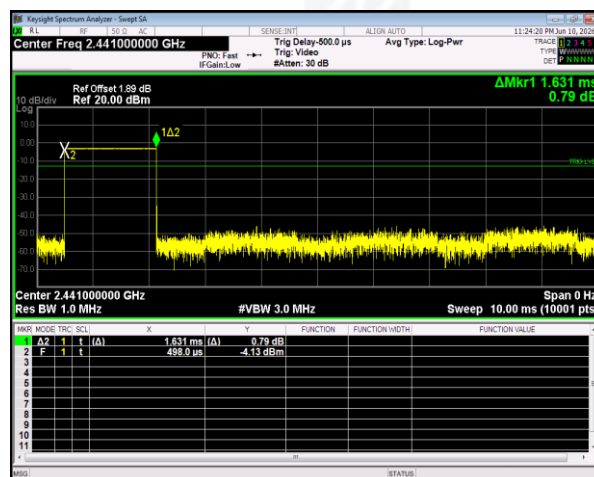
1-DH1 2441MHz One Burst



1-DH1 2441MHz Accumulated

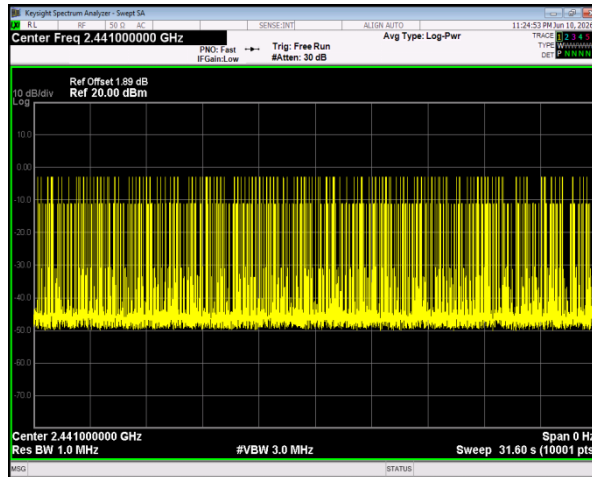


1-DH3 2441MHz One Burst

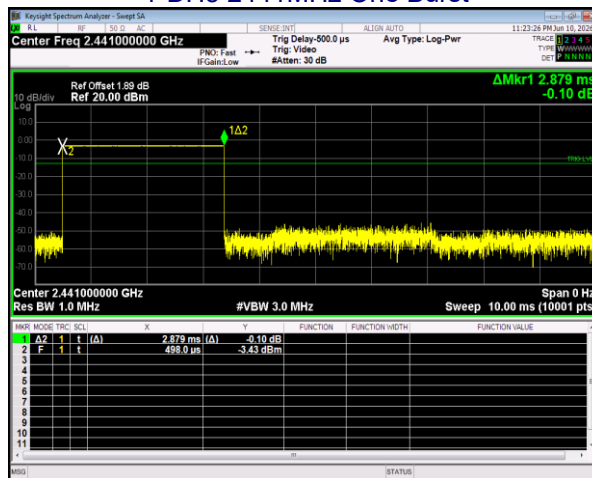




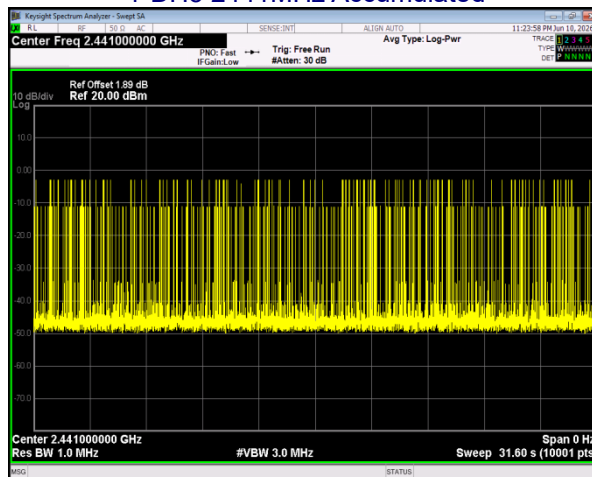
1-DH3 2441MHz Accumulated



1-DH5 2441MHz One Burst

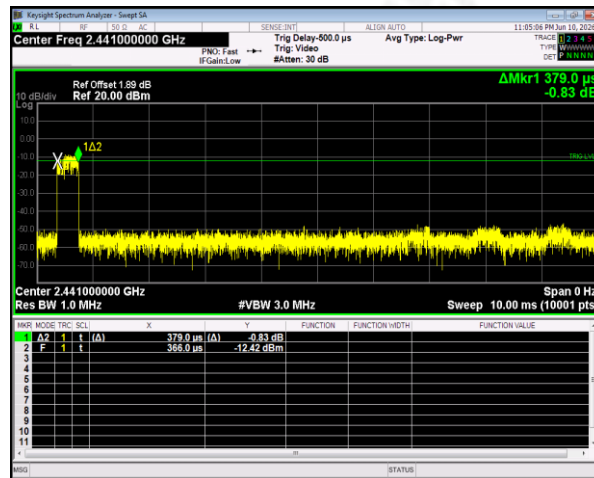


1-DH5 2441MHz Accumulated

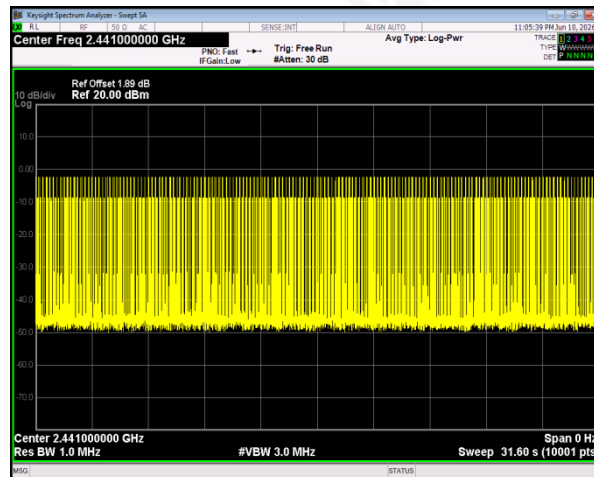




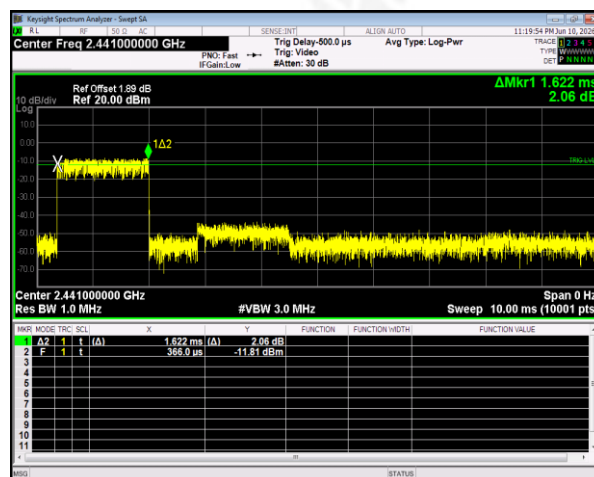
$\pi/4$ -DQPSK Test Plots 2-DH1 2441MHz One Burst



2-DH1 2441MHz Accumulated

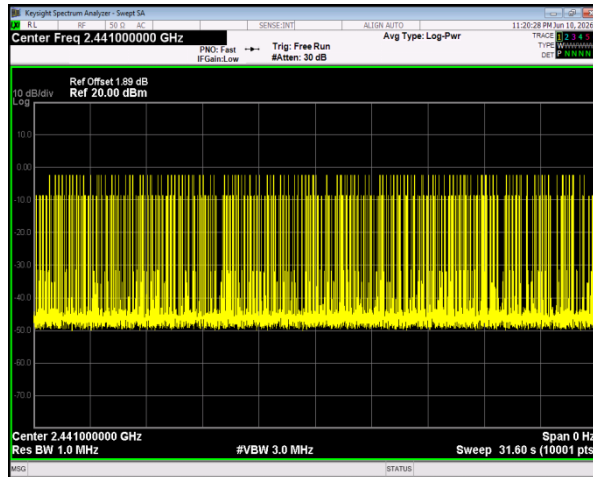


2-DH3 2441MHz One Burst

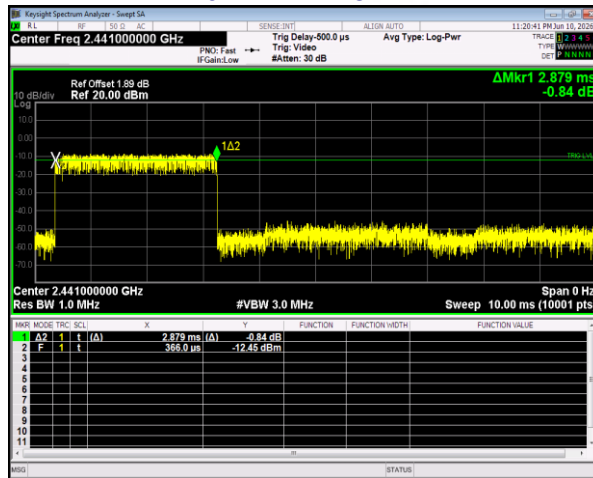




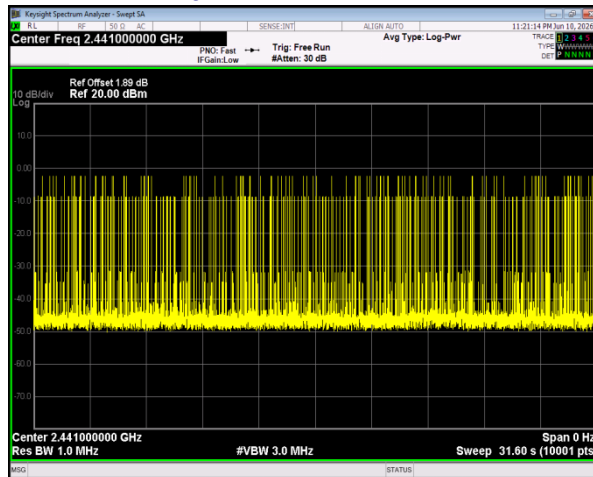
2-DH3 2441MHz Accumulated



2-DH5 2441MHz One Burst



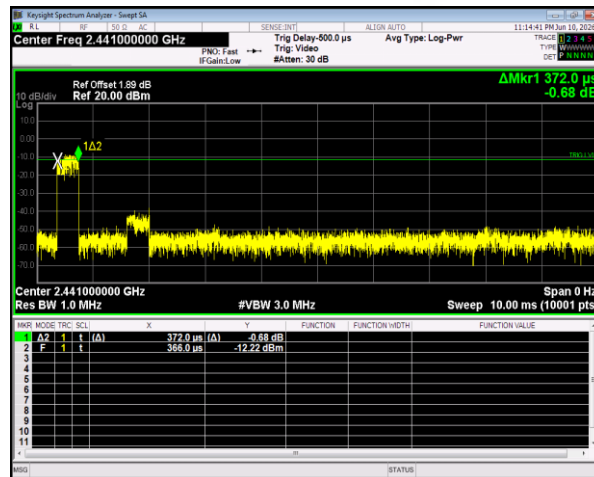
2-DH5 2441MHz Accumulated



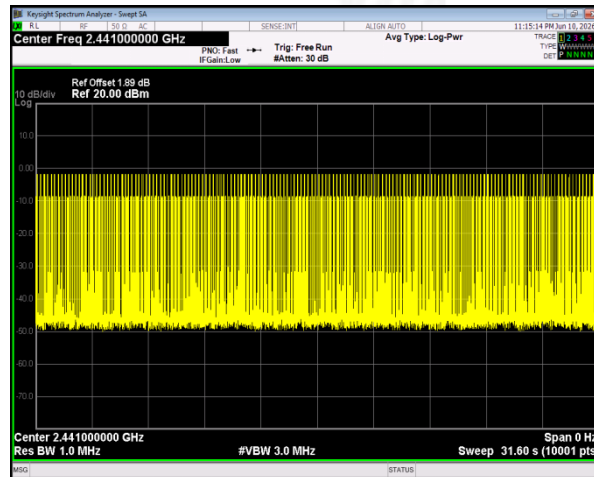


8-DPSK Test Plots

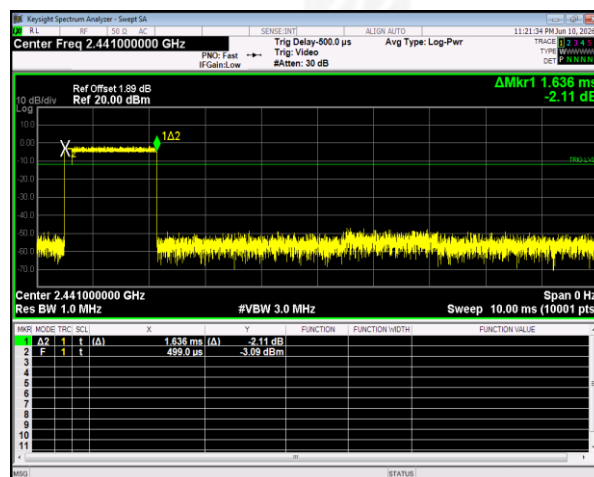
3-DH1 2441MHz One Burst



3-DH1 2441MHz Accumulated

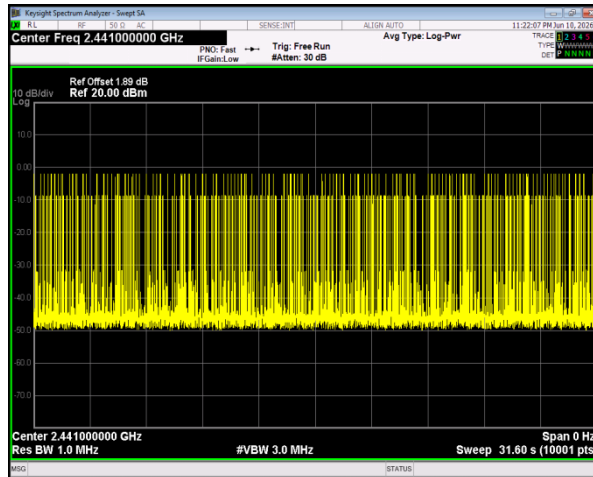


3-DH3 2441MHz One Burst

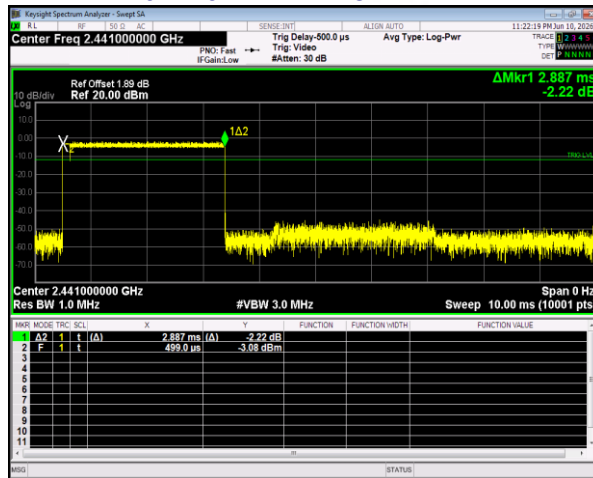




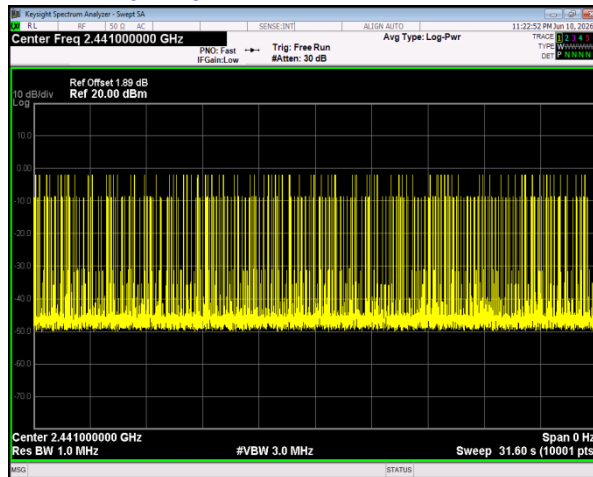
3-DH3 2441MHz Accumulated



3-DH5 2441MHz One Burst



3-DH5 2441MHz Accumulated





12. Antenna Requirement

Standard requirement:	FCC Part15 C Section 15.203 /247(c)
15.203 requirement: An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.	
15.247(c) (1)(i) requirement: (i) Systems operating in the 2400-2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.	
EUT Antenna:	
The antenna is PCB antenna, the best case gain of the antennas is -0.68dBi, reference to the appendix II for details.	



13. Test Setup Photo

Reference to the appendix I for details.

14. EUT Constructional Details

Reference to the appendix II for details.

***** END OF REPORT *****