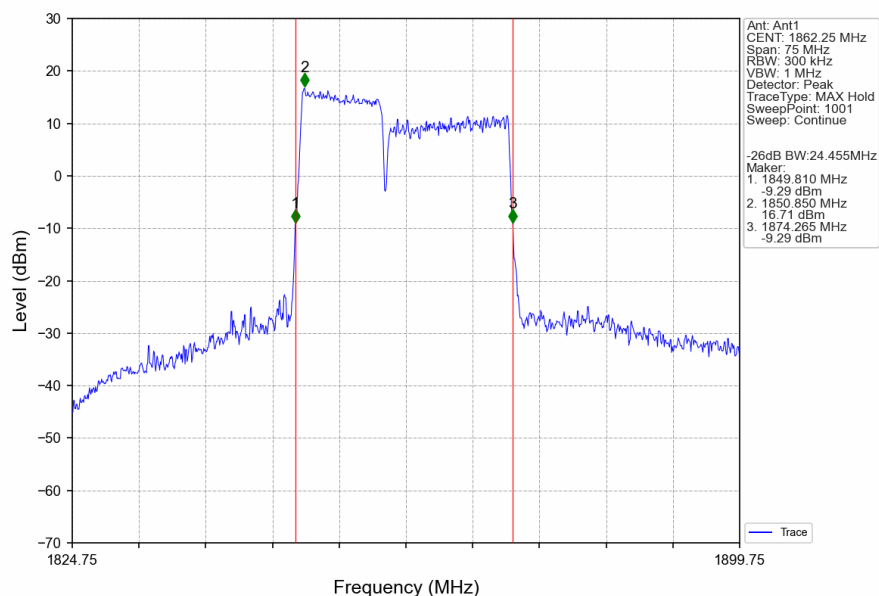
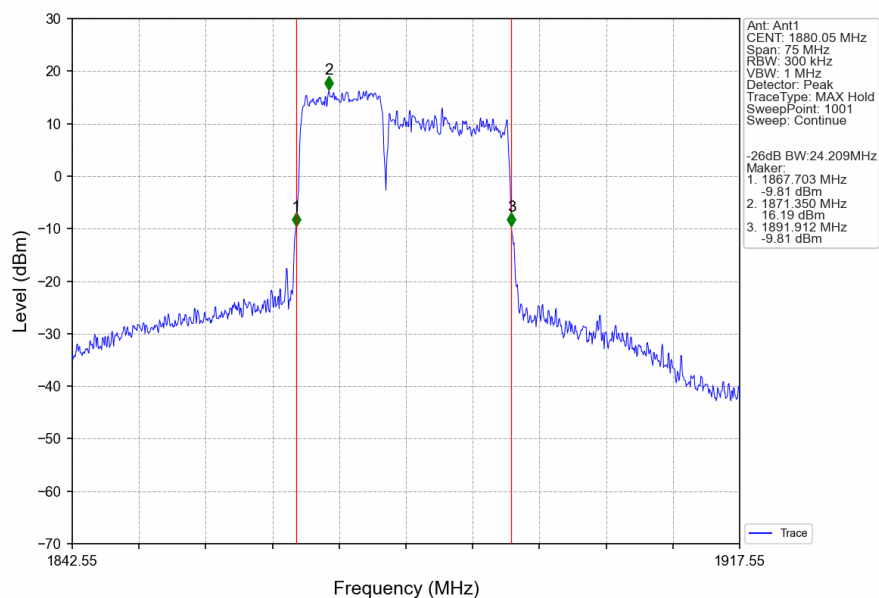


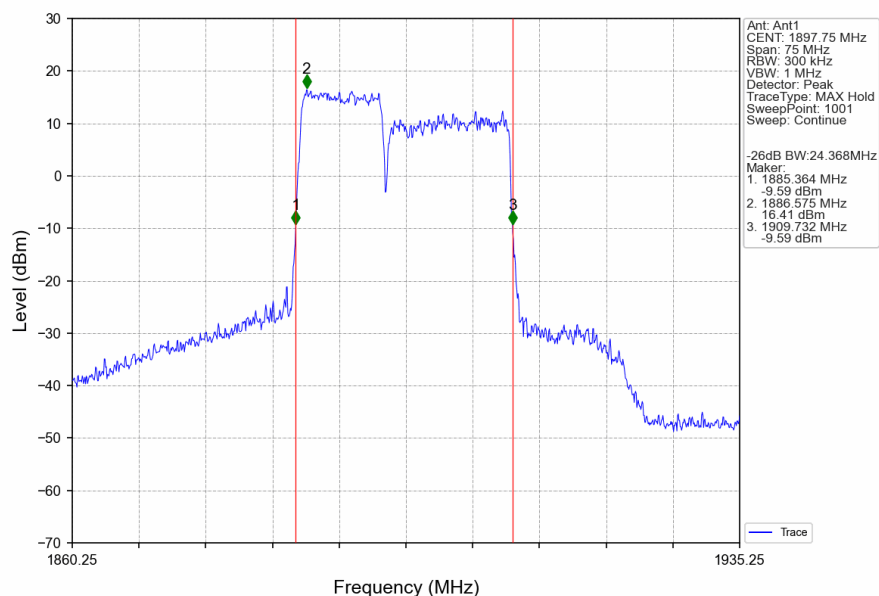
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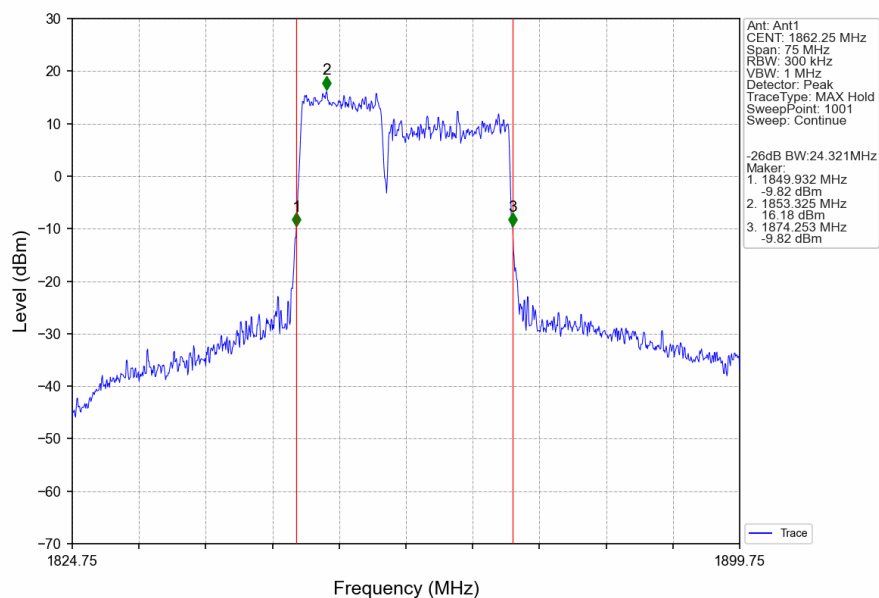
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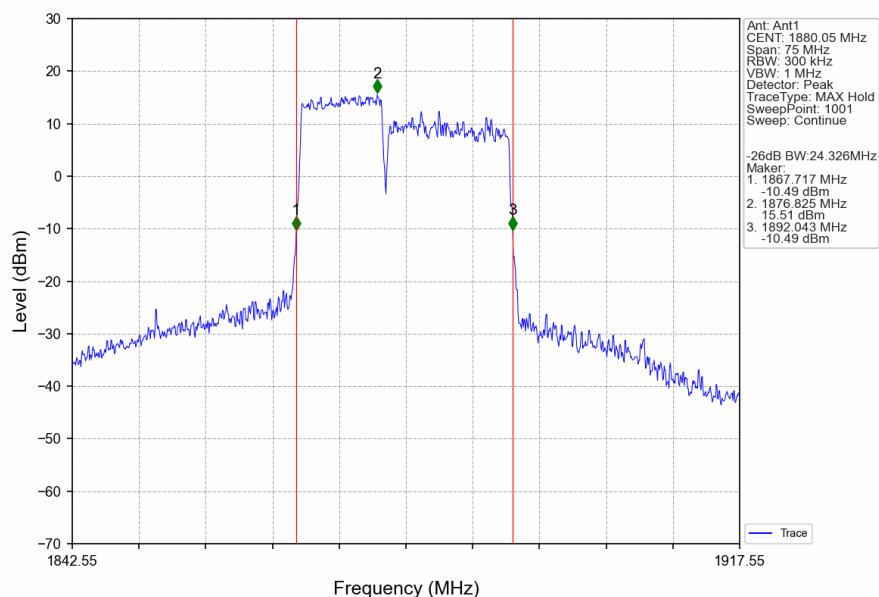
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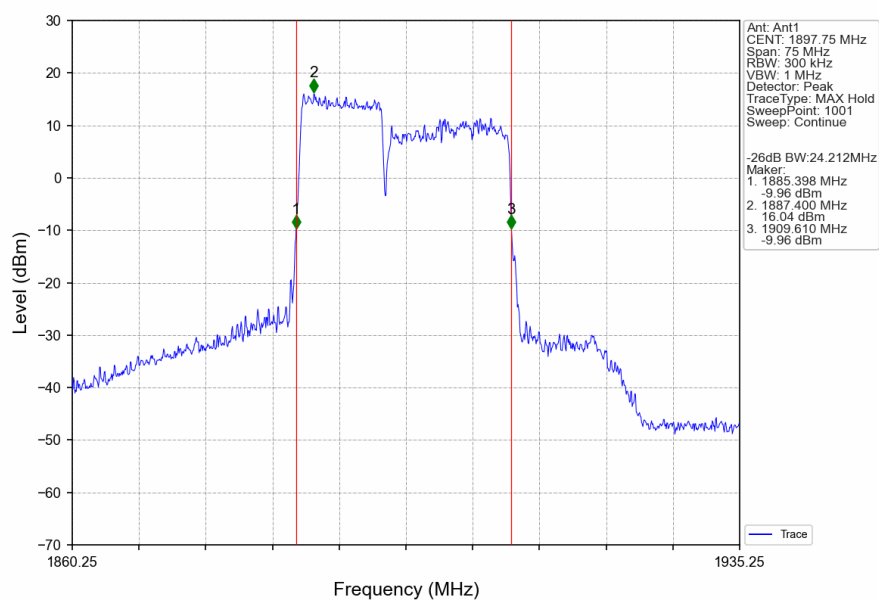
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CA\_2C\_SISO\_NTNV\_CC1:10 CC2:15MHz\_CC1:16QAM CC2:16QAM\_CC1:1872.8 CC2:1884.8MHz\_CC1: 50@0 CC2: 75@0



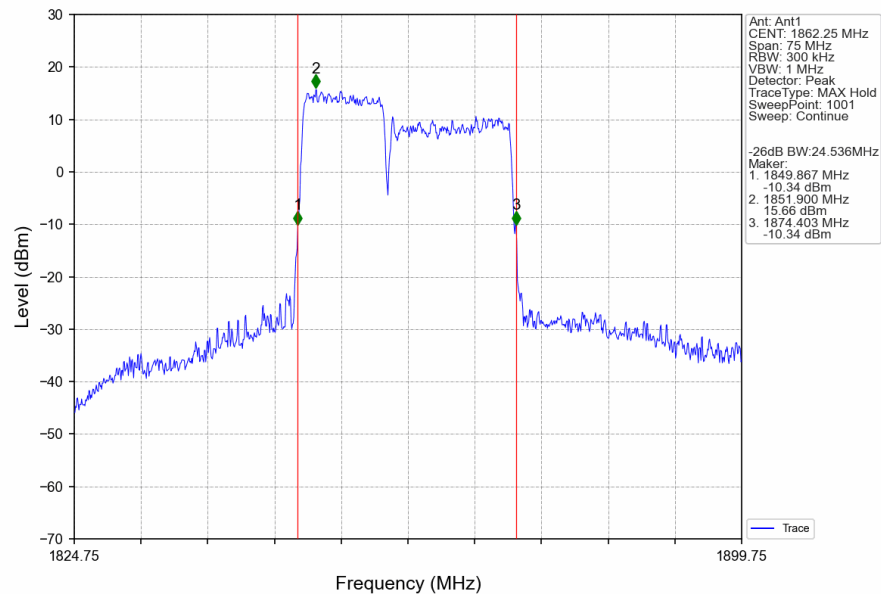
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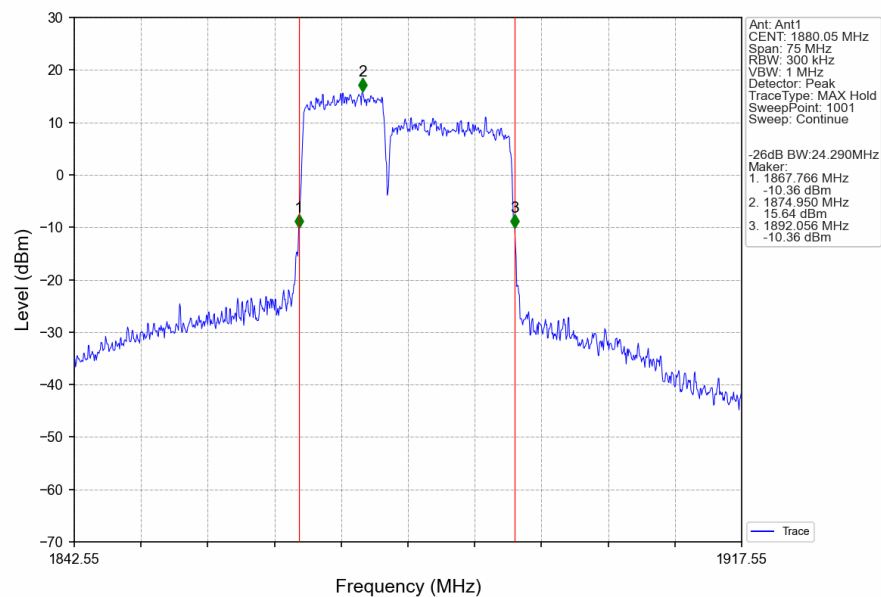


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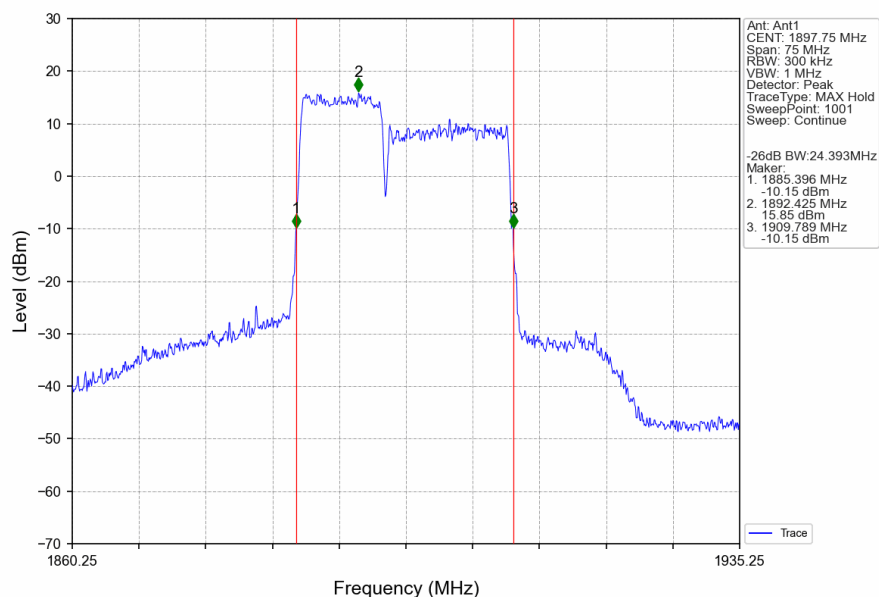
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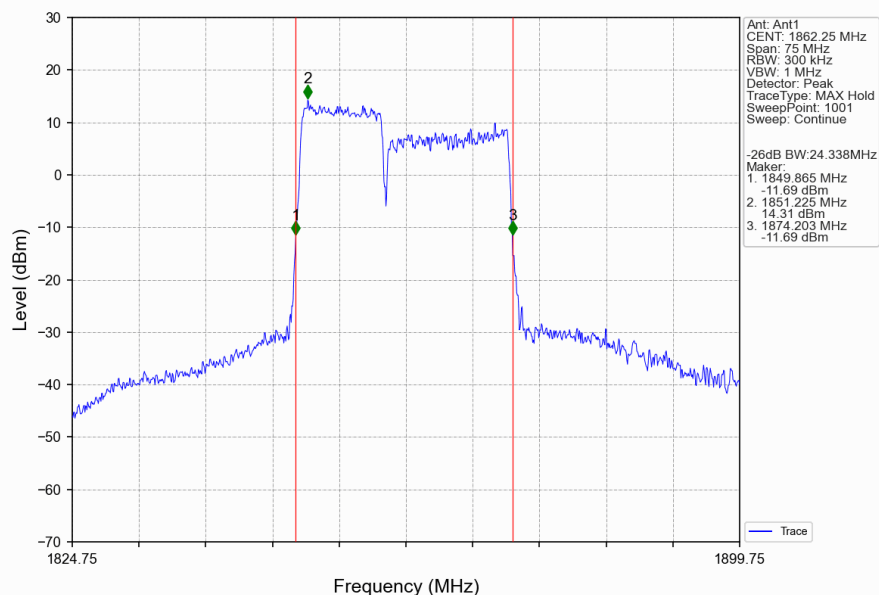
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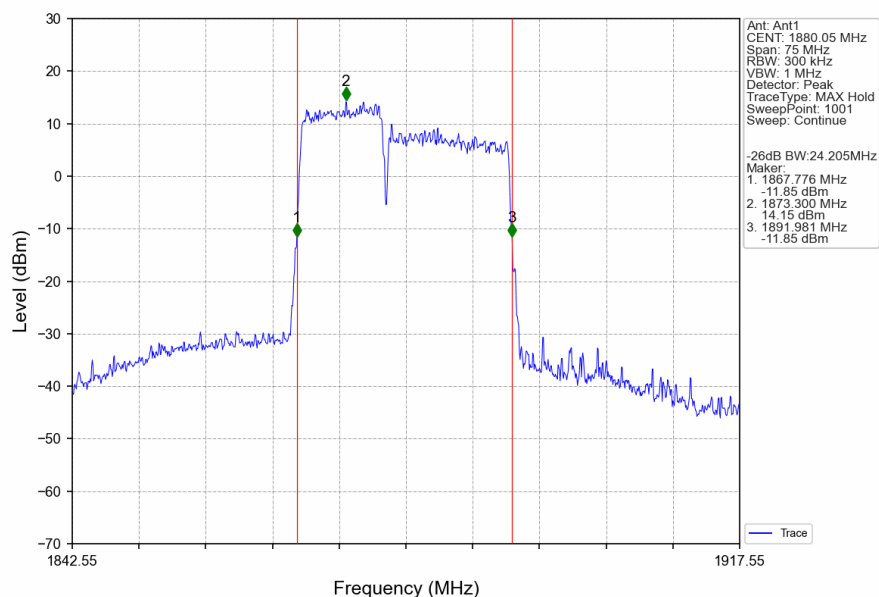
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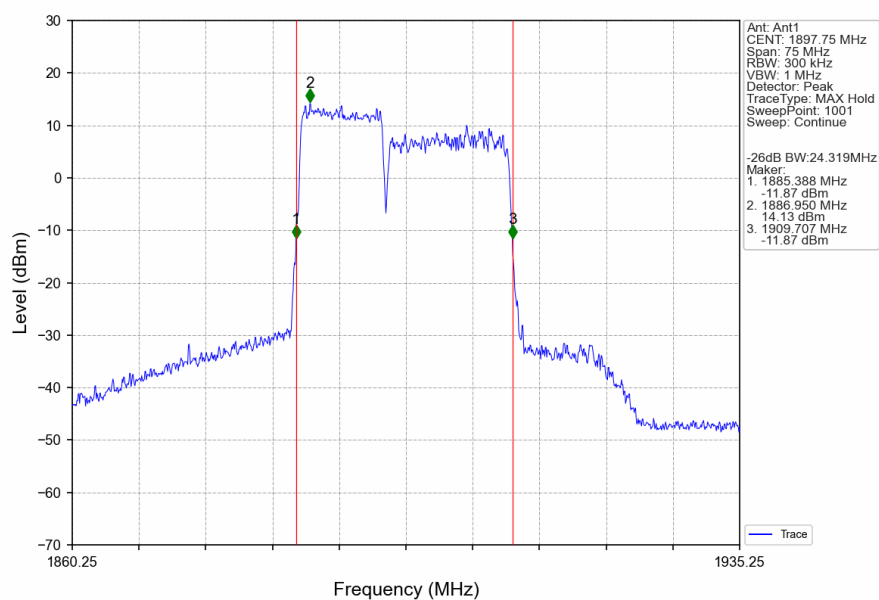
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CA\_2C\_SISO\_NTNV\_CC1:10 CC2:15MHz\_CC1:256QAM CC2:256QAM\_CC1:1872.8 CC2:1884.8MHz\_CC1: 50@0 CC2: 75@0



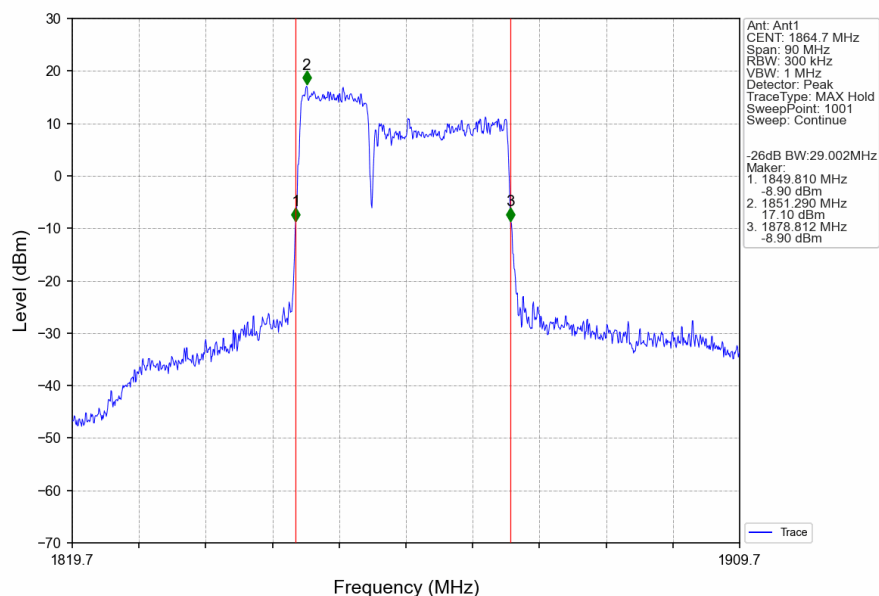
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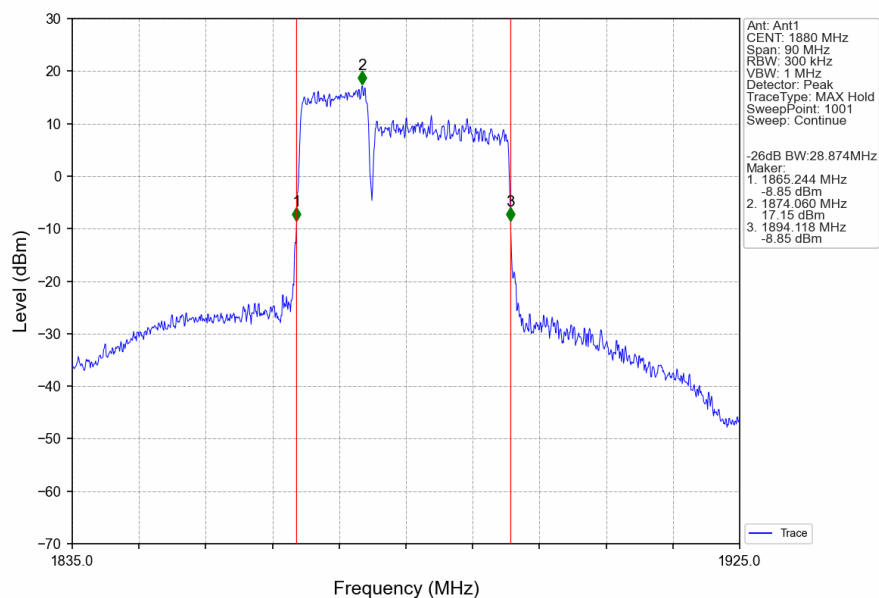


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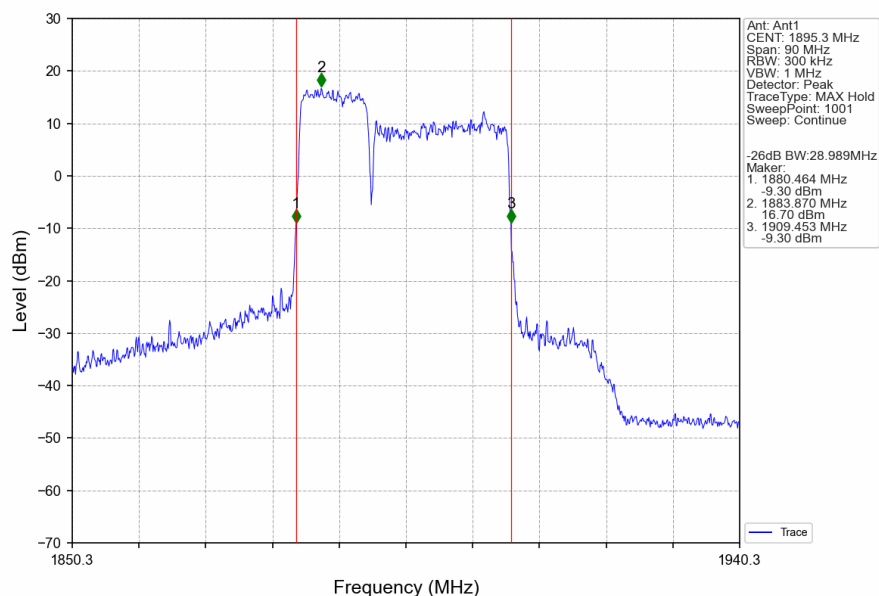
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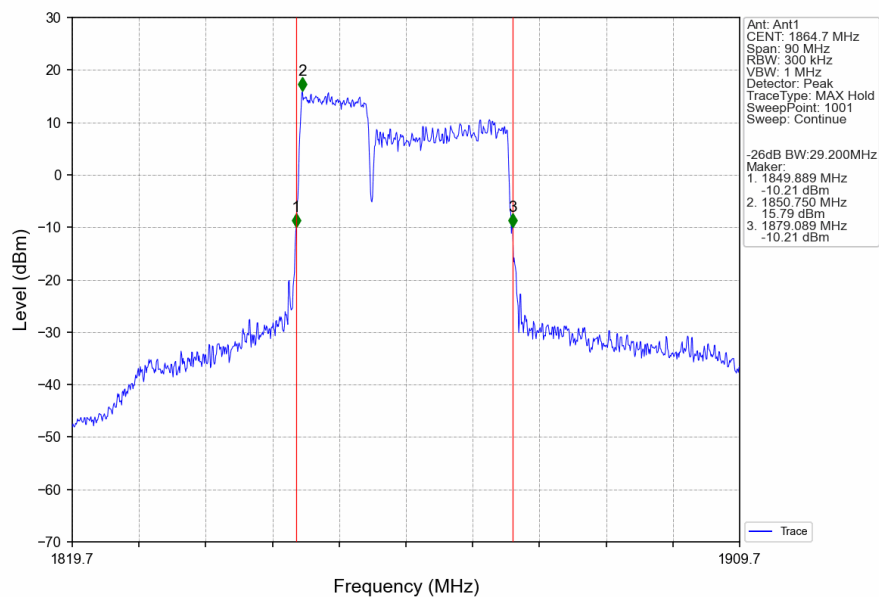
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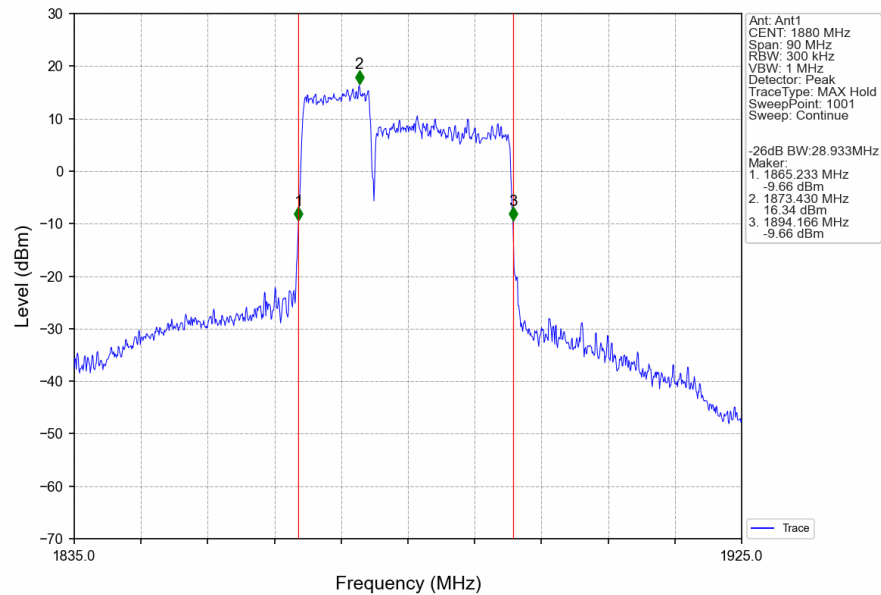
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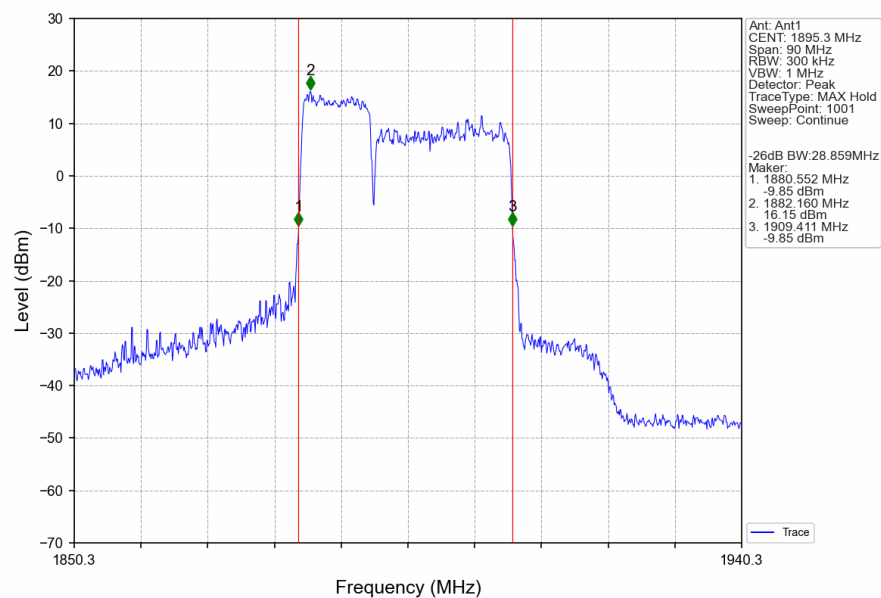
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CA\_2C\_SISO\_NTNV\_CC1:10 CC2:20MHz\_CC1:16QAM CC2:16QAM\_CC1:1870.3 CC2:1884.7MHz\_CC1: 50@0 CC2: 100@0



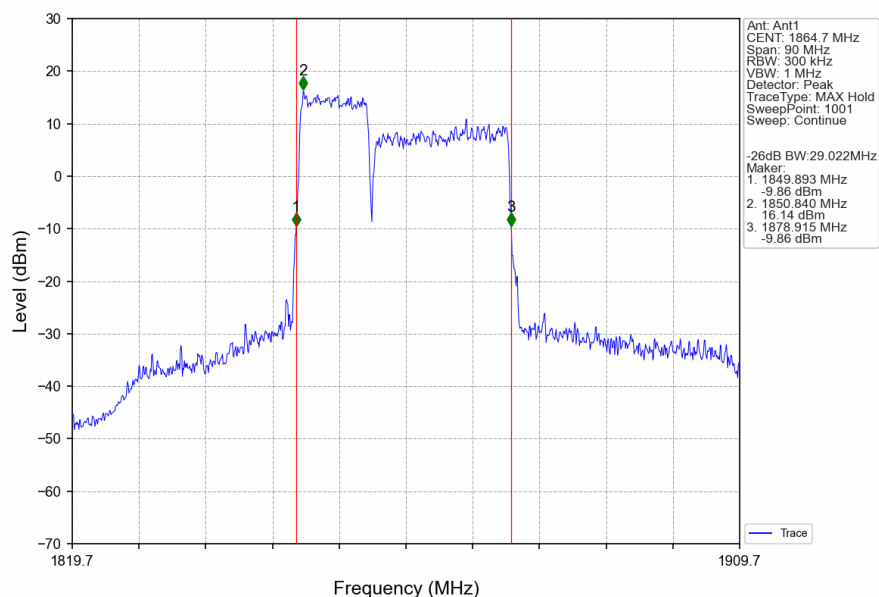
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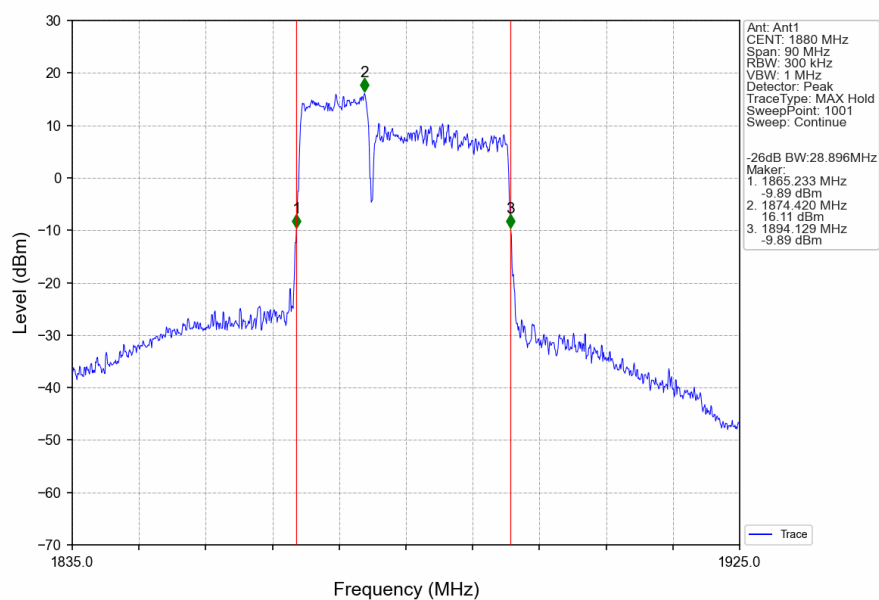


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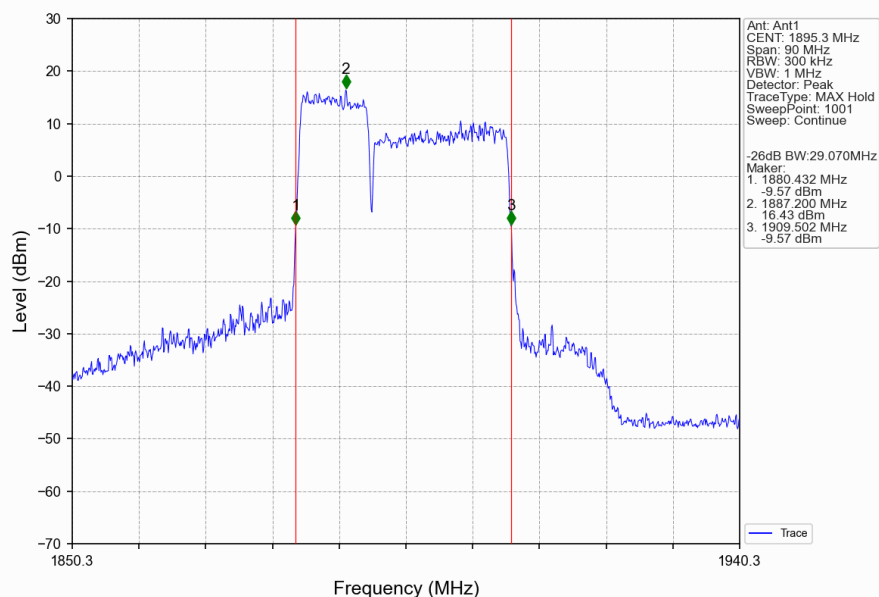
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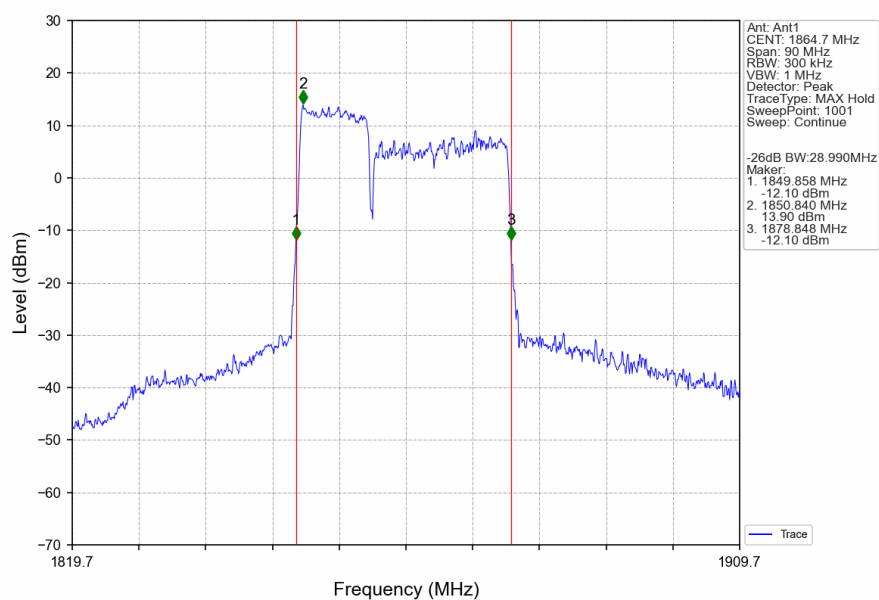


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CA\_2C\_SISO\_NTNV\_CC1:10 CC2:20MHz\_CC1:64QAM CC2:64QAM\_CC1:1885.6 CC2:1900MHz\_CC1: 50@0 CC2:  
100@0



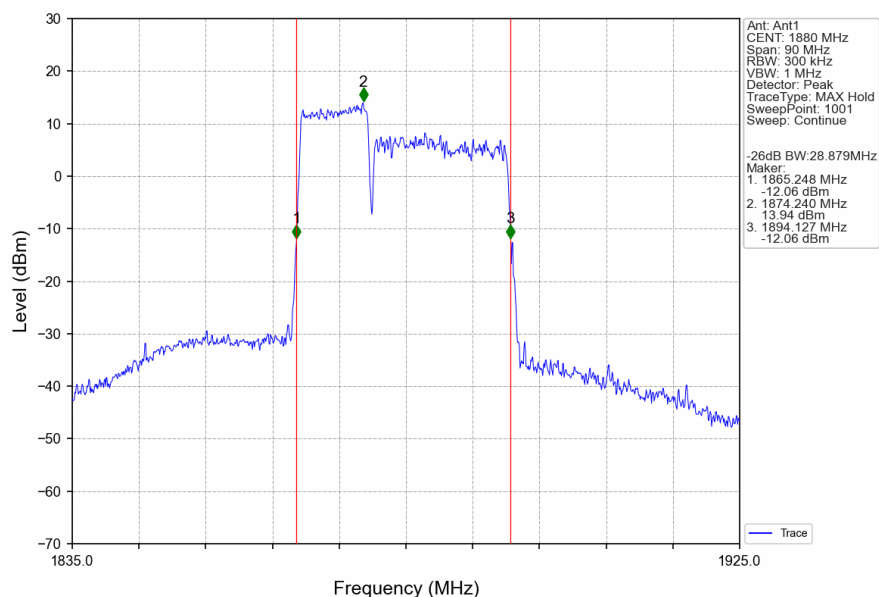
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100@0



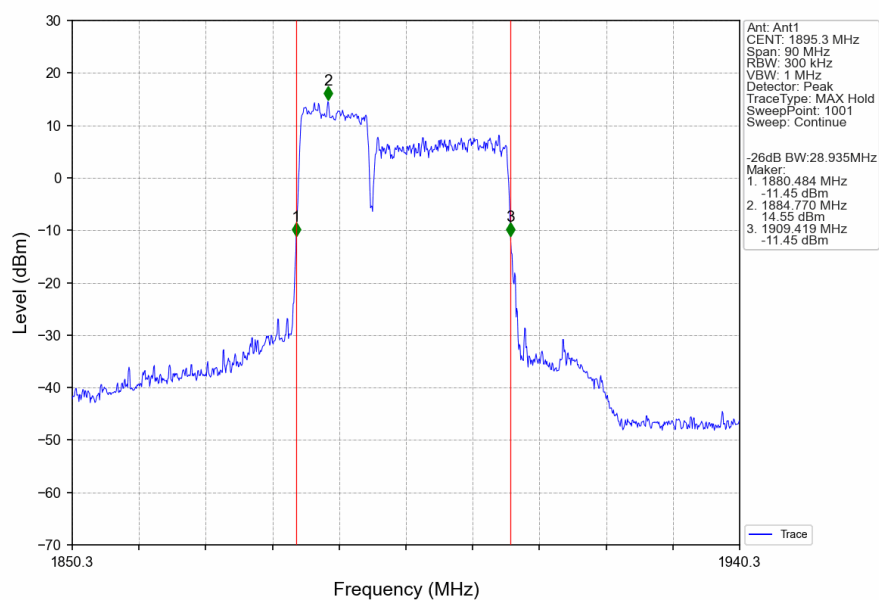


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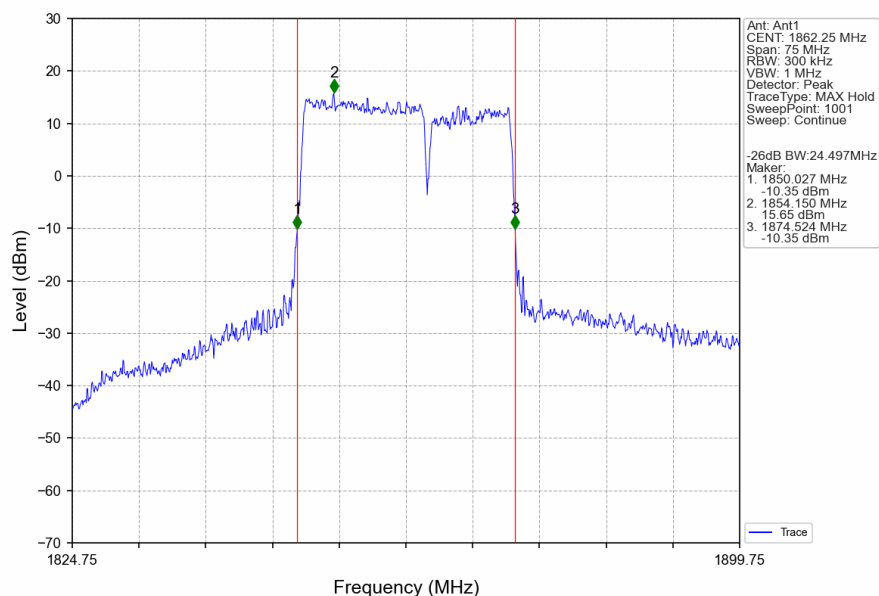
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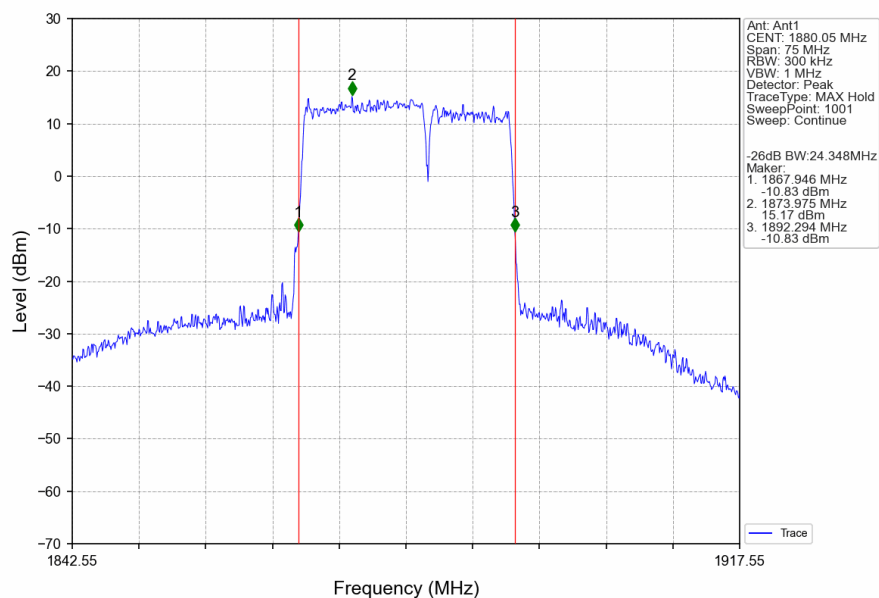


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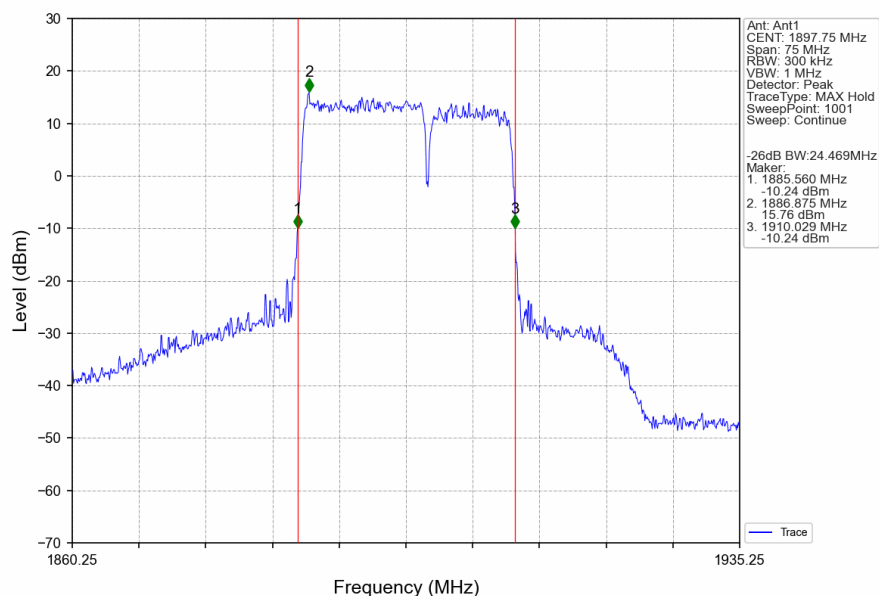
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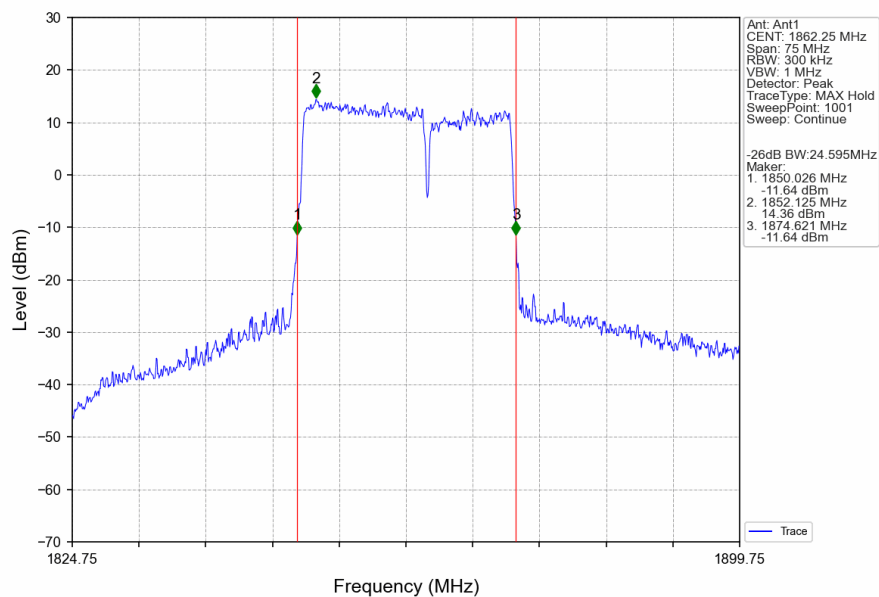
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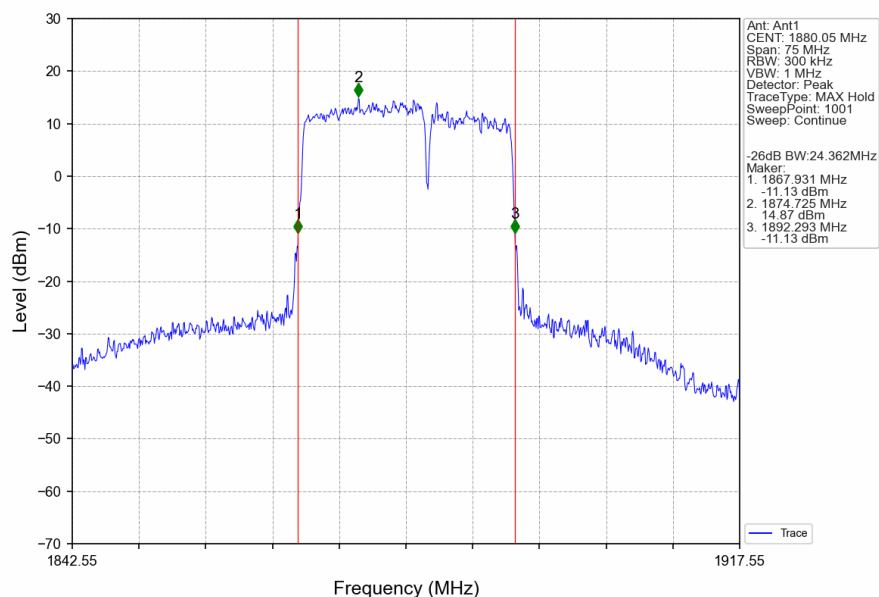
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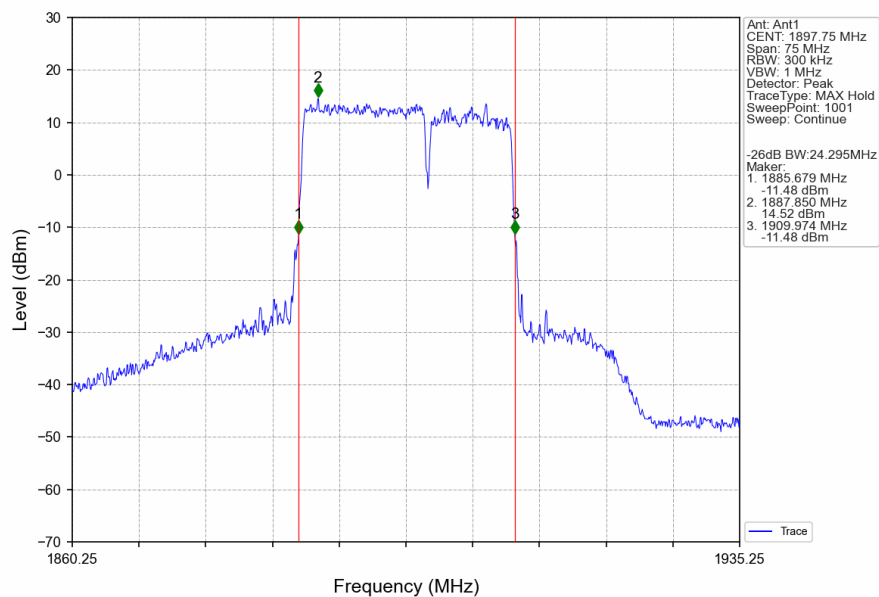
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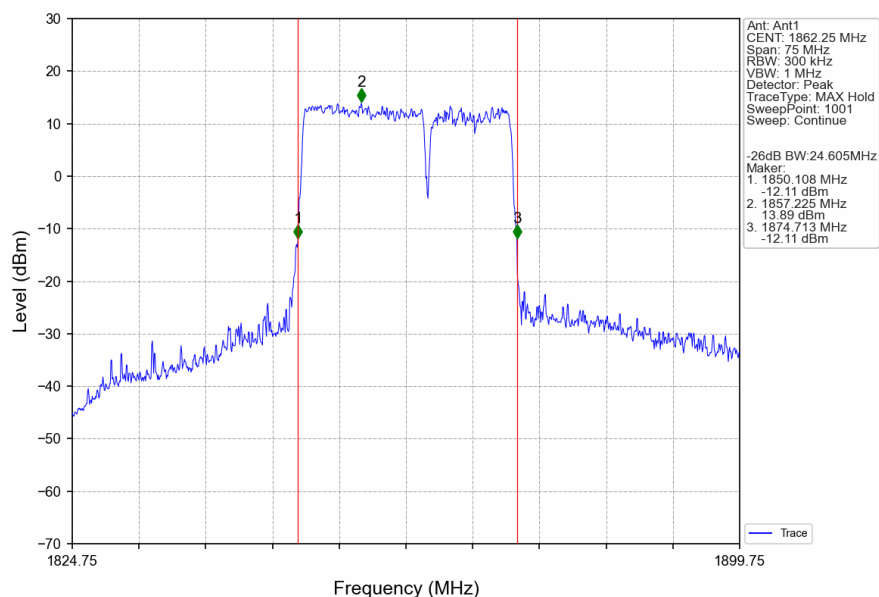
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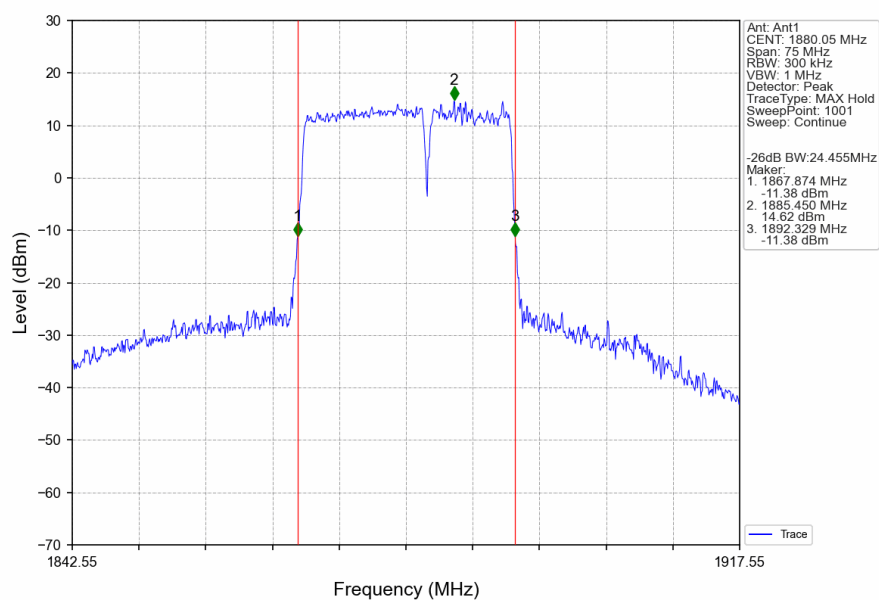
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CA\_2C\_SISO\_NTNV\_CC1:15 CC2:10MHz\_CC1:64QAM CC2:64QAM\_CC1:1857.5 CC2:1869.5MHz\_CC1: 75@0 CC2: 50@0



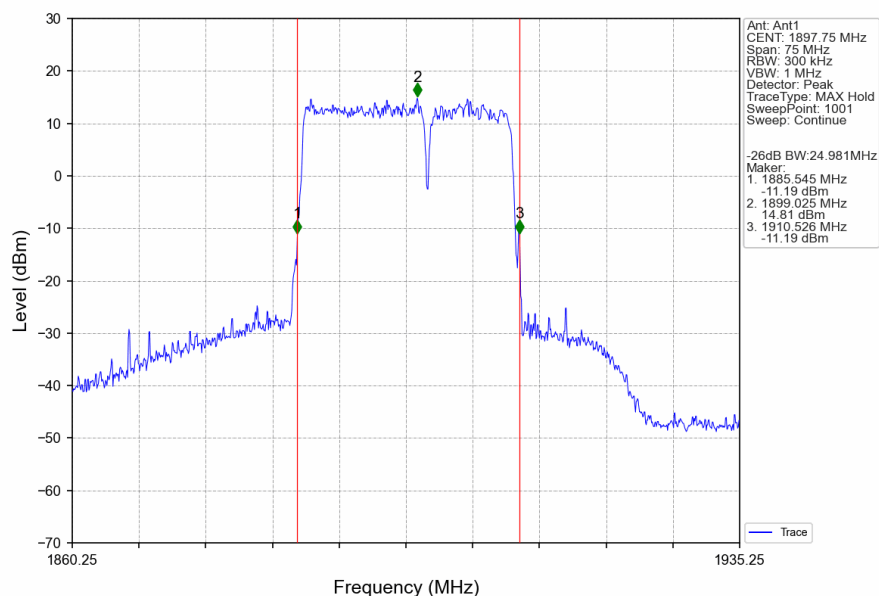
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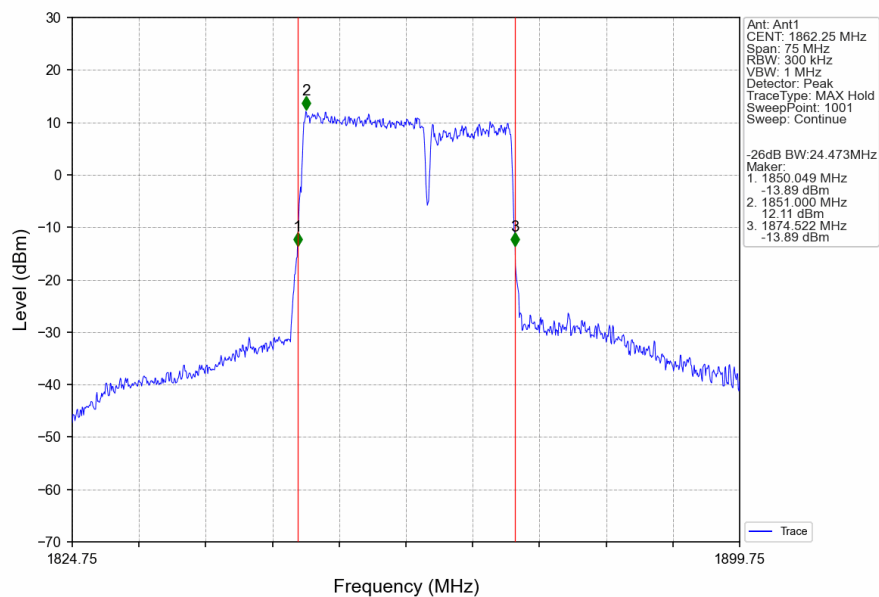


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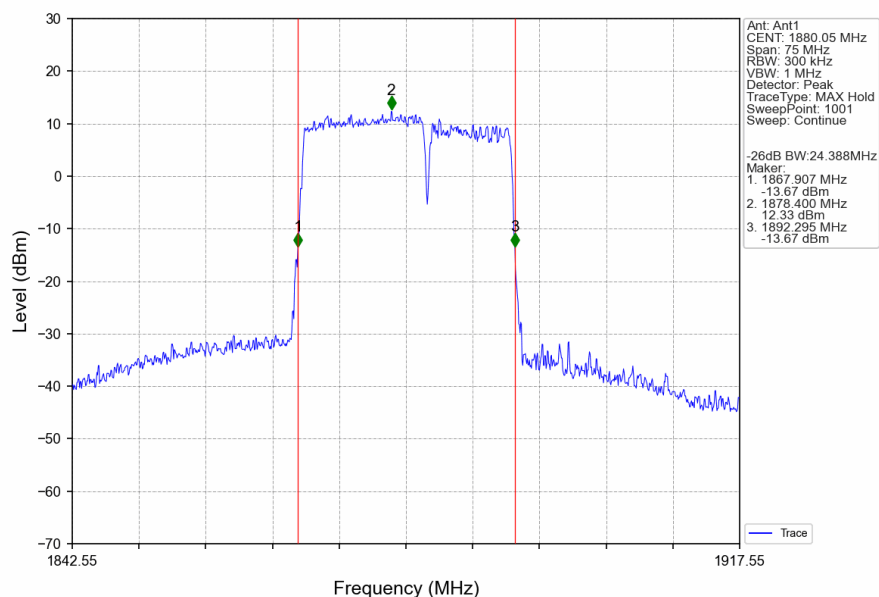
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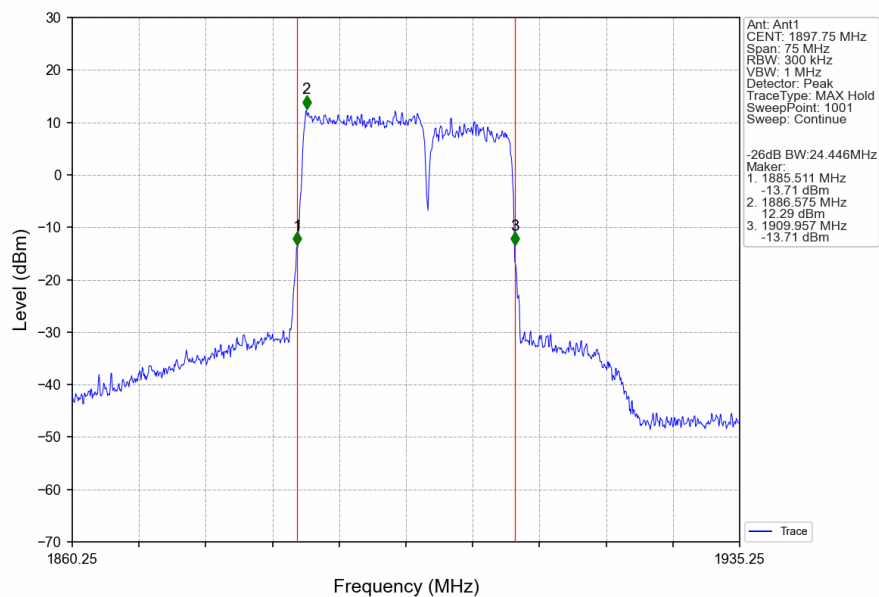
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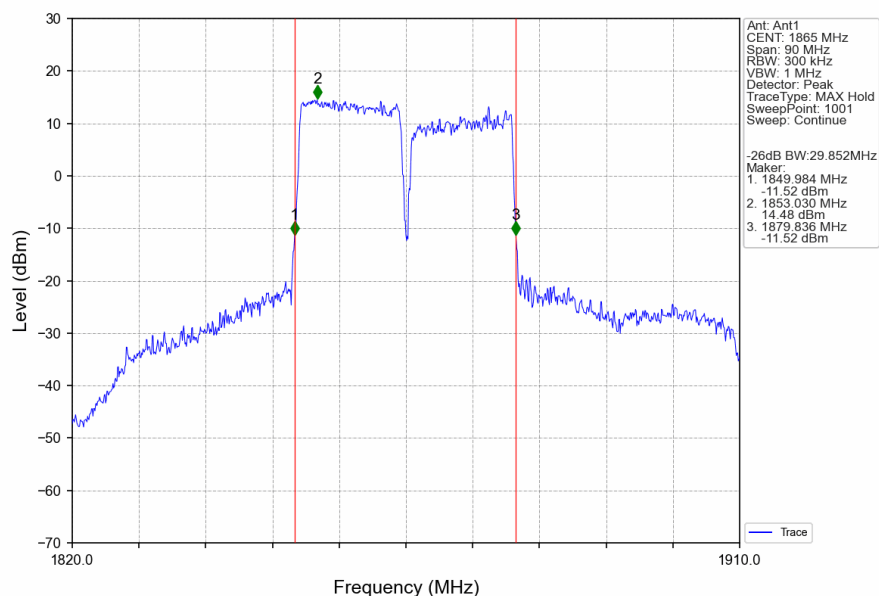
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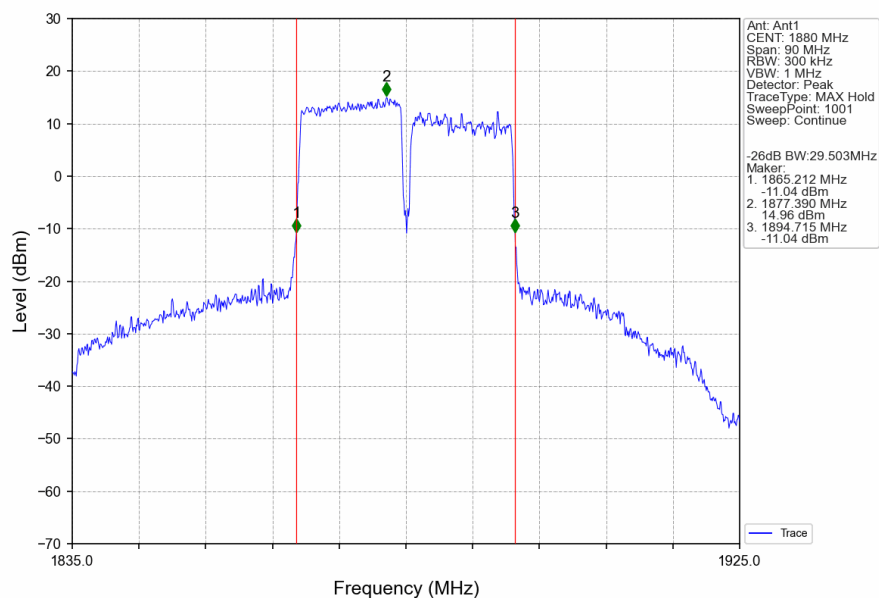
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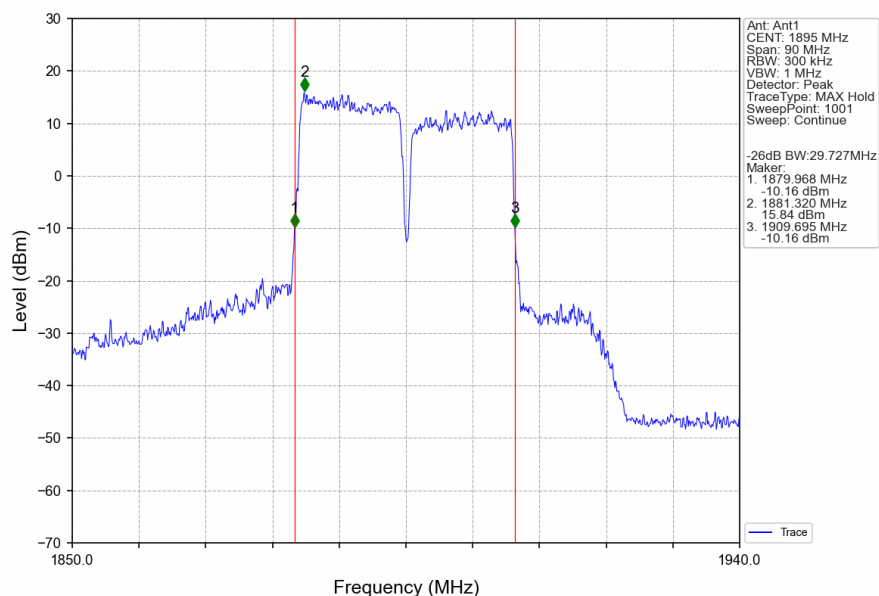
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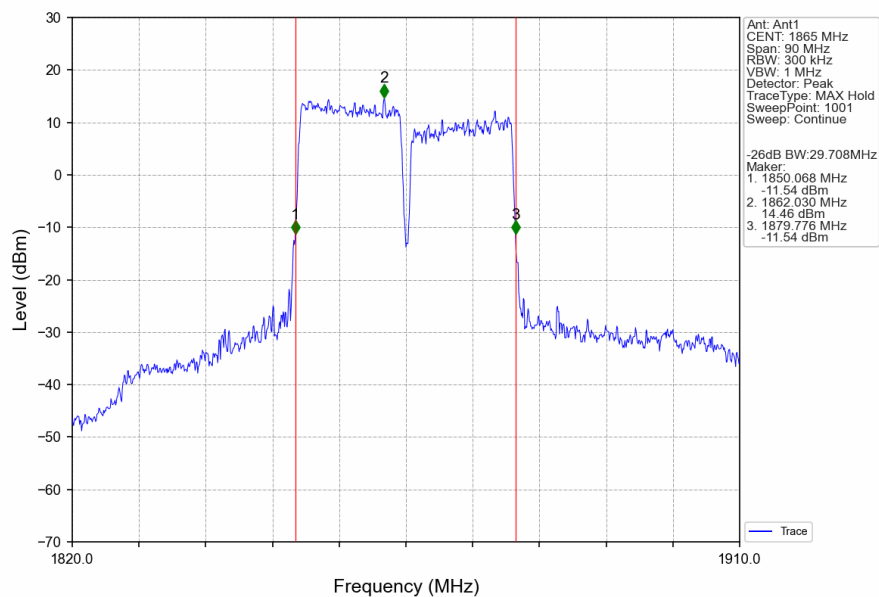
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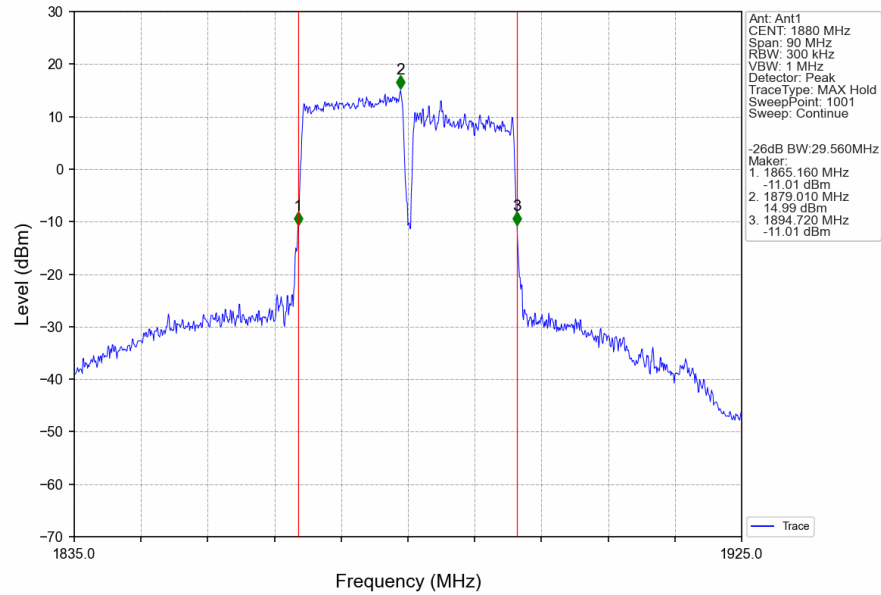
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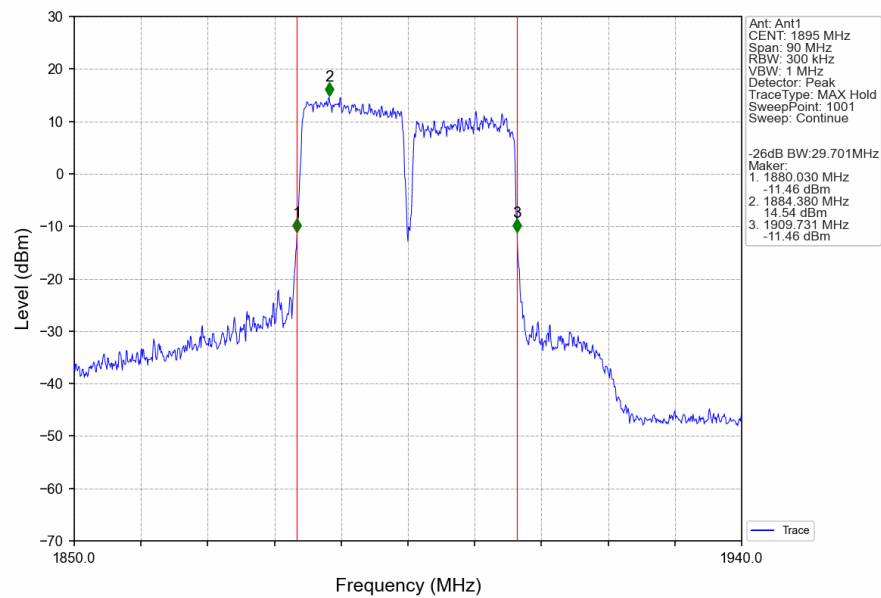
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CA\_2C\_SISO\_NTNV\_CC1:15 CC2:15MHz\_CC1:16QAM CC2:16QAM\_CC1:1872.5 CC2:1887.5MHz\_CC1: 75@0 CC2:  
75@0



CA\_2C\_SISO\_NTNV\_CC1:15 CC2:15MHz\_CC1:16QAM CC2:16QAM\_CC1:1887.5 CC2:1902.5MHz\_CC1: 75@0 CC2:  
75@0





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