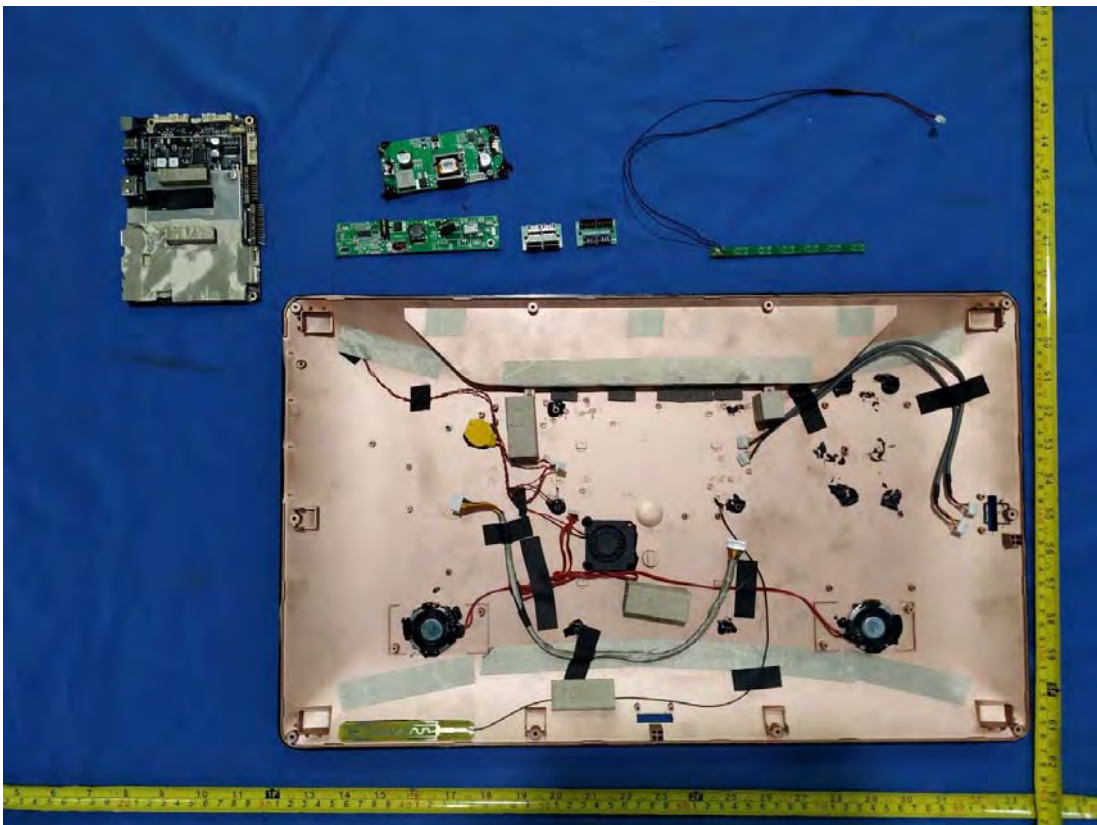
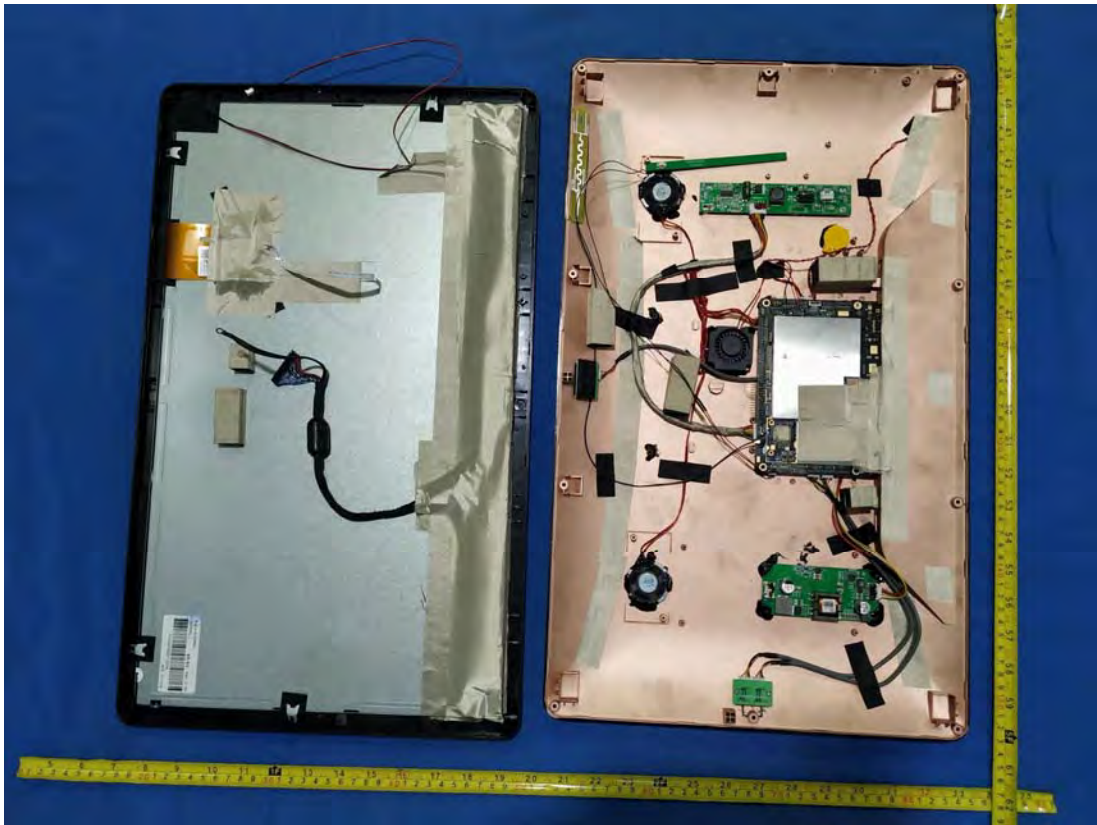
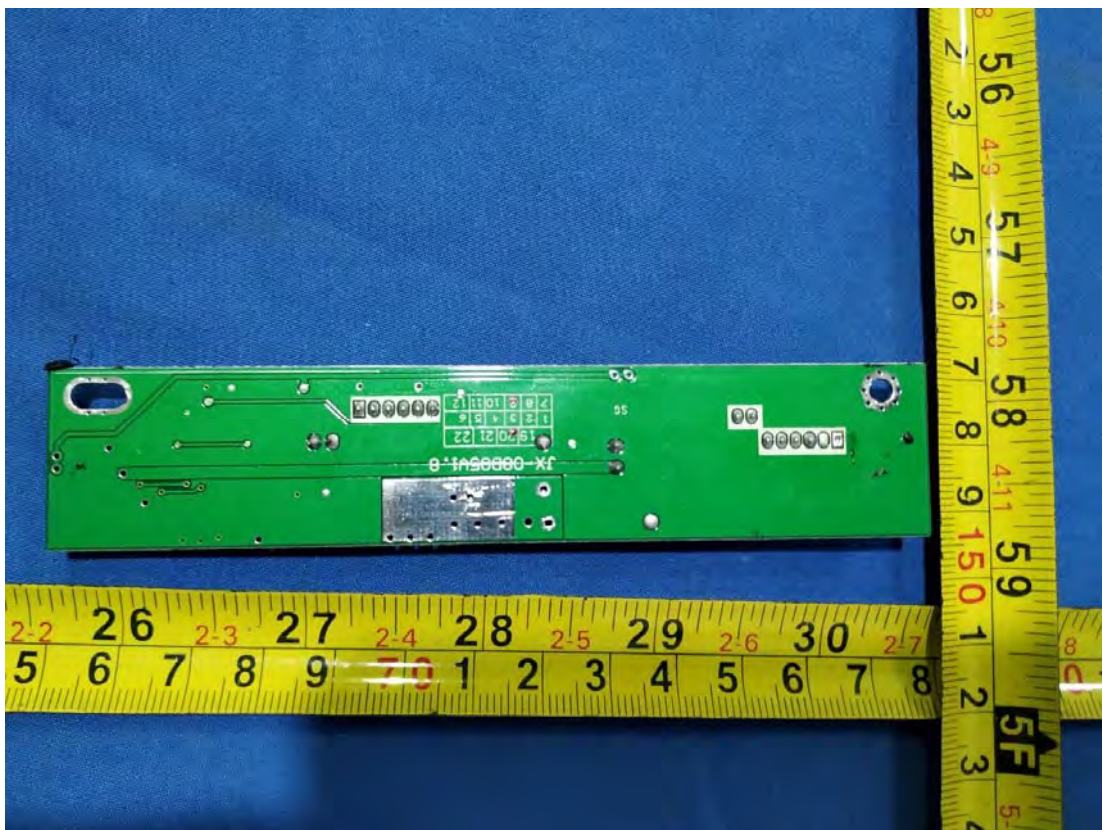
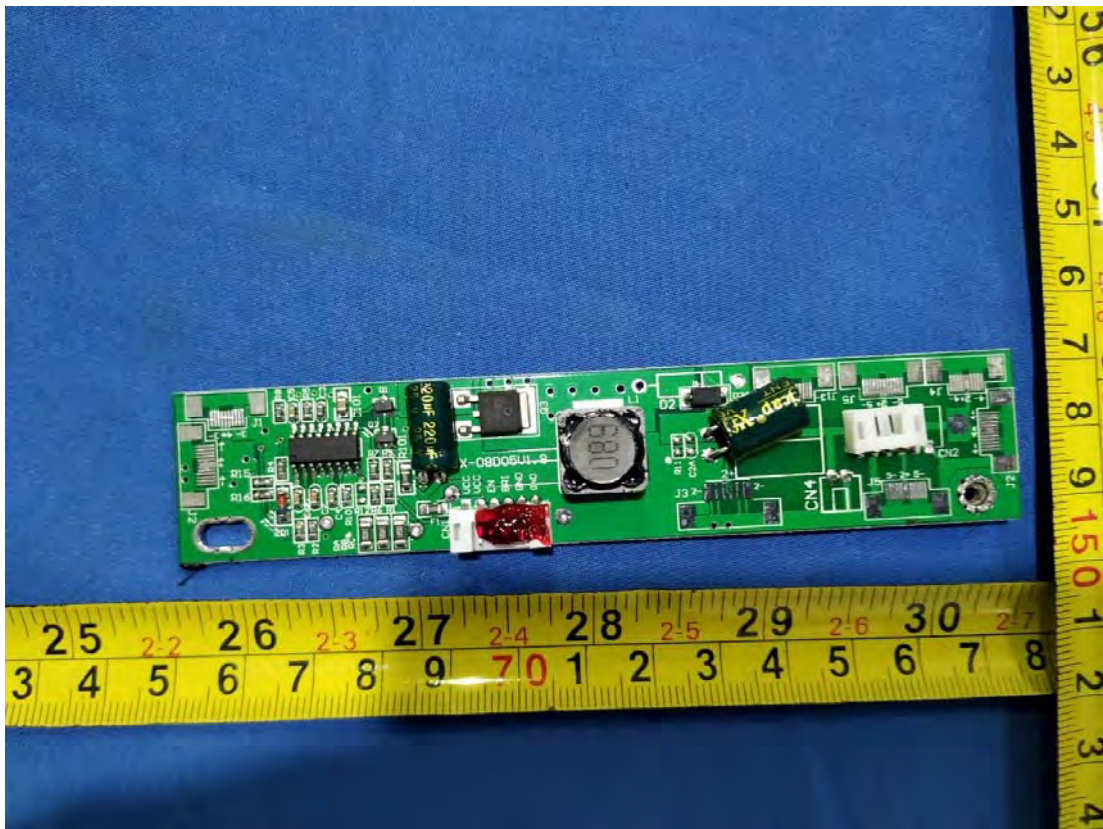
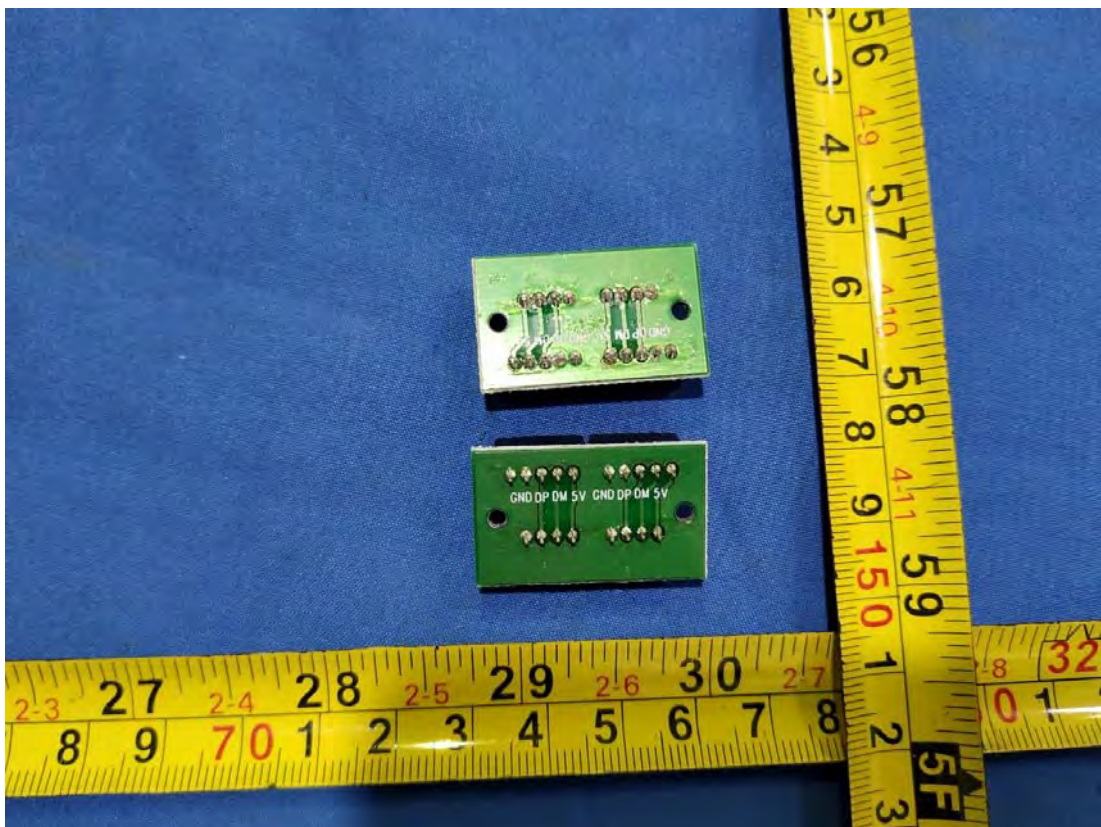
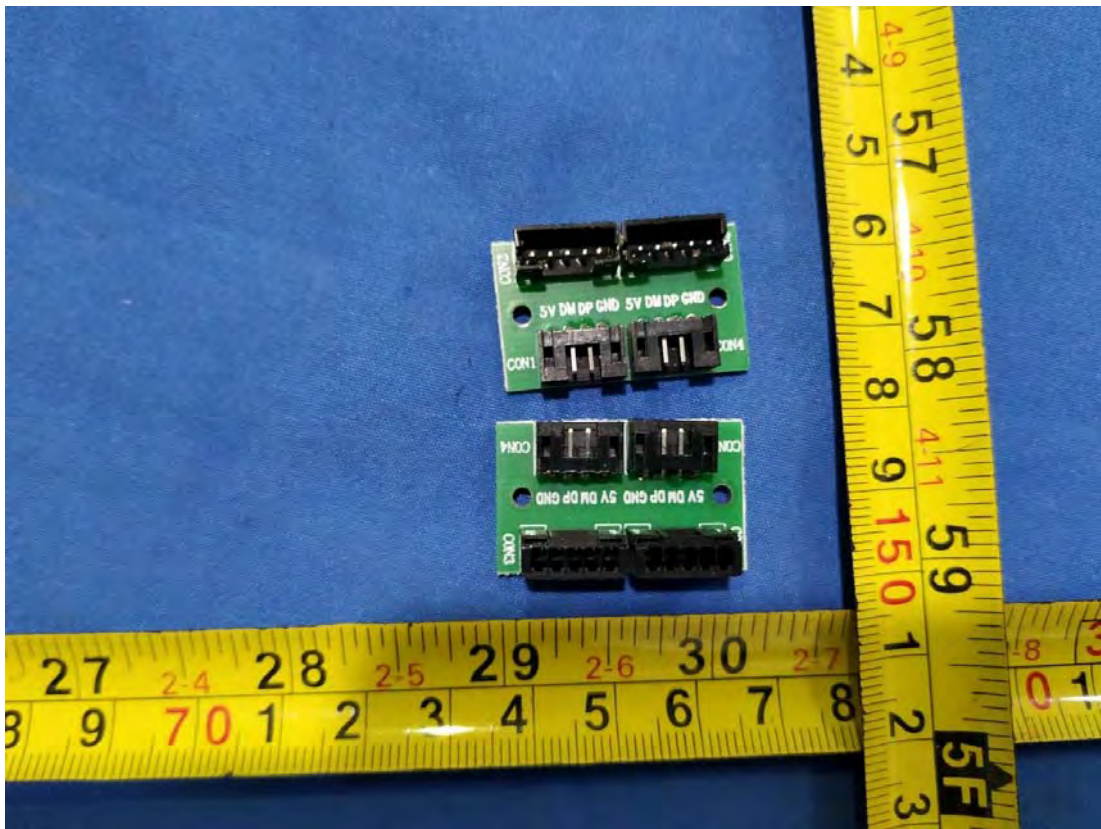


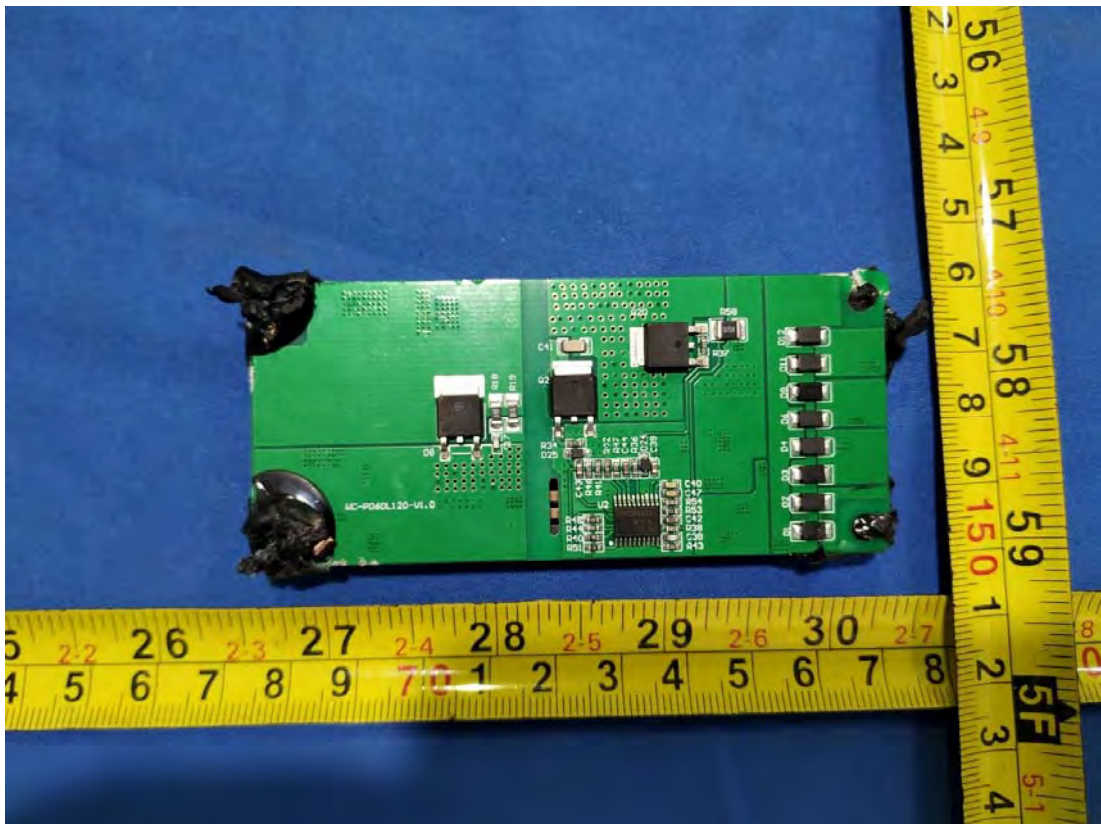
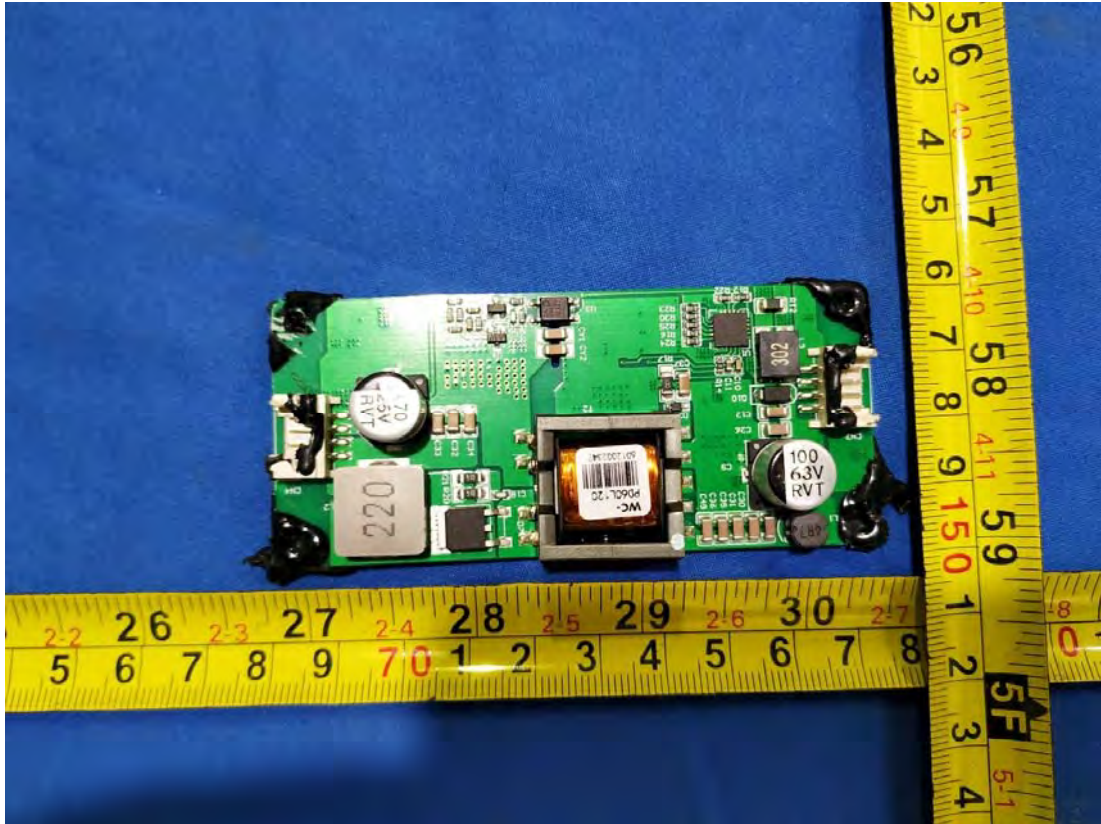
EXHIBIT C - EUT INTERNAL PHOTOGRAPHS

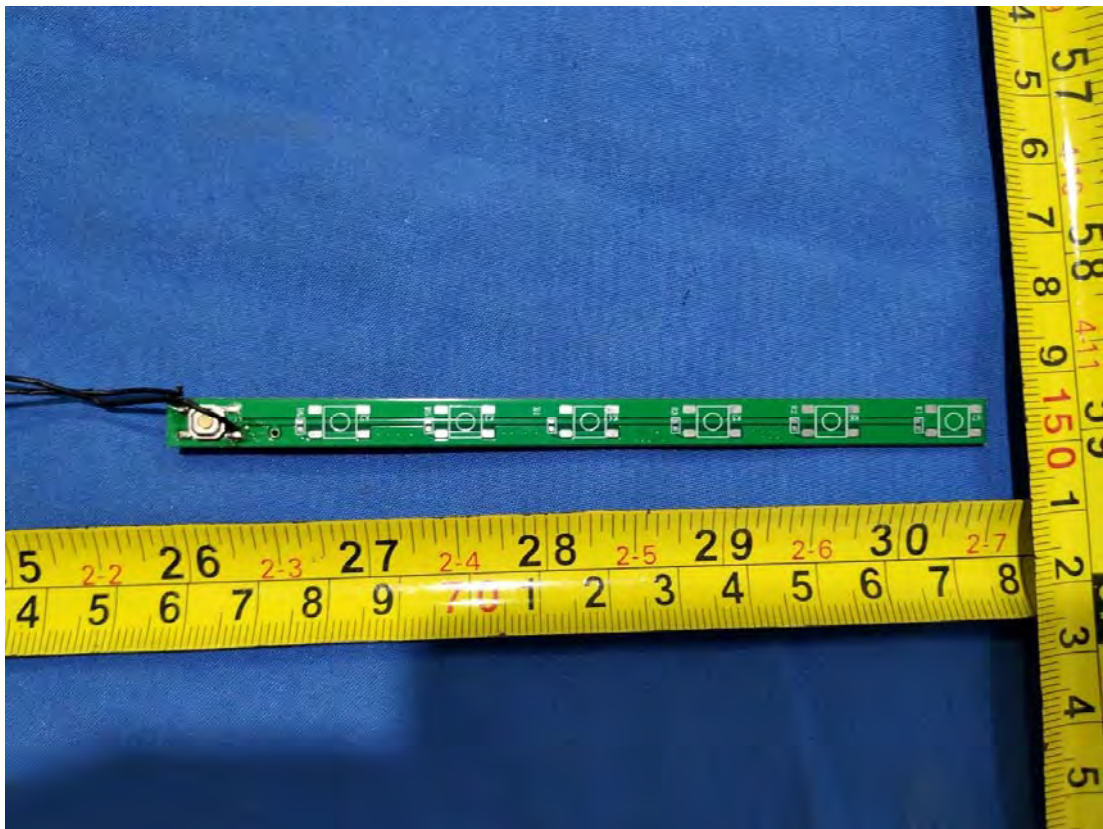
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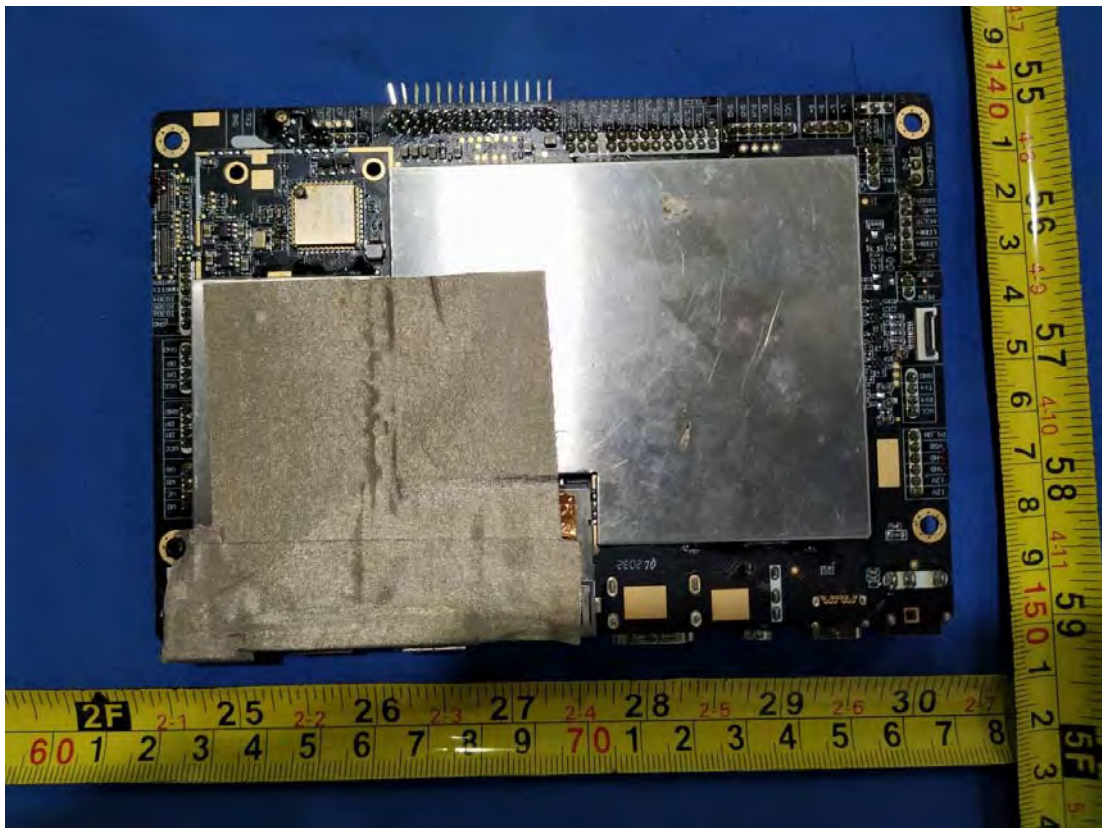
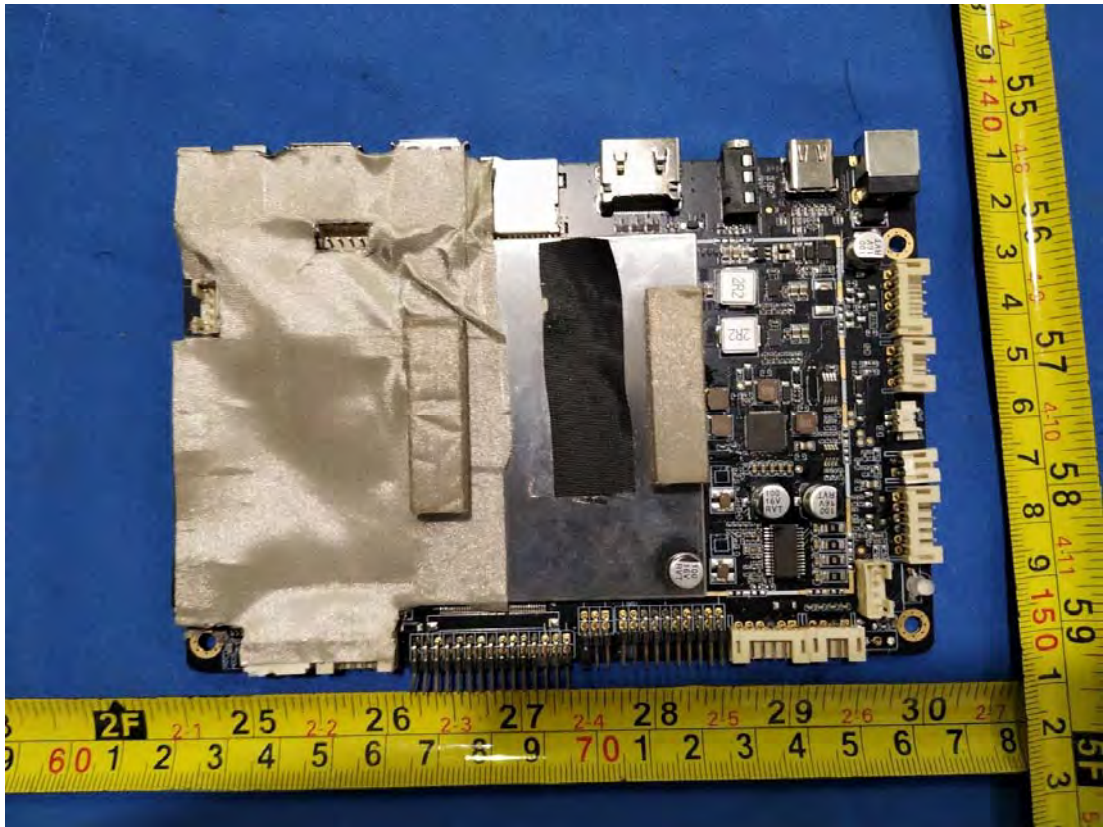


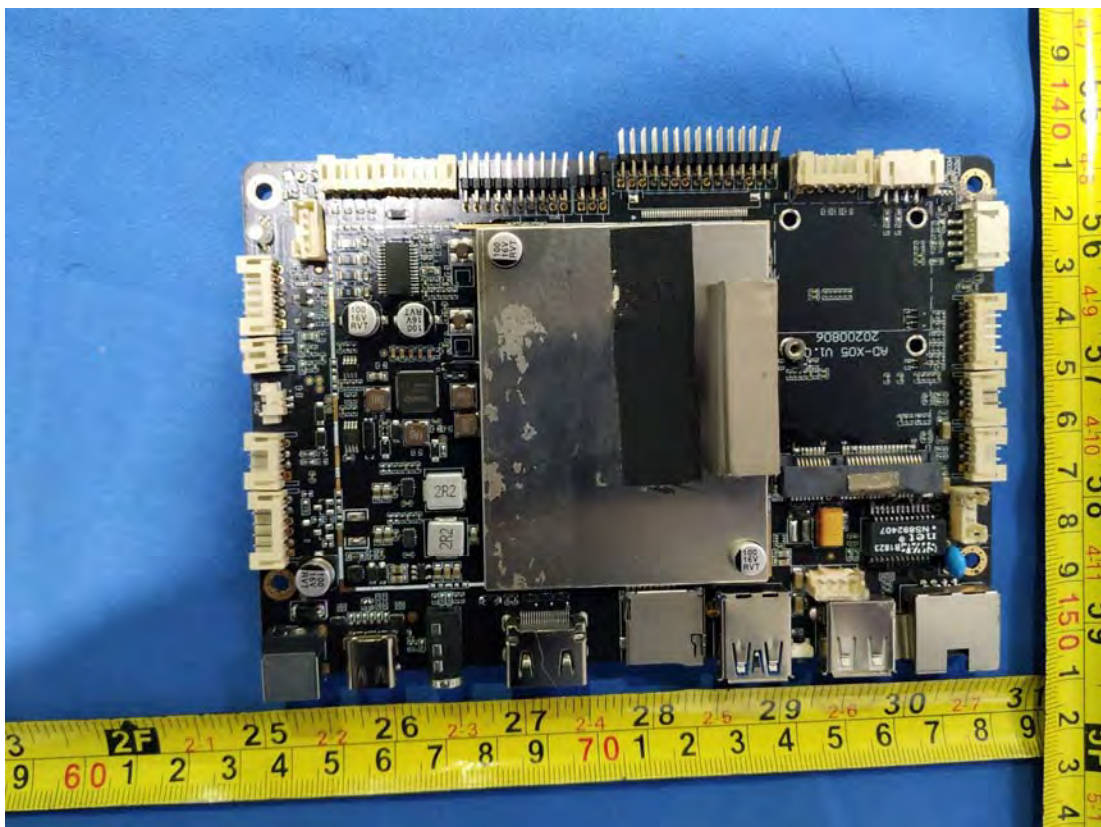
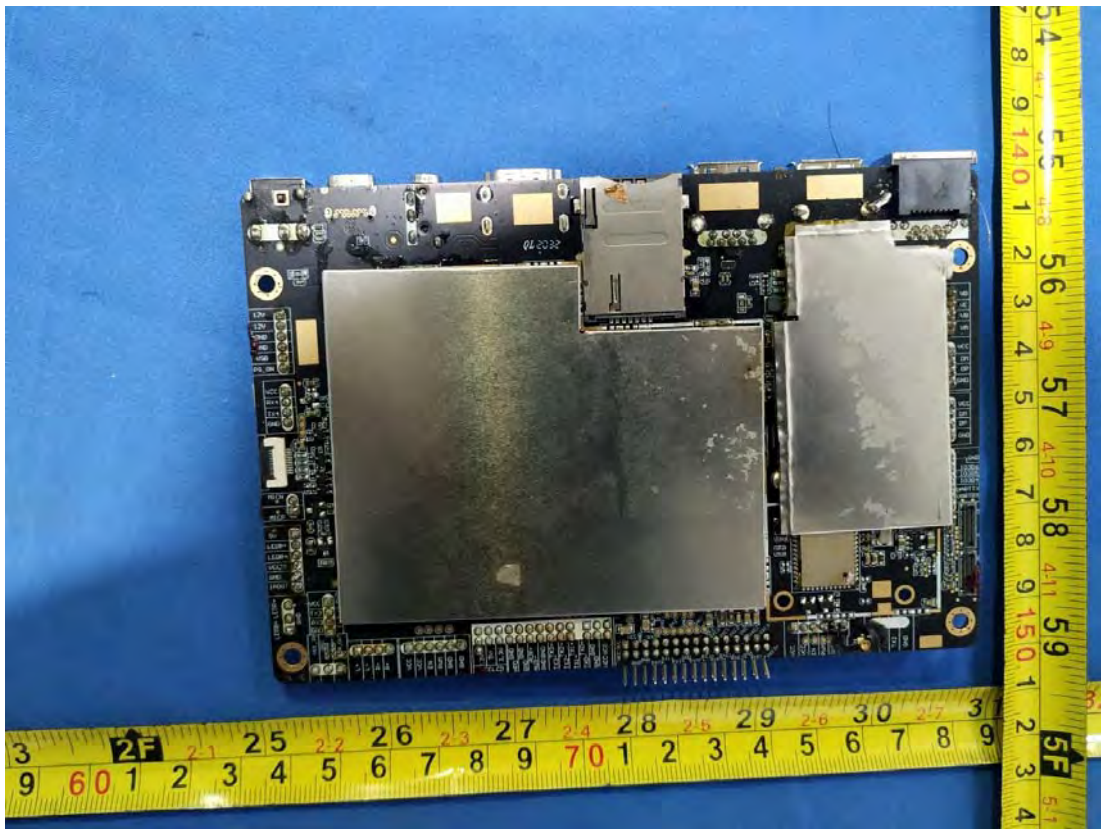


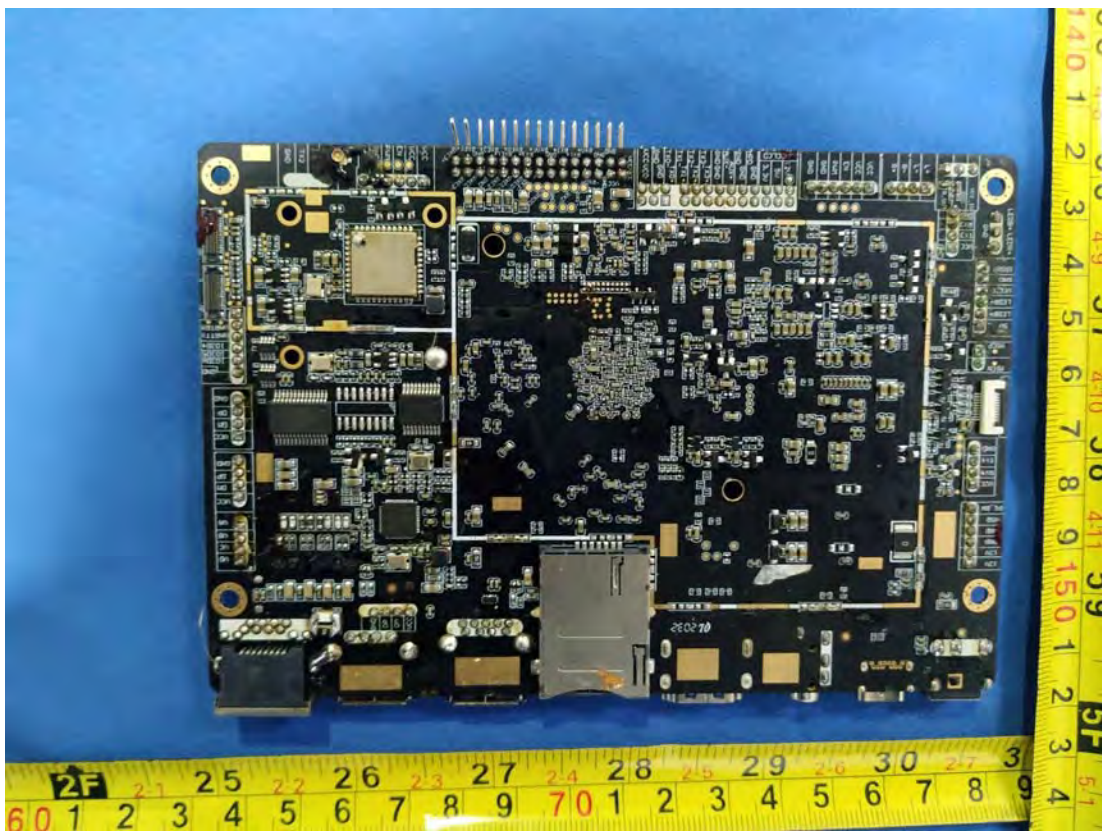
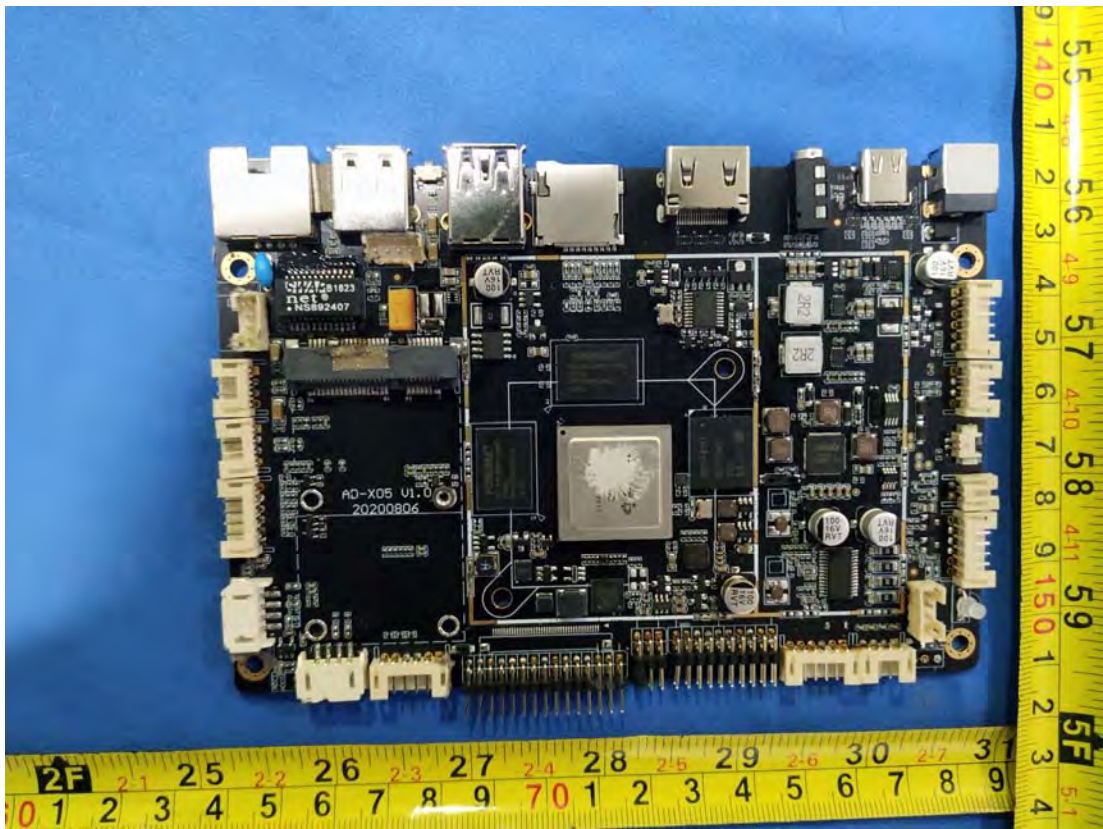


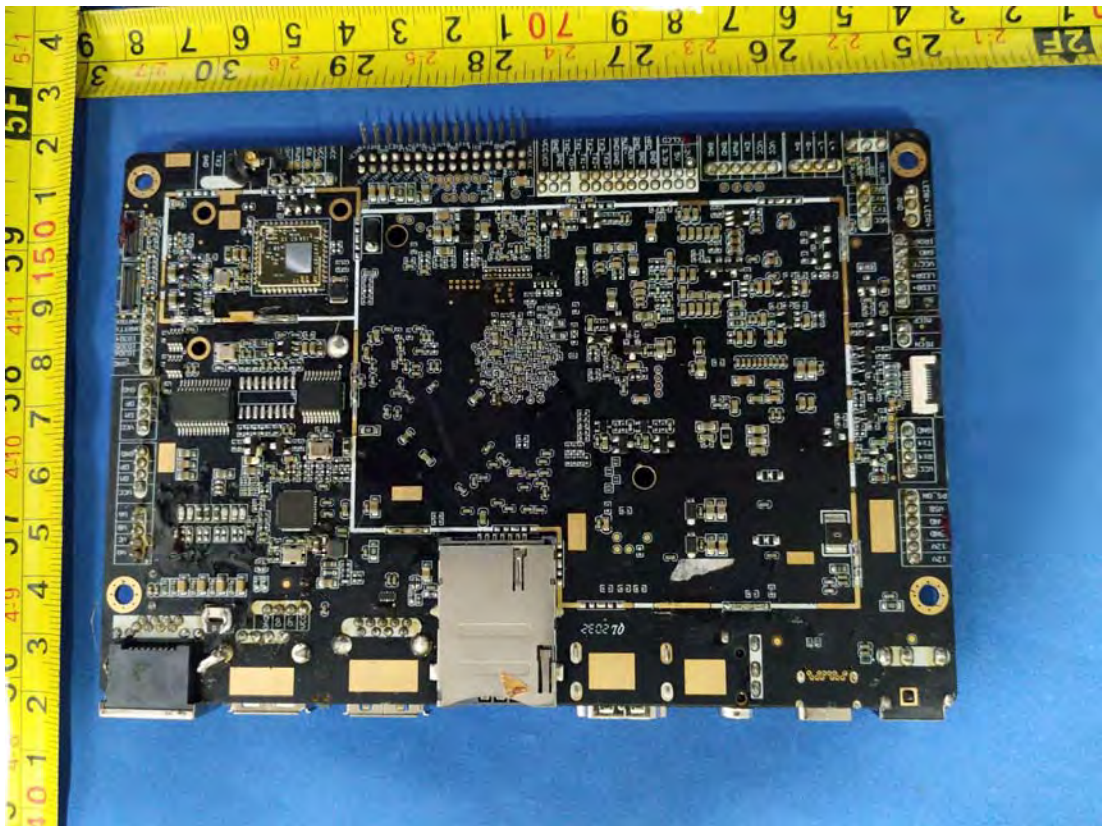
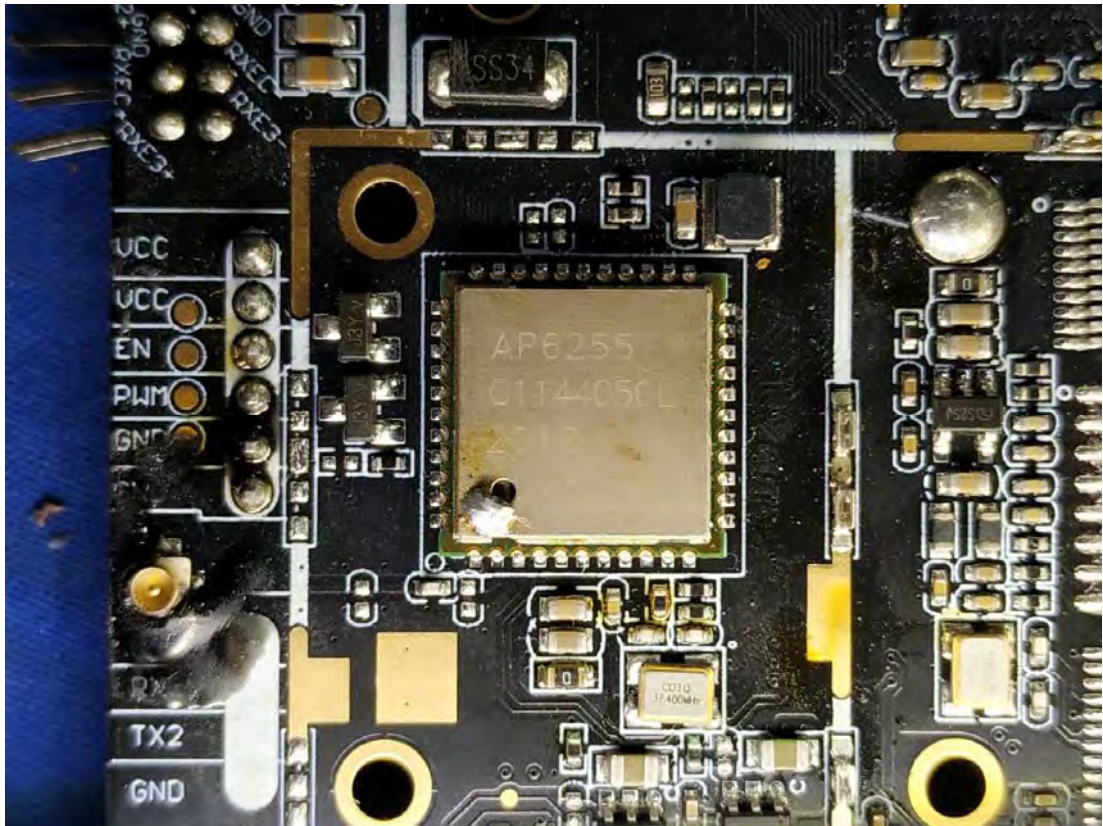


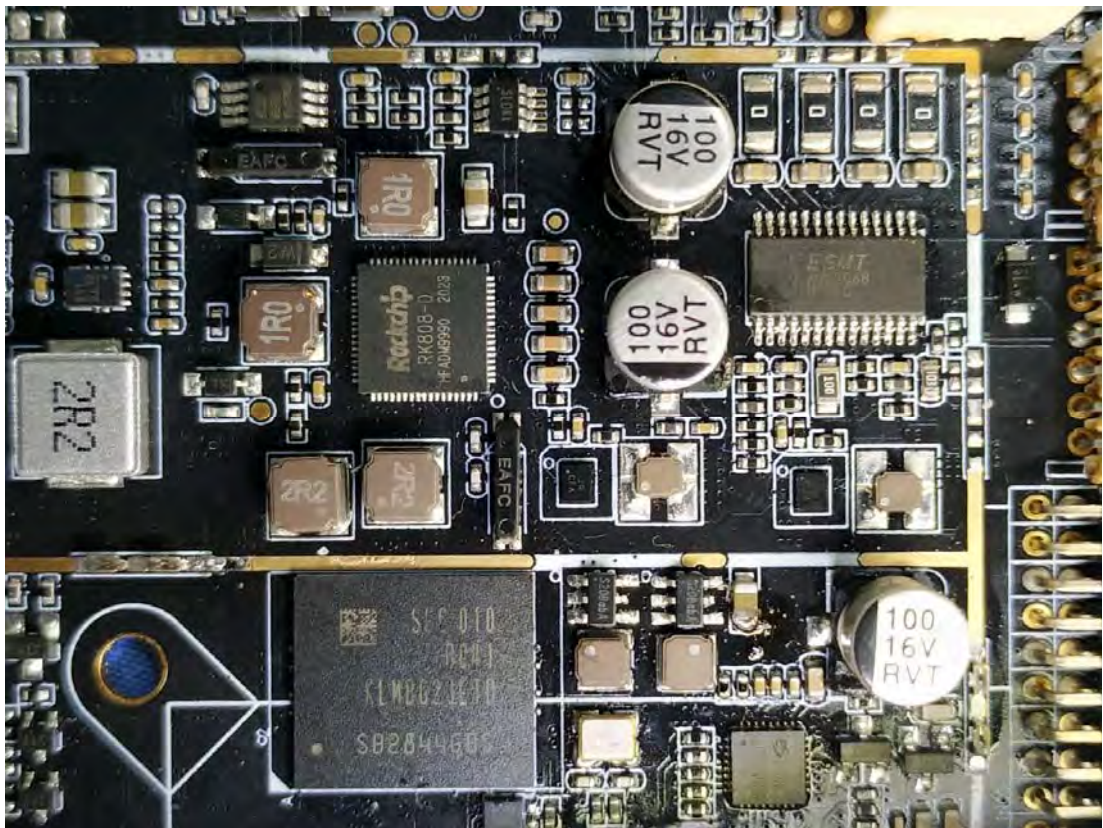
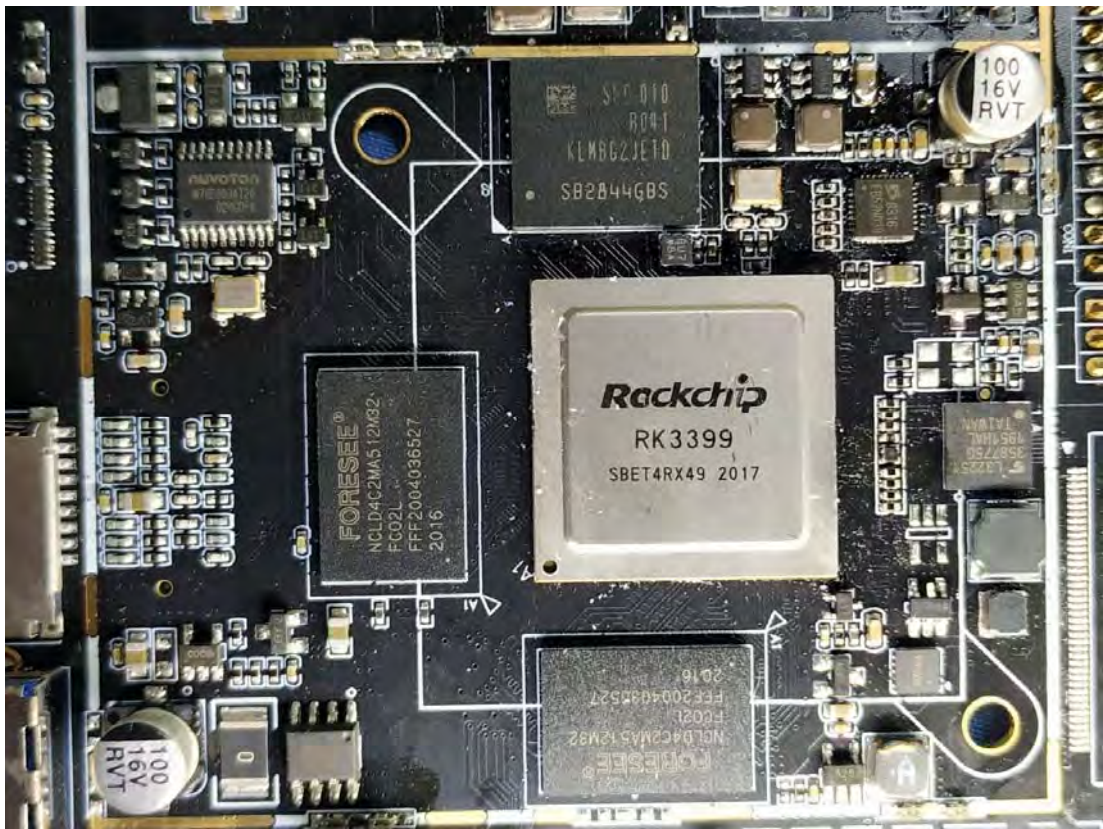


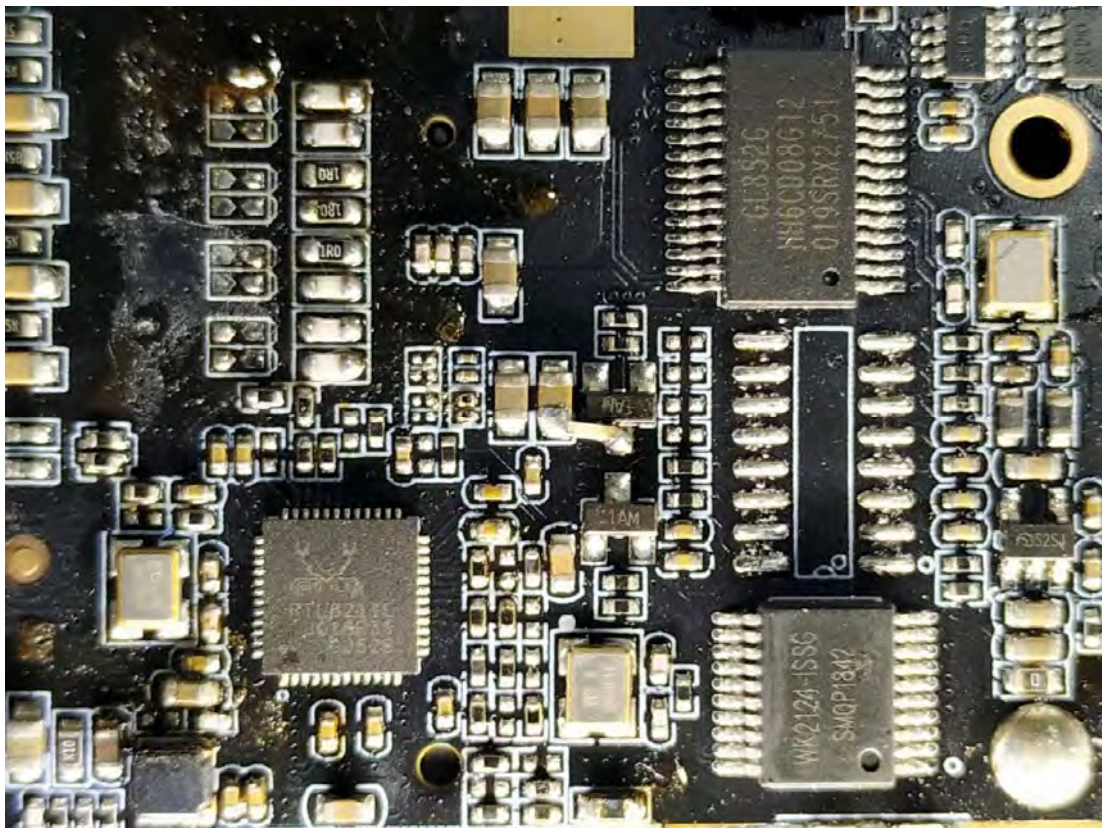
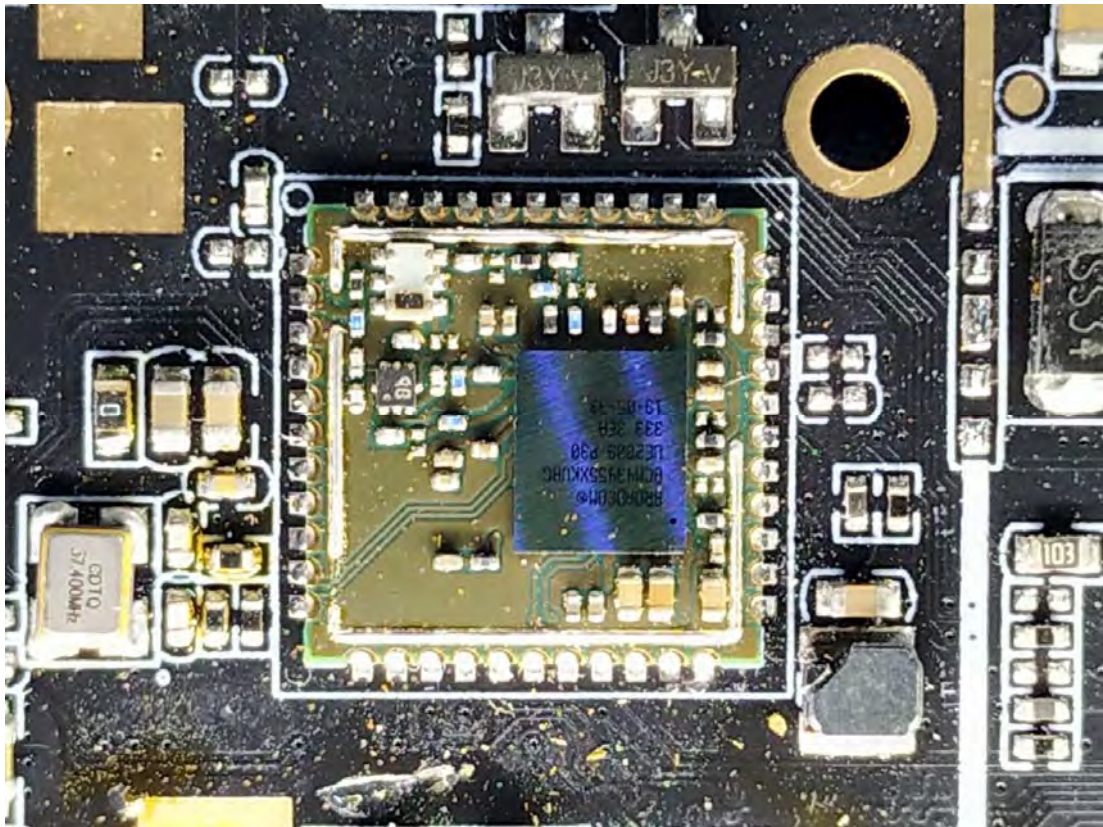








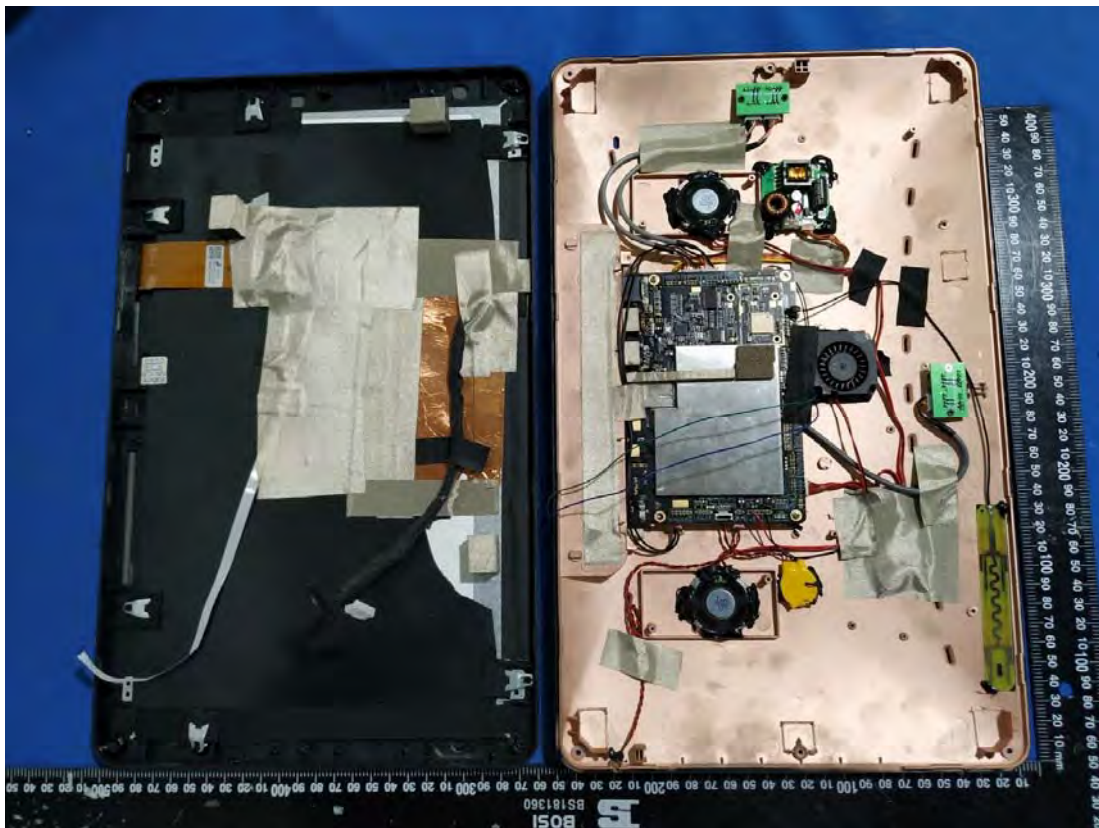


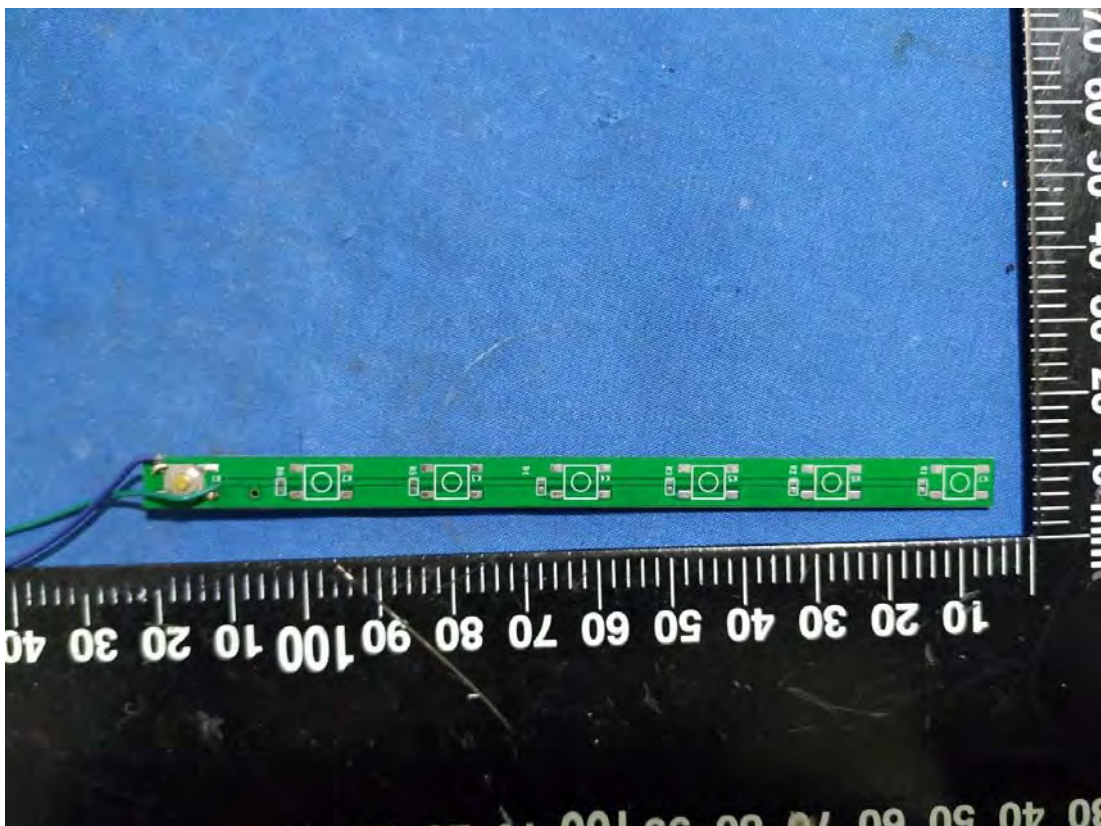
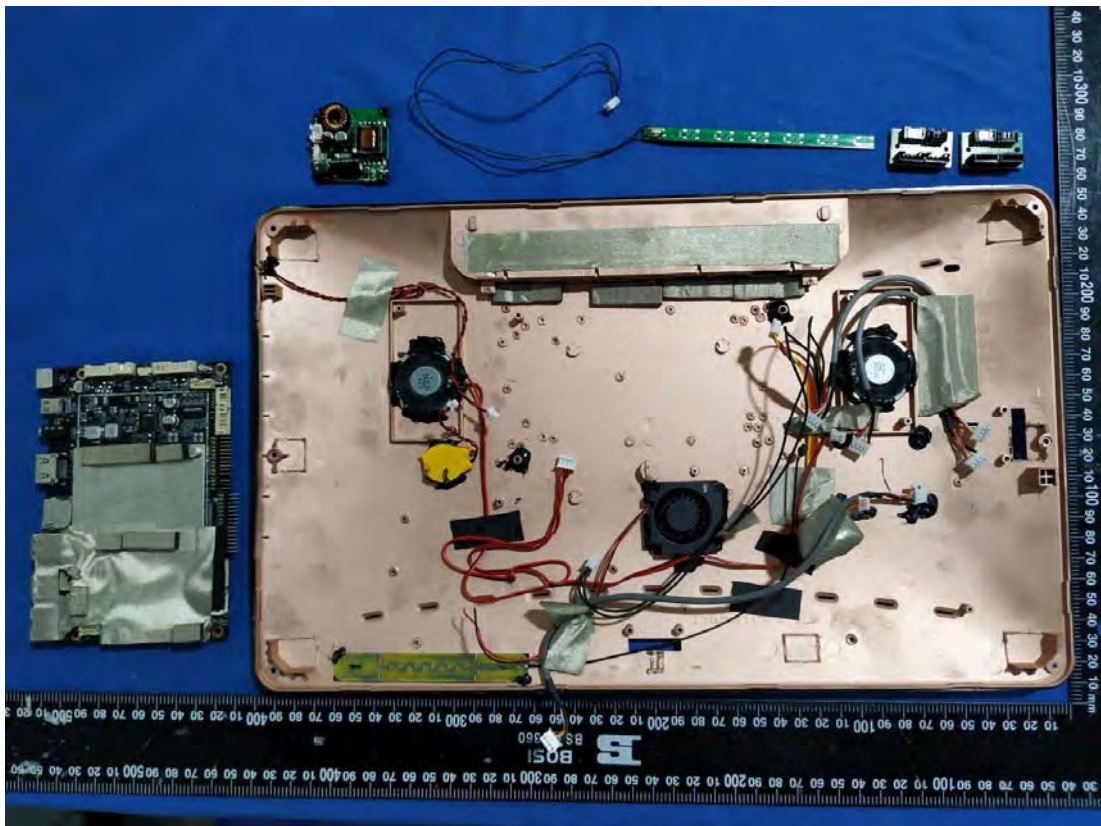


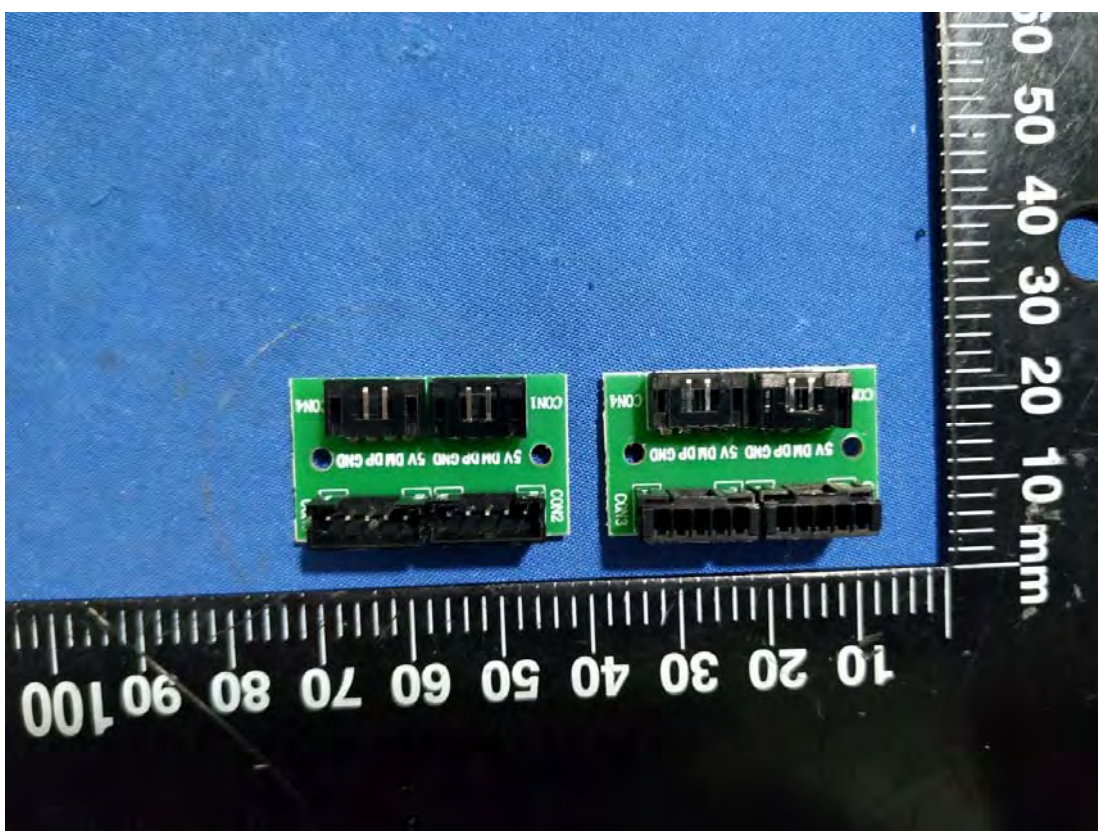
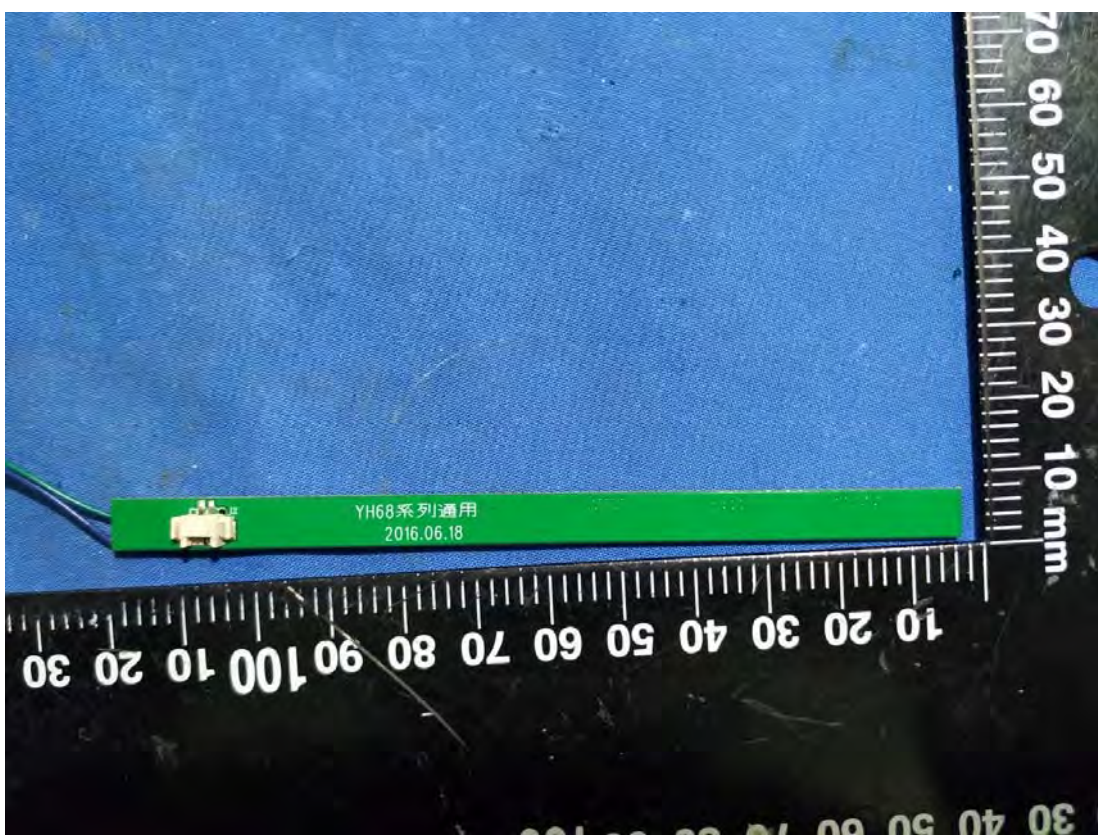


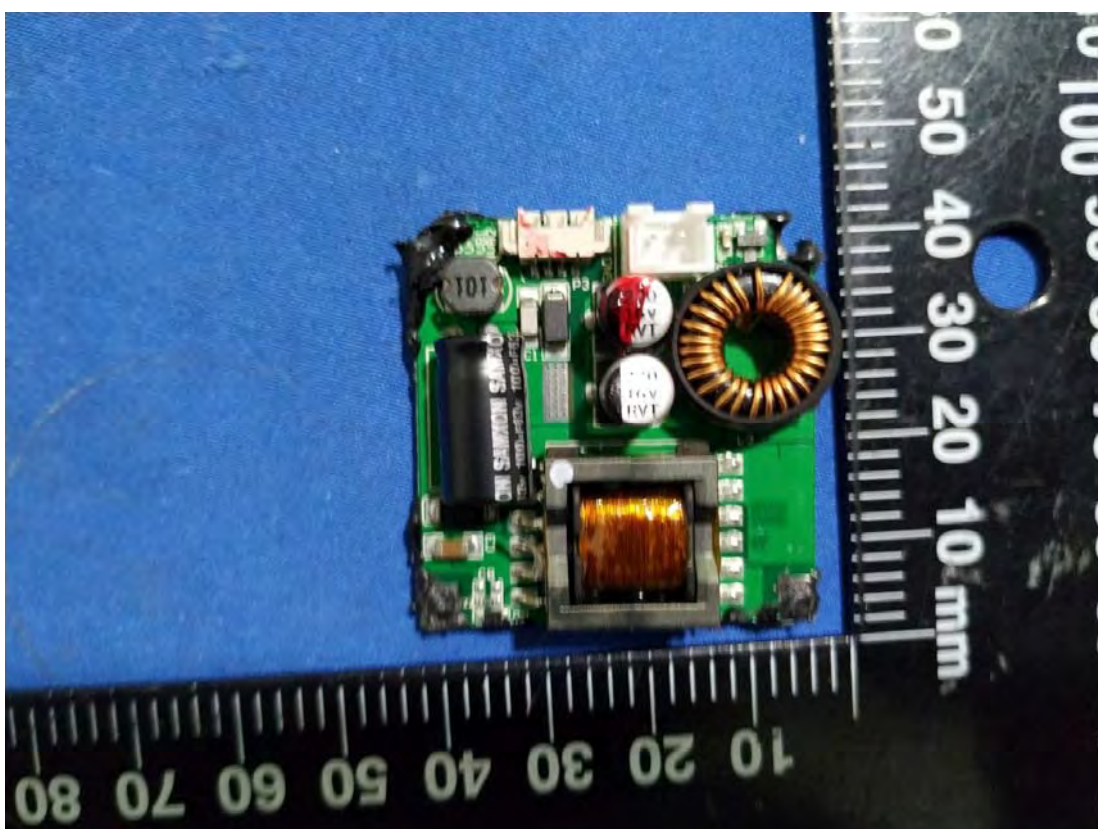
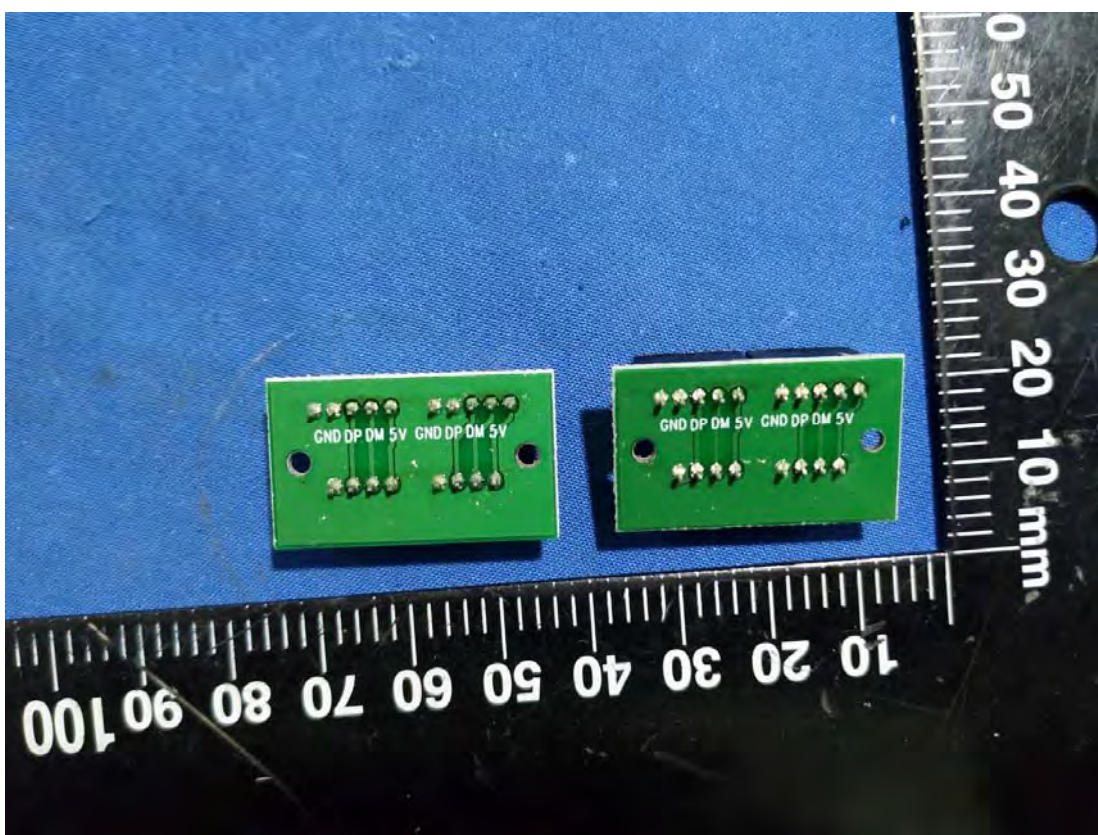


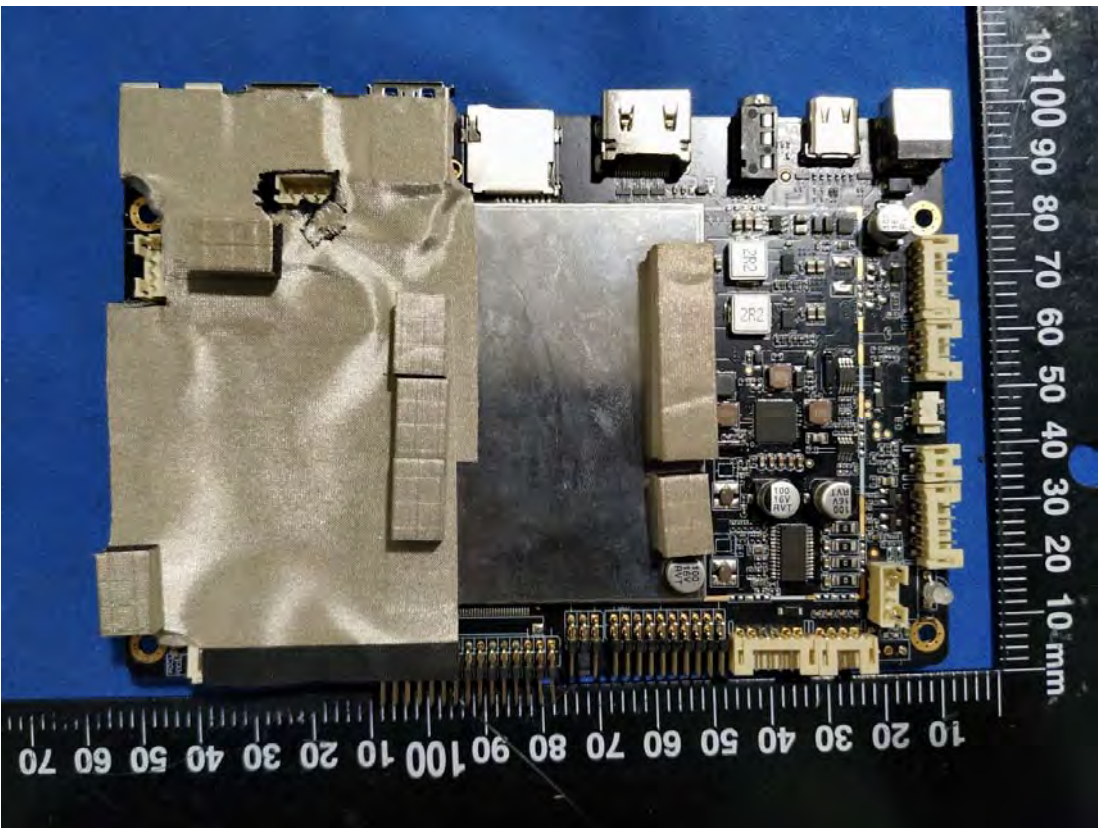
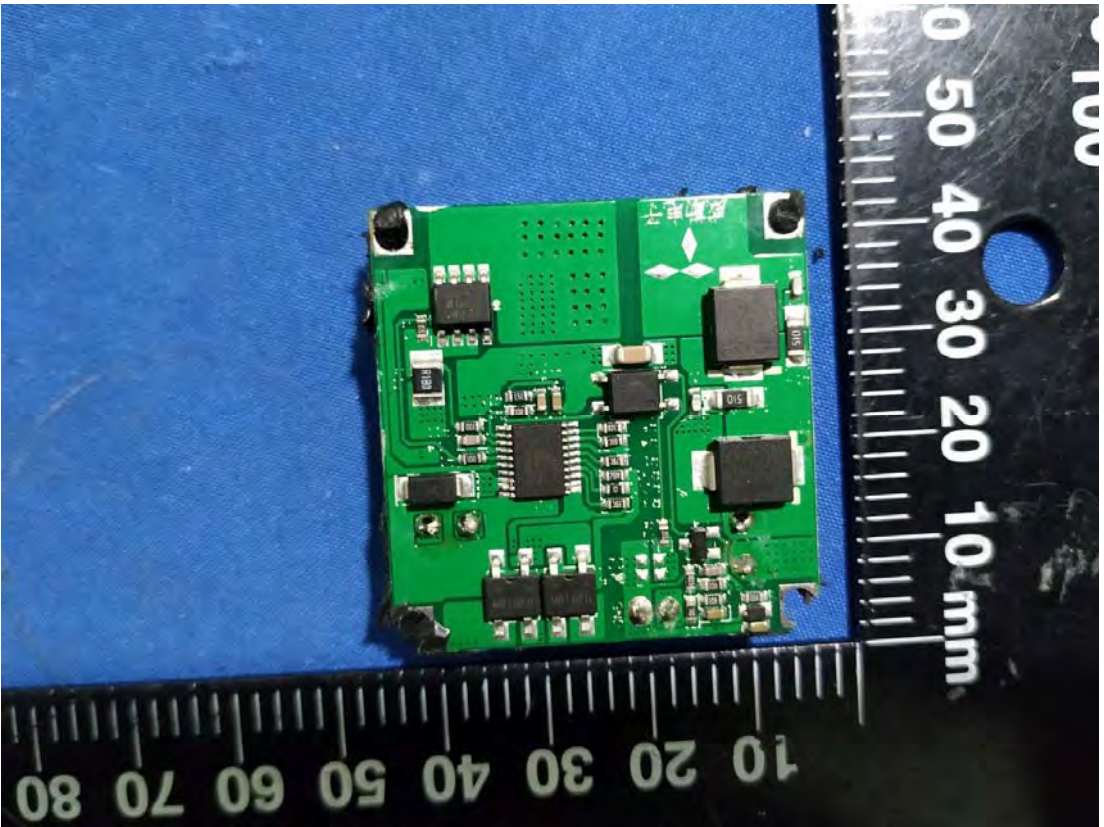
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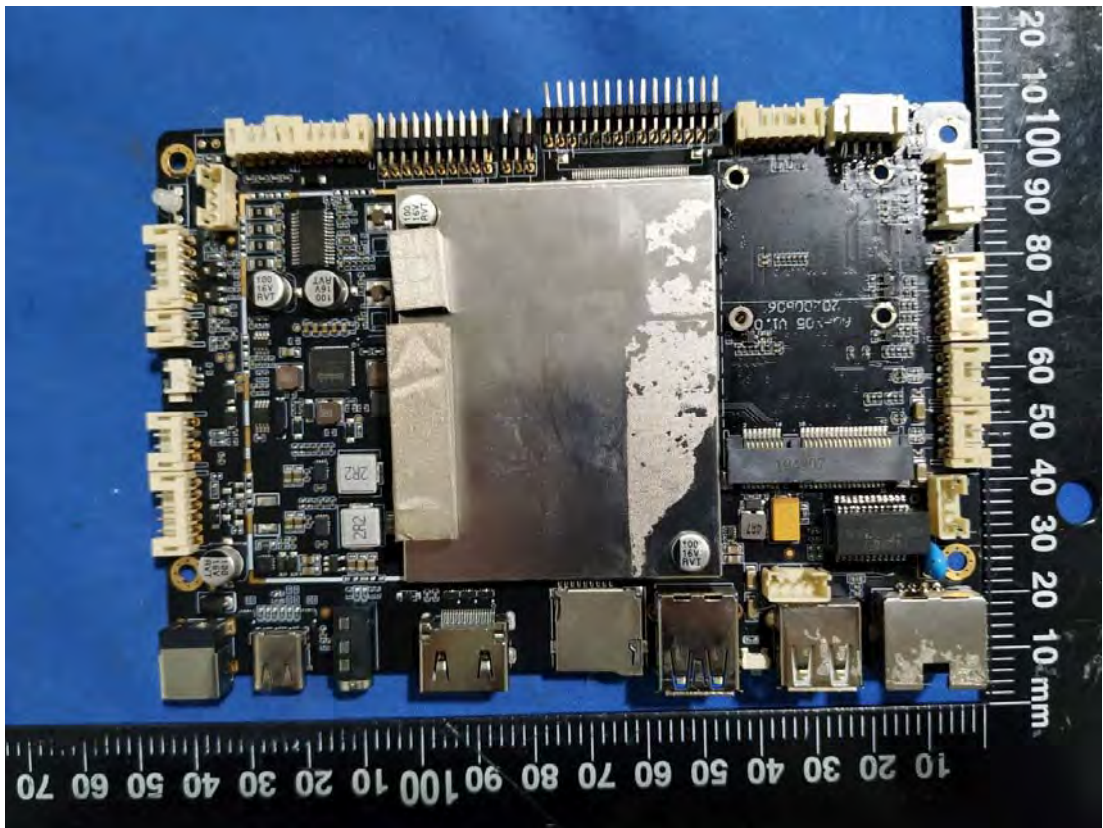
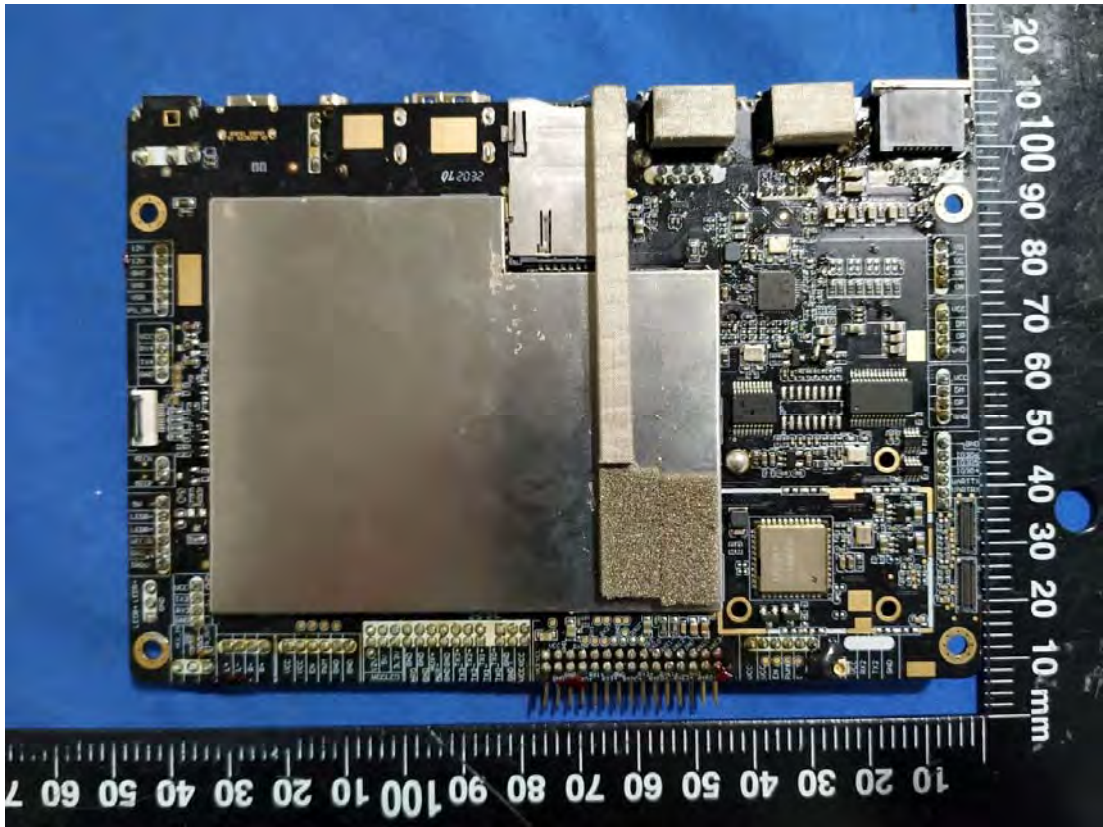


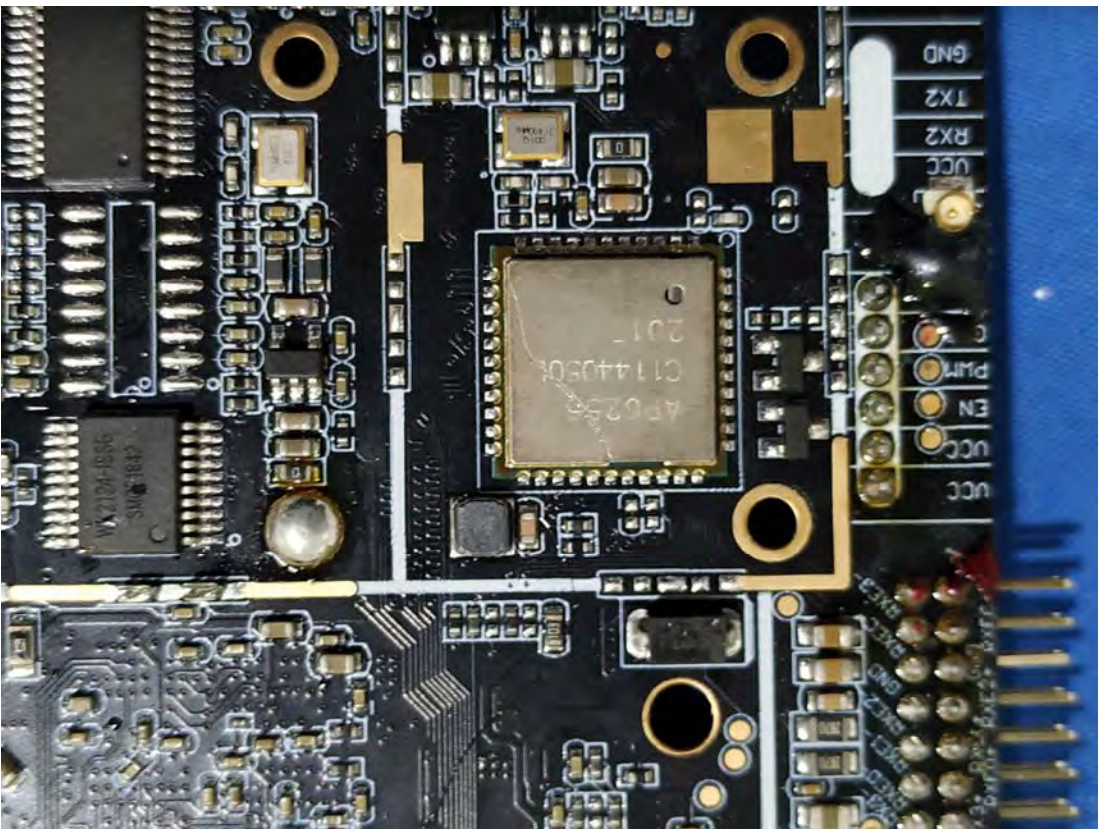
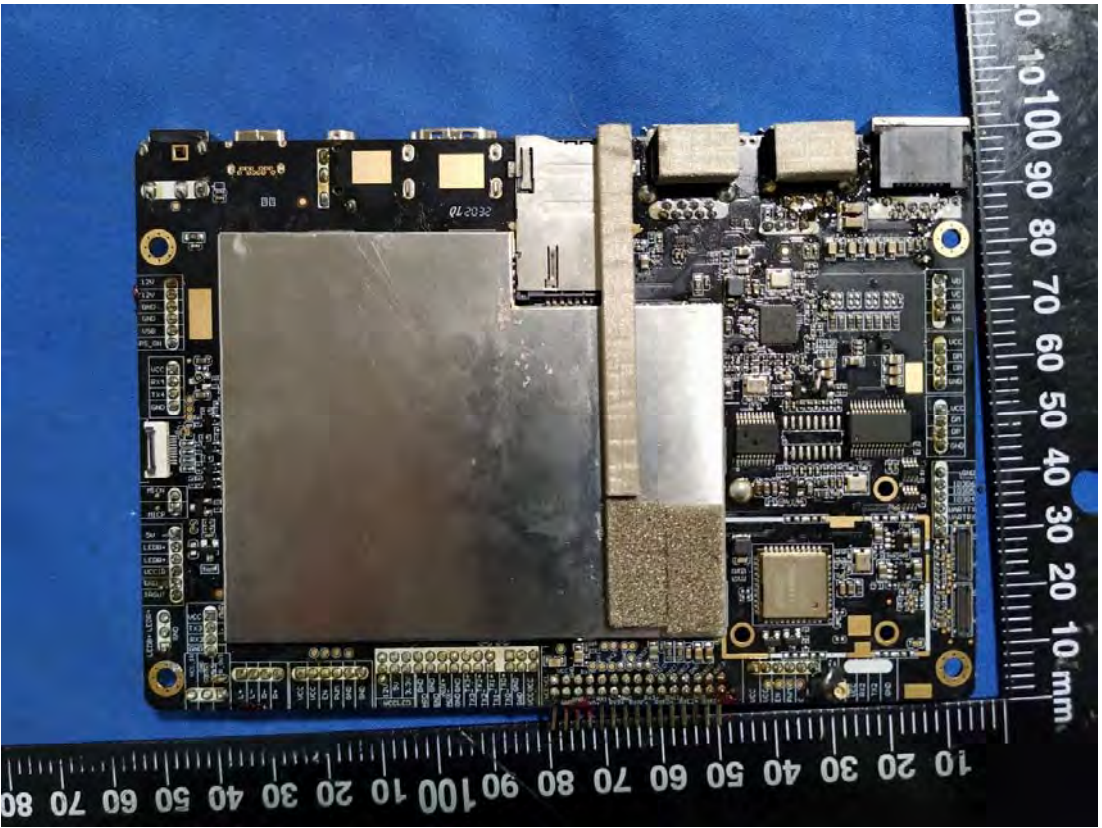


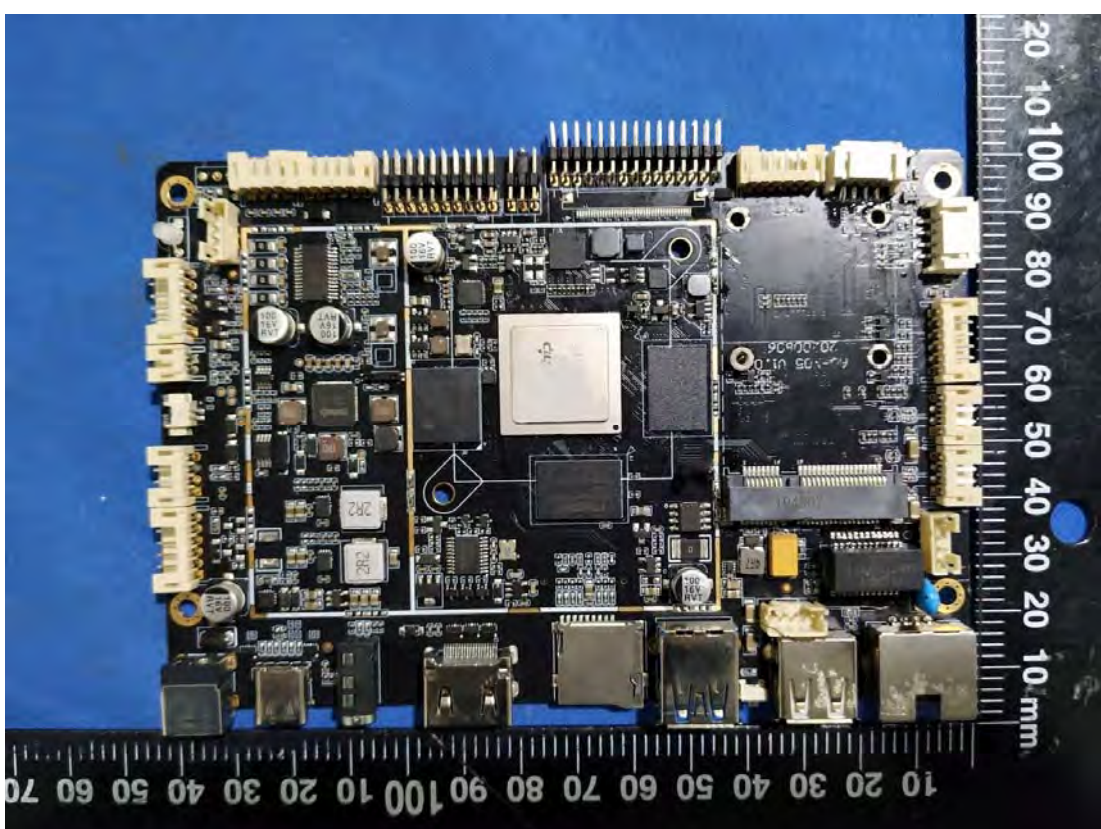
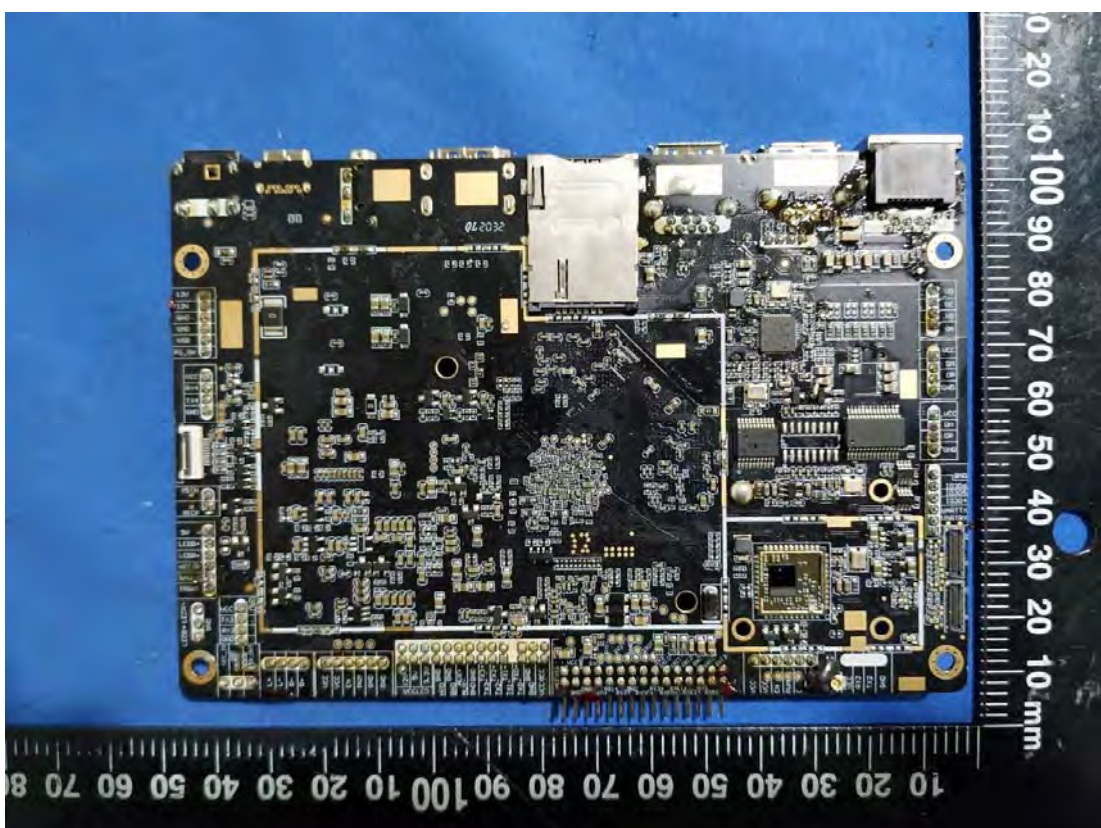


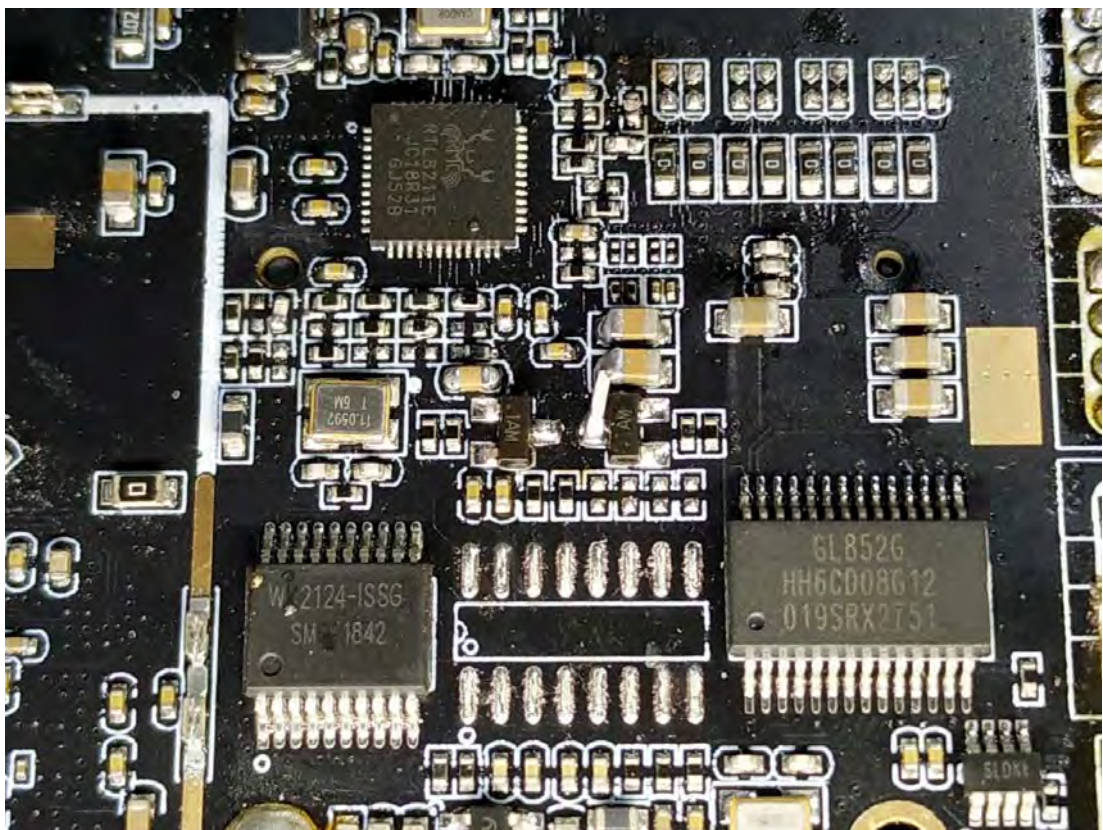
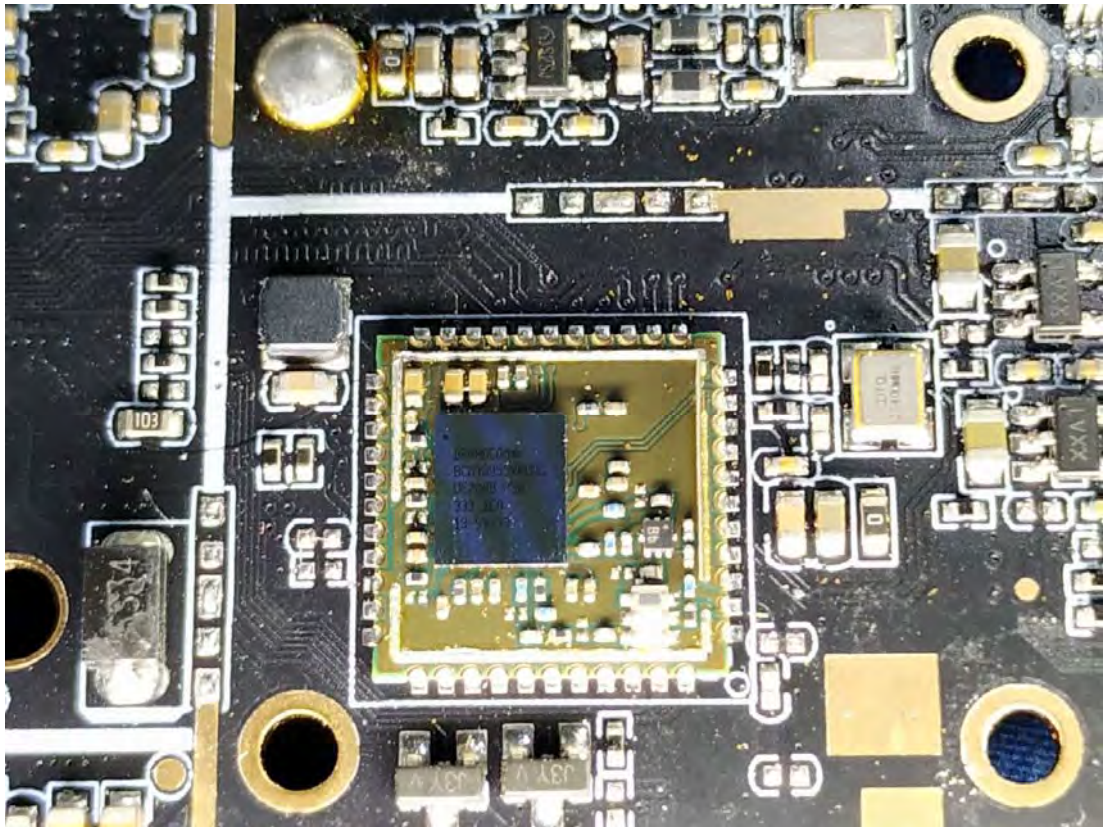


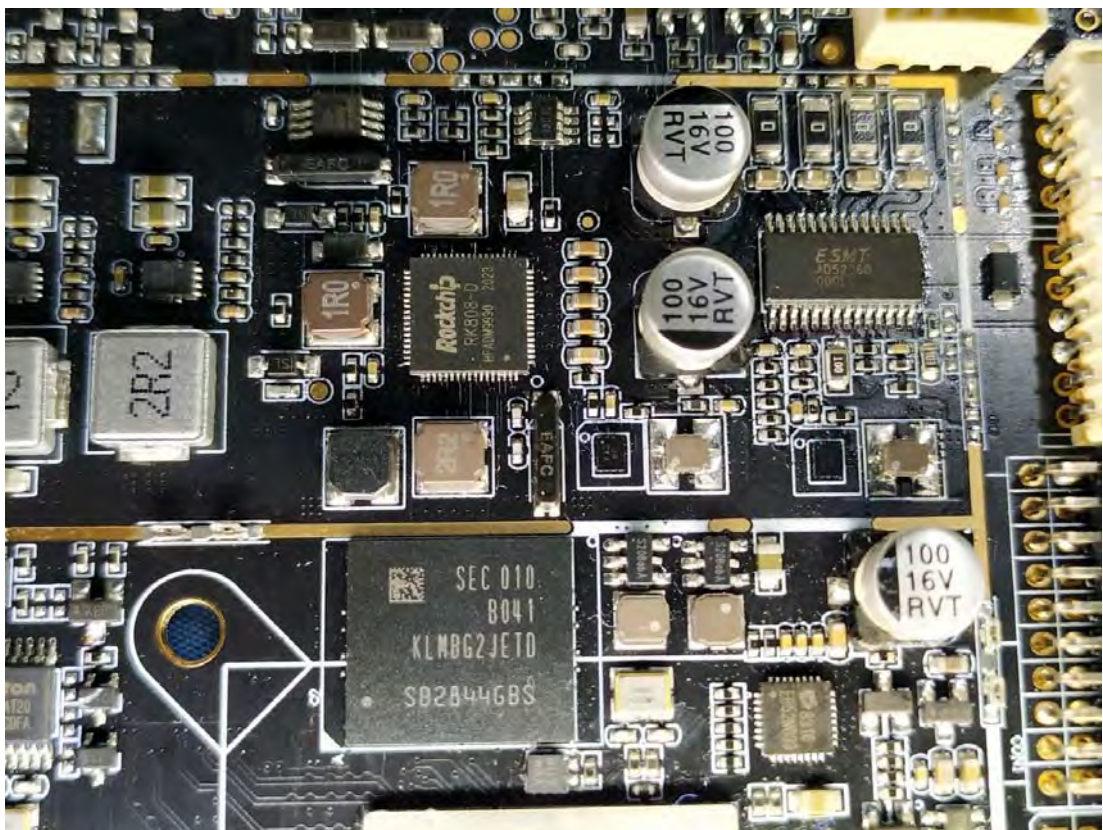
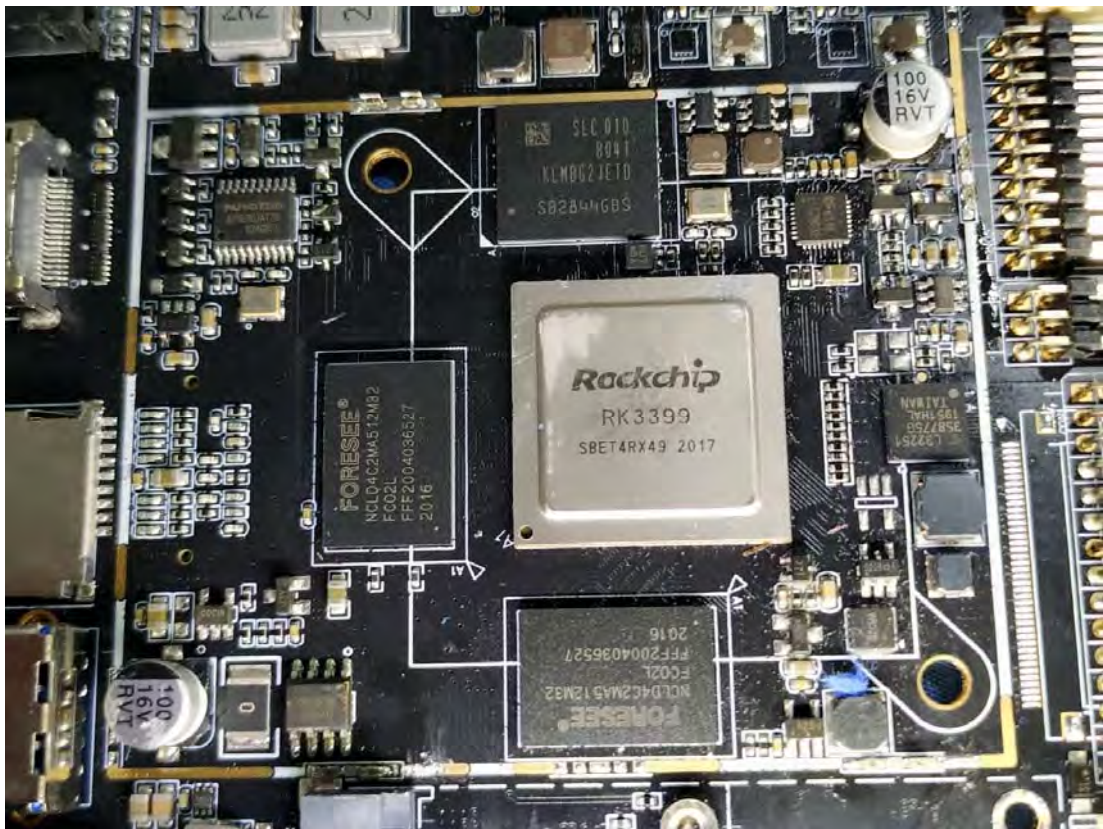


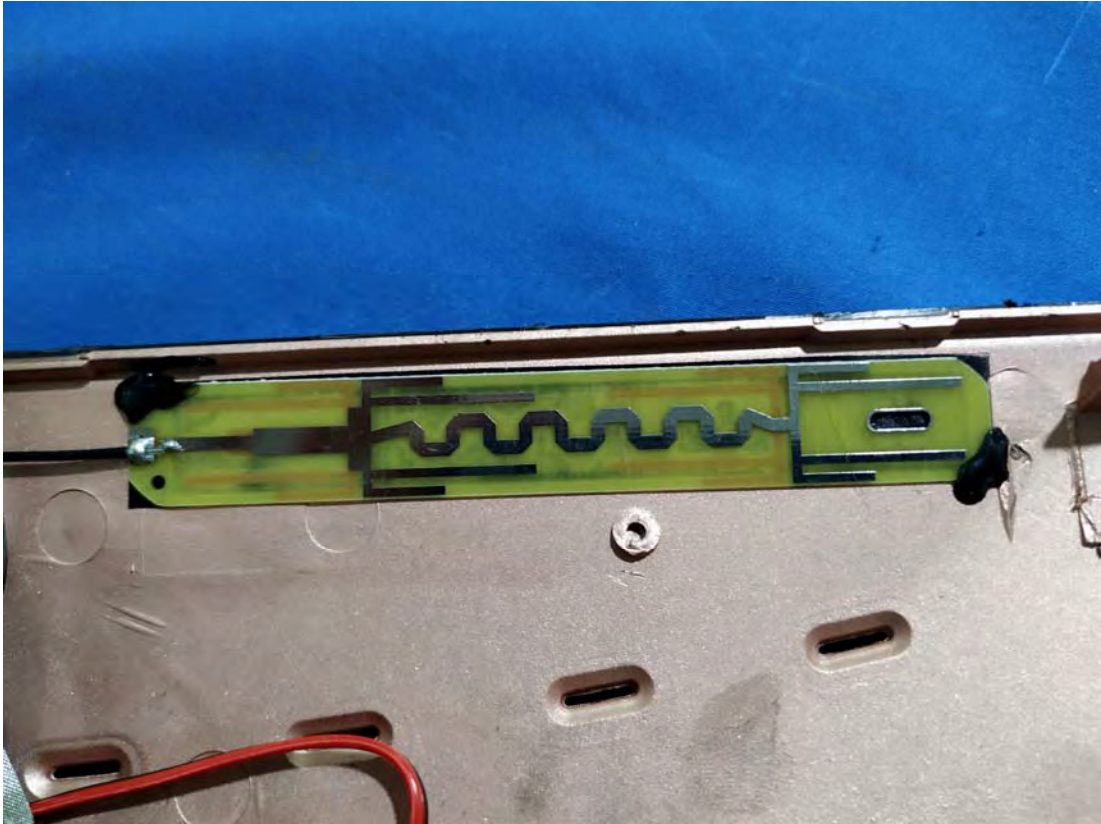






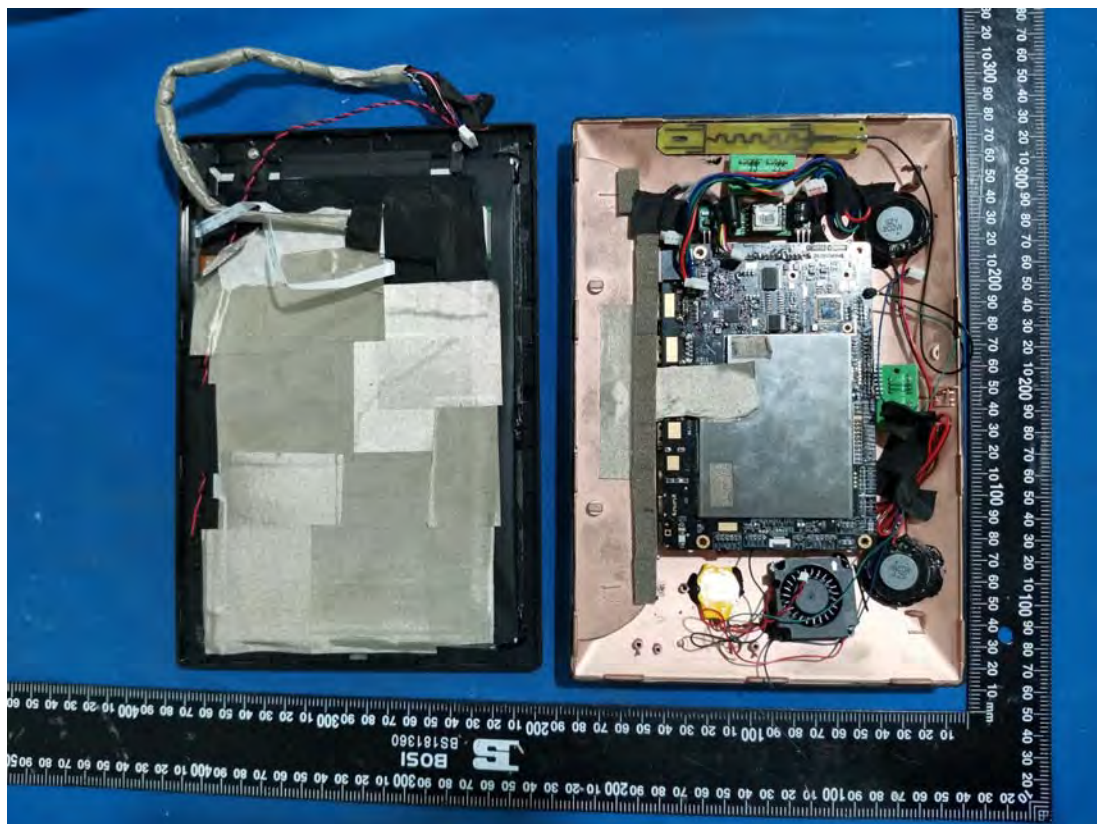


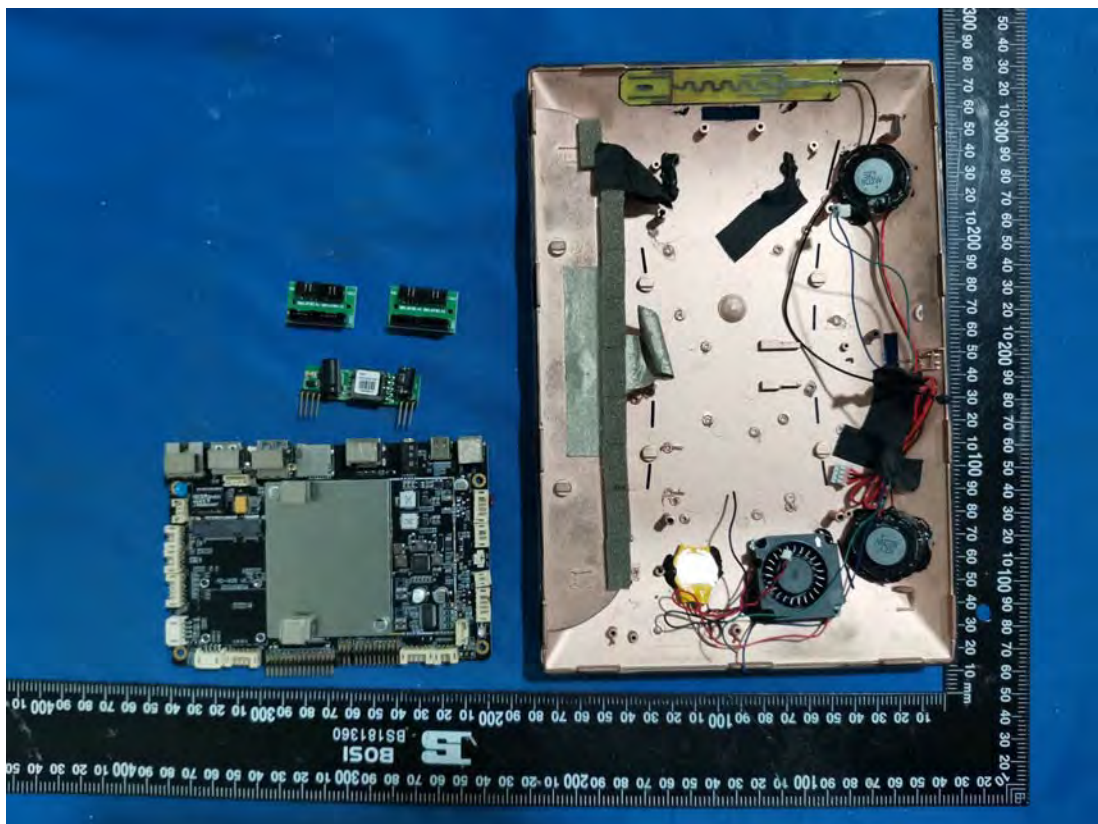


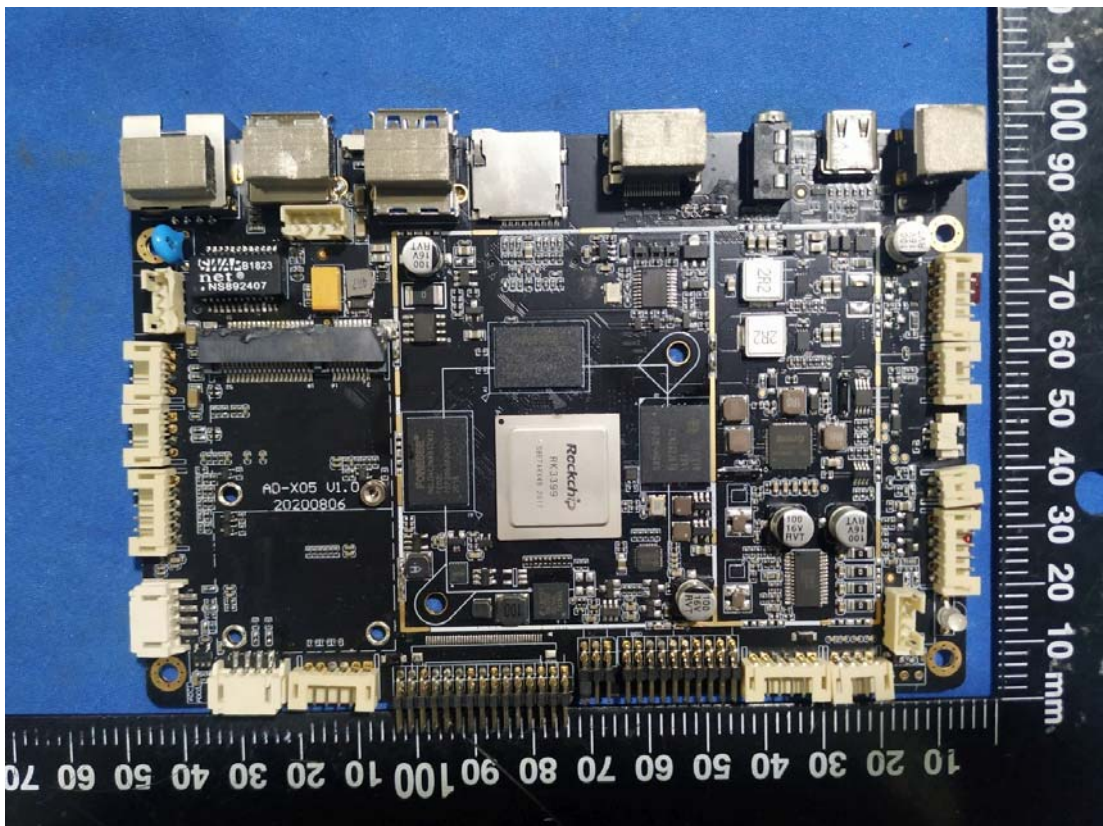
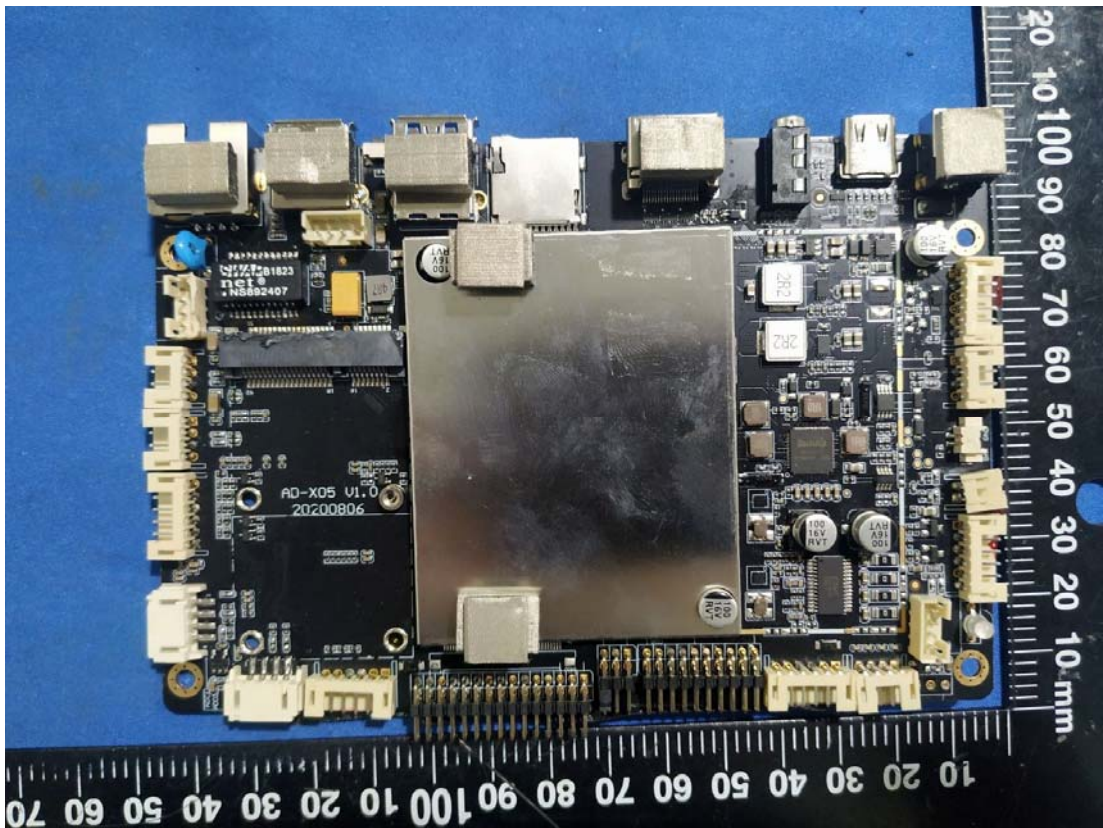


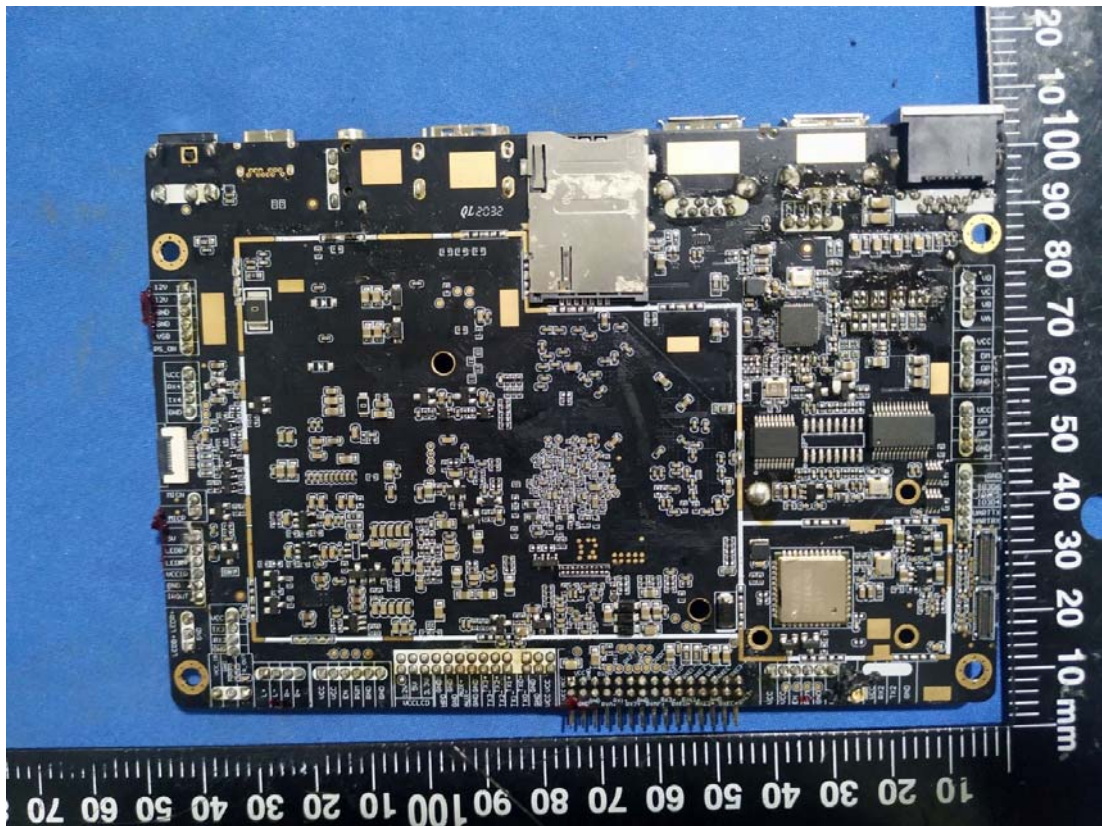


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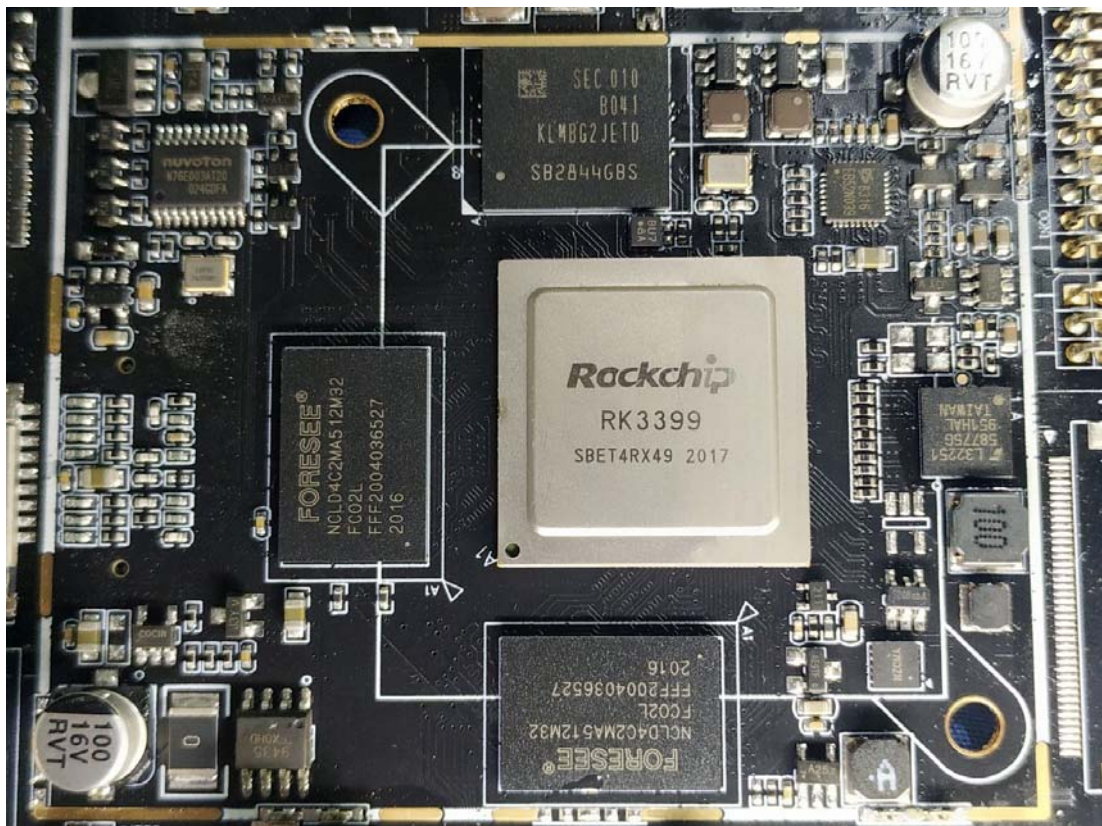




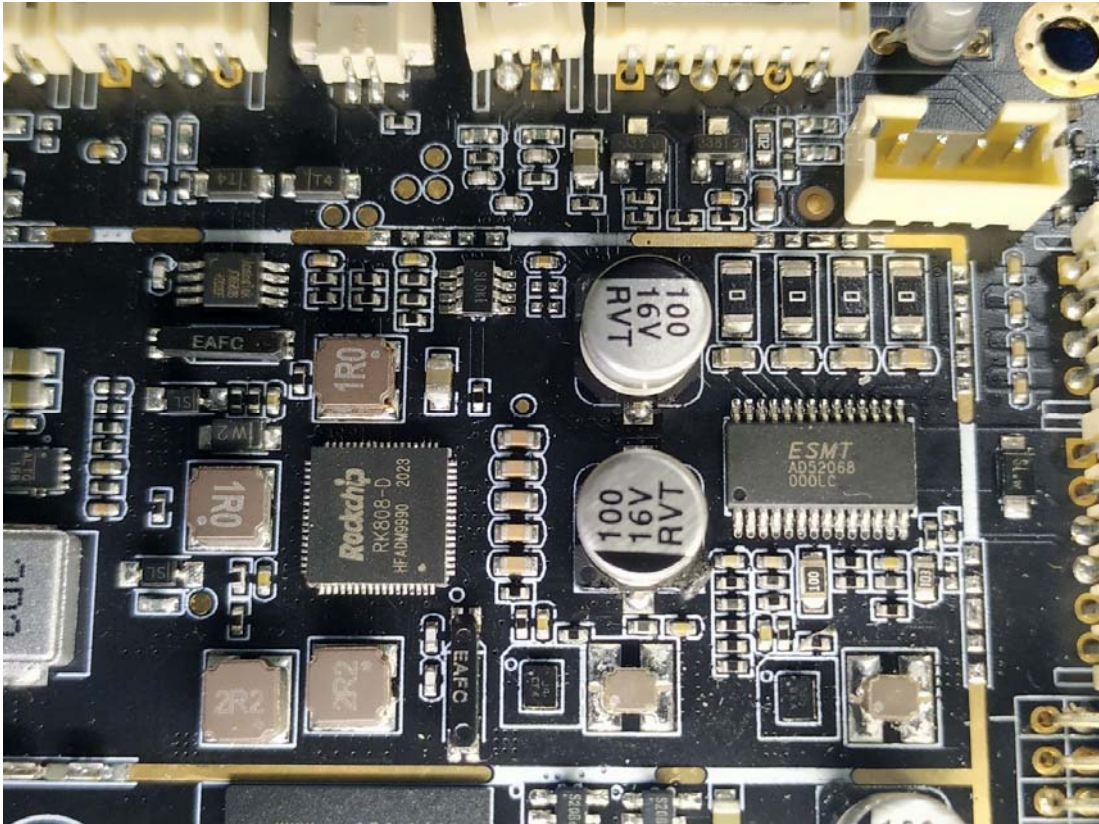




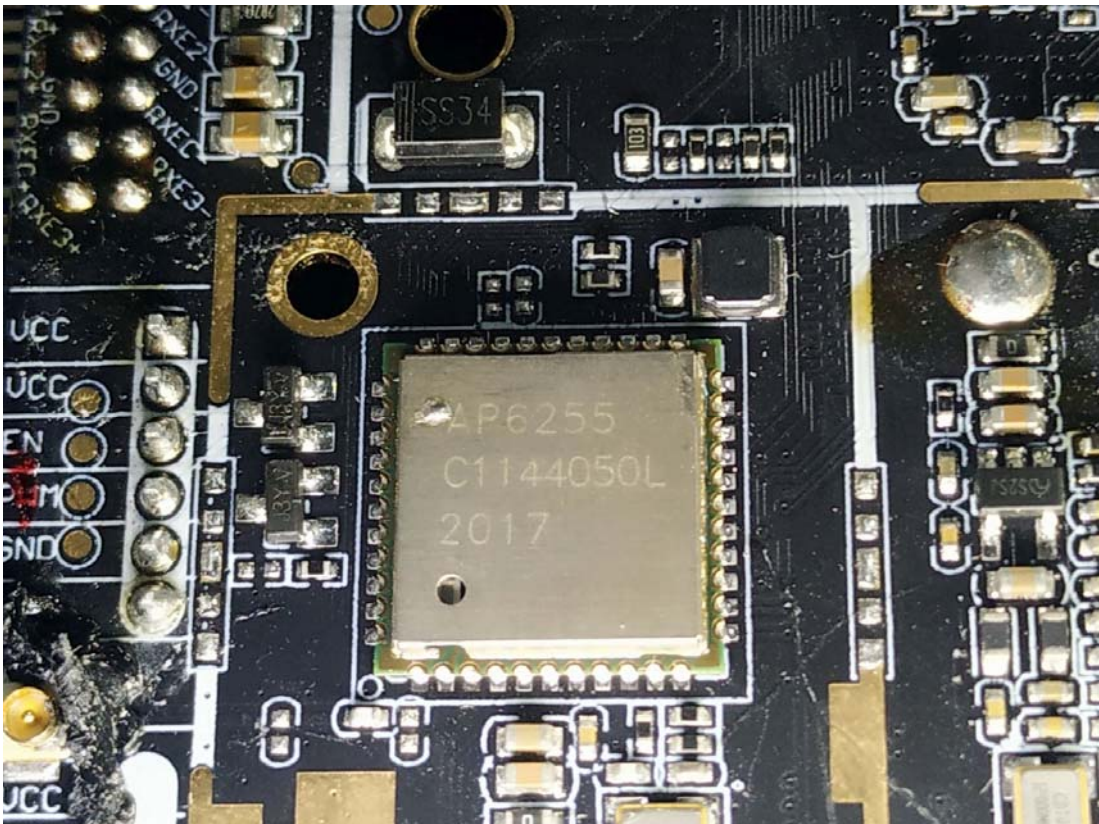
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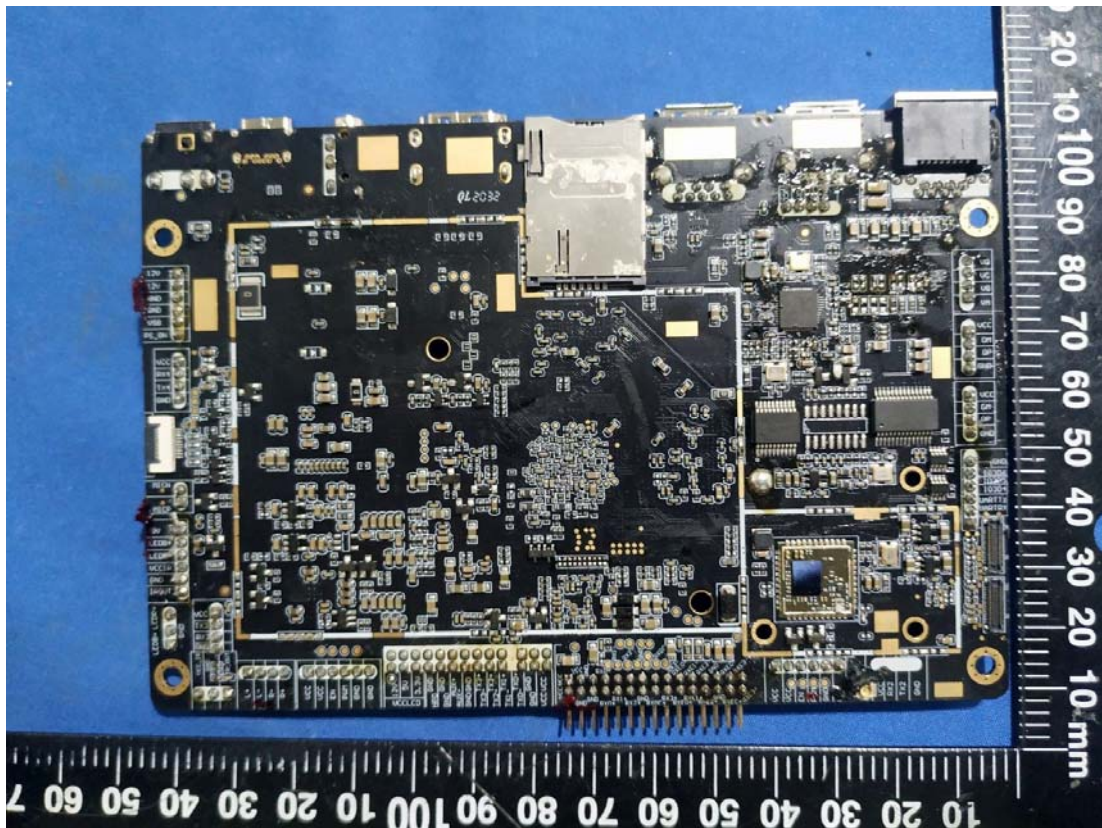


Chip 2

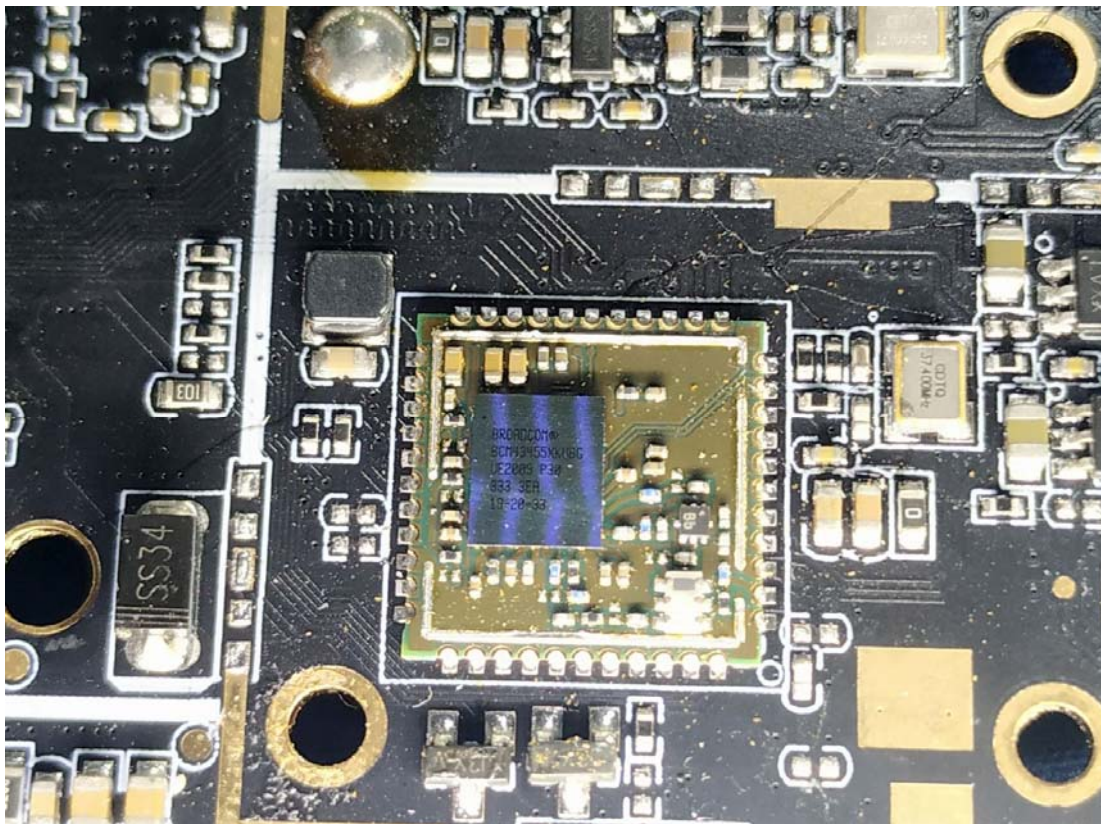


Chip 3





Chip 4



Chip 5

