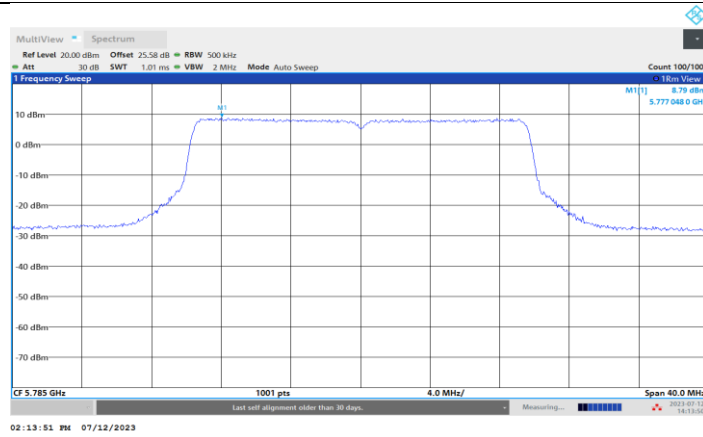
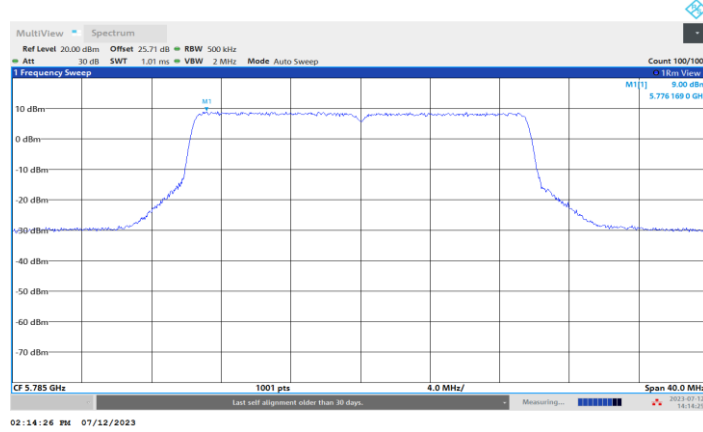
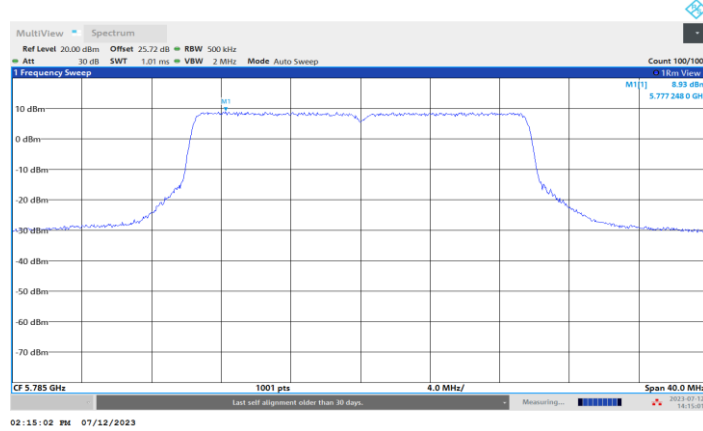




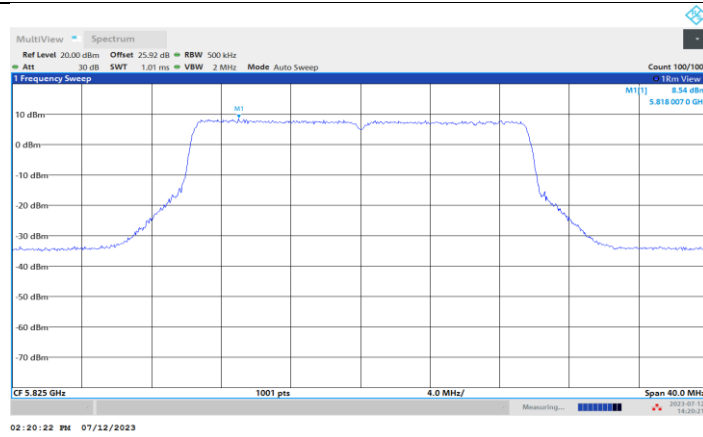
11BE20MIMO_Ant2_5785



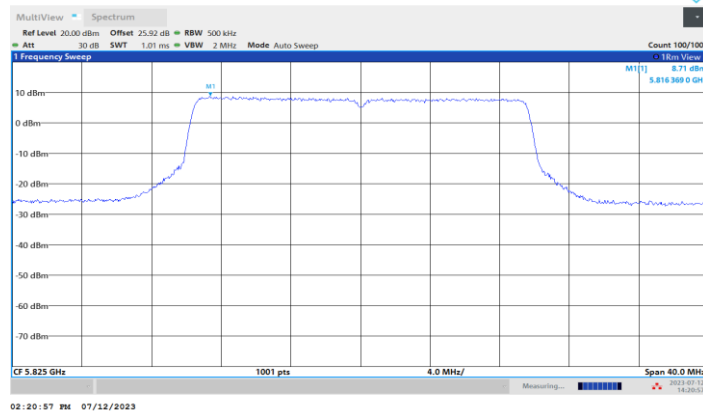
11BE20MIMO_Ant3_5785



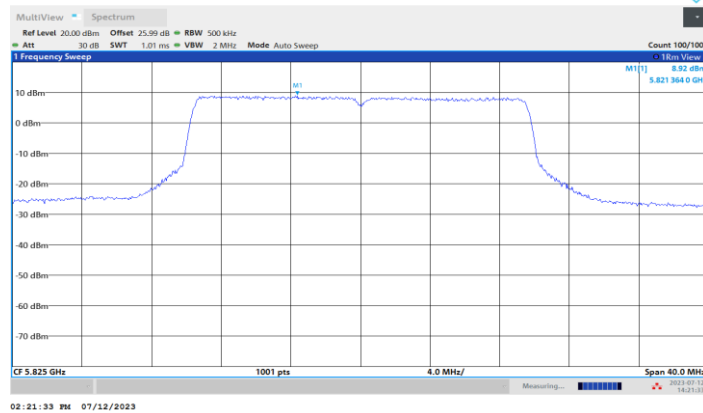
11BE20MIMO_Ant4_5785



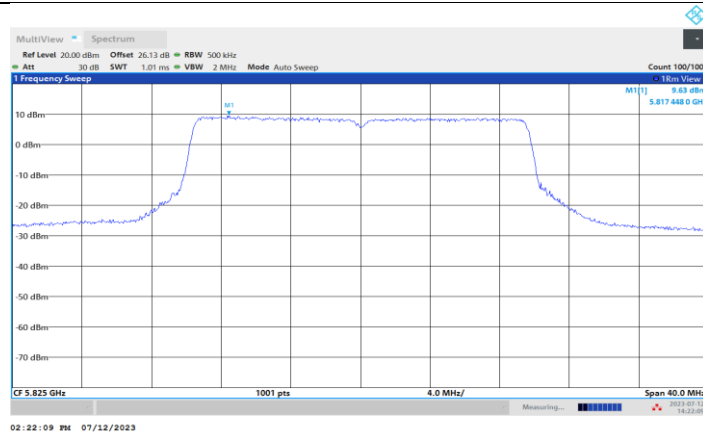
11BE20MIMO_Ant1_5825



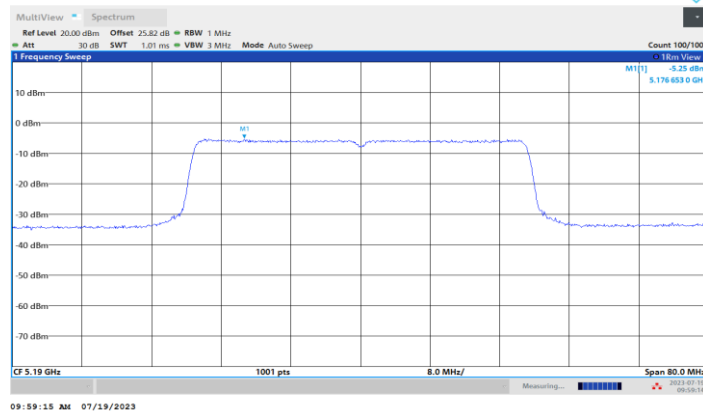
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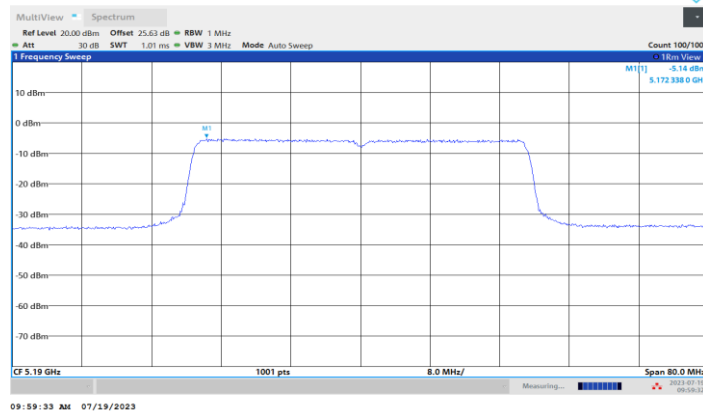
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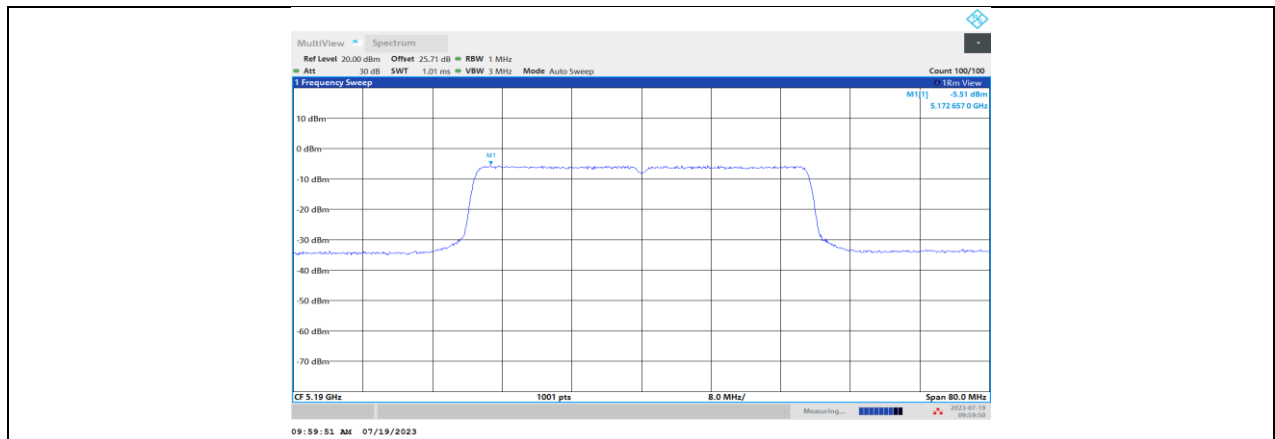
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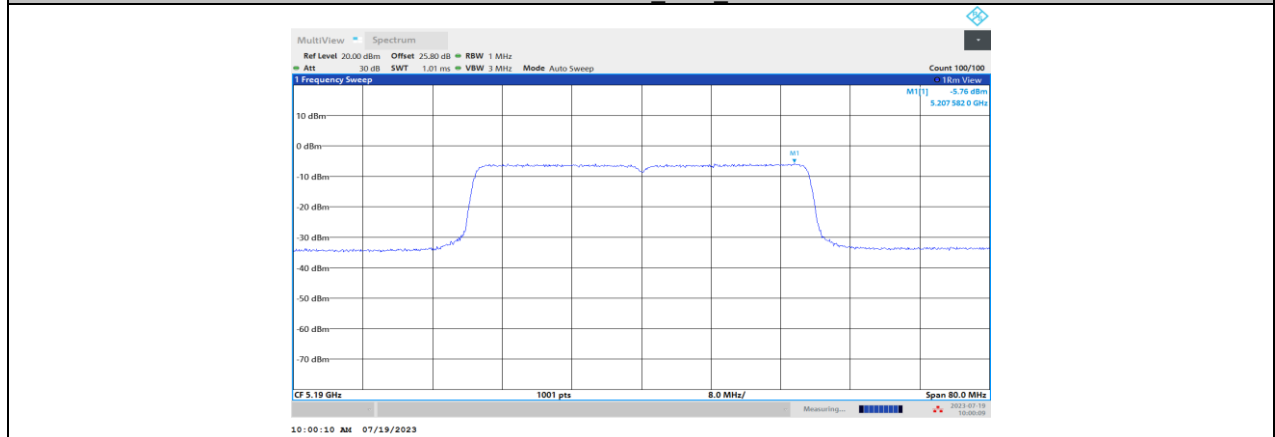
11BE40MIMO_Ant1_5190



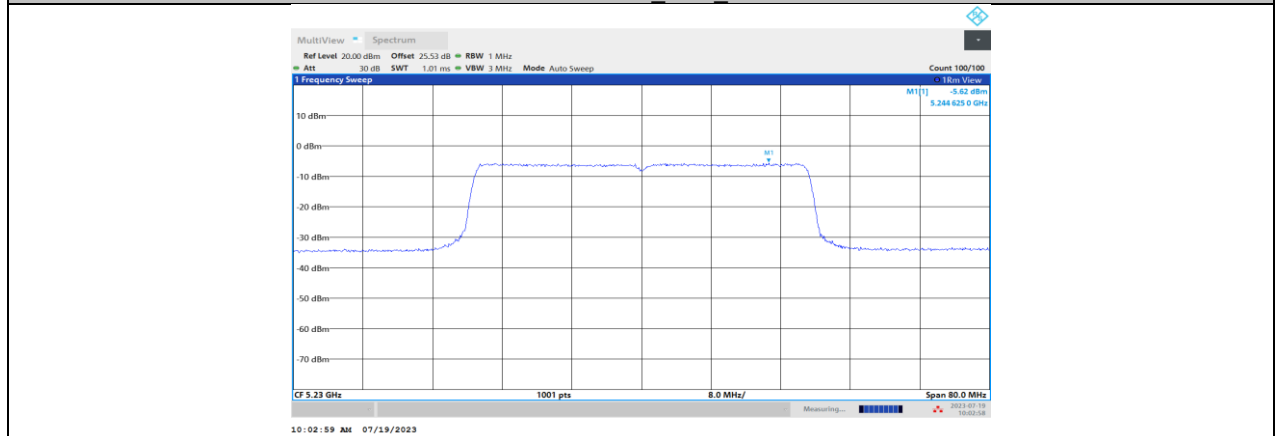
11BE40MIMO_Ant2_5190



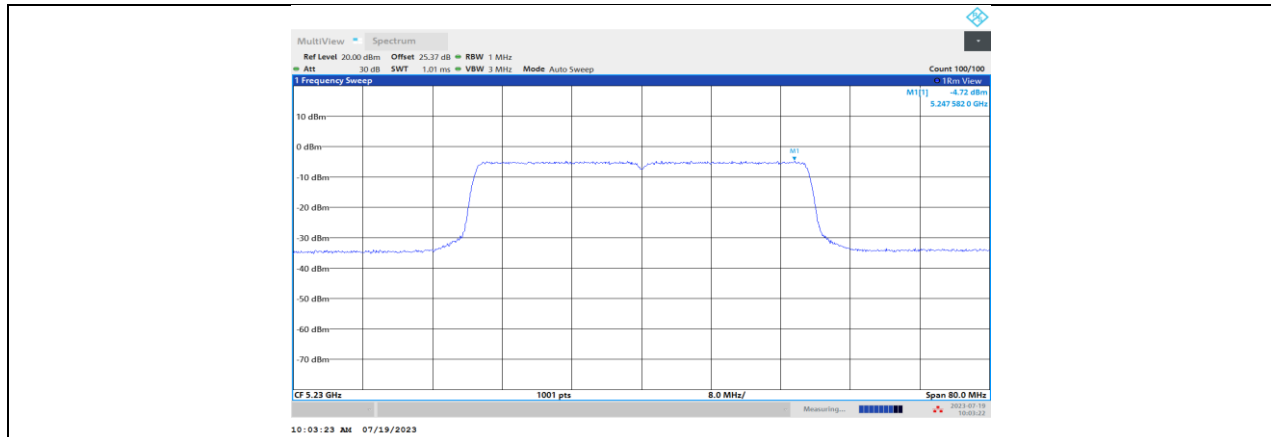
11BE40MIMO_Ant3_5190



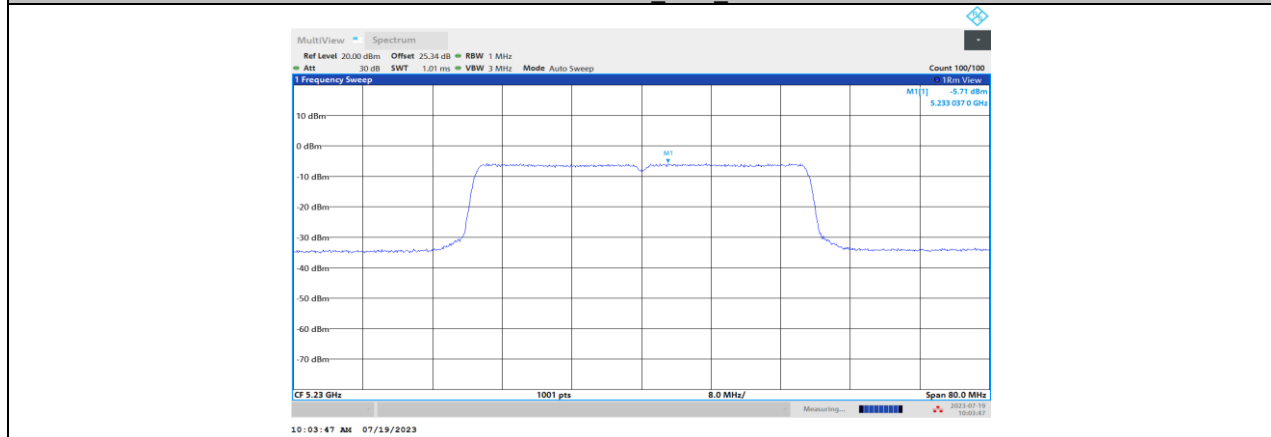
11BE40MIMO_Ant4_5190



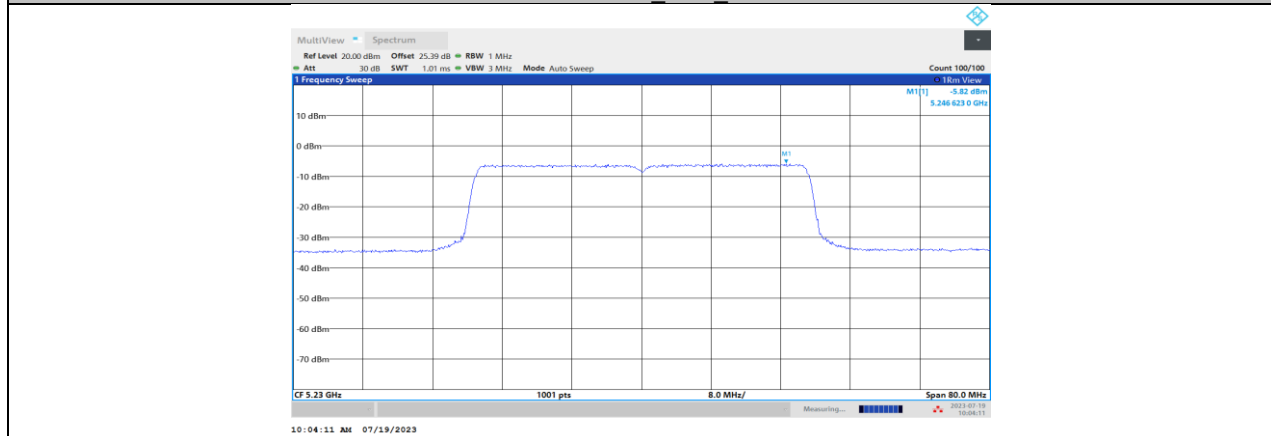
11BE40MIMO_Ant1_5230



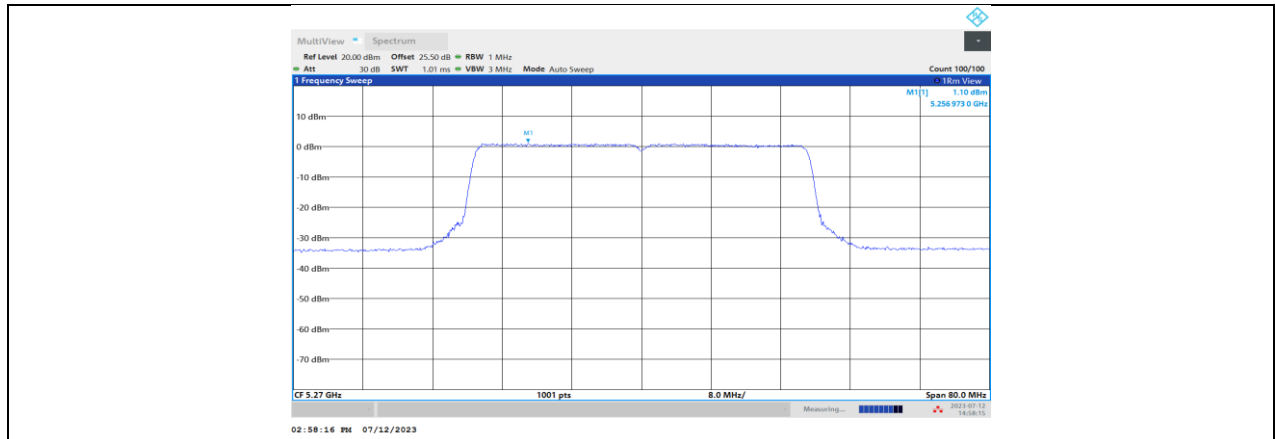
11BE40MIMO_Ant2_5230



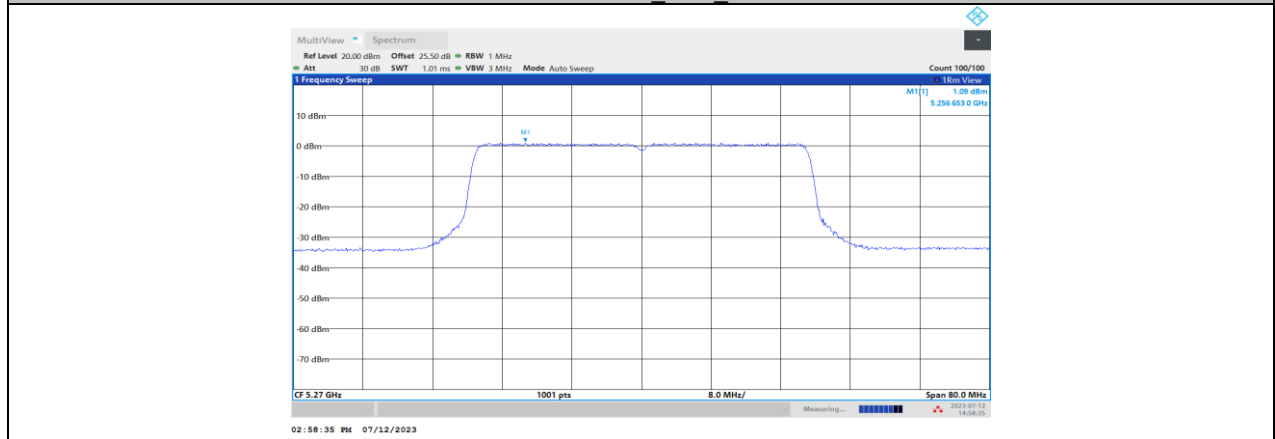
11BE40MIMO_Ant3_5230



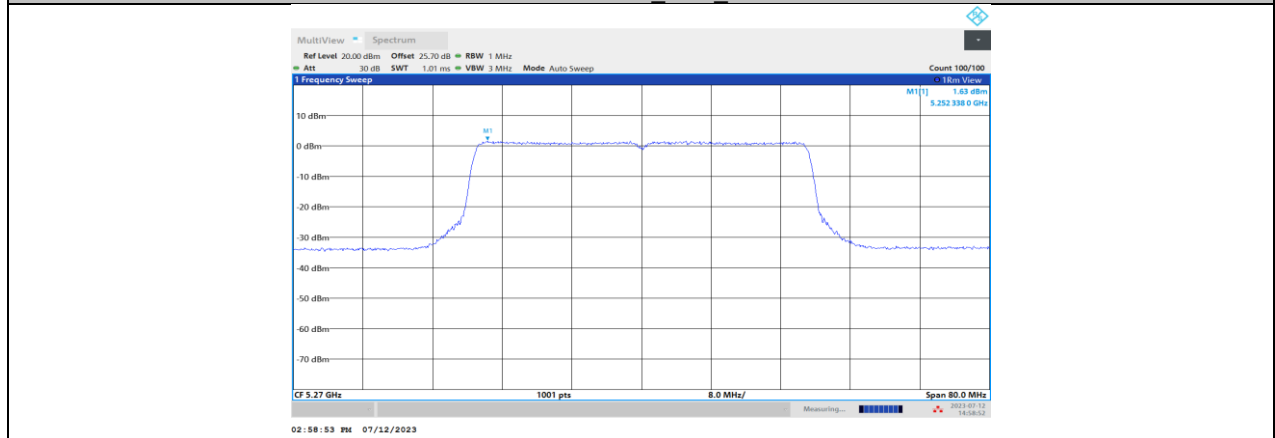
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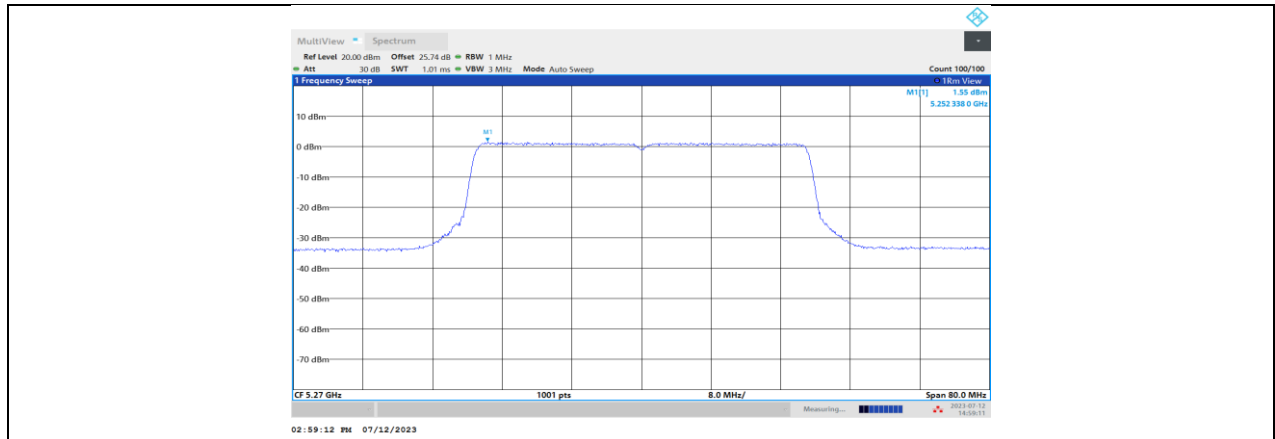
11BE40MIMO_Ant1_5270



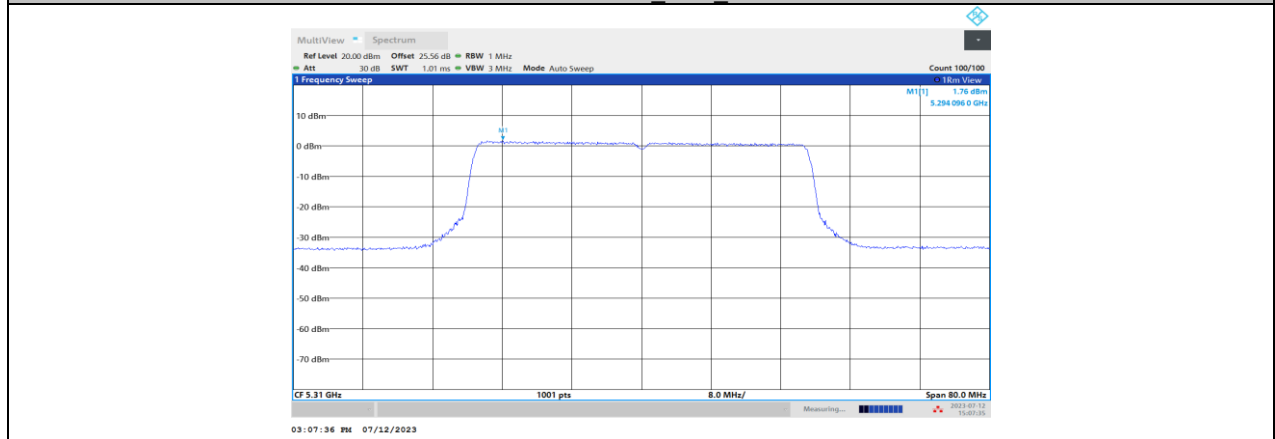
11BE40MIMO_Ant2_5270



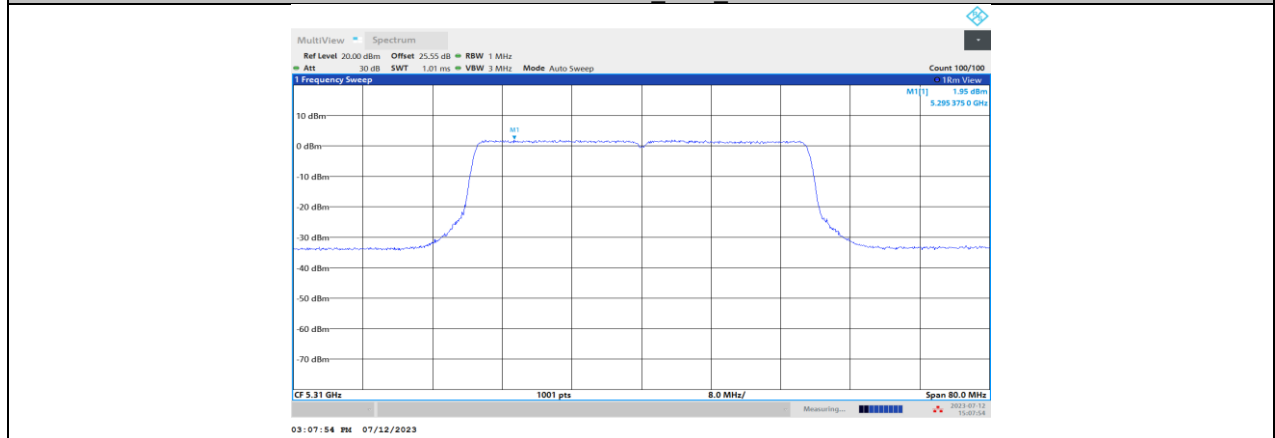
11BE40MIMO_Ant3_5270



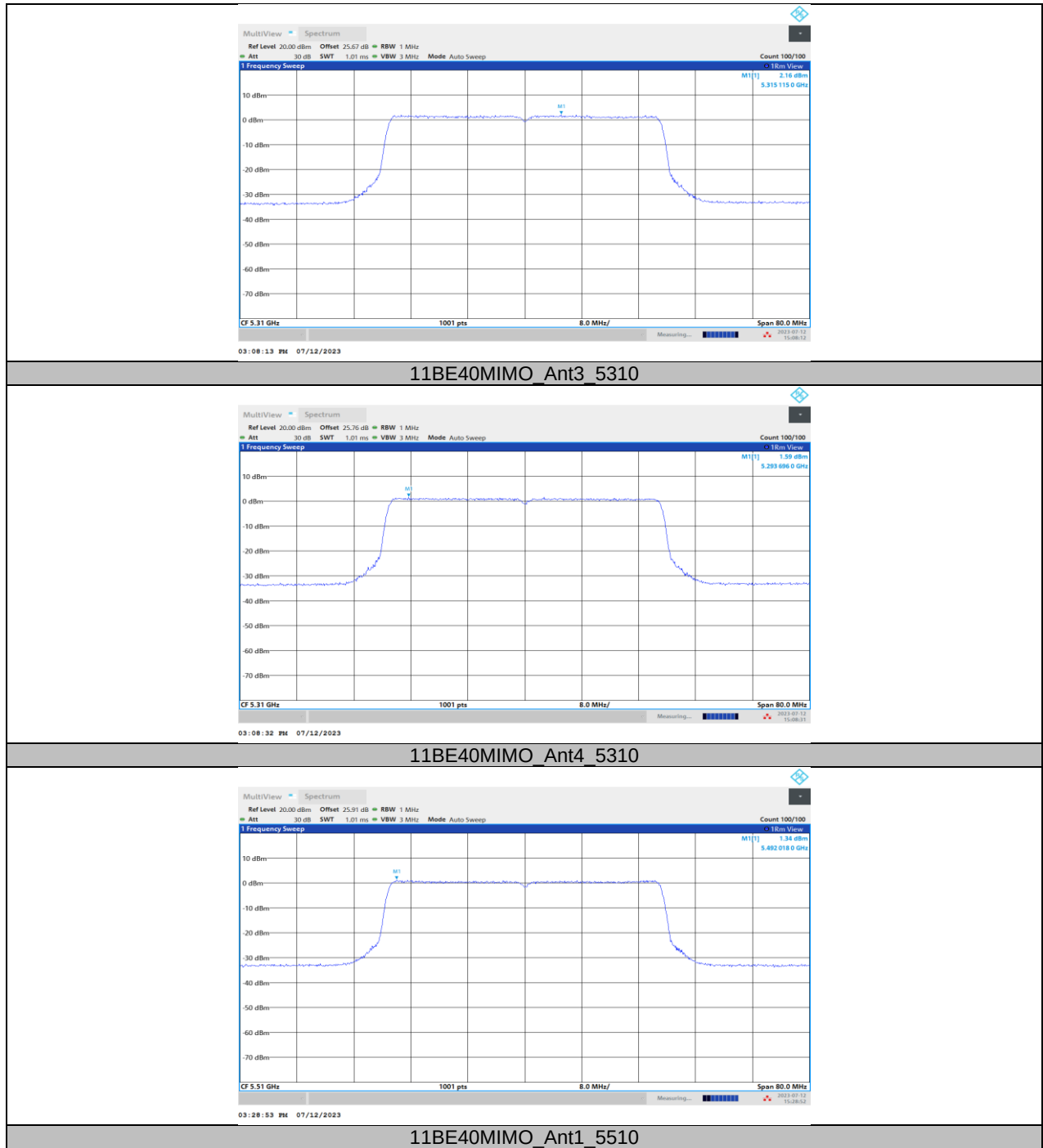
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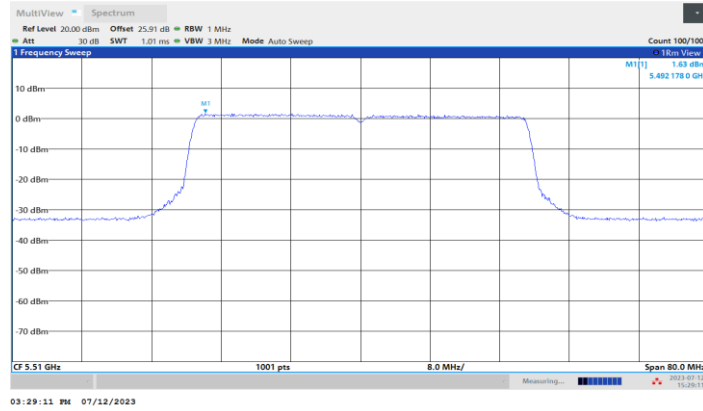


11BE40MIMO_Ant1_5310

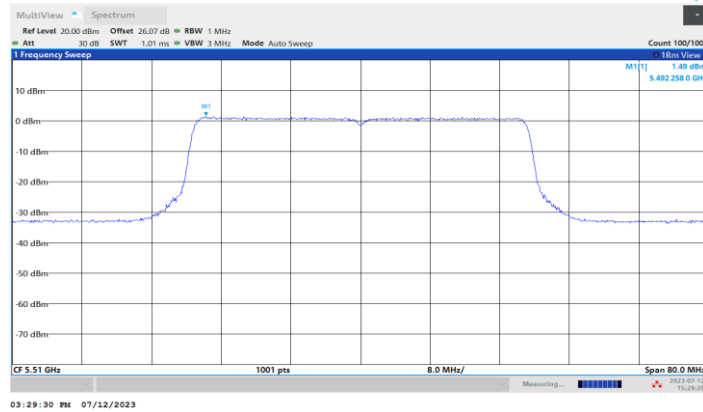


11BE40MIMO_Ant2_5310

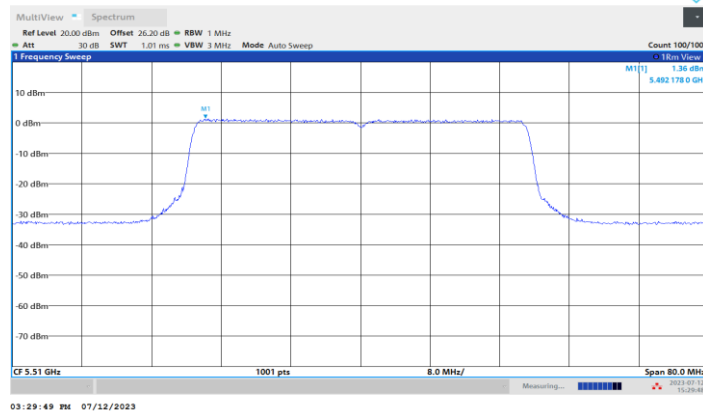




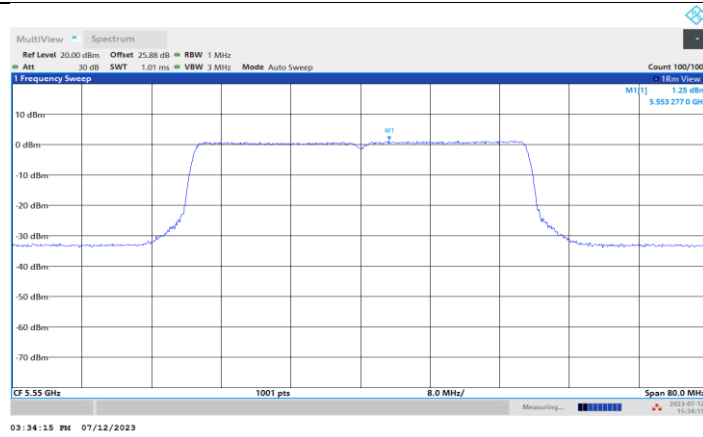
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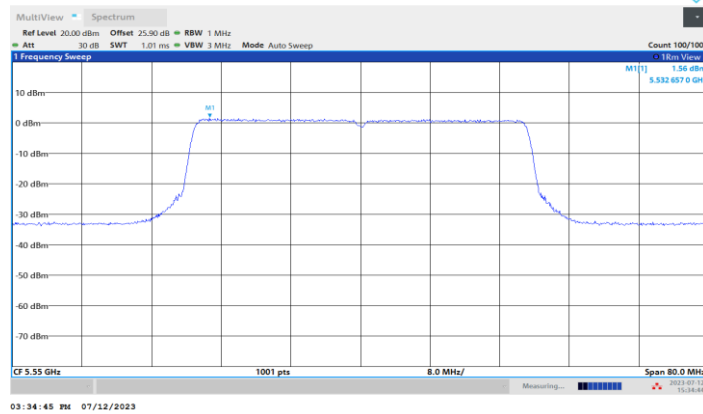
11BE40MIMO_Ant3_5510



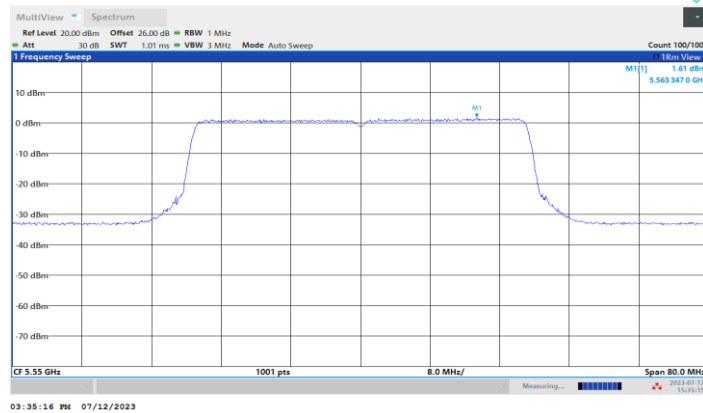
11BE40MIMO_Ant4_5510



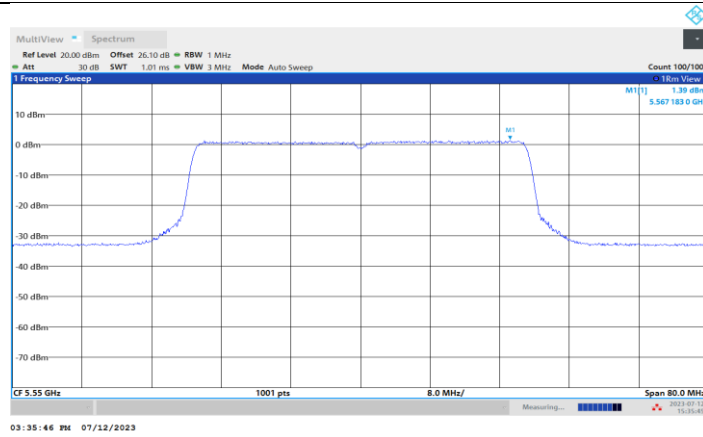
11BE40MIMO_Ant1_5550



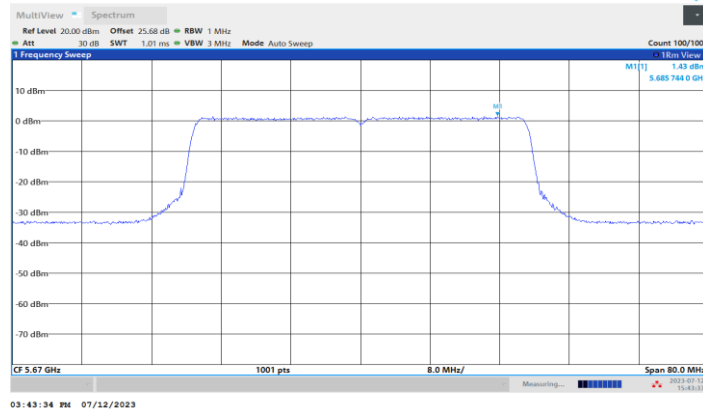
11BE40MIMO_Ant2_5550



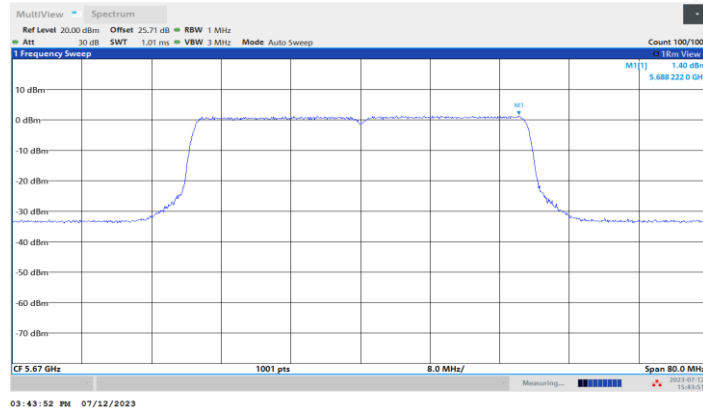
11BE40MIMO_Ant3_5550



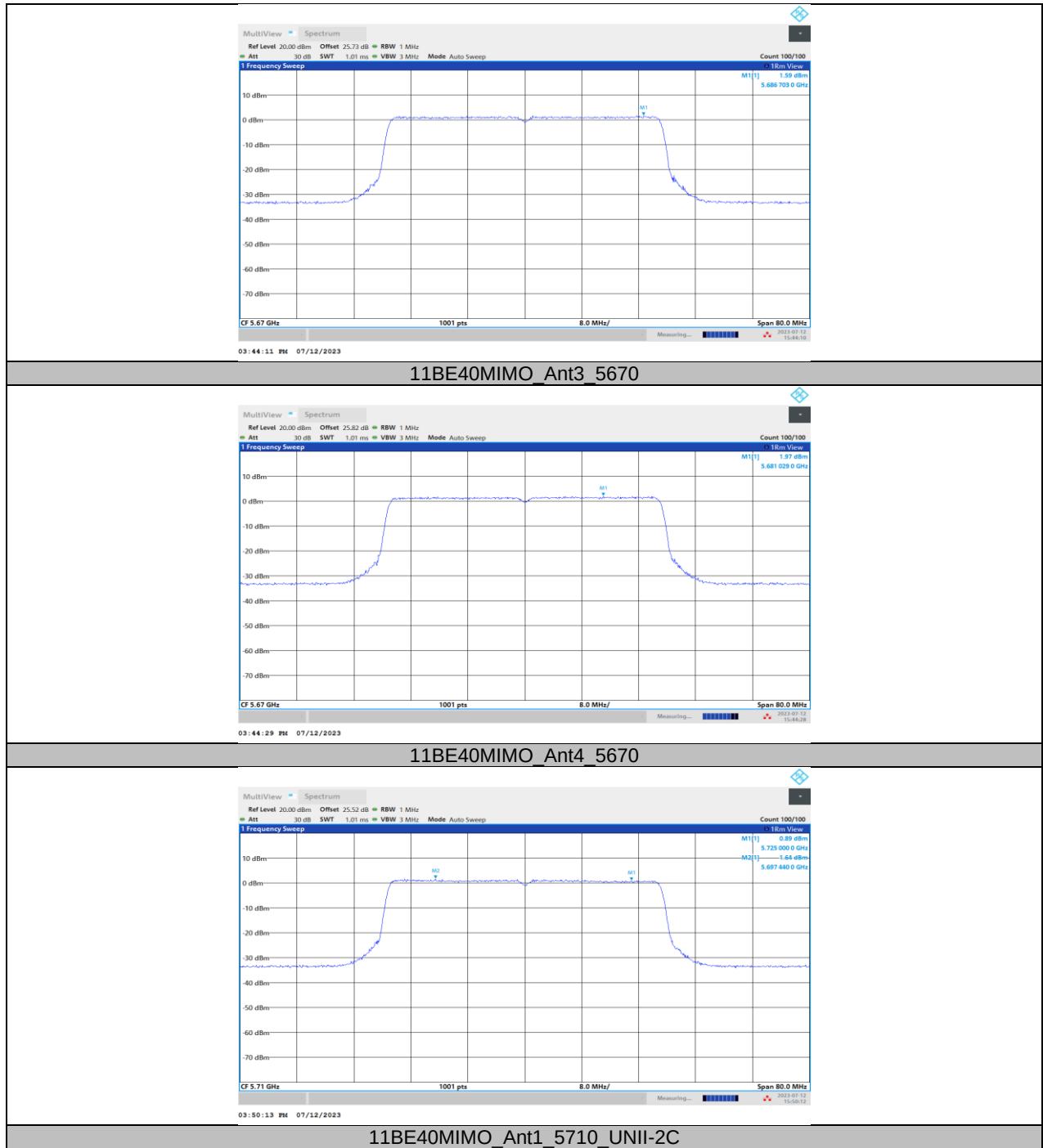
11BE40MIMO_Ant4_5550

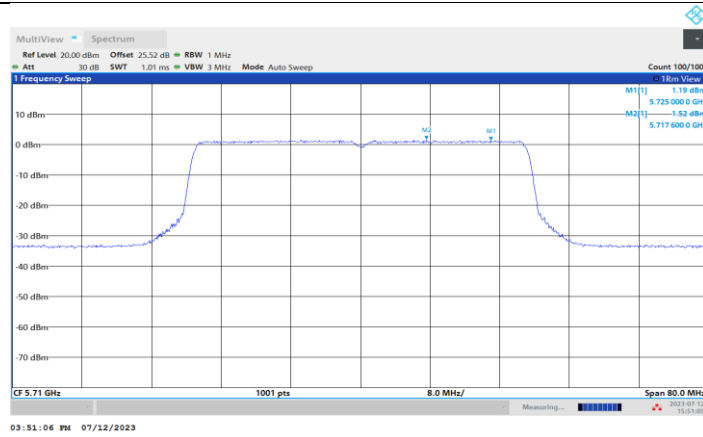


11BE40MIMO_Ant1_5670

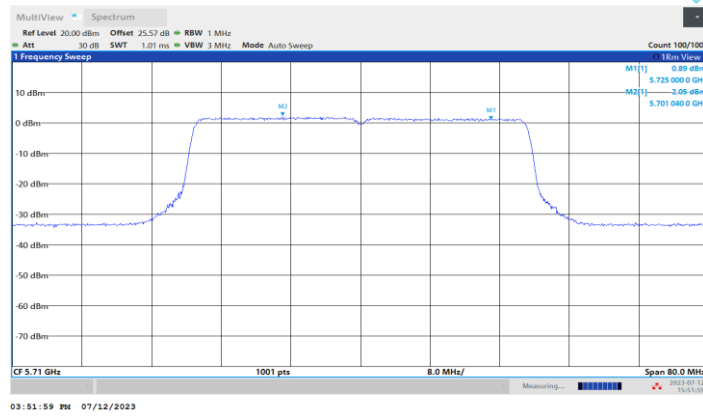


11BE40MIMO_Ant2_5670

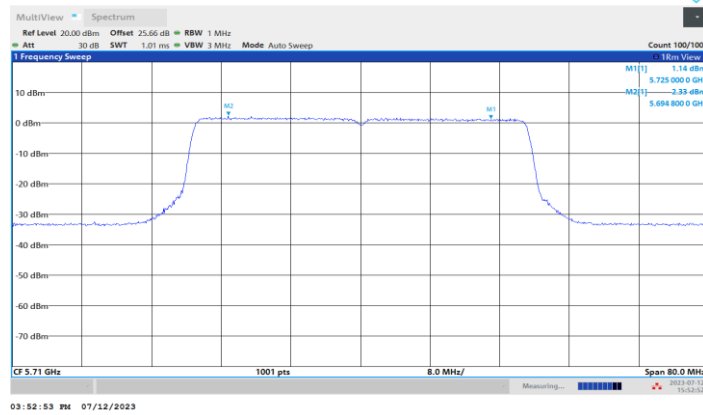




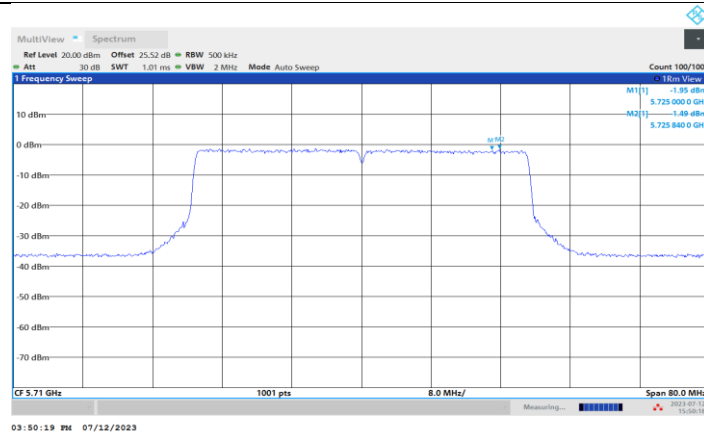
11BE40MIMO_Ant2_5710_UNII-2C



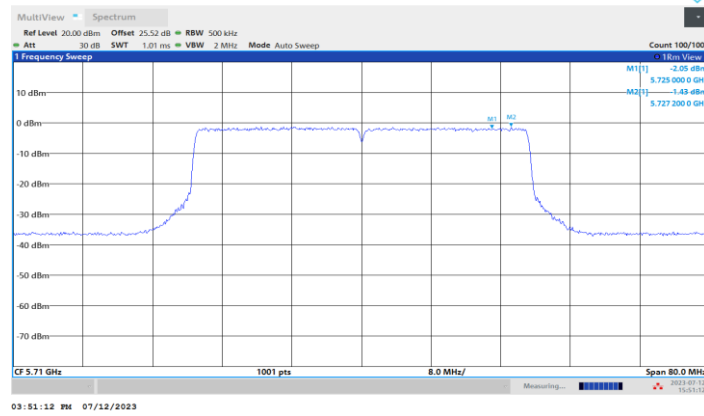
11BE40MIMO_Ant3_5710_UNII-2C



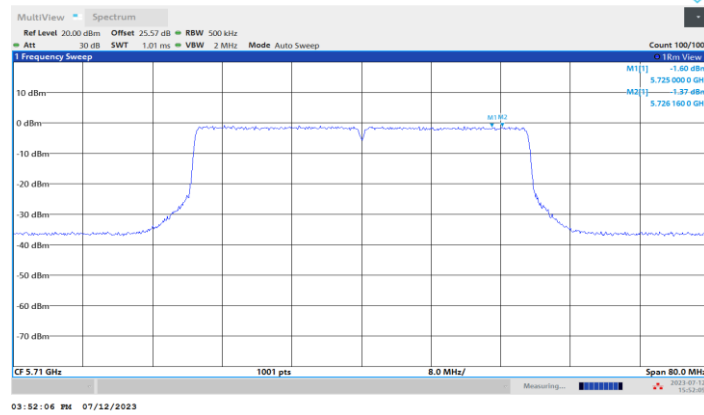
11BE40MIMO_Ant4_5710_UNII-2C



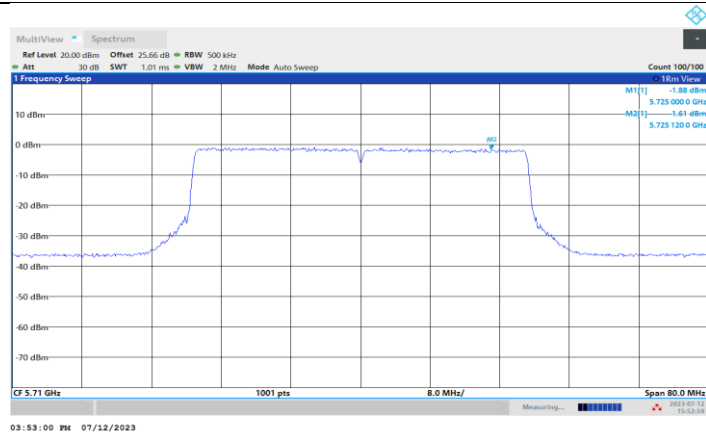
11BE40MIMO_Ant1_5710_UNII-3



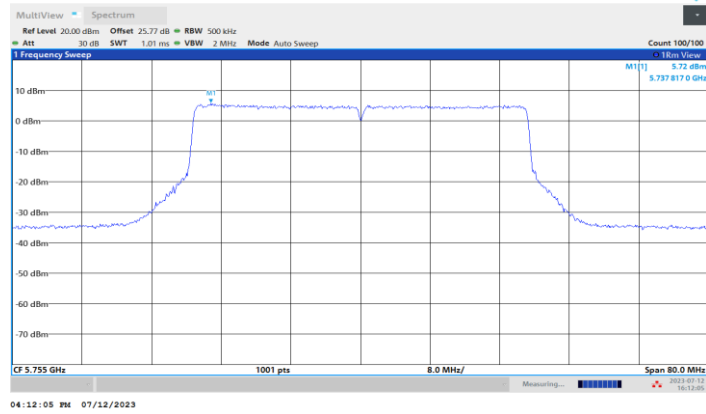
11BE40MIMO_Ant2_5710_UNII-3



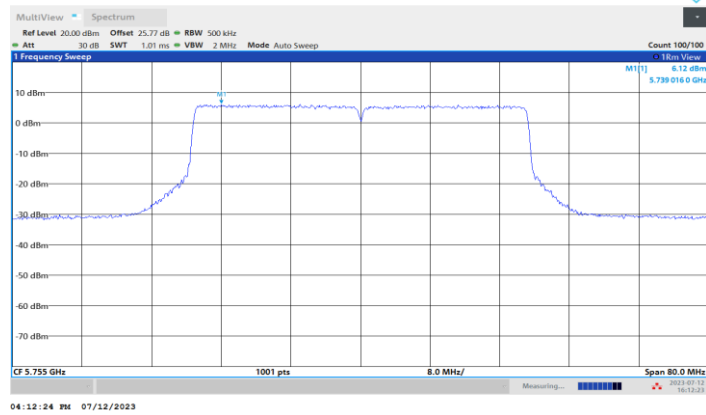
11BE40MIMO_Ant3_5710_UNII-3



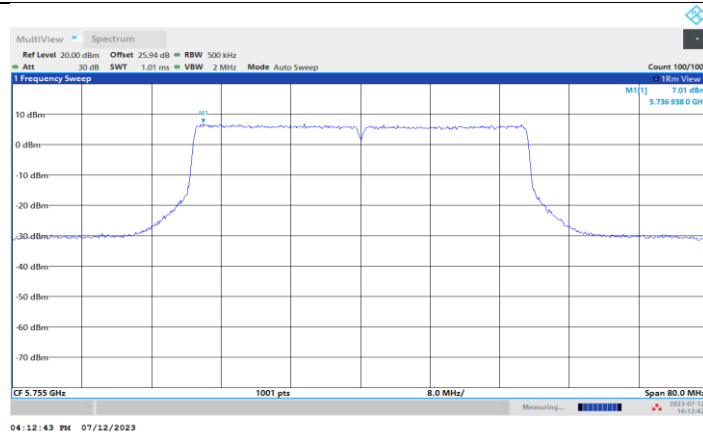
11BE40MIMO_Ant4_5710_UNII-3



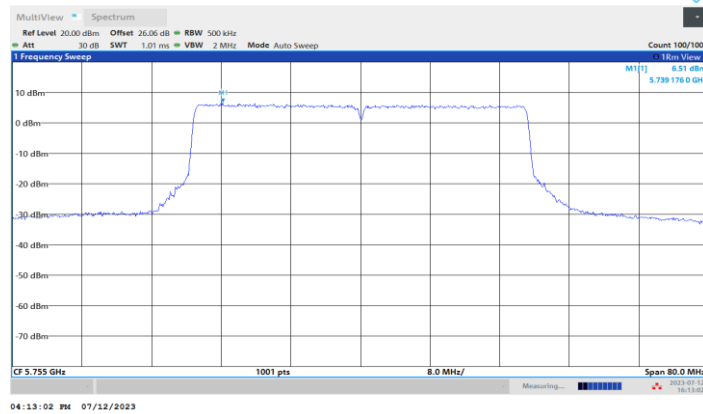
11BE40MIMO_Ant1_5755



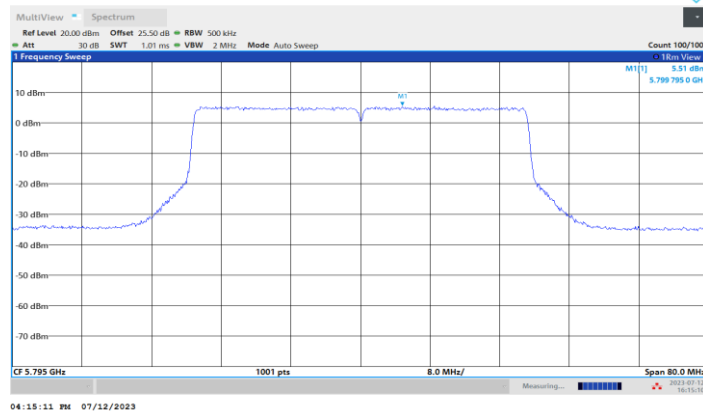
11BE40MIMO_Ant2_5755



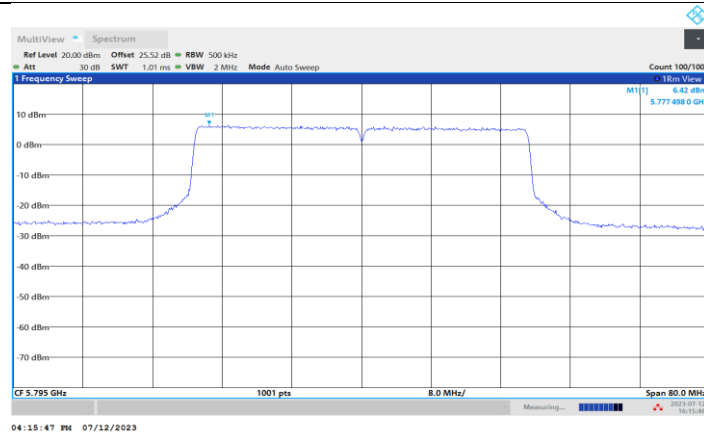
11BE40MIMO_Ant3_5755



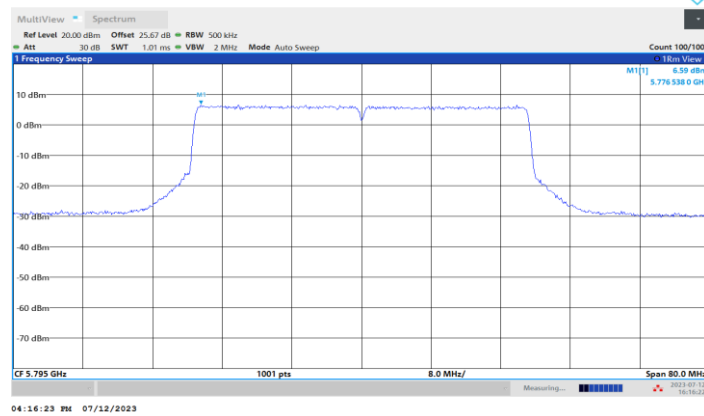
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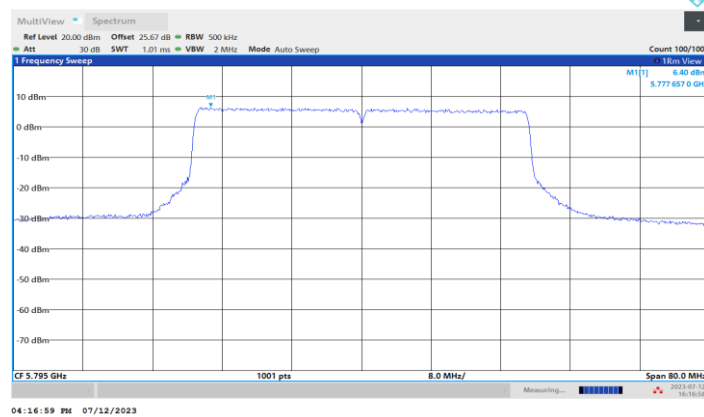
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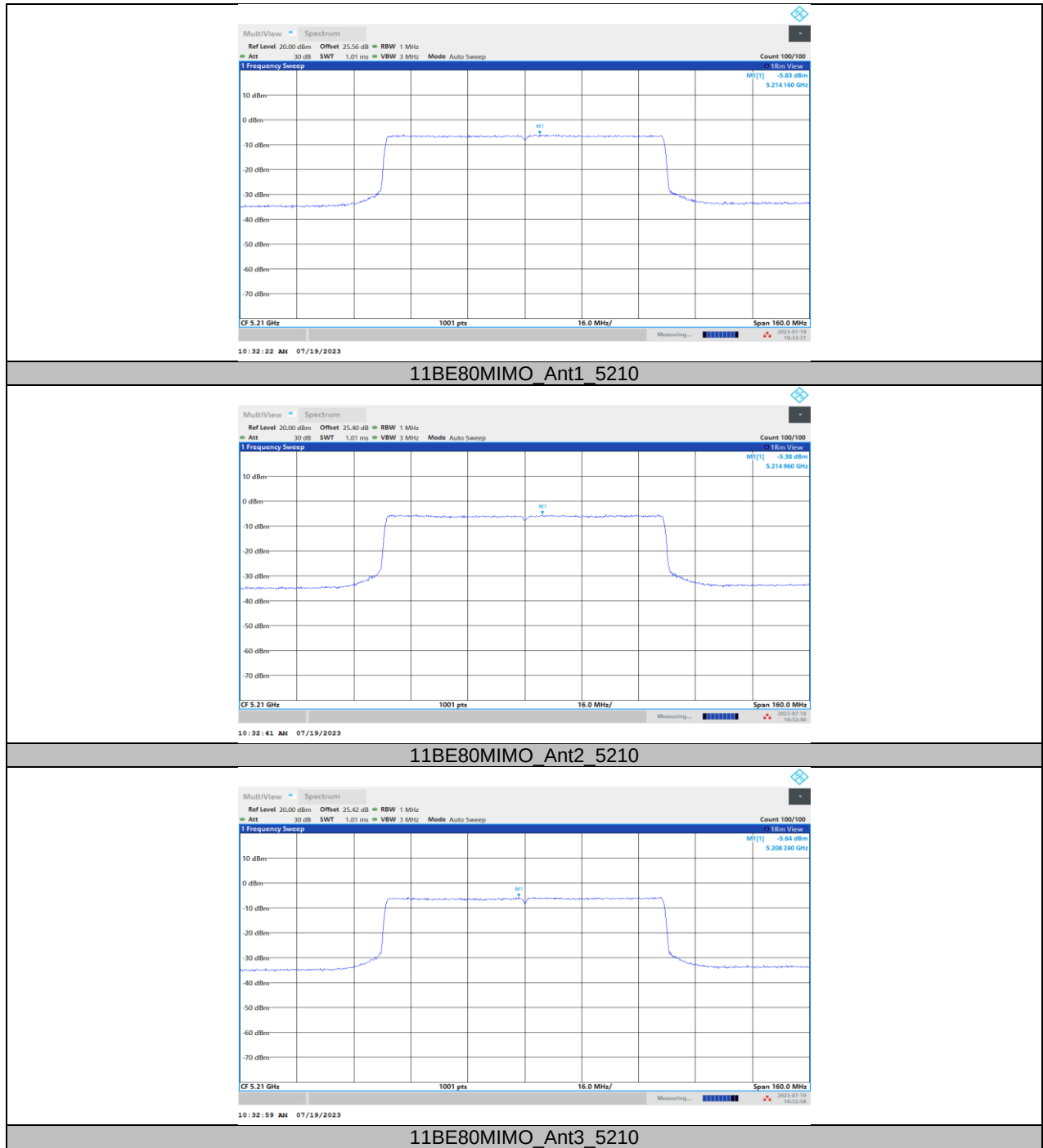
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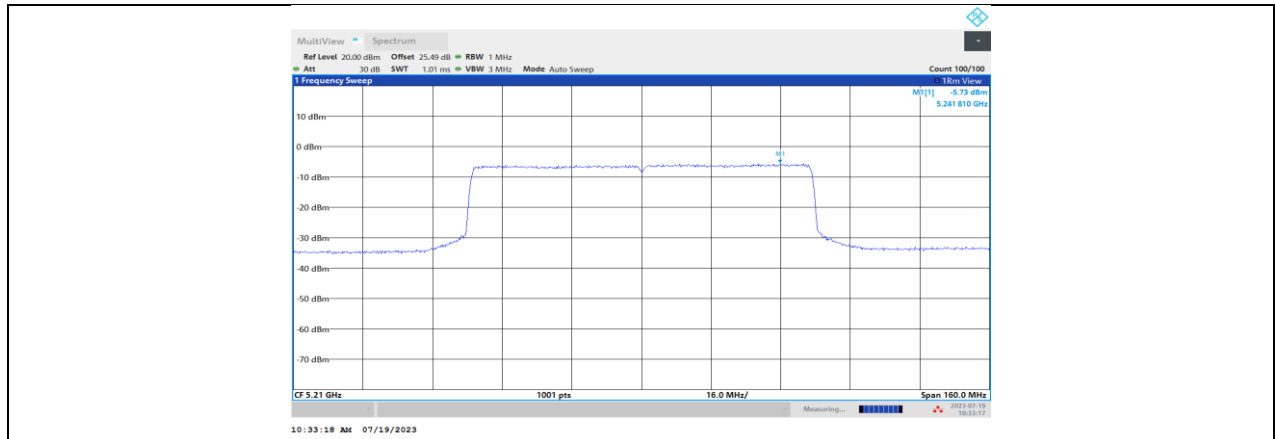


11BE40MIMO_Ant3_5795

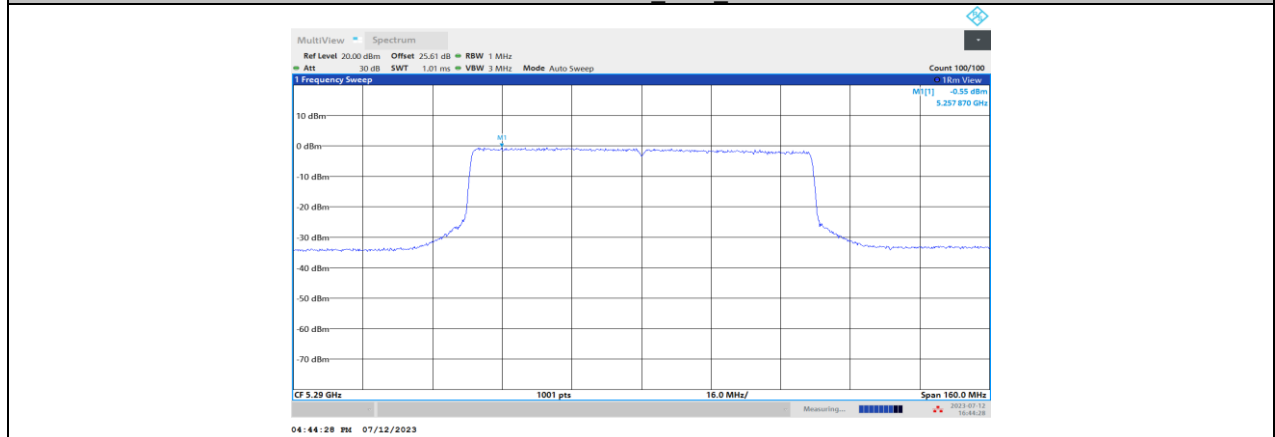


11BE40MIMO_Ant4_5795

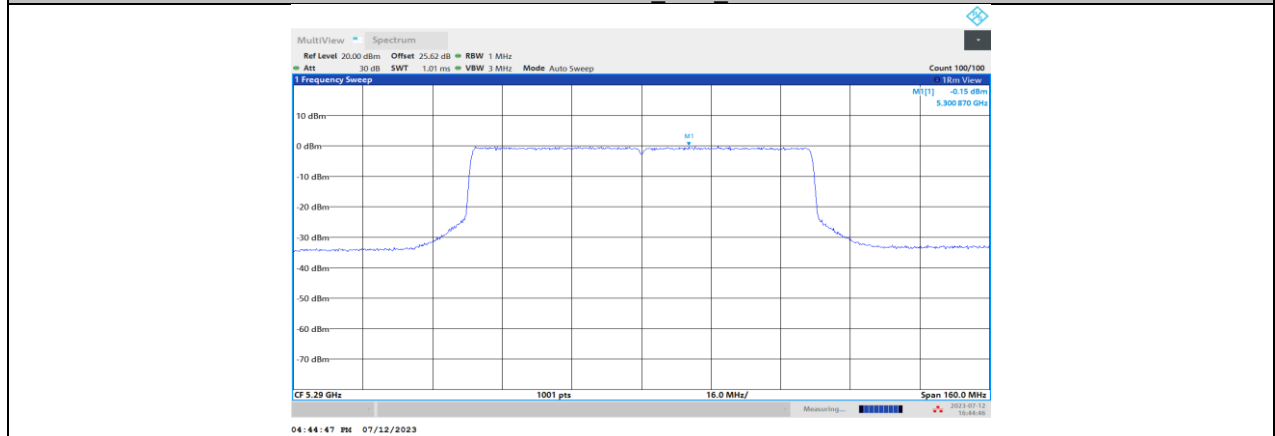




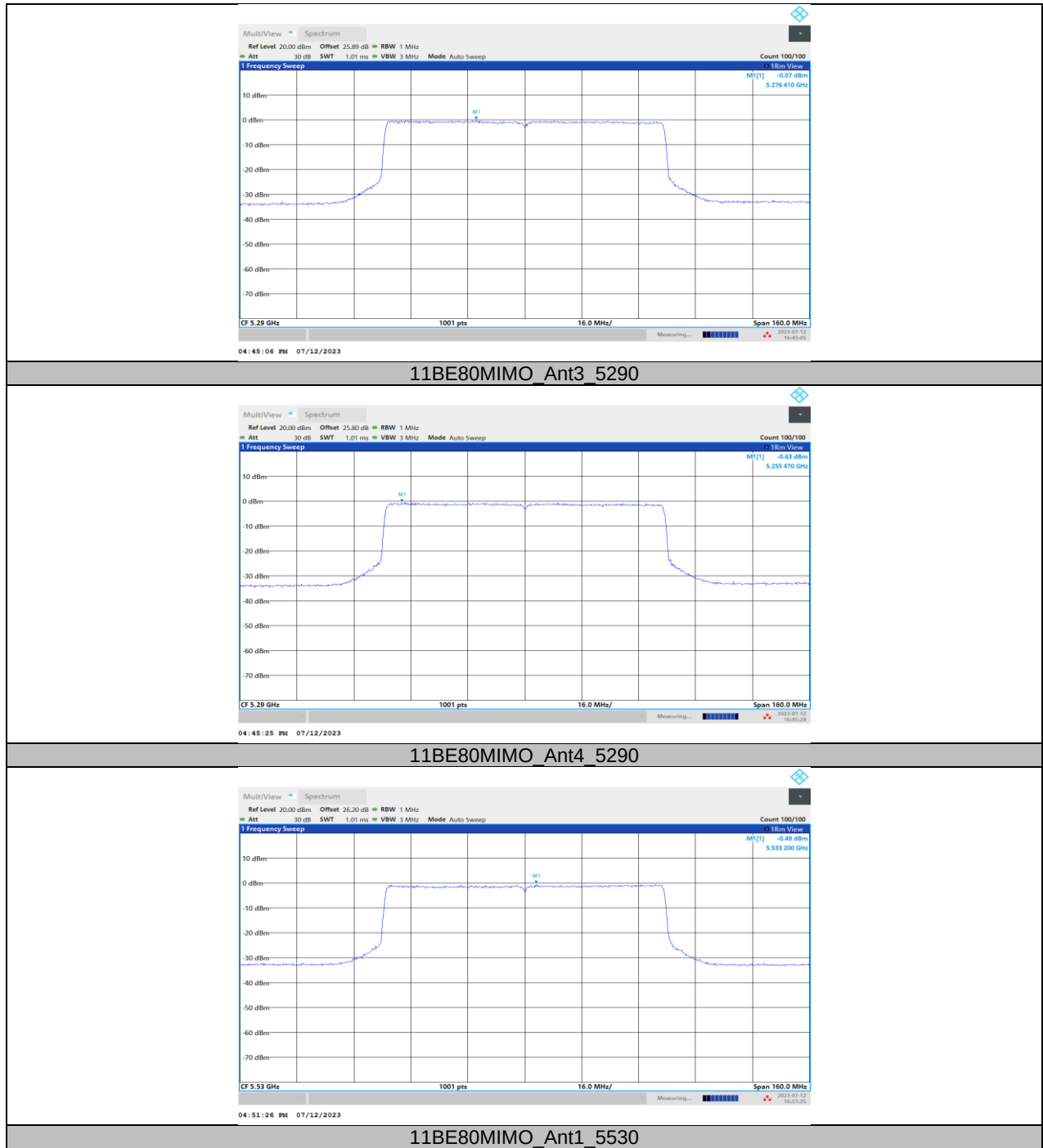
11BE80MIMO_Ant4_5210

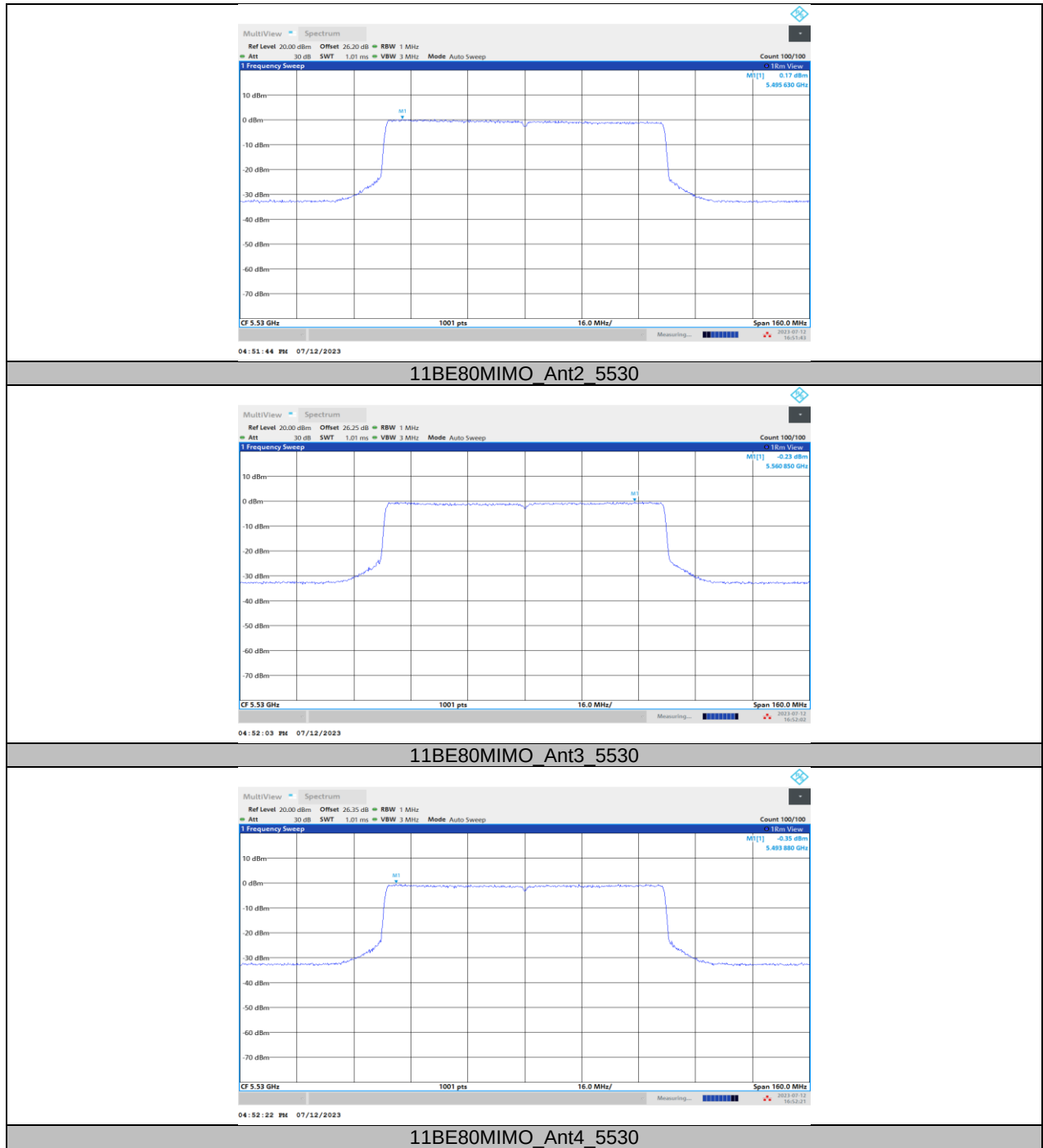


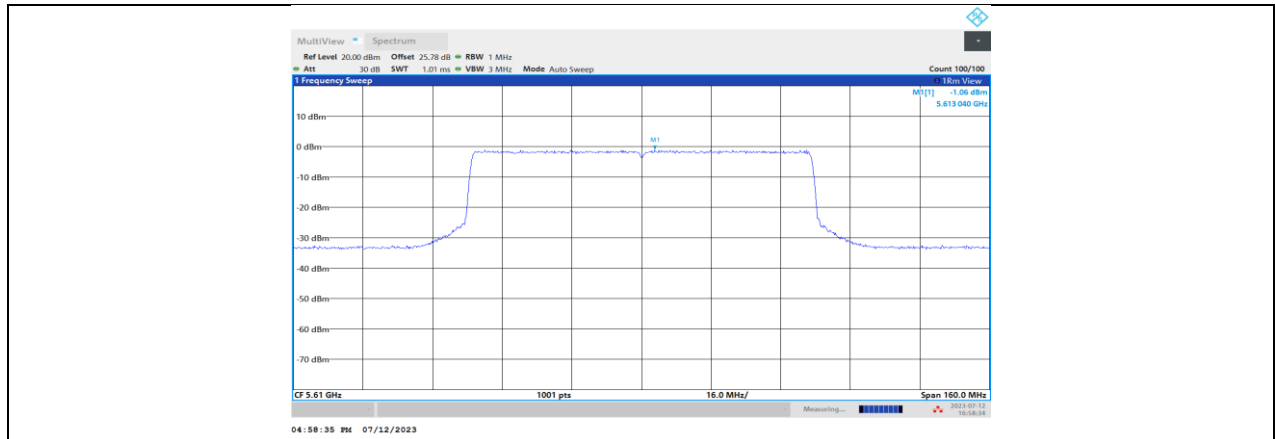
11BE80MIMO_Ant1_5290



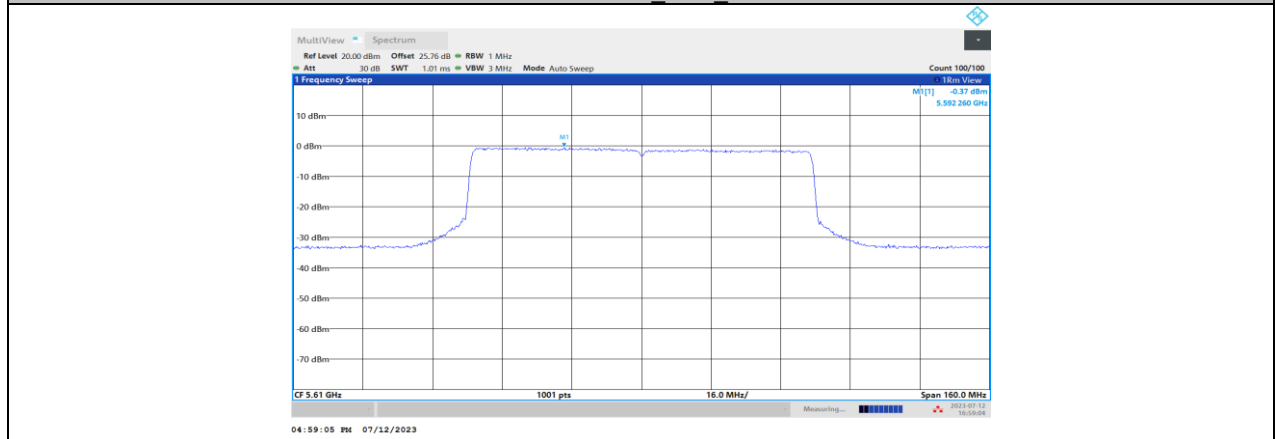
11BE80MIMO_Ant2_5290



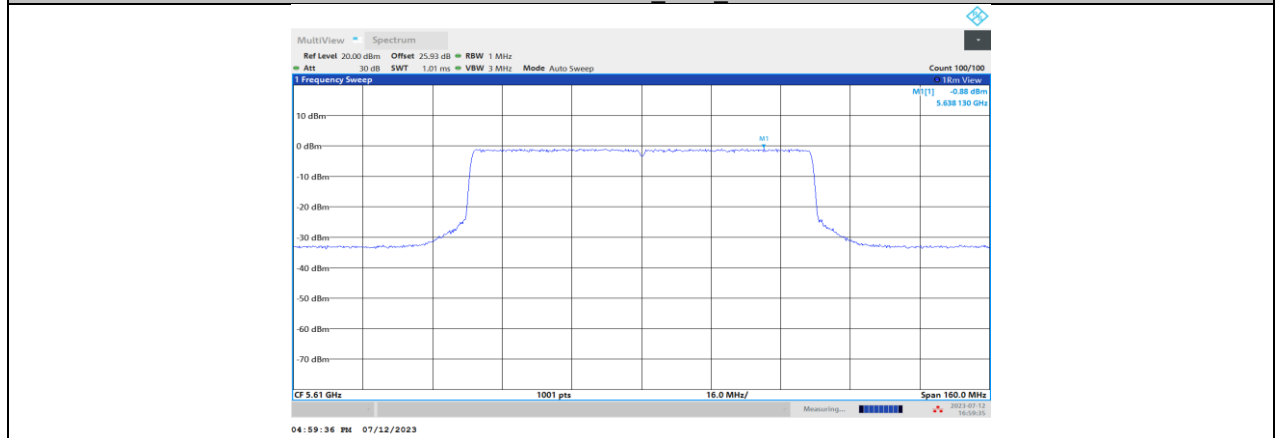




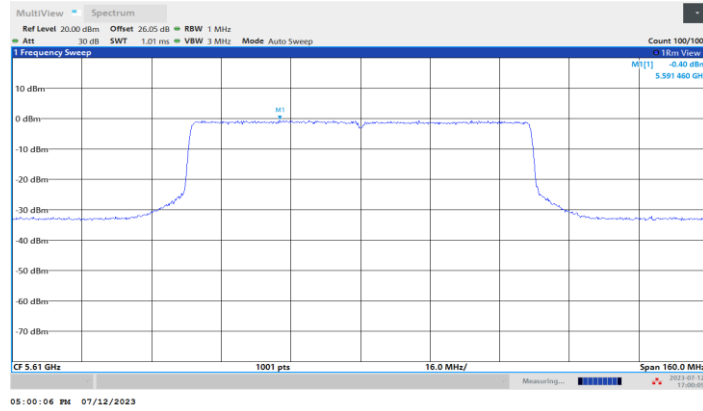
11BE80MIMO_Ant1_5610



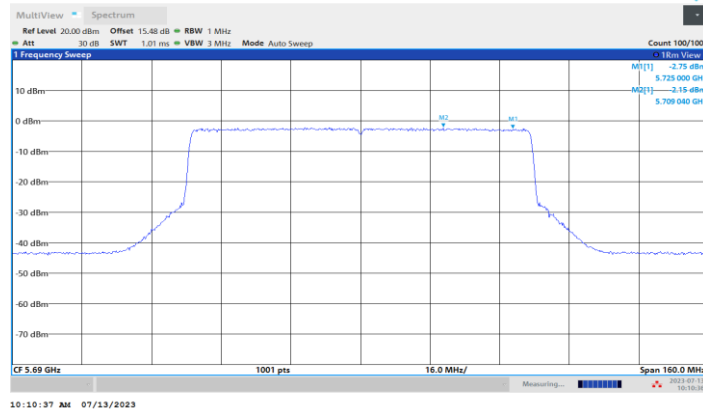
11BE80MIMO_Ant2_5610



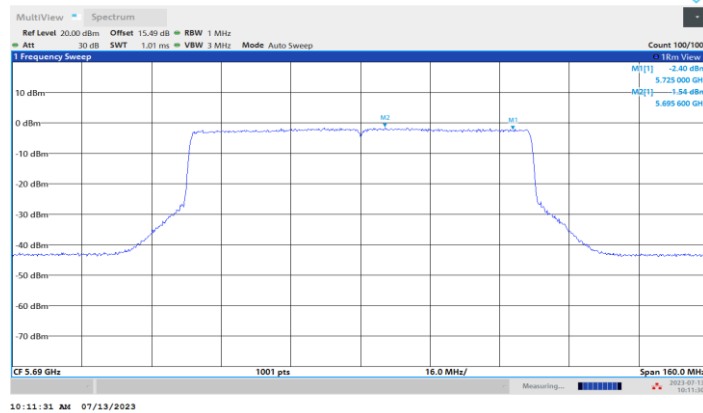
11BE80MIMO_Ant3_5610



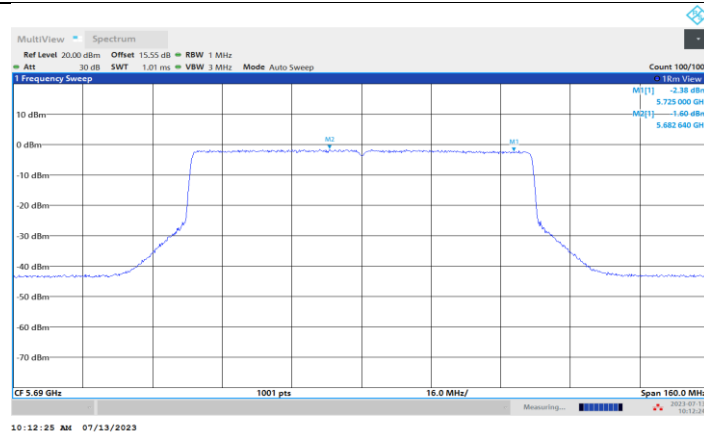
11BE80MIMO_Ant4_5610



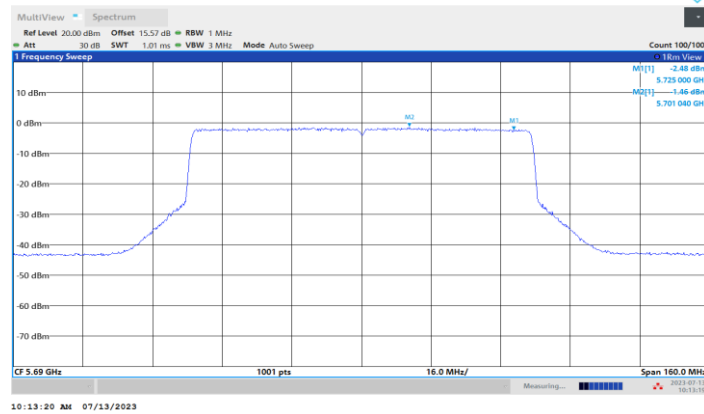
11BE80MIMO_Ant1_5690_UNII-2C



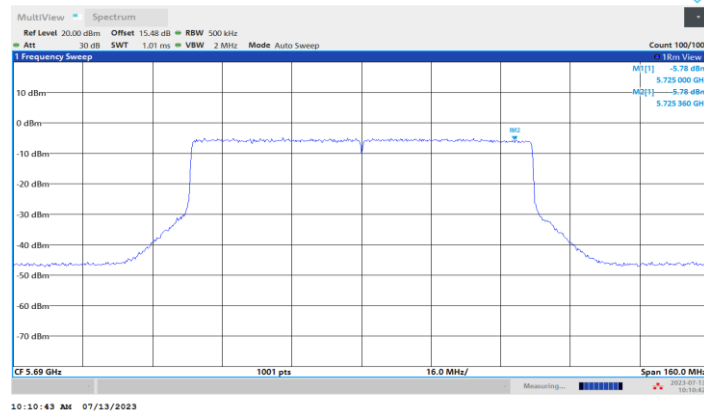
11BE80MIMO_Ant2_5690_UNII-2C



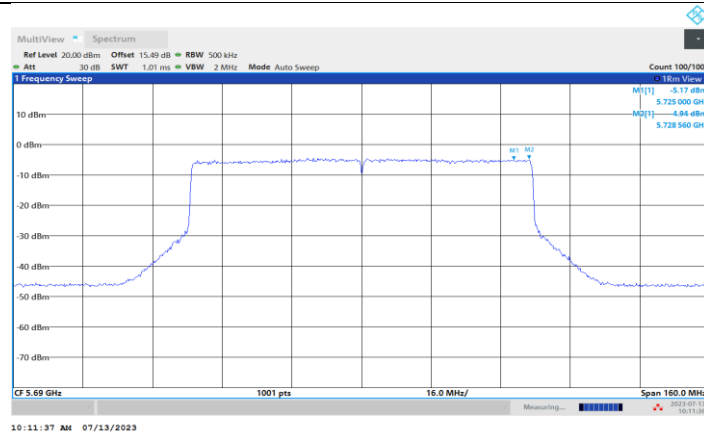
11BE80MIMO_Ant3_5690_UNII-2C



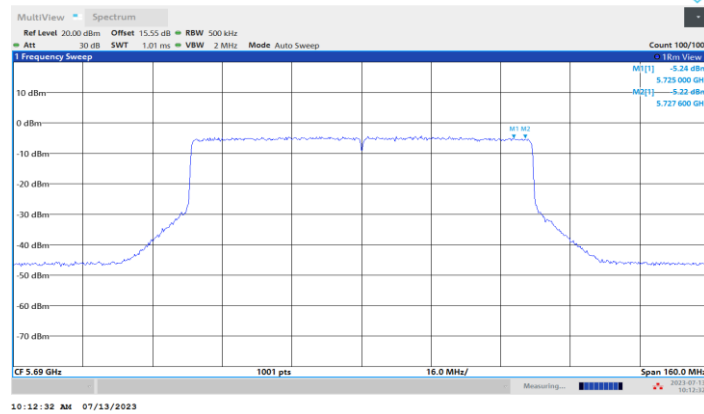
11BE80MIMO_Ant4_5690_UNII-2C



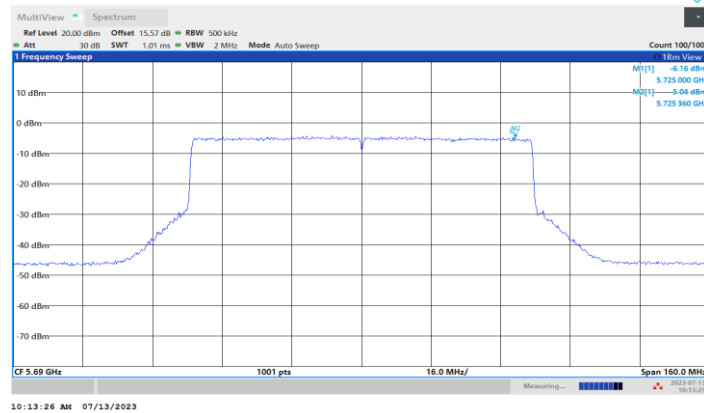
11BE80MIMO_Ant1_5690_UNII-3



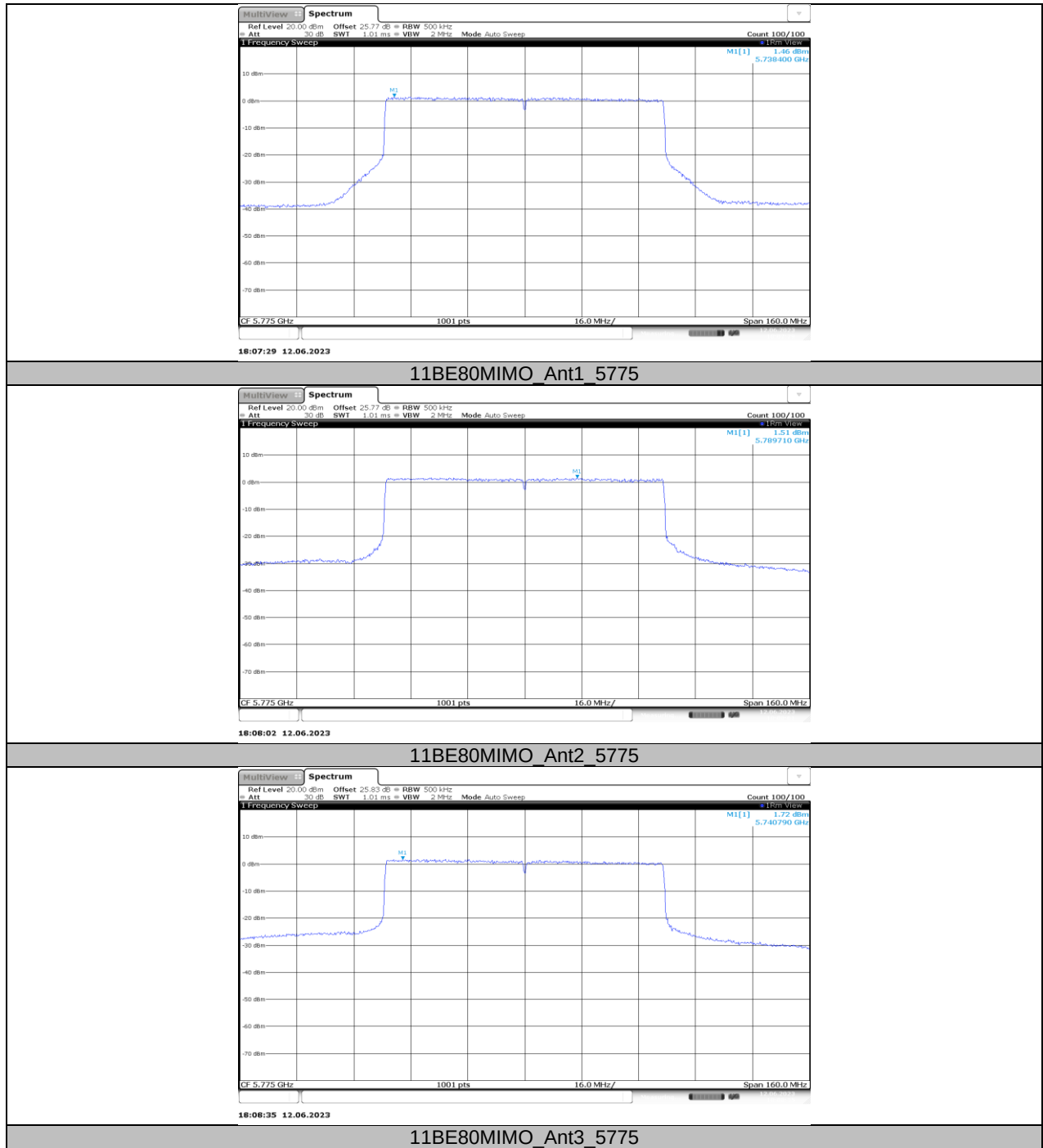
11BE80MIMO_Ant2_5690_UNII-3

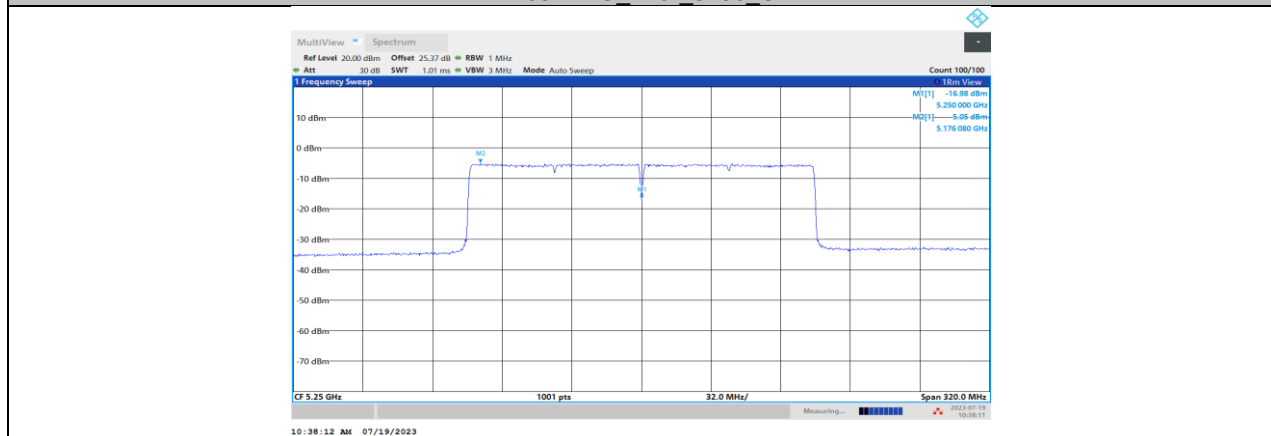
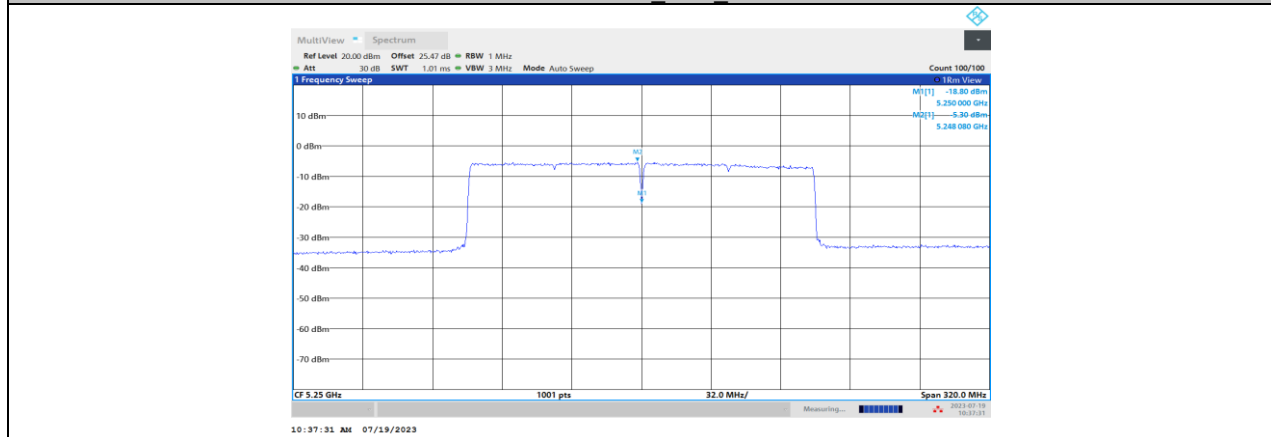
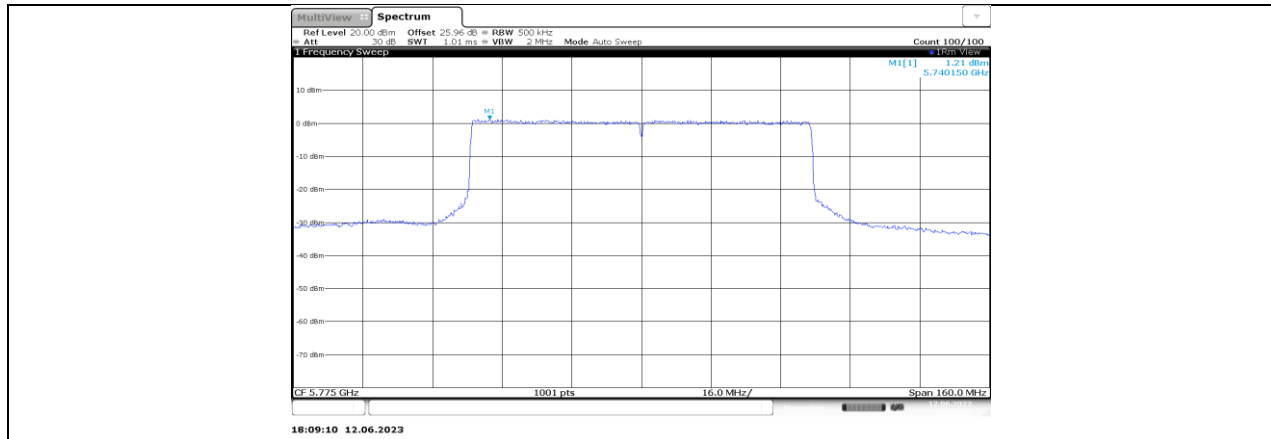


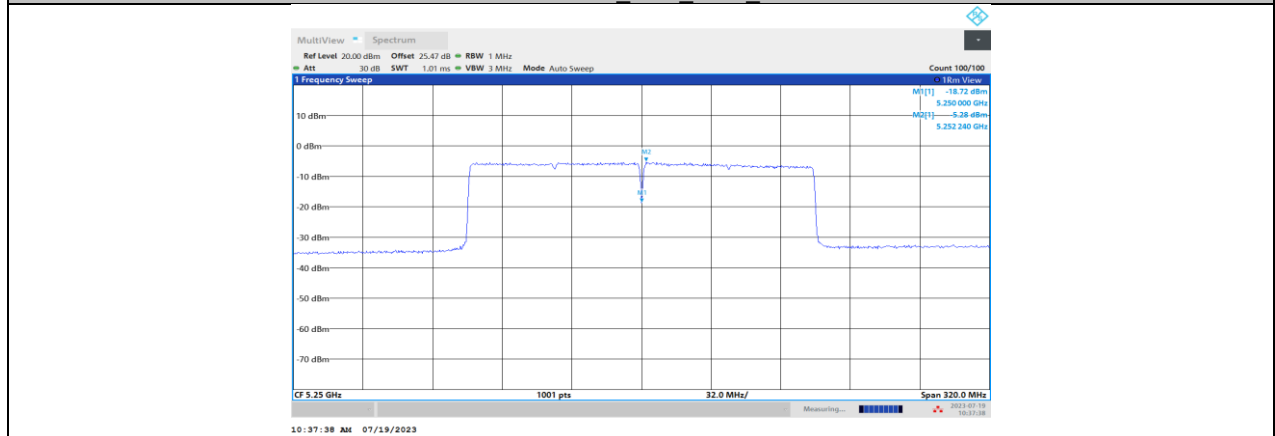
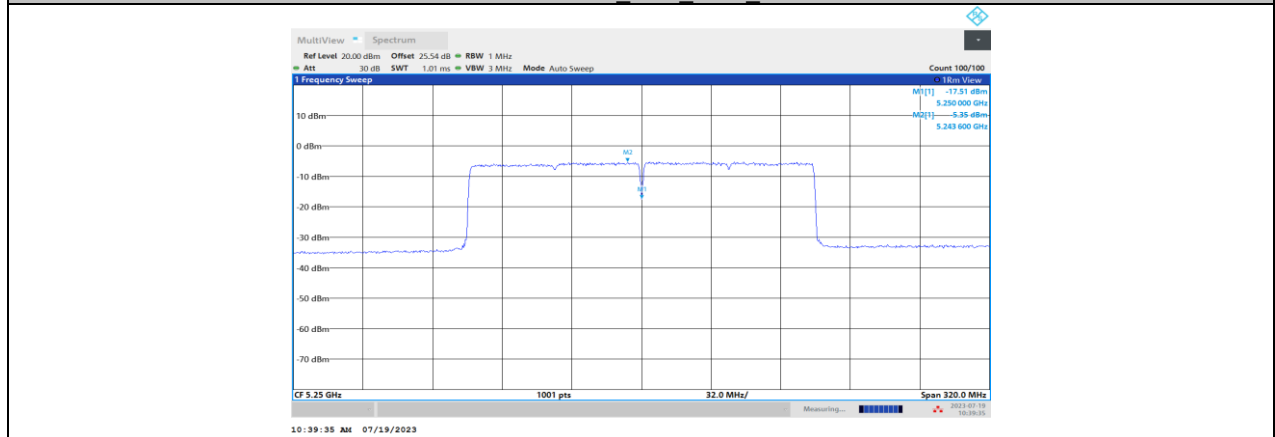
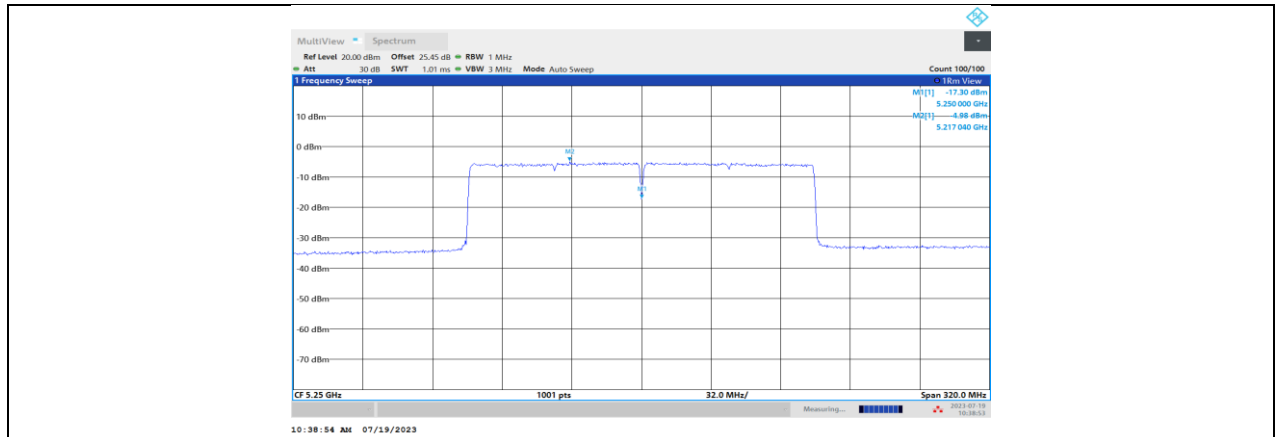
11BE80MIMO_Ant3_5690_UNII-3

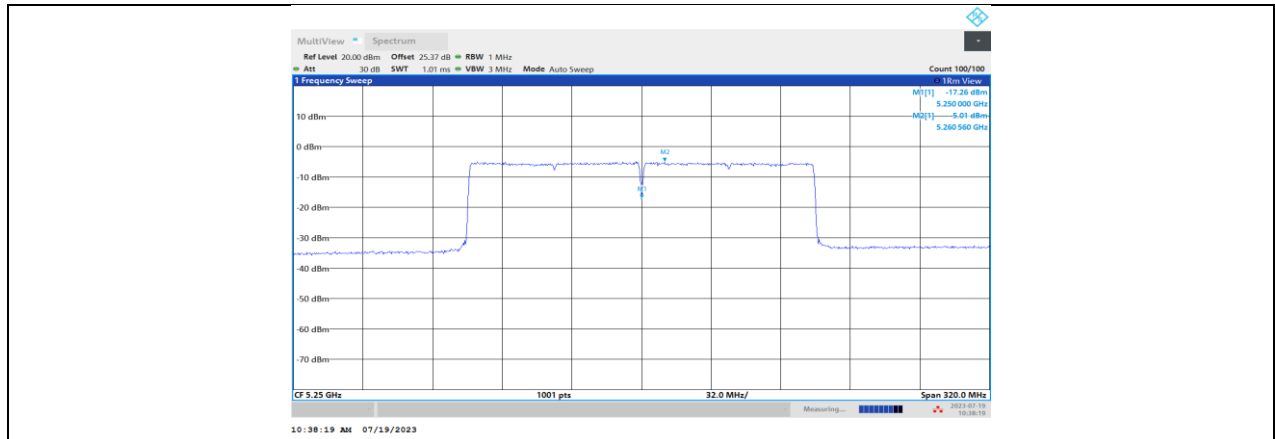


11BE80MIMO_Ant4_5690_UNII-3

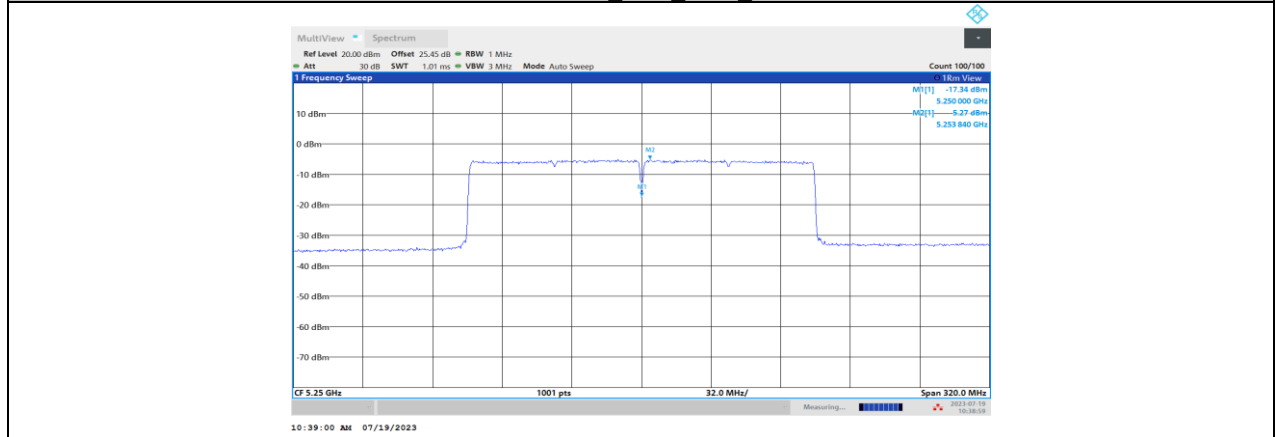




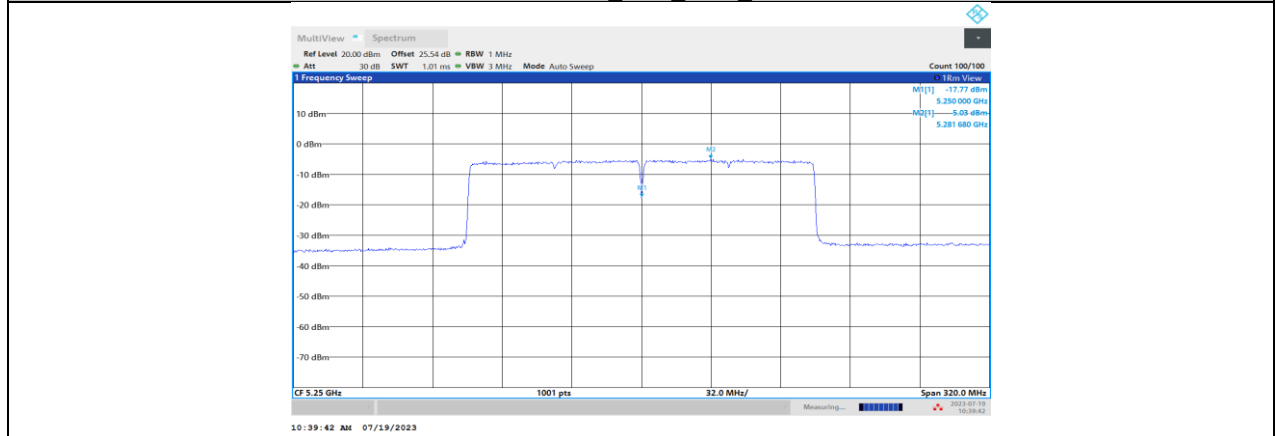




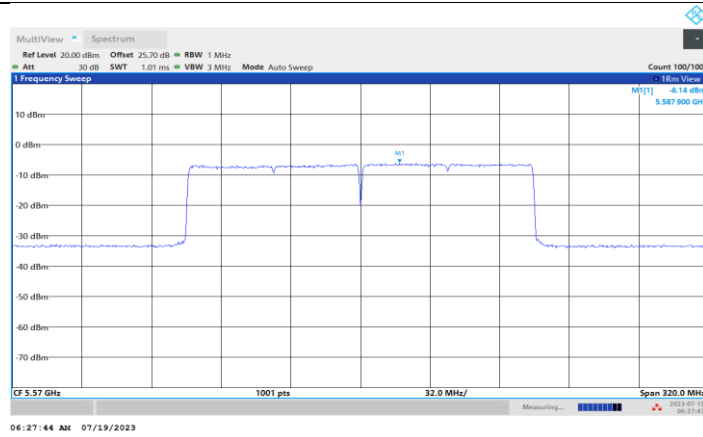
11BE160MIMO Ant2 5250 UNII-2A



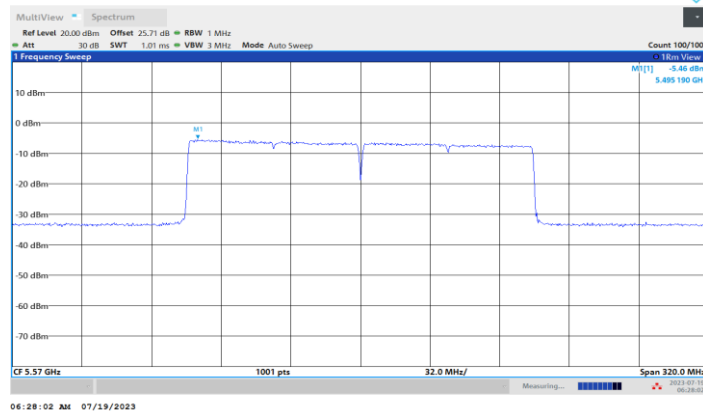
11BE160MIMO Ant3 5250 UNII-2A



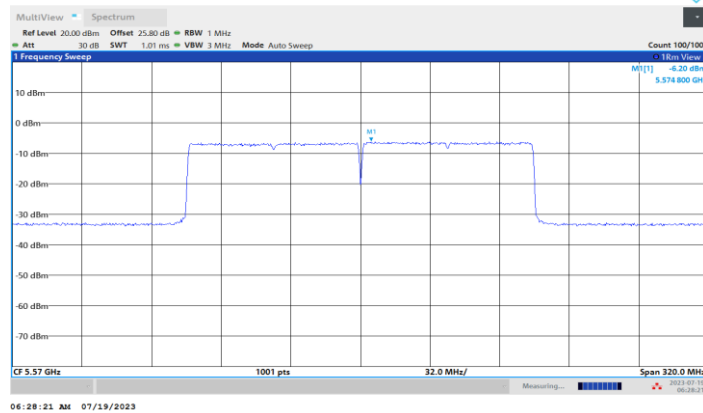
11BE160MIMO Ant4 5250 UNII-2A



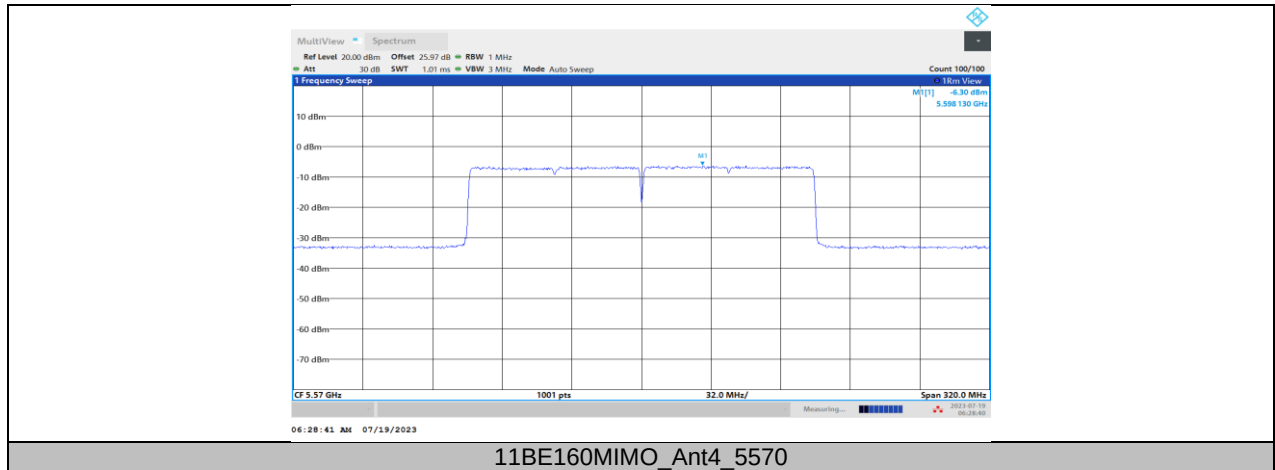
11BE160MIMO_Ant1_5570



11BE160MIMO_Ant2_5570



11BE160MIMO_Ant3_5570



11.6. APPENDIX G: DUTY CYCLE

11.6.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A-CDD	3.01	3.18	0.9465	94.65	0.24	0.33	1
11BE20MIMO	3.93	4.05	0.9704	97.04	0.13	0.25	1
11BE40MIMO	3.95	4.02	0.9826	98.26	0.08	0.25	1
11BE80MIMO	4.00	4.08	0.9804	98.04	0.09	0.25	1
11BE160MIMO	3.90	3.98	0.9799	97.99	0.09	0.26	1

Note:

Duty Cycle Correction Factor= $10\log(1/x)$.

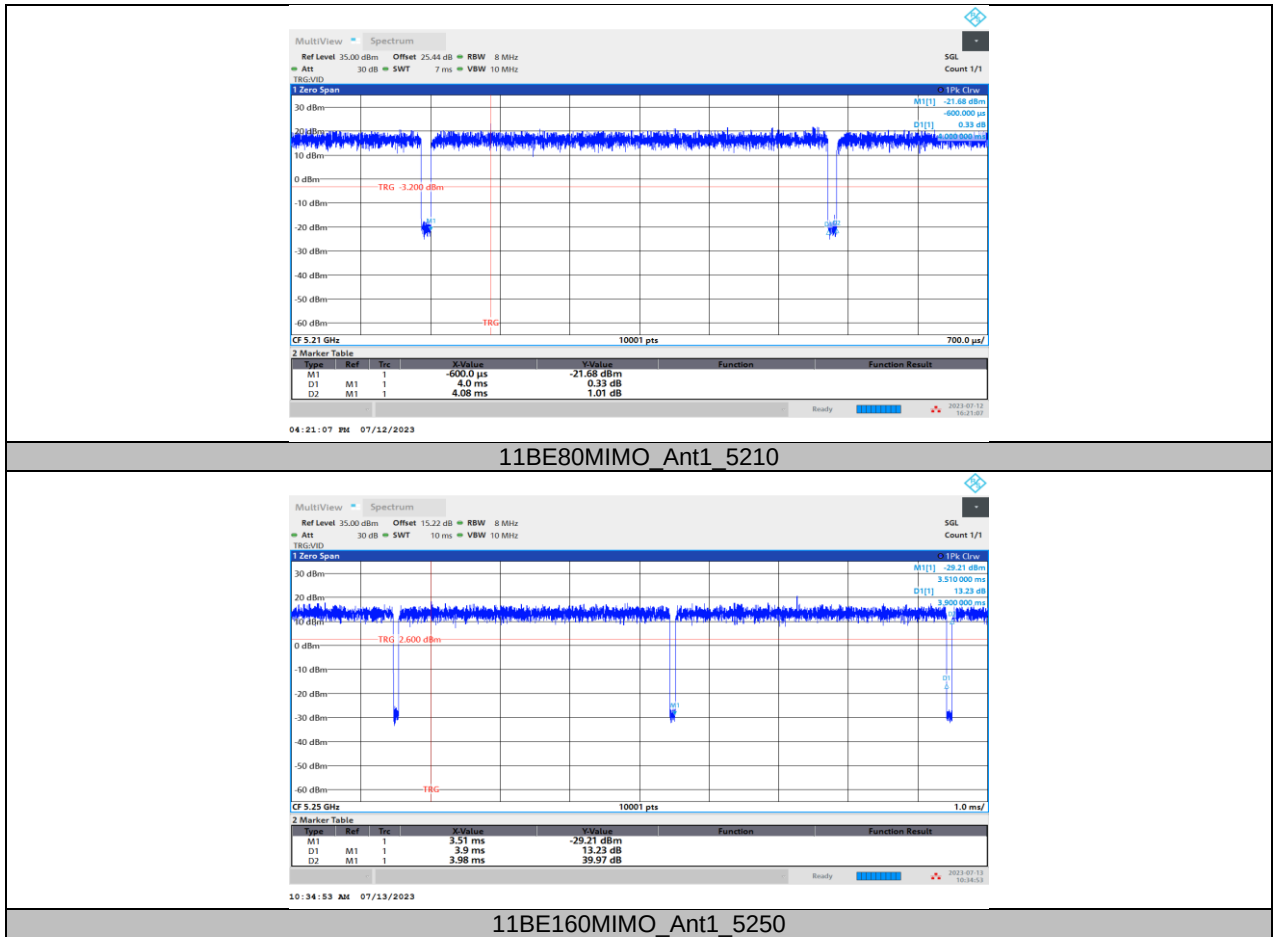
Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.6.2. Test Graphs





END OF REPORT