

Chicony Chip Antenna Test plan

- R&D Team, IPD BU
- Chicony Electronics Co., Ltd.
- Version: 0.1
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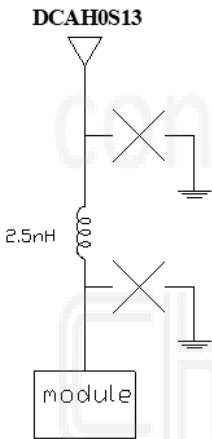
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1) Revision History

Date	Revision	Explanation of changes	Edit by
22-Dec-2023	0.1	Initial release	Eric Su

2) Matching circuit

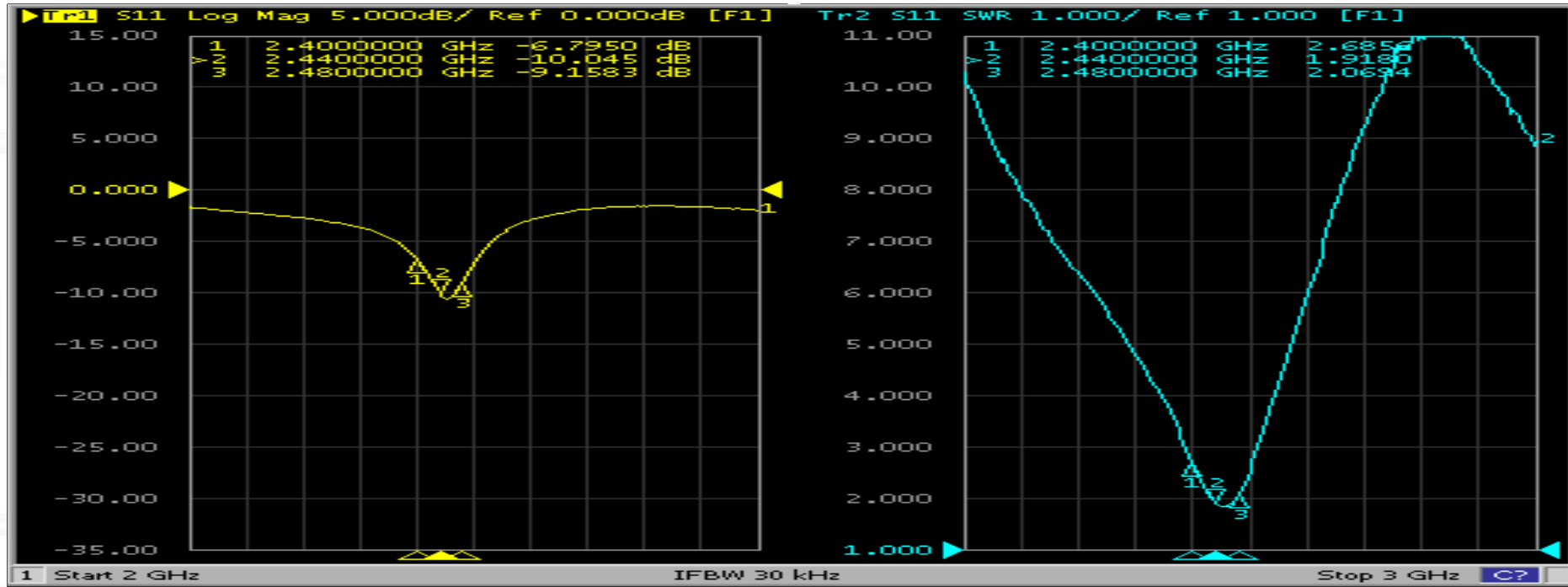
Example



Note:
Please use Murata 0201 series(LQP03/GRM03) for matching components.

Location	Value	Murata PN
	2.5nH	LQP03TG2N5B02

3) Antenna S11/VSWR



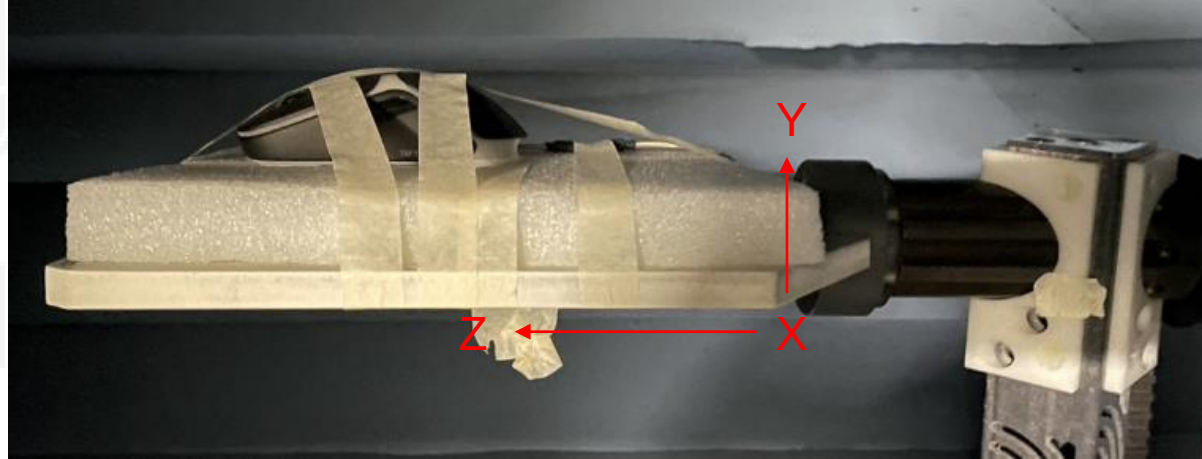
Note:

Start Freq.= 2GHz, Stop Freq.= 3GHz, Mark point= 2.4GHz, 2.44GHz, 2.48GHz

4) DUT setting

DUT Setting

Example



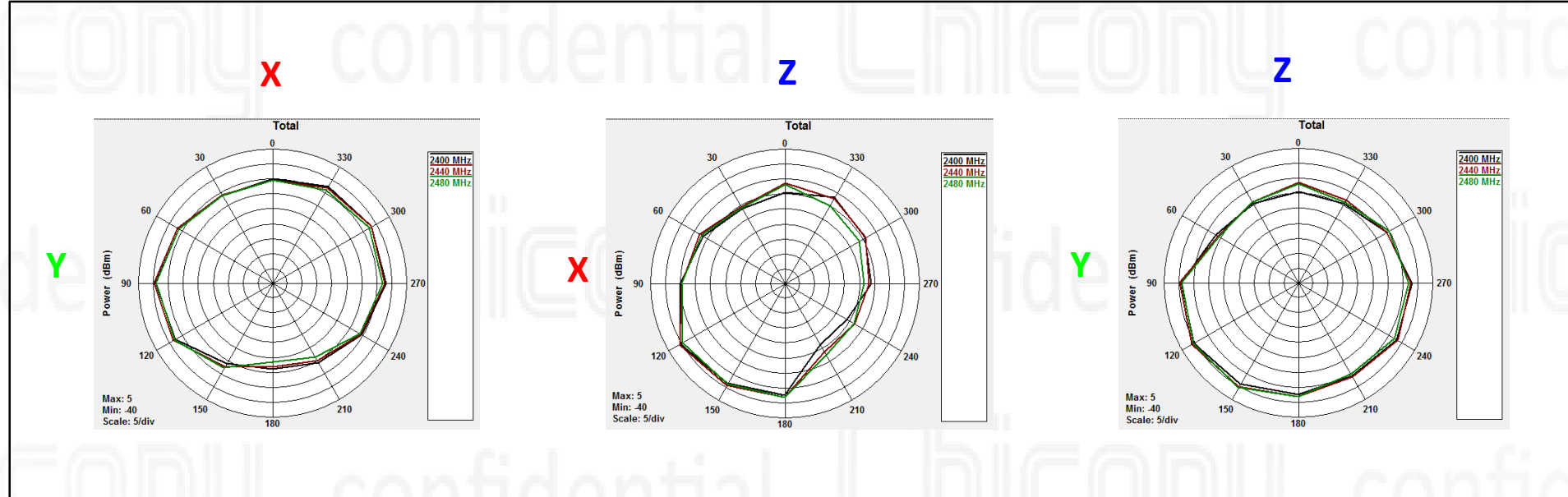
Note:

Please provide photos during testing

5) 2D & 3D pattern

2D Gain pattern

Example



Phi Polarization	XY Plane		XZ Plane		YZ Plane	
Frequency (MHz)	Peak Gain (dBi)	Avg. Gain (dBi)	Peak Gain (dBi)	Avg. Gain (dBi)	Peak Gain (dBi)	Avg. Gain (dBi)
2400MHz	-0.84	-4.07	0.21	-5.56	0.33	-3.34
2440MHz	-0.41	-4.02	0.62	-4.95	1.00	-2.82
2480MHz	-1.05	-4.54	-0.57	-5.68	0.48	-3.21

2D Pattern setting : Max=5 dB, Min=-40 dB, Step=5 dB

Thank You

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