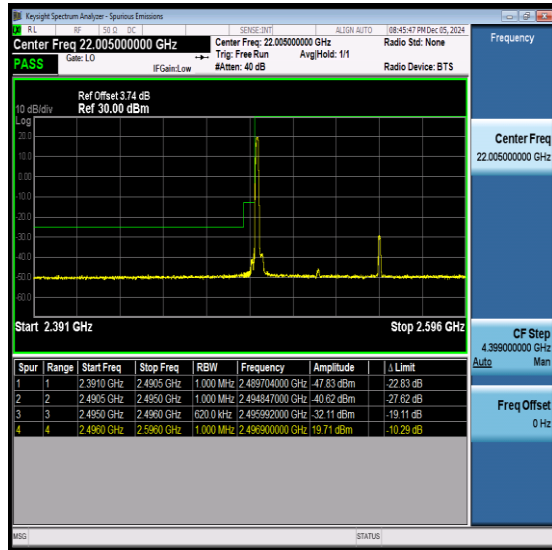
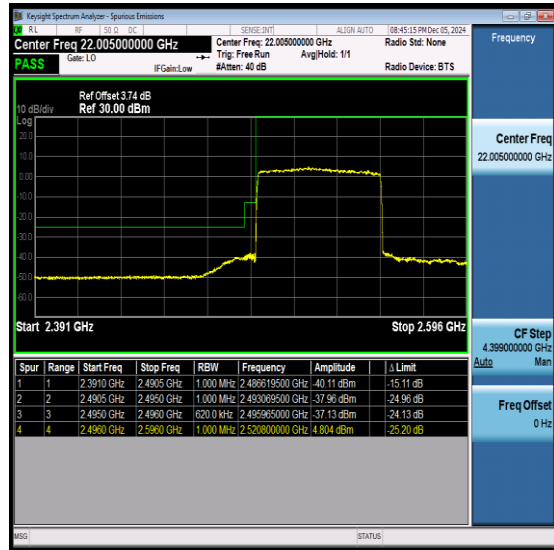
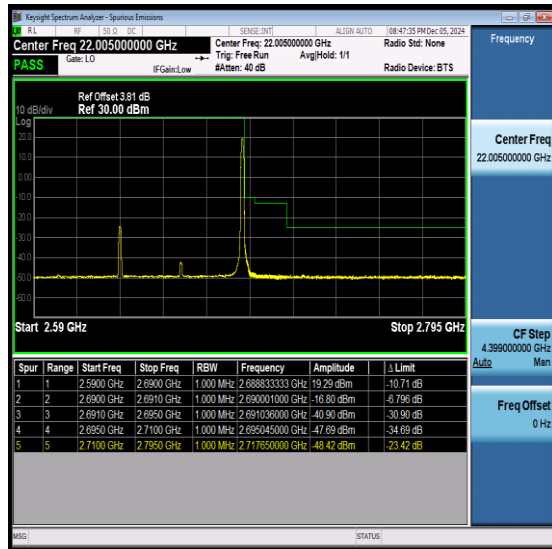




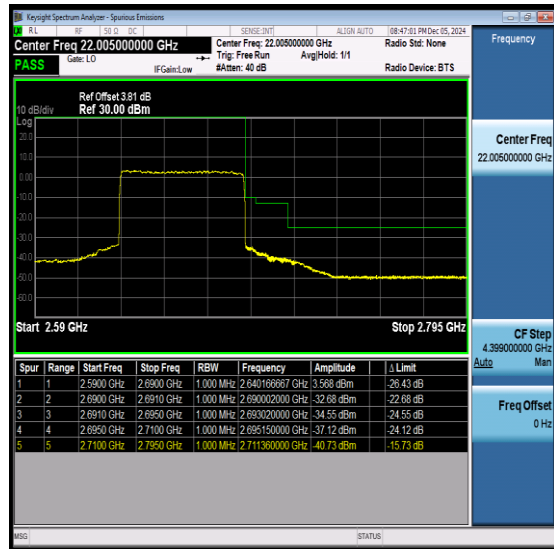
NTNV-N41-PC2-30-60-L-DFT-QPSK-
Edge_1RB_Left-Ant1-PASS



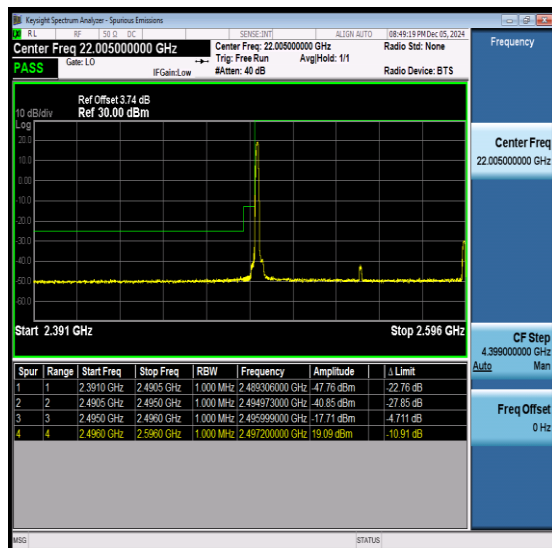
NTNV-N41-PC2-30-60-L-DFT-QPSK-Outer_Full-
Ant1-PASS



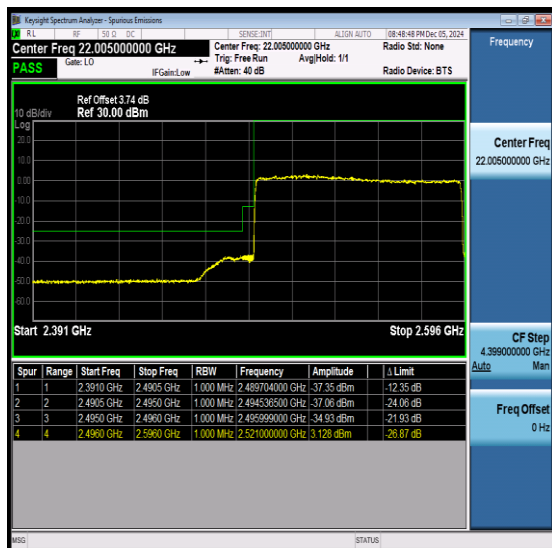
NTNV-N41-PC2-30-60-H-DFT-QPSK-
Edge_1RB_Right-Ant1-PASS



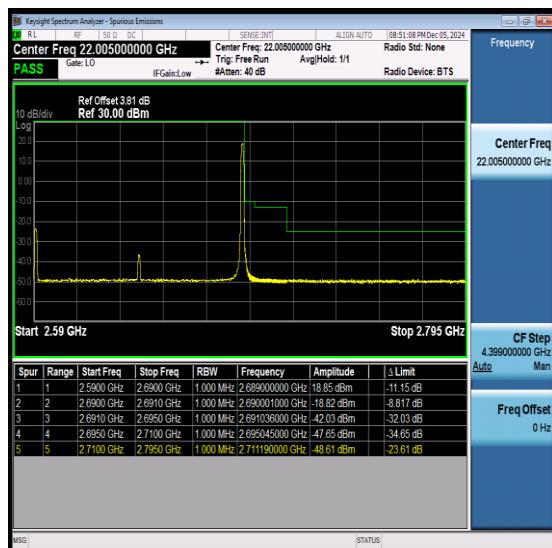
NTNV-N41-PC2-30-60-H-DFT-QPSK-Outer_Full-
Ant1-PASS



NTNV-N41-PC2-30-100-L-DFT-QPSK-
Edge_1RB_Left-Ant1-PASS



NTNV-N41-PC2-30-100-L-DFT-QPSK-Outer_Full-
Ant1-PASS



NTNV-N41-PC2-30-100-H-DFT-QPSK-
Edge_1RB_Right-Ant1-PASS



NTNV-N41-PC2-30-100-H-DFT-QPSK-Outer_Full-
Ant1-PASS



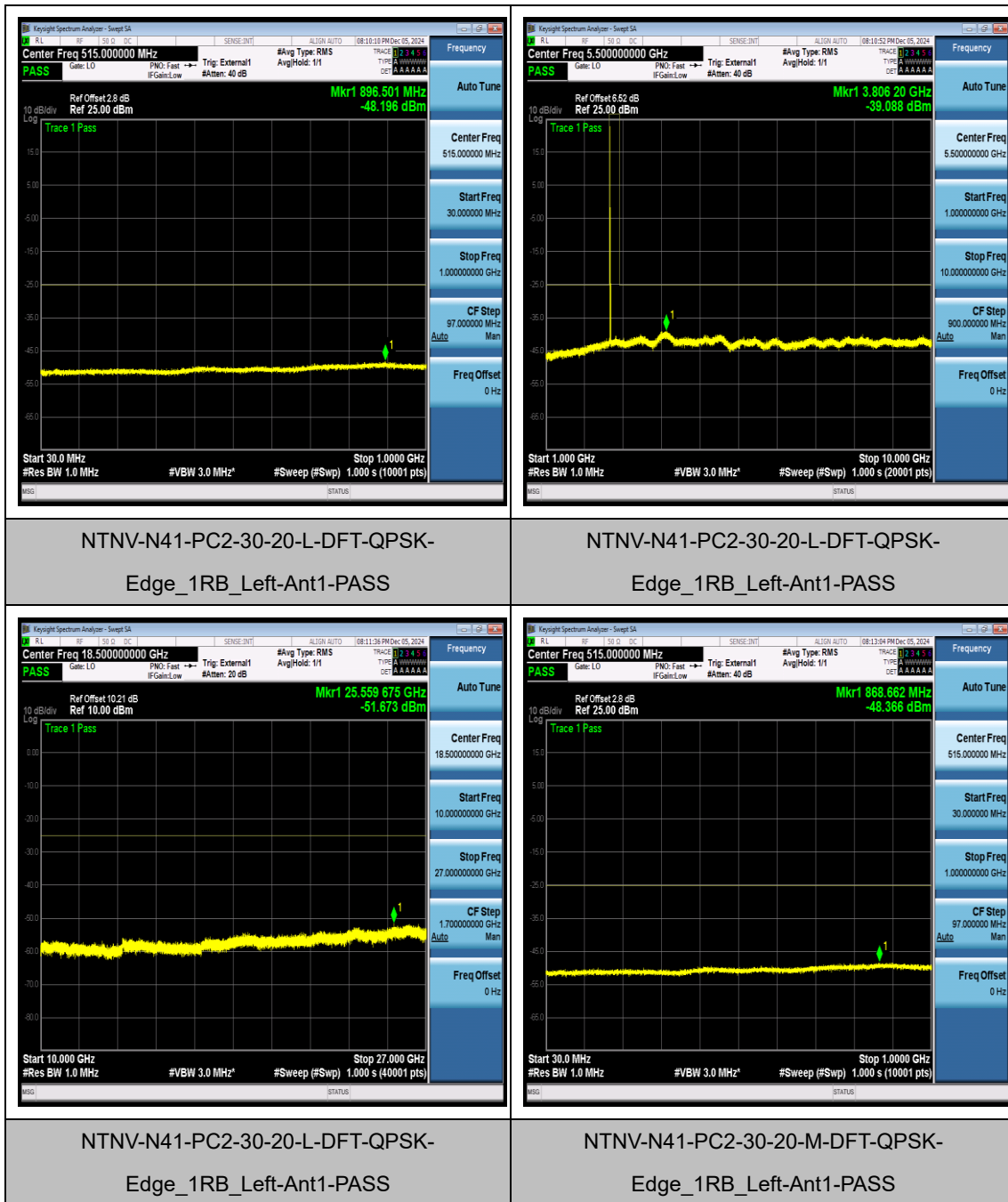
CONDUCTED SPURIOUS EMISSION FOR SA

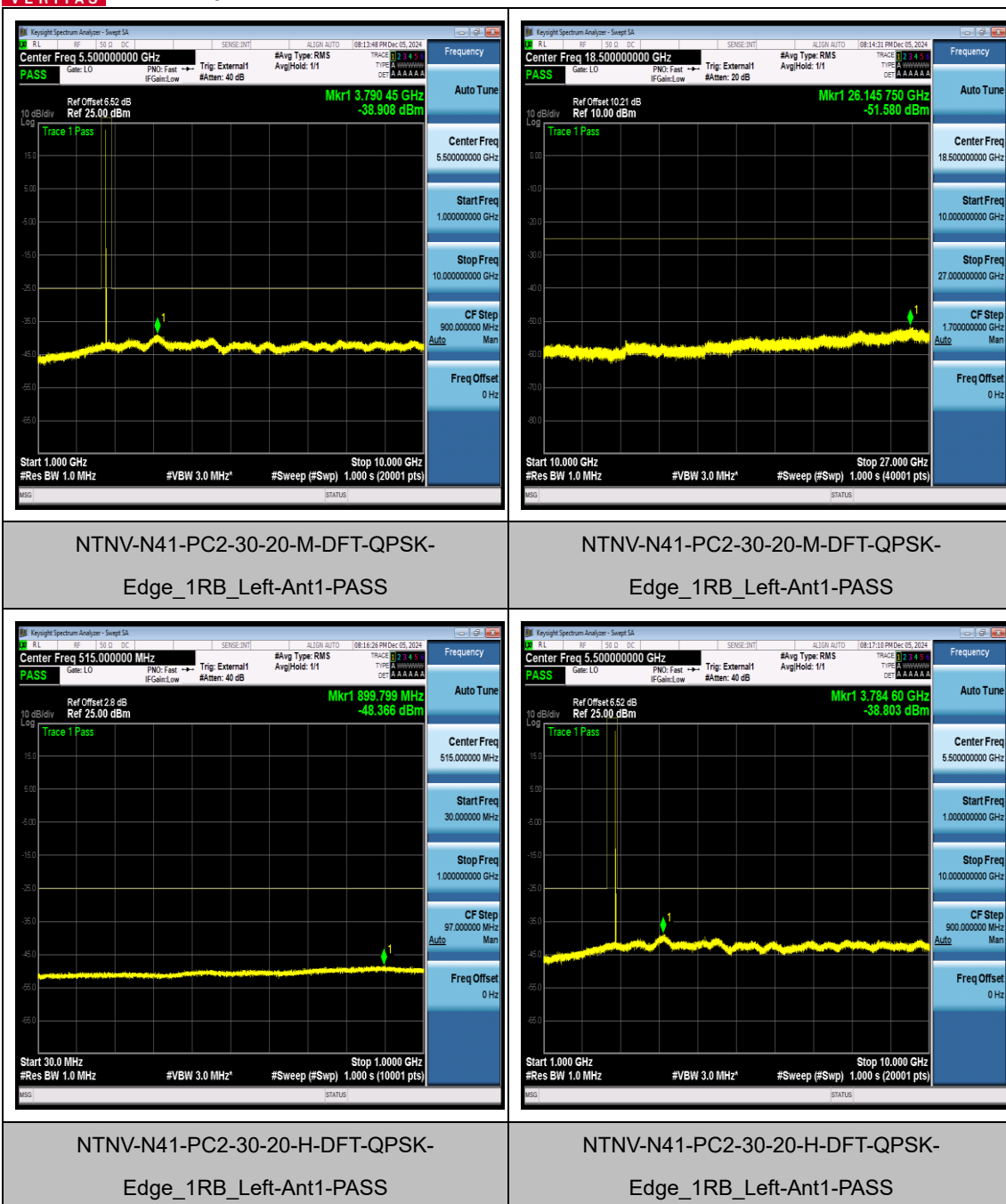
TEST RESULT

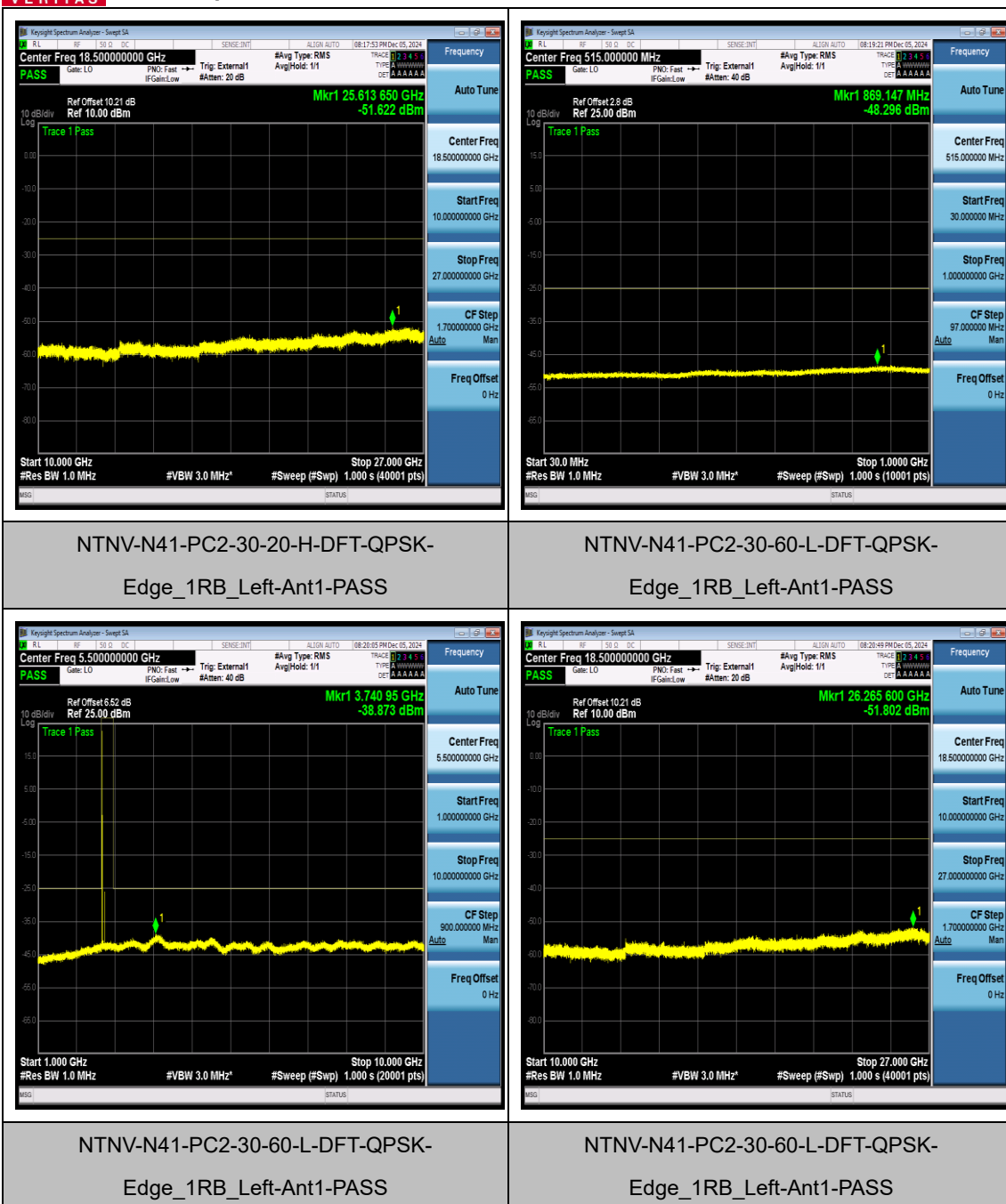
Band	SCS	Bandwidth	Modulation	Channel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	30	1000	-48.20	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	1000	10000	-39.09	-25	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	10000	27000	-51.67	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.37	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	1000	10000	-38.91	-25	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	10000	27000	-51.58	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	30	1000	-48.37	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	1000	10000	-38.80	-25	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	10000	27000	-51.62	-25	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	30	1000	-48.30	-25	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	1000	10000	-38.87	-25	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	10000	27000	-51.80	-25	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.20	-25	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	1000	10000	-38.81	-25	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	10000	27000	-51.44	-25	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	30	1000	-48.18	-25	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	1000	10000	-39.01	-25	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	10000	27000	-51.07	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	30	1000	-48.42	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	1000	10000	-38.92	-25	PASS
N41	30	100	DFT-QPSK	L	Edge_1RB_Left	10000	27000	-51.72	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	30	1000	-48.27	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	1000	10000	-38.64	-25	PASS
N41	30	100	DFT-QPSK	M	Edge_1RB_Left	10000	27000	-51.29	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	30	1000	-48.29	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	1000	10000	-38.77	-25	PASS
N41	30	100	DFT-QPSK	H	Edge_1RB_Left	10000	27000	-51.51	-25	PASS

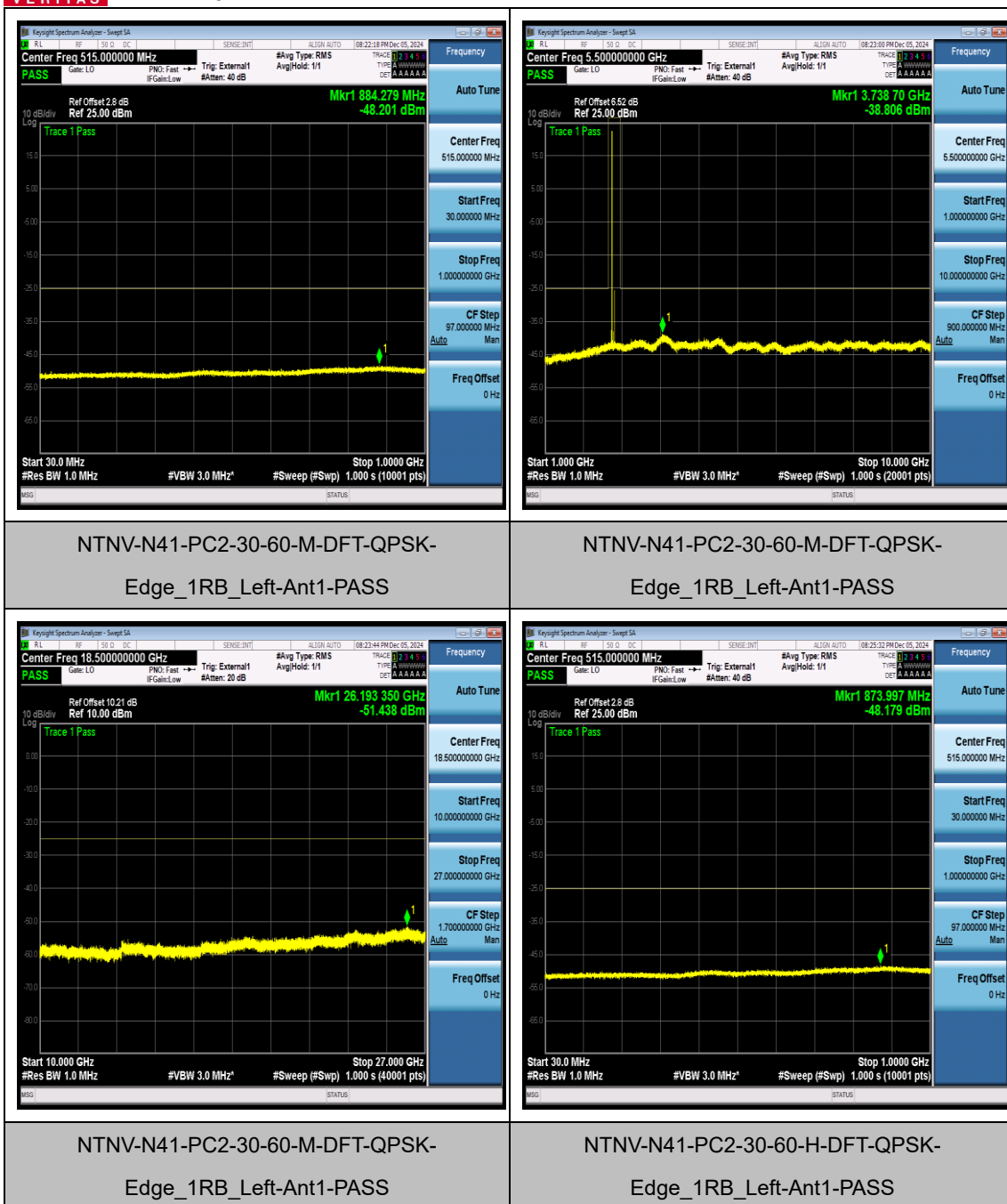


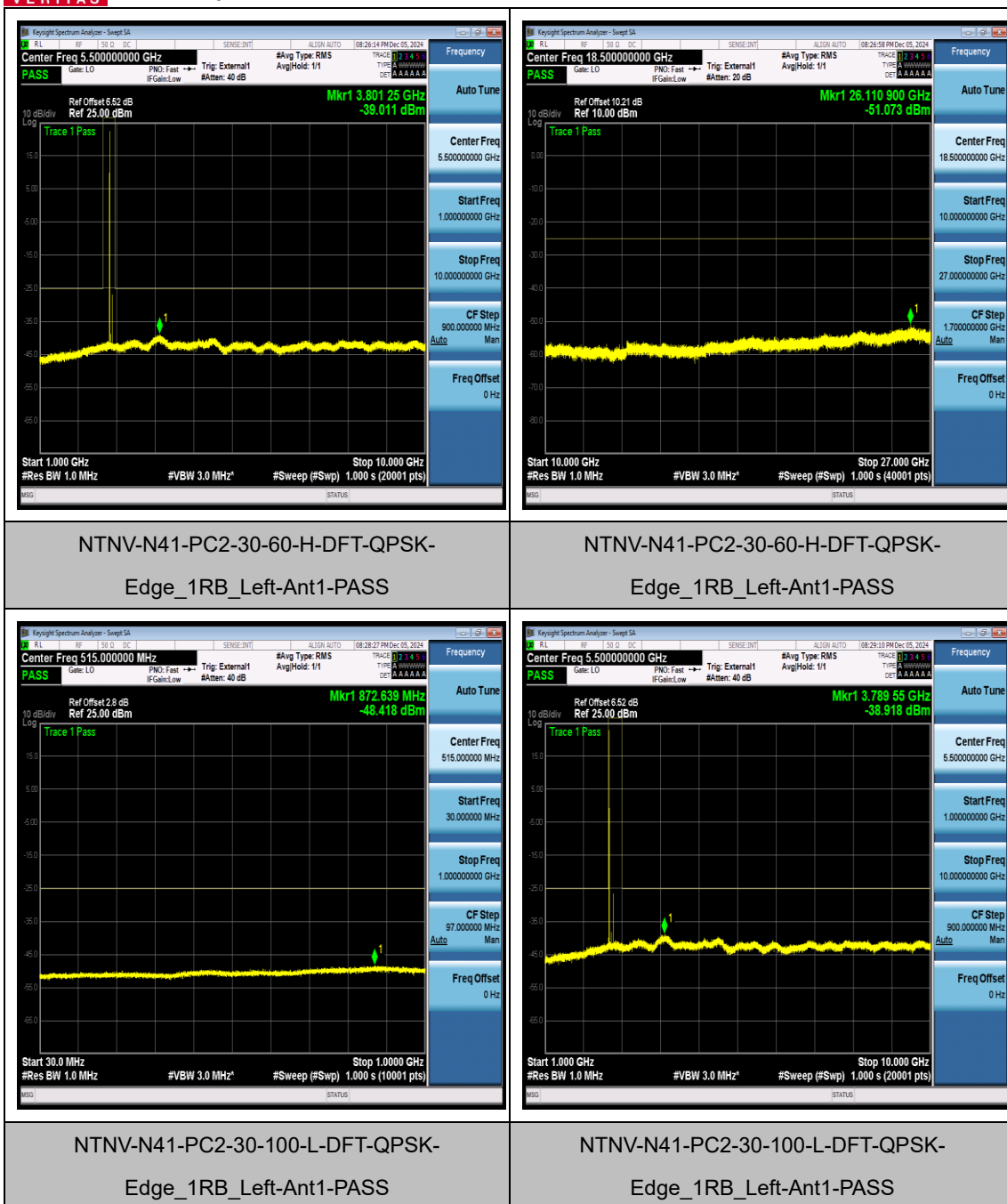
TEST GRAPHS

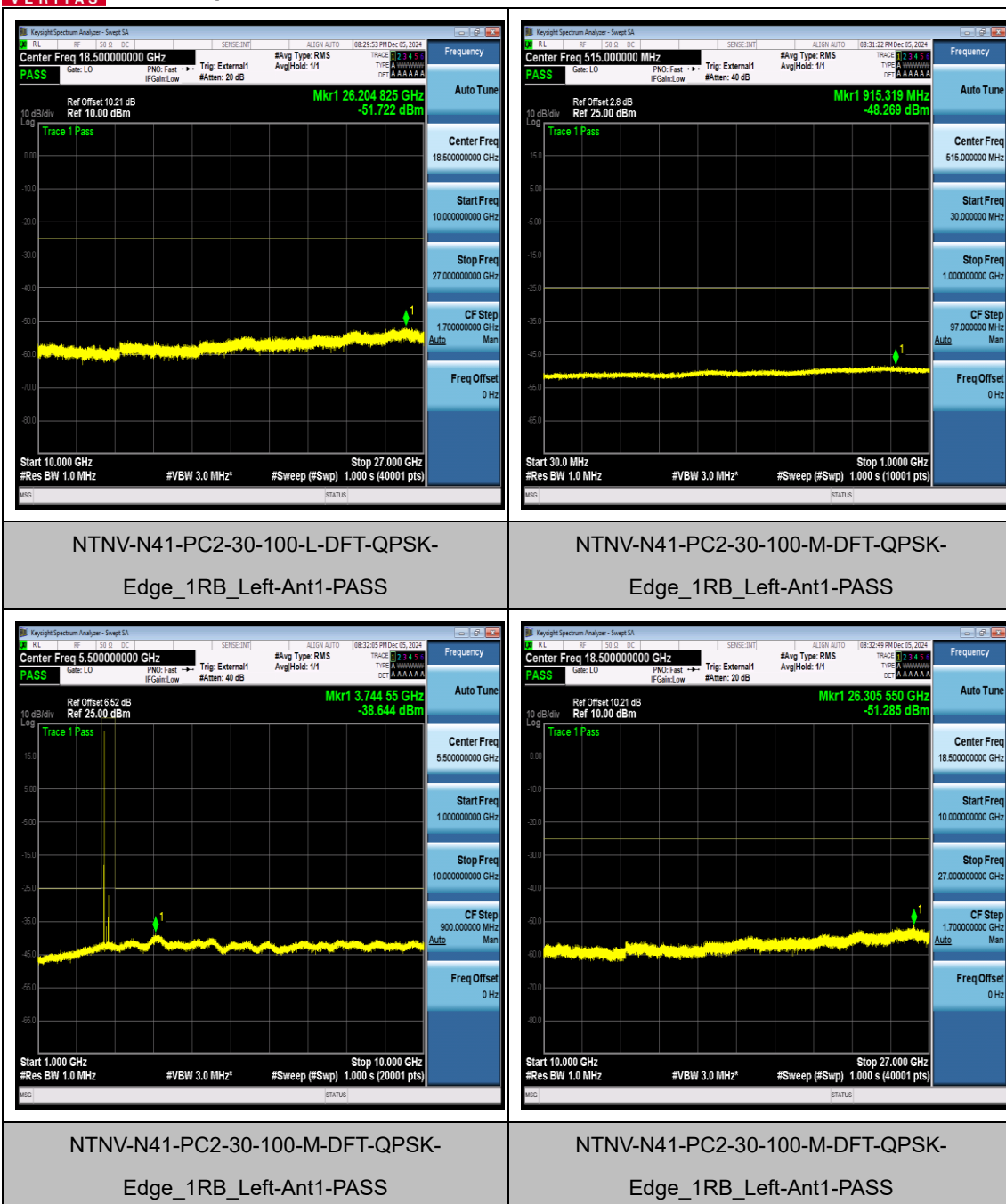


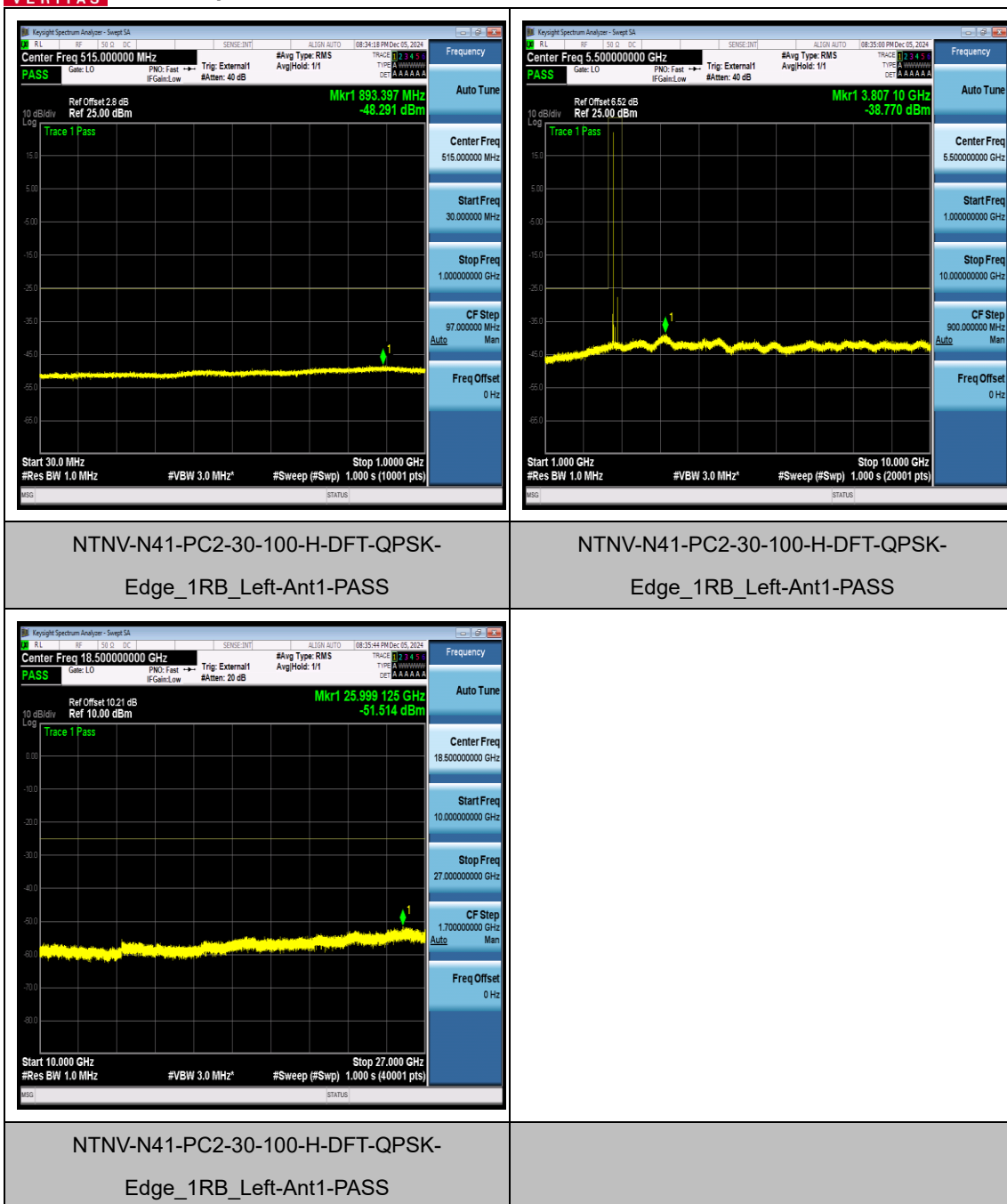














FREQUENCY STABILITY FOR SA

TEST RESULT

Frequency Error VS. Voltage

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N41	30	20	DFT-QPSK	L	Outer_Full	VL	NT	-8.959000	-0.003575	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	VN	NT	-9.230000	-0.003683	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	VH	NT	-10.313000	-0.004115	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	VL	NT	-13.845000	-0.005339	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	VN	NT	-14.241000	-0.005492	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	VH	NT	-5.193000	-0.002003	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	VL	NT	-9.193000	-0.003430	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	VN	NT	-11.105000	-0.004144	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	VH	NT	-9.256000	-0.003454	PASS

Frequency Error VS. Temperature

Voltage										
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
N41	30	20	DFT-QPSK	L	Outer_Full	NV	-30	-7.806000	-0.003115	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	-20	-8.832000	-0.003524	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	-10	-9.503000	-0.003792	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	0	-5.763000	-0.002300	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	10	-9.854000	-0.003931	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	20	-10.398000	-0.004149	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	30	-9.799000	-0.003910	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	40	-7.051000	-0.002814	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	NV	50	-8.823000	-0.003521	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	-30	-8.899000	-0.003432	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	-20	-10.303000	-0.003973	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	-10	-9.388000	-0.003621	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	0	-8.239000	-0.003177	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	10	-10.321000	-0.003980	PASS



N41	30	20	DFT-QPSK	M	Outer_Full	NV	20	-10.078000	-0.003887	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	30	-6.520000	-0.002514	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	40	-11.242000	-0.004336	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	NV	50	-5.593000	-0.002157	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	-30	-11.730000	-0.004377	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	-20	-7.530000	-0.002810	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	-10	-8.359999	-0.003119	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	0	-11.869000	-0.004429	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	10	-6.478000	-0.002417	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	20	-3.283000	-0.001225	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	30	-8.128000	-0.003033	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	40	-14.027000	-0.005234	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	NV	50	-4.938000	-0.001843	PASS

N41 MIMO

PEAK-TO-AVERAGE RATIO FOR MIMO

PEAK-TO-AVERAGE RATIO(CCDF)

TEST RESULT-ANT0

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	L	Outer_Full	5.54	≤13	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	1.60	≤13	PASS
N41	30	20	CP-16QPSK	L	Outer_Full	5.36	≤13	PASS
N41	30	20	CP-16QPSK	L	Edge_1RB_Left	2.26	≤13	PASS
N41	30	20	CP-64QPSK	L	Outer_Full	4.22	≤13	PASS
N41	30	20	CP-64QPSK	L	Edge_1RB_Left	2.62	≤13	PASS
N41	30	20	CP-256QPSK	L	Outer_Full	7.06	≤13	PASS
N41	30	20	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS
N41	30	30	CP-QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	2.26	≤13	PASS
N41	30	30	CP-16QPSK	L	Outer_Full	6.96	≤13	PASS
N41	30	30	CP-16QPSK	L	Edge_1RB_Left	1.70	≤13	PASS
N41	30	30	CP-64QPSK	L	Outer_Full	4.79	≤13	PASS
N41	30	30	CP-64QPSK	L	Edge_1RB_Left	1.12	≤13	PASS
N41	30	30	CP-256QPSK	L	Outer_Full	4.60	≤13	PASS
N41	30	30	CP-256QPSK	L	Edge_1RB_Left	1.42	≤13	PASS
N41	30	40	CP-QPSK	L	Outer_Full	5.64	≤13	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	1.88	≤13	PASS
N41	30	40	CP-16QPSK	L	Outer_Full	5.64	≤13	PASS
N41	30	40	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-64QPSK	L	Outer_Full	4.80	≤13	PASS
N41	30	40	CP-64QPSK	L	Edge_1RB_Left	2.06	≤13	PASS
N41	30	40	CP-256QPSK	L	Outer_Full	6.68	≤13	PASS
N41	30	40	CP-256QPSK	L	Edge_1RB_Left	1.04	≤13	PASS
N41	30	50	CP-QPSK	L	Outer_Full	6.10	≤13	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	2.08	≤13	PASS
N41	30	50	CP-16QPSK	L	Outer_Full	6.02	≤13	PASS



N41	30	50	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	50	CP-64QPSK	L	Outer_Full	6.02	≤13	PASS
N41	30	50	CP-64QPSK	L	Edge_1RB_Left	1.04	≤13	PASS
N41	30	50	CP-256QPSK	L	Outer_Full	6.48	≤13	PASS
N41	30	50	CP-256QPSK	L	Edge_1RB_Left	0.94	≤13	PASS
N41	30	60	CP-QPSK	L	Outer_Full	6.20	≤13	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	1.78	≤13	PASS
N41	30	60	CP-16QPSK	L	Outer_Full	5.36	≤13	PASS
N41	30	60	CP-16QPSK	L	Edge_1RB_Left	2.24	≤13	PASS
N41	30	60	CP-64QPSK	L	Outer_Full	6.68	≤13	PASS
N41	30	60	CP-64QPSK	L	Edge_1RB_Left	1.98	≤13	PASS
N41	30	60	CP-256QPSK	L	Outer_Full	6.58	≤13	PASS
N41	30	60	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS
N41	30	70	CP-QPSK	L	Outer_Full	6.20	≤13	PASS
N41	30	70	CP-QPSK	L	Edge_1RB_Left	1.78	≤13	PASS
N41	30	70	CP-16QPSK	L	Outer_Full	6.40	≤13	PASS
N41	30	70	CP-16QPSK	L	Edge_1RB_Left	1.14	≤13	PASS
N41	30	70	CP-64QPSK	L	Outer_Full	7.14	≤13	PASS
N41	30	70	CP-64QPSK	L	Edge_1RB_Left	0.38	≤13	PASS
N41	30	70	CP-256QPSK	L	Outer_Full	6.40	≤13	PASS
N41	30	70	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS
N41	30	80	CP-QPSK	L	Outer_Full	6.58	≤13	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	1.88	≤13	PASS
N41	30	80	CP-16QPSK	L	Outer_Full	5.46	≤13	PASS
N41	30	80	CP-16QPSK	L	Edge_1RB_Left	2.08	≤13	PASS
N41	30	80	CP-64QPSK	L	Outer_Full	6.68	≤13	PASS
N41	30	80	CP-64QPSK	L	Edge_1RB_Left	0.96	≤13	PASS
N41	30	80	CP-256QPSK	L	Outer_Full	5.82	≤13	PASS
N41	30	80	CP-256QPSK	L	Edge_1RB_Left	1.04	≤13	PASS
N41	30	90	CP-QPSK	L	Outer_Full	6.40	≤13	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	2.64	≤13	PASS
N41	30	90	CP-16QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	90	CP-16QPSK	L	Edge_1RB_Left	2.08	≤13	PASS
N41	30	90	CP-64QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	90	CP-64QPSK	L	Edge_1RB_Left	1.78	≤13	PASS
N41	30	90	CP-256QPSK	L	Outer_Full	6.48	≤13	PASS
N41	30	90	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS



N41	30	100	CP-QPSK	L	Outer_Full	6.20	≤13	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	2.64	≤13	PASS
N41	30	100	CP-16QPSK	L	Outer_Full	5.44	≤13	PASS
N41	30	100	CP-16QPSK	L	Edge_1RB_Left	2.08	≤13	PASS
N41	30	100	CP-64QPSK	L	Outer_Full	6.76	≤13	PASS
N41	30	100	CP-64QPSK	L	Edge_1RB_Left	1.60	≤13	PASS
N41	30	100	CP-256QPSK	L	Outer_Full	6.68	≤13	PASS
N41	30	100	CP-256QPSK	L	Edge_1RB_Left	0.58	≤13	PASS

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	M	Outer_Full	5.26	≤13	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	0.94	≤13	PASS
N41	30	20	CP-16QPSK	M	Outer_Full	5.36	≤13	PASS
N41	30	20	CP-16QPSK	M	Edge_1RB_Left	2.26	≤13	PASS
N41	30	20	CP-64QPSK	M	Outer_Full	4.22	≤13	PASS
N41	30	20	CP-64QPSK	M	Edge_1RB_Left	2.62	≤13	PASS
N41	30	20	CP-256QPSK	M	Outer_Full	6.48	≤13	PASS
N41	30	20	CP-256QPSK	M	Edge_1RB_Left	1.88	≤13	PASS
N41	30	30	CP-QPSK	M	Outer_Full	6.02	≤13	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	0.94	≤13	PASS
N41	30	30	CP-16QPSK	M	Outer_Full	5.46	≤13	PASS
N41	30	30	CP-16QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	30	CP-64QPSK	M	Outer_Full	4.70	≤13	PASS
N41	30	30	CP-64QPSK	M	Edge_1RB_Left	1.96	≤13	PASS
N41	30	30	CP-256QPSK	M	Outer_Full	7.52	≤13	PASS
N41	30	30	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS
N41	30	40	CP-QPSK	M	Outer_Full	4.80	≤13	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	1.32	≤13	PASS
N41	30	40	CP-16QPSK	M	Outer_Full	7.06	≤13	PASS
N41	30	40	CP-16QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-64QPSK	M	Outer_Full	4.98	≤13	PASS
N41	30	40	CP-64QPSK	M	Edge_1RB_Left	1.52	≤13	PASS
N41	30	40	CP-256QPSK	M	Outer_Full	8.28	≤13	PASS
N41	30	40	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS
N41	30	50	CP-QPSK	M	Outer_Full	6.10	≤13	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	0.76	≤13	PASS



N41	30	50	CP-16QPSK	M	Outer_Full	5.36	≤13	PASS
N41	30	50	CP-16QPSK	M	Edge_1RB_Left	1.60	≤13	PASS
N41	30	50	CP-64QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	50	CP-64QPSK	M	Edge_1RB_Left	1.78	≤13	PASS
N41	30	50	CP-256QPSK	M	Outer_Full	6.48	≤13	PASS
N41	30	50	CP-256QPSK	M	Edge_1RB_Left	0.84	≤13	PASS
N41	30	60	CP-QPSK	M	Outer_Full	5.92	≤13	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	60	CP-16QPSK	M	Outer_Full	6.22	≤13	PASS
N41	30	60	CP-16QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	60	CP-64QPSK	M	Outer_Full	6.86	≤13	PASS
N41	30	60	CP-64QPSK	M	Edge_1RB_Left	2.54	≤13	PASS
N41	30	60	CP-256QPSK	M	Outer_Full	8.08	≤13	PASS
N41	30	60	CP-256QPSK	M	Edge_1RB_Left	1.24	≤13	PASS
N41	30	70	CP-QPSK	M	Outer_Full	6.02	≤13	PASS
N41	30	70	CP-QPSK	M	Edge_1RB_Left	2.46	≤13	PASS
N41	30	70	CP-16QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	70	CP-16QPSK	M	Edge_1RB_Left	2.26	≤13	PASS
N41	30	70	CP-64QPSK	M	Outer_Full	6.02	≤13	PASS
N41	30	70	CP-64QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	70	CP-256QPSK	M	Outer_Full	7.24	≤13	PASS
N41	30	70	CP-256QPSK	M	Edge_1RB_Left	1.32	≤13	PASS
N41	30	80	CP-QPSK	M	Outer_Full	6.02	≤13	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	80	CP-16QPSK	M	Outer_Full	6.58	≤13	PASS
N41	30	80	CP-16QPSK	M	Edge_1RB_Left	2.06	≤13	PASS
N41	30	80	CP-64QPSK	M	Outer_Full	6.40	≤13	PASS
N41	30	80	CP-64QPSK	M	Edge_1RB_Left	1.04	≤13	PASS
N41	30	80	CP-256QPSK	M	Outer_Full	7.34	≤13	PASS
N41	30	80	CP-256QPSK	M	Edge_1RB_Left	1.04	≤13	PASS
N41	30	90	CP-QPSK	M	Outer_Full	6.48	≤13	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	90	CP-16QPSK	M	Outer_Full	6.30	≤13	PASS
N41	30	90	CP-16QPSK	M	Edge_1RB_Left	2.26	≤13	PASS
N41	30	90	CP-64QPSK	M	Outer_Full	6.20	≤13	PASS
N41	30	90	CP-64QPSK	M	Edge_1RB_Left	1.78	≤13	PASS
N41	30	90	CP-256QPSK	M	Outer_Full	5.74	≤13	PASS



N41	30	90	CP-256QPSK	M	Edge_1RB_Left	1.24	≤13	PASS
N41	30	100	CP-QPSK	M	Outer_Full	6.40	≤13	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	2.36	≤13	PASS
N41	30	100	CP-16QPSK	M	Outer_Full	6.58	≤13	PASS
N41	30	100	CP-16QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	100	CP-64QPSK	M	Outer_Full	6.58	≤13	PASS
N41	30	100	CP-64QPSK	M	Edge_1RB_Left	1.04	≤13	PASS
N41	30	100	CP-256QPSK	M	Outer_Full	8.08	≤13	PASS
N41	30	100	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	H	Outer_Full	6.30	≤13	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	2.24	≤13	PASS
N41	30	20	CP-16QPSK	H	Outer_Full	5.54	≤13	PASS
N41	30	20	CP-16QPSK	H	Edge_1RB_Left	1.52	≤13	PASS
N41	30	20	CP-64QPSK	H	Outer_Full	5.82	≤13	PASS
N41	30	20	CP-64QPSK	H	Edge_1RB_Left	1.96	≤13	PASS
N41	30	20	CP-256QPSK	H	Outer_Full	4.04	≤13	PASS
N41	30	20	CP-256QPSK	H	Edge_1RB_Left	1.40	≤13	PASS
N41	30	30	CP-QPSK	H	Outer_Full	5.64	≤13	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	1.04	≤13	PASS
N41	30	30	CP-16QPSK	H	Outer_Full	8.36	≤13	PASS
N41	30	30	CP-16QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	30	CP-64QPSK	H	Outer_Full	7.52	≤13	PASS
N41	30	30	CP-64QPSK	H	Edge_1RB_Left	1.78	≤13	PASS
N41	30	30	CP-256QPSK	H	Outer_Full	5.64	≤13	PASS
N41	30	30	CP-256QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	40	CP-QPSK	H	Outer_Full	5.92	≤13	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-16QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	40	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-64QPSK	H	Outer_Full	5.26	≤13	PASS
N41	30	40	CP-64QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-256QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	40	CP-256QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	50	CP-QPSK	H	Outer_Full	5.74	≤13	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	1.98	≤13	PASS



N41	30	50	CP-16QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	50	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	50	CP-64QPSK	H	Outer_Full	6.20	≤13	PASS
N41	30	50	CP-64QPSK	H	Edge_1RB_Left	2.24	≤13	PASS
N41	30	50	CP-256QPSK	H	Outer_Full	8.00	≤13	PASS
N41	30	50	CP-256QPSK	H	Edge_1RB_Left	1.04	≤13	PASS
N41	30	60	CP-QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	2.24	≤13	PASS
N41	30	60	CP-16QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	60	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	60	CP-64QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	60	CP-64QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	60	CP-256QPSK	H	Outer_Full	5.16	≤13	PASS
N41	30	60	CP-256QPSK	H	Edge_1RB_Left	1.22	≤13	PASS
N41	30	70	CP-QPSK	H	Outer_Full	6.02	≤13	PASS
N41	30	70	CP-QPSK	H	Edge_1RB_Left	1.80	≤13	PASS
N41	30	70	CP-16QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	70	CP-16QPSK	H	Edge_1RB_Left	2.18	≤13	PASS
N41	30	70	CP-64QPSK	H	Outer_Full	6.10	≤13	PASS
N41	30	70	CP-64QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	70	CP-256QPSK	H	Outer_Full	6.30	≤13	PASS
N41	30	70	CP-256QPSK	H	Edge_1RB_Left	1.32	≤13	PASS
N41	30	80	CP-QPSK	H	Outer_Full	6.86	≤13	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	1.98	≤13	PASS
N41	30	80	CP-16QPSK	H	Outer_Full	6.48	≤13	PASS
N41	30	80	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	80	CP-64QPSK	H	Outer_Full	6.68	≤13	PASS
N41	30	80	CP-64QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	80	CP-256QPSK	H	Outer_Full	6.30	≤13	PASS
N41	30	80	CP-256QPSK	H	Edge_1RB_Left	1.32	≤13	PASS
N41	30	90	CP-QPSK	H	Outer_Full	6.10	≤13	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	2.26	≤13	PASS
N41	30	90	CP-16QPSK	H	Outer_Full	6.48	≤13	PASS
N41	30	90	CP-16QPSK	H	Edge_1RB_Left	2.08	≤13	PASS
N41	30	90	CP-64QPSK	H	Outer_Full	5.94	≤13	PASS
N41	30	90	CP-64QPSK	H	Edge_1RB_Left	1.80	≤13	PASS
N41	30	90	CP-256QPSK	H	Outer_Full	6.40	≤13	PASS



N41	30	90	CP-256QPSK	H	Edge_1RB_Left	1.32	≤13	PASS
N41	30	100	CP-QPSK	H	Outer_Full	7.42	≤13	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	2.72	≤13	PASS
N41	30	100	CP-16QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	100	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	100	CP-64QPSK	H	Outer_Full	7.14	≤13	PASS
N41	30	100	CP-64QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	100	CP-256QPSK	H	Outer_Full	5.64	≤13	PASS
N41	30	100	CP-256QPSK	H	Edge_1RB_Left	1.50	≤13	PASS

TEST RESULT-ANT2

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	1.88	≤13	PASS
N41	30	20	CP-16QPSK	L	Outer_Full	4.80	≤13	PASS
N41	30	20	CP-16QPSK	L	Edge_1RB_Left	1.88	≤13	PASS
N41	30	20	CP-64QPSK	L	Outer_Full	4.98	≤13	PASS
N41	30	20	CP-64QPSK	L	Edge_1RB_Left	1.04	≤13	PASS
N41	30	20	CP-256QPSK	L	Outer_Full	7.14	≤13	PASS
N41	30	20	CP-256QPSK	L	Edge_1RB_Left	1.22	≤13	PASS
N41	30	30	CP-QPSK	L	Outer_Full	6.12	≤13	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	1.80	≤13	PASS
N41	30	30	CP-16QPSK	L	Outer_Full	6.96	≤13	PASS
N41	30	30	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	30	CP-64QPSK	L	Outer_Full	6.40	≤13	PASS
N41	30	30	CP-64QPSK	L	Edge_1RB_Left	1.98	≤13	PASS
N41	30	30	CP-256QPSK	L	Outer_Full	6.48	≤13	PASS
N41	30	30	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS
N41	30	40	CP-QPSK	L	Outer_Full	5.74	≤13	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-16QPSK	L	Outer_Full	5.54	≤13	PASS
N41	30	40	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	40	CP-64QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	40	CP-64QPSK	L	Edge_1RB_Left	1.68	≤13	PASS
N41	30	40	CP-256QPSK	L	Outer_Full	6.20	≤13	PASS
N41	30	40	CP-256QPSK	L	Edge_1RB_Left	1.12	≤13	PASS
N41	30	50	CP-QPSK	L	Outer_Full	5.82	≤13	PASS



N41	30	50	CP-QPSK	L	Edge_1RB_Left	1.14	≤13	PASS
N41	30	50	CP-16QPSK	L	Outer_Full	5.16	≤13	PASS
N41	30	50	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	50	CP-64QPSK	L	Outer_Full	5.92	≤13	PASS
N41	30	50	CP-64QPSK	L	Edge_1RB_Left	1.80	≤13	PASS
N41	30	50	CP-256QPSK	L	Outer_Full	5.36	≤13	PASS
N41	30	50	CP-256QPSK	L	Edge_1RB_Left	0.74	≤13	PASS
N41	30	60	CP-QPSK	L	Outer_Full	7.06	≤13	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	2.64	≤13	PASS
N41	30	60	CP-16QPSK	L	Outer_Full	6.40	≤13	PASS
N41	30	60	CP-16QPSK	L	Edge_1RB_Left	2.16	≤13	PASS
N41	30	60	CP-64QPSK	L	Outer_Full	6.58	≤13	PASS
N41	30	60	CP-64QPSK	L	Edge_1RB_Left	1.96	≤13	PASS
N41	30	60	CP-256QPSK	L	Outer_Full	5.82	≤13	PASS
N41	30	60	CP-256QPSK	L	Edge_1RB_Left	0.38	≤13	PASS
N41	30	70	CP-QPSK	L	Outer_Full	4.88	≤13	PASS
N41	30	70	CP-QPSK	L	Edge_1RB_Left	1.33	≤13	PASS
N41	30	70	CP-16QPSK	L	Outer_Full	7.24	≤13	PASS
N41	30	70	CP-16QPSK	L	Edge_1RB_Left	1.22	≤13	PASS
N41	30	70	CP-64QPSK	L	Outer_Full	5.36	≤13	PASS
N41	30	70	CP-64QPSK	L	Edge_1RB_Left	2.06	≤13	PASS
N41	30	70	CP-256QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	70	CP-256QPSK	L	Edge_1RB_Left	1.42	≤13	PASS
N41	30	80	CP-QPSK	L	Outer_Full	6.58	≤13	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	0.76	≤13	PASS
N41	30	80	CP-16QPSK	L	Outer_Full	6.10	≤13	PASS
N41	30	80	CP-16QPSK	L	Edge_1RB_Left	1.30	≤13	PASS
N41	30	80	CP-64QPSK	L	Outer_Full	6.48	≤13	PASS
N41	30	80	CP-64QPSK	L	Edge_1RB_Left	2.24	≤13	PASS
N41	30	80	CP-256QPSK	L	Outer_Full	8.08	≤13	PASS
N41	30	80	CP-256QPSK	L	Edge_1RB_Left	1.30	≤13	PASS
N41	30	90	CP-QPSK	L	Outer_Full	6.30	≤13	PASS
N41	30	90	CP-QPSK	L	Edge_1RB_Left	0.84	≤13	PASS
N41	30	90	CP-16QPSK	L	Outer_Full	7.34	≤13	PASS
N41	30	90	CP-16QPSK	L	Edge_1RB_Left	1.58	≤13	PASS
N41	30	90	CP-64QPSK	L	Outer_Full	7.62	≤13	PASS
N41	30	90	CP-64QPSK	L	Edge_1RB_Left	2.16	≤13	PASS



N41	30	90	CP-256QPSK	L	Outer_Full	7.24	≤13	PASS
N41	30	90	CP-256QPSK	L	Edge_1RB_Left	1.42	≤13	PASS
N41	30	100	CP-QPSK	L	Outer_Full	6.68	≤13	PASS
N41	30	100	CP-QPSK	L	Edge_1RB_Left	0.76	≤13	PASS
N41	30	100	CP-16QPSK	L	Outer_Full	5.64	≤13	PASS
N41	30	100	CP-16QPSK	L	Edge_1RB_Left	0.38	≤13	PASS
N41	30	100	CP-64QPSK	L	Outer_Full	7.34	≤13	PASS
N41	30	100	CP-64QPSK	L	Edge_1RB_Left	2.24	≤13	PASS
N41	30	100	CP-256QPSK	L	Outer_Full	7.06	≤13	PASS
N41	30	100	CP-256QPSK	L	Edge_1RB_Left	1.32	≤13	PASS

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	M	Outer_Full	5.54	≤13	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Left	2.08	≤13	PASS
N41	30	20	CP-16QPSK	M	Outer_Full	4.90	≤13	PASS
N41	30	20	CP-16QPSK	M	Edge_1RB_Left	2.36	≤13	PASS
N41	30	20	CP-64QPSK	M	Outer_Full	5.92	≤13	PASS
N41	30	20	CP-64QPSK	M	Edge_1RB_Left	2.08	≤13	PASS
N41	30	20	CP-256QPSK	M	Outer_Full	5.92	≤13	PASS
N41	30	20	CP-256QPSK	M	Edge_1RB_Left	0.48	≤13	PASS
N41	30	30	CP-QPSK	M	Outer_Full	5.16	≤13	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	2.72	≤13	PASS
N41	30	30	CP-16QPSK	M	Outer_Full	5.08	≤13	PASS
N41	30	30	CP-16QPSK	M	Edge_1RB_Left	2.08	≤13	PASS
N41	30	30	CP-64QPSK	M	Outer_Full	6.30	≤13	PASS
N41	30	30	CP-64QPSK	M	Edge_1RB_Left	1.52	≤13	PASS
N41	30	30	CP-256QPSK	M	Outer_Full	7.52	≤13	PASS
N41	30	30	CP-256QPSK	M	Edge_1RB_Left	0.48	≤13	PASS
N41	30	40	CP-QPSK	M	Outer_Full	5.94	≤13	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	2.54	≤13	PASS
N41	30	40	CP-16QPSK	M	Outer_Full	5.74	≤13	PASS
N41	30	40	CP-16QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	40	CP-64QPSK	M	Outer_Full	6.76	≤13	PASS
N41	30	40	CP-64QPSK	M	Edge_1RB_Left	2.06	≤13	PASS
N41	30	40	CP-256QPSK	M	Outer_Full	5.84	≤13	PASS



N41	30	40	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS
N41	30	50	CP-QPSK	M	Outer_Full	5.54	≤13	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	50	CP-16QPSK	M	Outer_Full	6.40	≤13	PASS
N41	30	50	CP-16QPSK	M	Edge_1RB_Left	2.24	≤13	PASS
N41	30	50	CP-64QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	50	CP-64QPSK	M	Edge_1RB_Left	1.02	≤13	PASS
N41	30	50	CP-256QPSK	M	Outer_Full	6.10	≤13	PASS
N41	30	50	CP-256QPSK	M	Edge_1RB_Left	1.14	≤13	PASS
N41	30	60	CP-QPSK	M	Outer_Full	6.10	≤13	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	1.98	≤13	PASS
N41	30	60	CP-16QPSK	M	Outer_Full	4.98	≤13	PASS
N41	30	60	CP-16QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	60	CP-64QPSK	M	Outer_Full	6.76	≤13	PASS
N41	30	60	CP-64QPSK	M	Edge_1RB_Left	1.98	≤13	PASS
N41	30	60	CP-256QPSK	M	Outer_Full	6.96	≤13	PASS
N41	30	60	CP-256QPSK	M	Edge_1RB_Left	1.14	≤13	PASS
N41	30	70	CP-QPSK	M	Outer_Full	6.20	≤13	PASS
N41	30	70	CP-QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	70	CP-16QPSK	M	Outer_Full	6.58	≤13	PASS
N41	30	70	CP-16QPSK	M	Edge_1RB_Left	2.16	≤13	PASS
N41	30	70	CP-64QPSK	M	Outer_Full	6.20	≤13	PASS
N41	30	70	CP-64QPSK	M	Edge_1RB_Left	1.52	≤13	PASS
N41	30	70	CP-256QPSK	M	Outer_Full	6.10	≤13	PASS
N41	30	70	CP-256QPSK	M	Edge_1RB_Left	0.94	≤13	PASS
N41	30	80	CP-QPSK	M	Outer_Full	7.34	≤13	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	0.76	≤13	PASS
N41	30	80	CP-16QPSK	M	Outer_Full	2.16	≤13	PASS
N41	30	80	CP-16QPSK	M	Edge_1RB_Left	1.70	≤13	PASS
N41	30	80	CP-64QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	80	CP-64QPSK	M	Edge_1RB_Left	6.68	≤13	PASS
N41	30	80	CP-256QPSK	M	Outer_Full	6.76	≤13	PASS
N41	30	80	CP-256QPSK	M	Edge_1RB_Left	0.86	≤13	PASS
N41	30	90	CP-QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	90	CP-QPSK	M	Edge_1RB_Left	2.36	≤13	PASS
N41	30	90	CP-16QPSK	M	Outer_Full	6.68	≤13	PASS
N41	30	90	CP-16QPSK	M	Edge_1RB_Left	2.36	≤13	PASS



N41	30	90	CP-64QPSK	M	Outer_Full	7.34	≤13	PASS
N41	30	90	CP-64QPSK	M	Edge_1RB_Left	2.26	≤13	PASS
N41	30	90	CP-256QPSK	M	Outer_Full	6.40	≤13	PASS
N41	30	90	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS
N41	30	100	CP-QPSK	M	Outer_Full	6.58	≤13	PASS
N41	30	100	CP-QPSK	M	Edge_1RB_Left	0.76	≤13	PASS
N41	30	100	CP-16QPSK	M	Outer_Full	6.48	≤13	PASS
N41	30	100	CP-16QPSK	M	Edge_1RB_Left	1.12	≤13	PASS
N41	30	100	CP-64QPSK	M	Outer_Full	7.42	≤13	PASS
N41	30	100	CP-64QPSK	M	Edge_1RB_Left	2.26	≤13	PASS
N41	30	100	CP-256QPSK	M	Outer_Full	6.48	≤13	PASS
N41	30	100	CP-256QPSK	M	Edge_1RB_Left	1.42	≤13	PASS

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
N41	30	20	CP-QPSK	H	Outer_Full	7.06	≤13	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	2.06	≤13	PASS
N41	30	20	CP-16QPSK	H	Outer_Full	5.54	≤13	PASS
N41	30	20	CP-16QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	20	CP-64QPSK	H	Outer_Full	4.80	≤13	PASS
N41	30	20	CP-64QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	20	CP-256QPSK	H	Outer_Full	7.42	≤13	PASS
N41	30	20	CP-256QPSK	H	Edge_1RB_Left	1.02	≤13	PASS
N41	30	30	CP-QPSK	H	Outer_Full	6.76	≤13	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	30	CP-16QPSK	H	Outer_Full	4.60	≤13	PASS
N41	30	30	CP-16QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	30	CP-64QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	30	CP-64QPSK	H	Edge_1RB_Left	2.06	≤13	PASS
N41	30	30	CP-256QPSK	H	Outer_Full	7.52	≤13	PASS
N41	30	30	CP-256QPSK	H	Edge_1RB_Left	1.60	≤13	PASS
N41	30	40	CP-QPSK	H	Outer_Full	6.20	≤13	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	2.36	≤13	PASS
N41	30	40	CP-16QPSK	H	Outer_Full	5.92	≤13	PASS
N41	30	40	CP-16QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	40	CP-64QPSK	H	Outer_Full	5.26	≤13	PASS



N41	30	40	CP-64QPSK	H	Edge_1RB_Left	1.98	≤13	PASS
N41	30	40	CP-256QPSK	H	Outer_Full	6.86	≤13	PASS
N41	30	40	CP-256QPSK	H	Edge_1RB_Left	1.12	≤13	PASS
N41	30	50	CP-QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	2.06	≤13	PASS
N41	30	50	CP-16QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	50	CP-16QPSK	H	Edge_1RB_Left	2.08	≤13	PASS
N41	30	50	CP-64QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	50	CP-64QPSK	H	Edge_1RB_Left	1.70	≤13	PASS
N41	30	50	CP-256QPSK	H	Outer_Full	6.10	≤13	PASS
N41	30	50	CP-256QPSK	H	Edge_1RB_Left	0.94	≤13	PASS
N41	30	60	CP-QPSK	H	Outer_Full	6.12	≤13	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	2.26	≤13	PASS
N41	30	60	CP-16QPSK	H	Outer_Full	6.86	≤13	PASS
N41	30	60	CP-16QPSK	H	Edge_1RB_Left	2.36	≤13	PASS
N41	30	60	CP-64QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	60	CP-64QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	60	CP-256QPSK	H	Outer_Full	6.98	≤13	PASS
N41	30	60	CP-256QPSK	H	Edge_1RB_Left	1.04	≤13	PASS
N41	30	70	CP-QPSK	H	Outer_Full	6.68	≤13	PASS
N41	30	70	CP-QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	70	CP-16QPSK	H	Outer_Full	6.48	≤13	PASS
N41	30	70	CP-16QPSK	H	Edge_1RB_Left	1.78	≤13	PASS
N41	30	70	CP-64QPSK	H	Outer_Full	6.86	≤13	PASS
N41	30	70	CP-64QPSK	H	Edge_1RB_Left	0.94	≤13	PASS
N41	30	70	CP-256QPSK	H	Outer_Full	7.42	≤13	PASS
N41	30	70	CP-256QPSK	H	Edge_1RB_Left	0.58	≤13	PASS
N41	30	80	CP-QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	80	CP-QPSK	H	Edge_1RB_Left	0.76	≤13	PASS
N41	30	80	CP-16QPSK	H	Outer_Full	6.40	≤13	PASS
N41	30	80	CP-16QPSK	H	Edge_1RB_Left	0.86	≤13	PASS
N41	30	80	CP-64QPSK	H	Outer_Full	6.86	≤13	PASS
N41	30	80	CP-64QPSK	H	Edge_1RB_Left	2.16	≤13	PASS
N41	30	80	CP-256QPSK	H	Outer_Full	8.18	≤13	PASS
N41	30	80	CP-256QPSK	H	Edge_1RB_Left	1.32	≤13	PASS
N41	30	90	CP-QPSK	H	Outer_Full	6.76	≤13	PASS
N41	30	90	CP-QPSK	H	Edge_1RB_Left	1.78	≤13	PASS



N41	30	90	CP-16QPSK	H	Outer_Full	6.96	≤13	PASS
N41	30	90	CP-16QPSK	H	Edge_1RB_Left	1.88	≤13	PASS
N41	30	90	CP-64QPSK	H	Outer_Full	7.42	≤13	PASS
N41	30	90	CP-64QPSK	H	Edge_1RB_Left	0.86	≤13	PASS
N41	30	90	CP-256QPSK	H	Outer_Full	6.58	≤13	PASS
N41	30	90	CP-256QPSK	H	Edge_1RB_Left	0.66	≤13	PASS
N41	30	100	CP-QPSK	H	Outer_Full	6.68	≤13	PASS
N41	30	100	CP-QPSK	H	Edge_1RB_Left	1.50	≤13	PASS
N41	30	100	CP-16QPSK	H	Outer_Full	7.16	≤13	PASS
N41	30	100	CP-16QPSK	H	Edge_1RB_Left	1.98	≤13	PASS
N41	30	100	CP-64QPSK	H	Outer_Full	7.14	≤13	PASS
N41	30	100	CP-64QPSK	H	Edge_1RB_Left	1.98	≤13	PASS
N41	30	100	CP-256QPSK	H	Outer_Full	7.16	≤13	PASS
N41	30	100	CP-256QPSK	H	Edge_1RB_Left	0.56	≤13	PASS

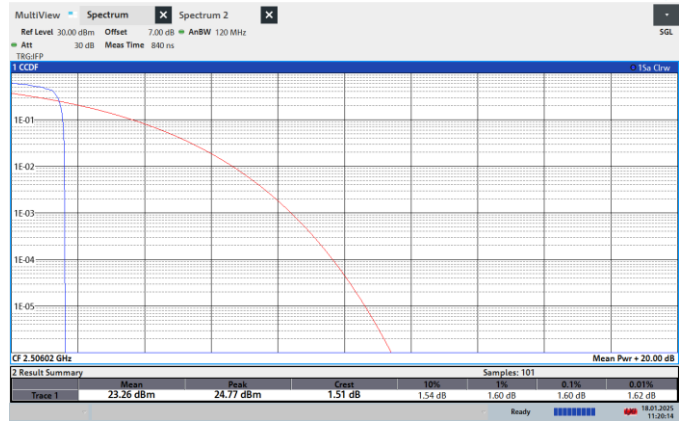


BUREAU
VERITAS

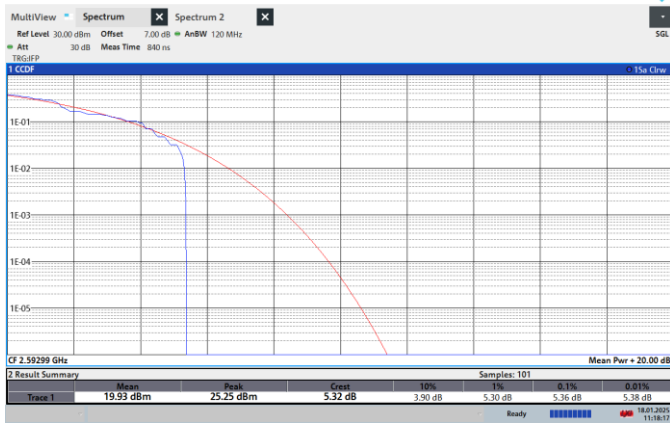
Test Report No.: PSU-QSU2411180418
TEST GRAPHS-ANT0



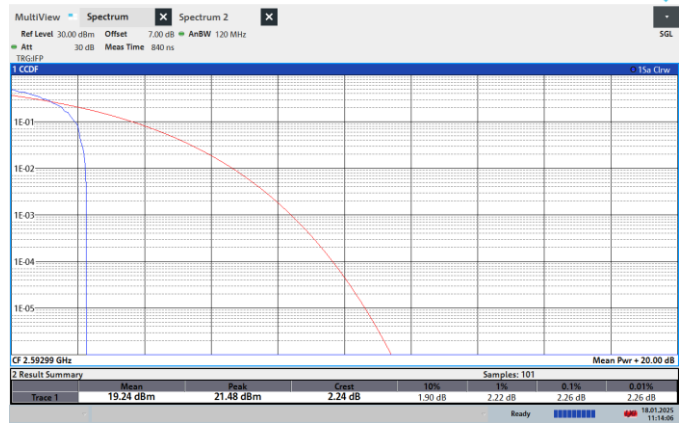
NTNV-N41-PC2-30-20-L-CP-QPSK-Outer_Full-PASS



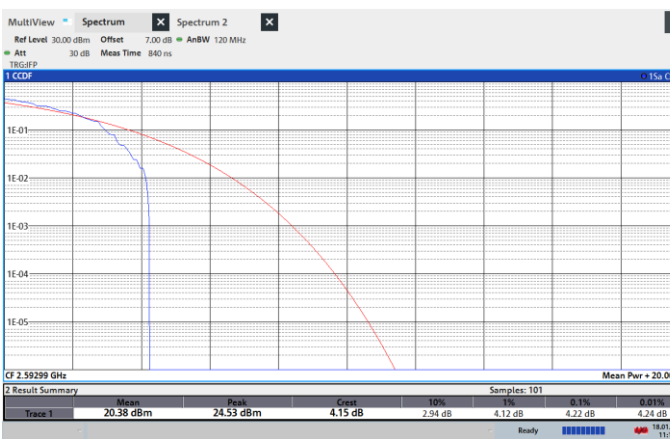
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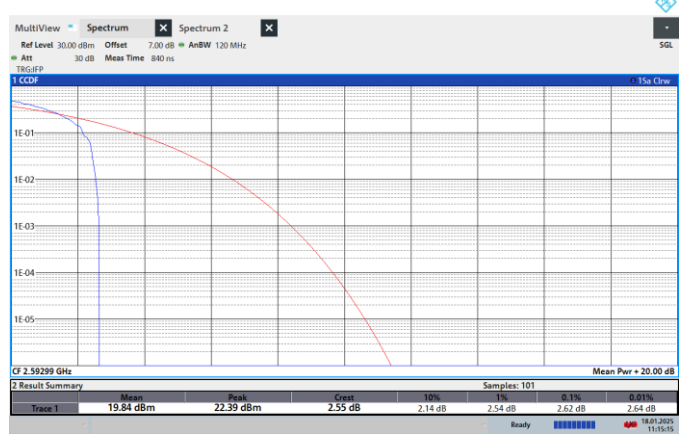
NTNV-N41-PC2-30-20-L-CP-16QPSK-Outer_Full-PASS



NTNV-N41-PC2-30-20-L-CP-16QPSK-Edge_1RB_Left-PASS



NTNV-N41-PC2-30-20-L-CP-64QPSK-Outer_Full-PASS



NTNV-N41-PC2-30-20-L-CP-64QPSK-Edge_1RB_Left-PASS