

5 4 3 2 1

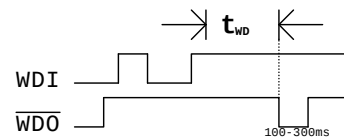
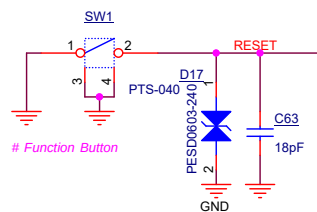
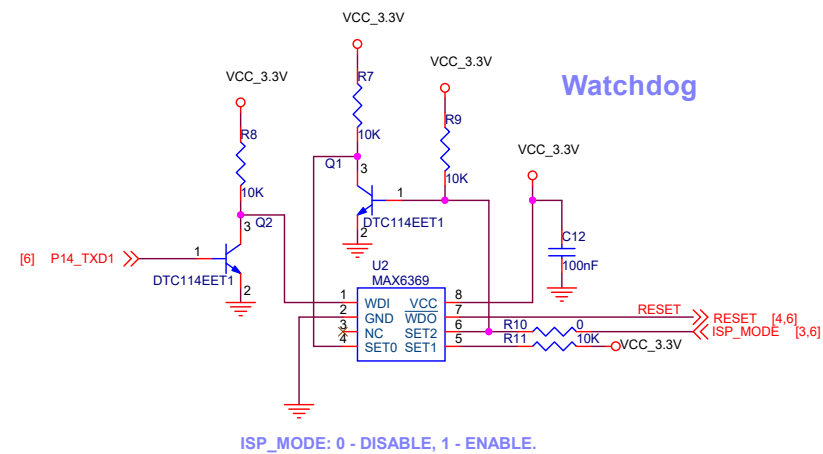
D

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B

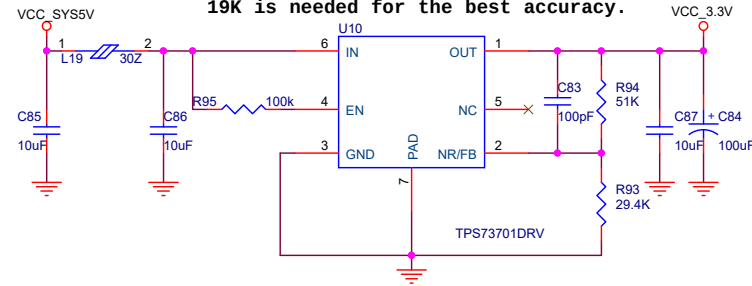


Minimum Timeout Settings

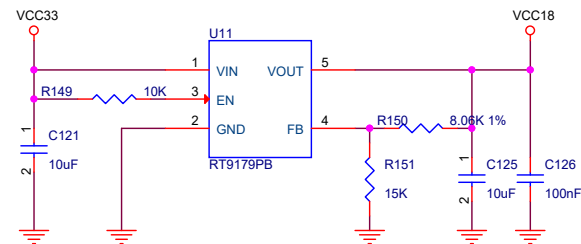
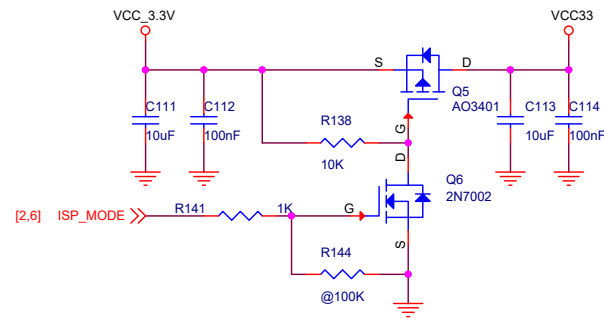
SET2	SET1	SET0	t_{WD}
0	1	1	Disabled
1	0	1	1s
1	1	0	10s
1	1	1	60s

$$VCC_3.3V = 1.204 * (1 + 51K/29.4K) = 3.293V$$

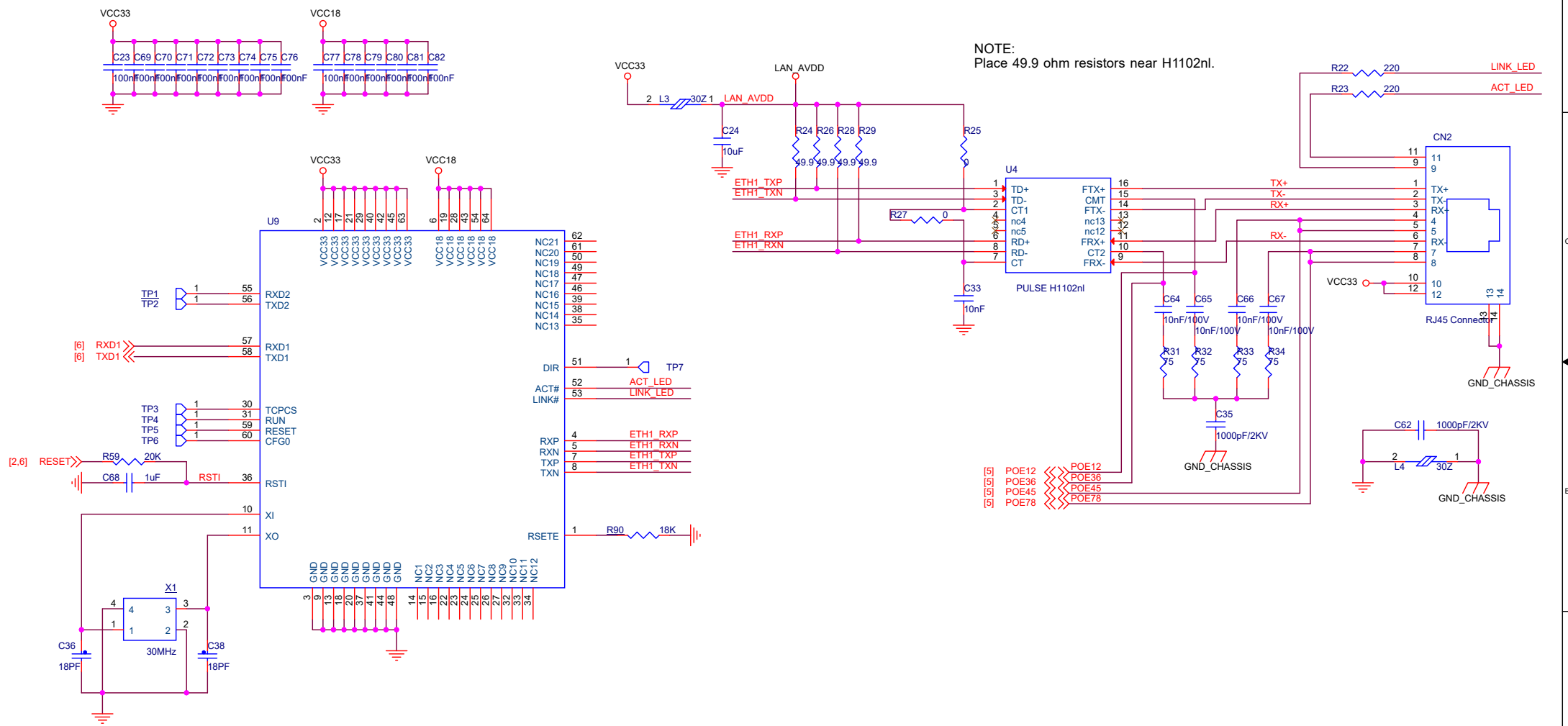
**51K//29.4K=51*29.4/(51+29.4)K=18.65K,
19K is needed for the best accuracy.**



$$VCC18 = 1.175 * (1 + 8.06K/15K) = 1.8064V$$

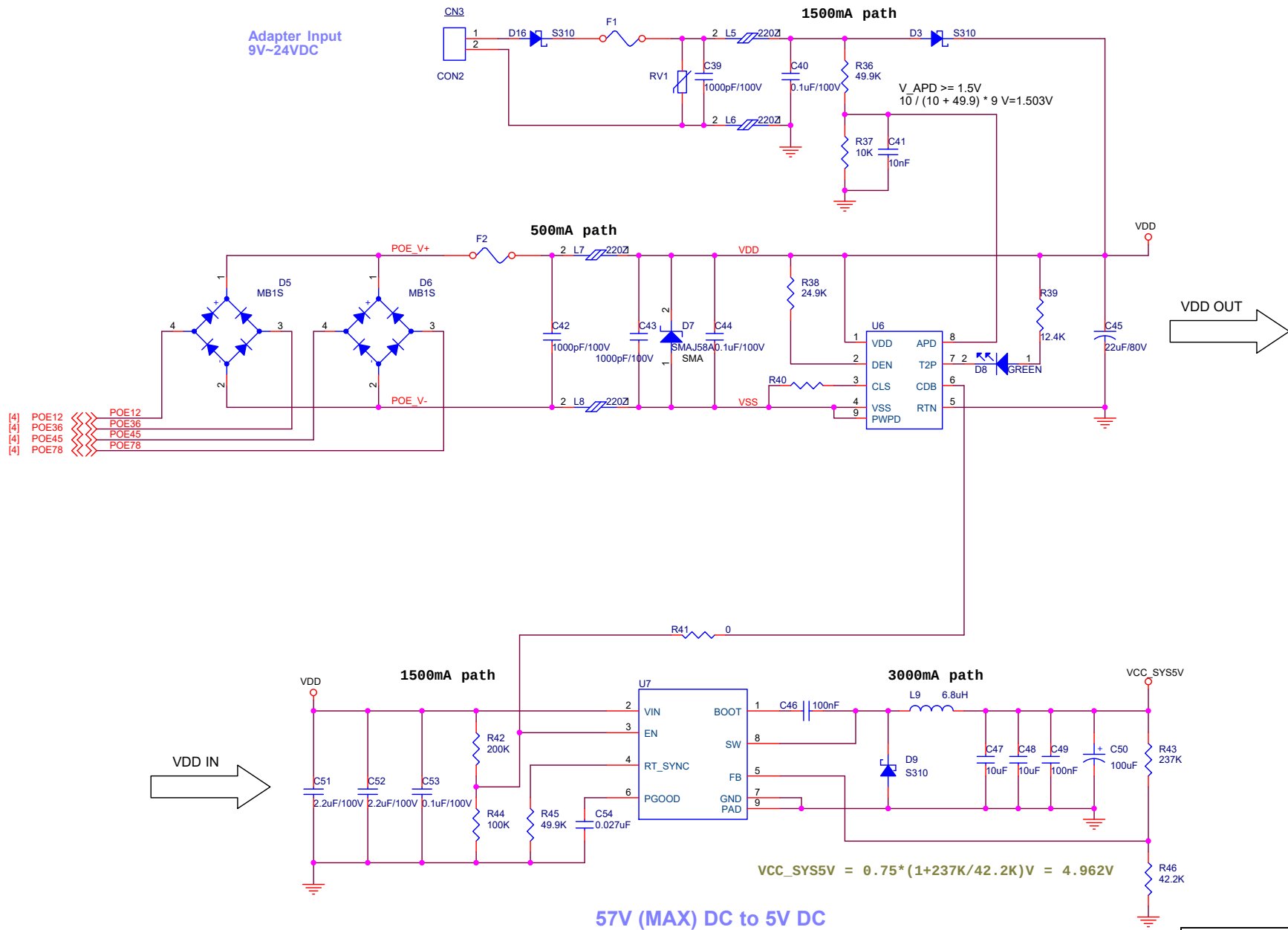


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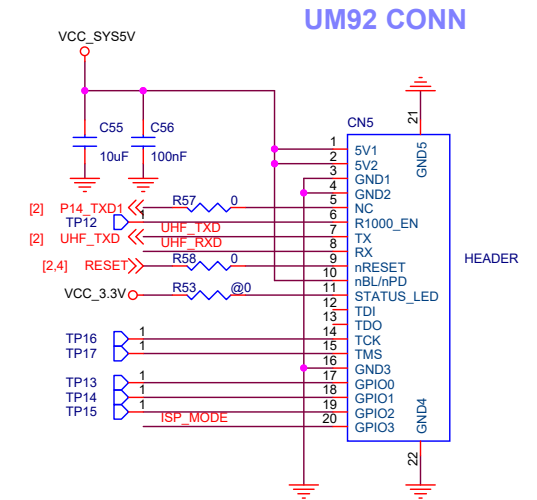
NOTE:
Place 49.9 ohm resistors near H1102nl.

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POE		
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The schematic diagram illustrates the RS232 interface circuit. It features a DSUB_RS232 connector (Female) with pins 1 through 11. The circuit includes a VCC_5V_EXT supply, a VCC_SYSSV supply, a D101 @RB160M-40 diode, and a L11 inductor. The RS232_TXD signal is connected to the TP11 pin.



HEADER

Four identical circuit diagrams are shown, each representing a transformer. The primary side is labeled with voltage $M2.3$ and $U3.6$. The secondary side is labeled with voltage $H1$ and is connected to ground. The diagrams are identical, showing the same components and connections.

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