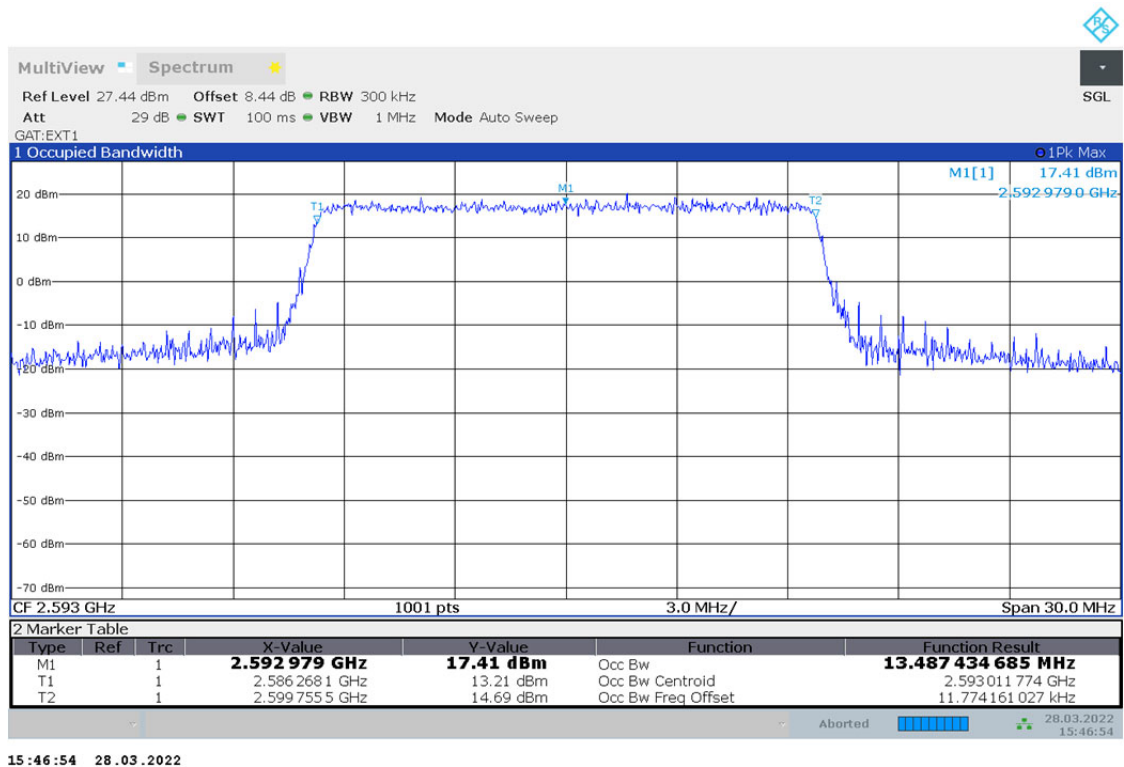
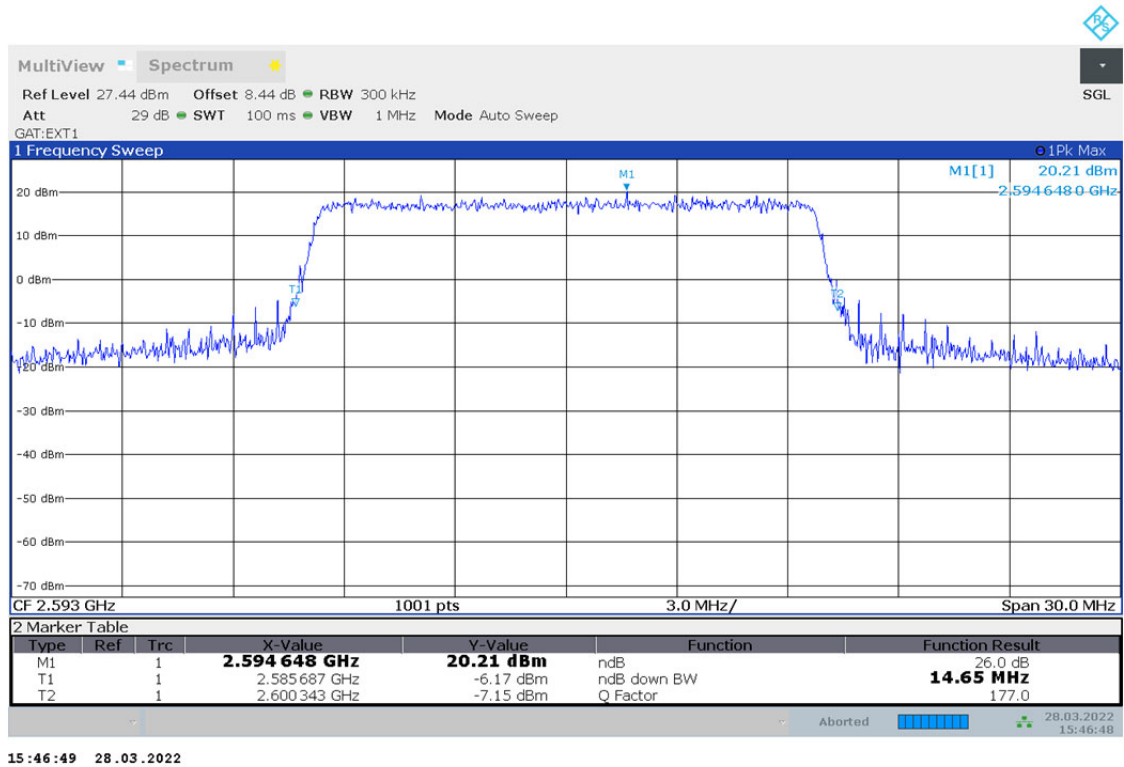
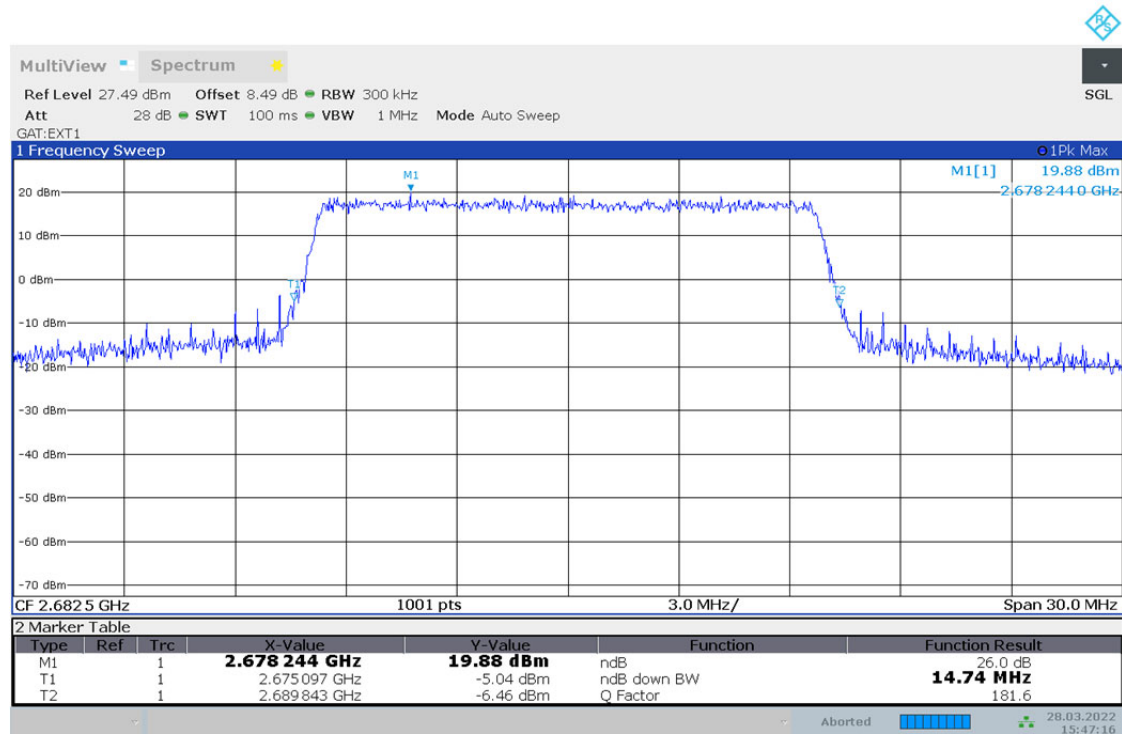


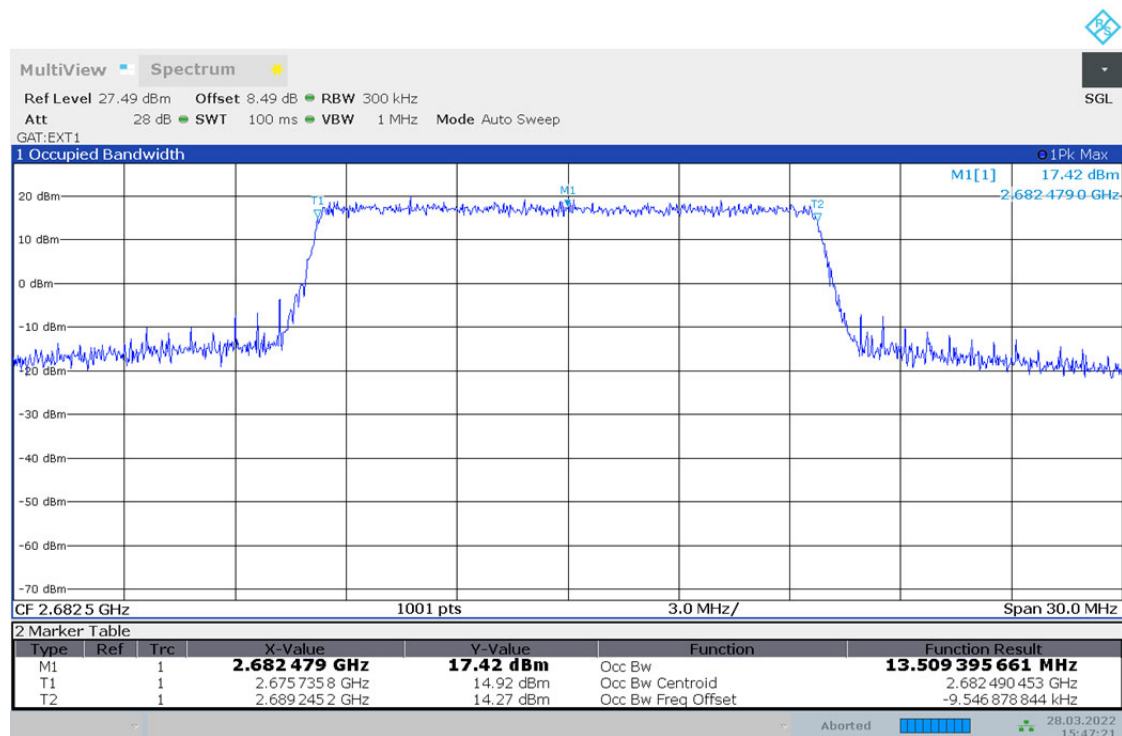
## 4.2.20 TM2\_15MHz\_MCH\_RB75#0



## 4.2.21 TM2\_15MHz\_HCH\_RB75#0

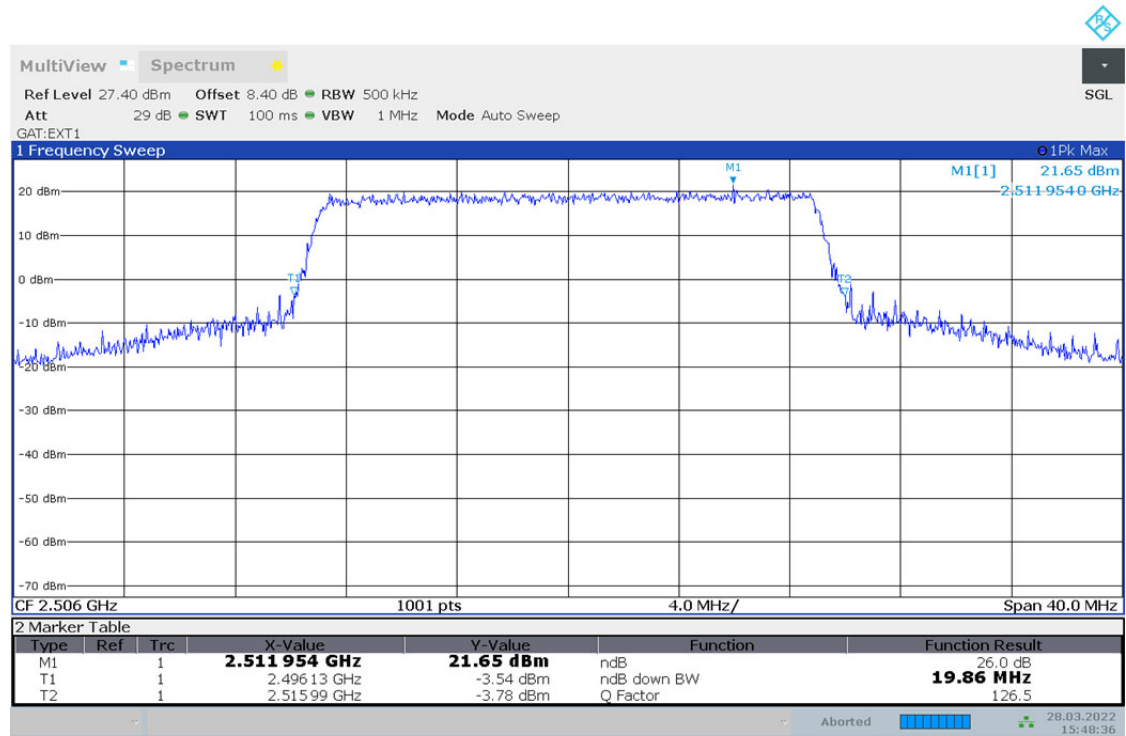


15:47:17 28.03.2022

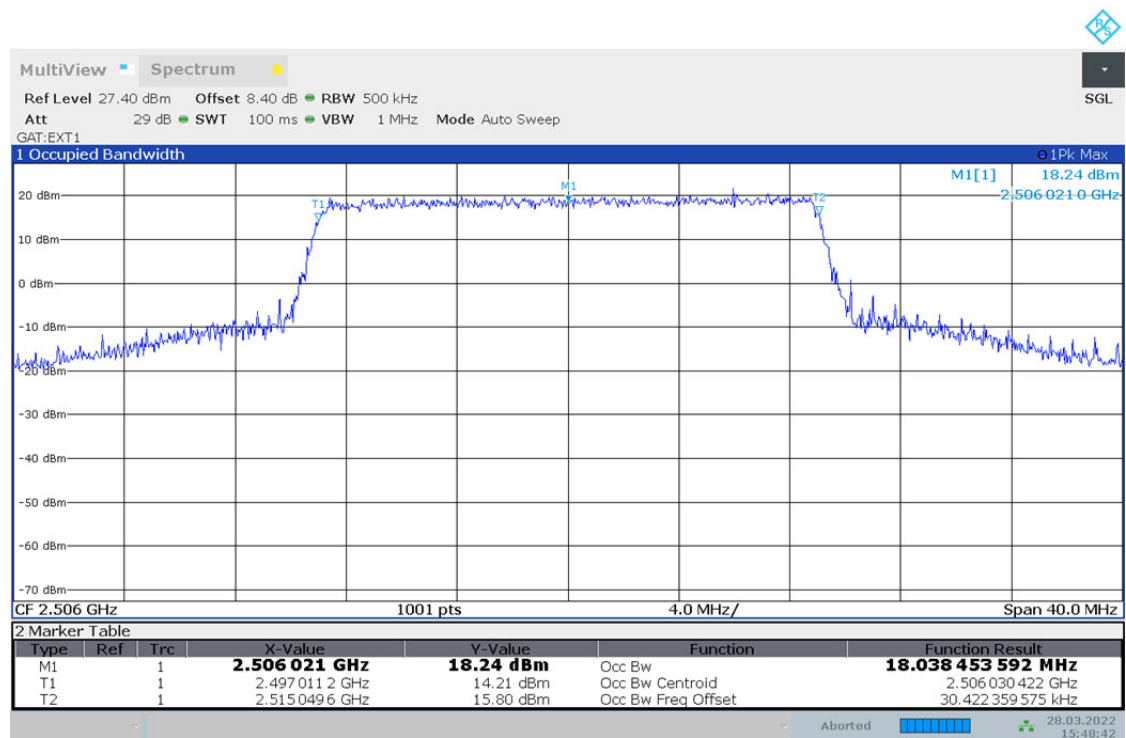


15:47:22 28.03.2022

## 4.2.22 TM2\_20MHz\_LCH\_RB100#0

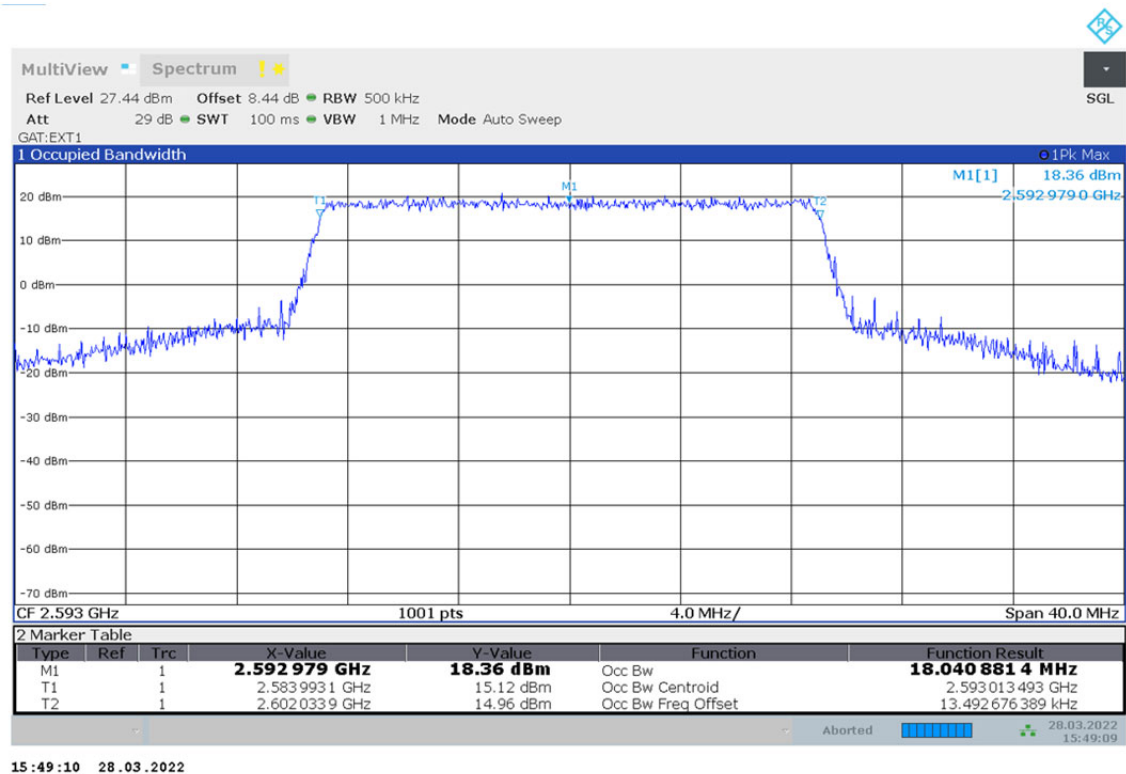
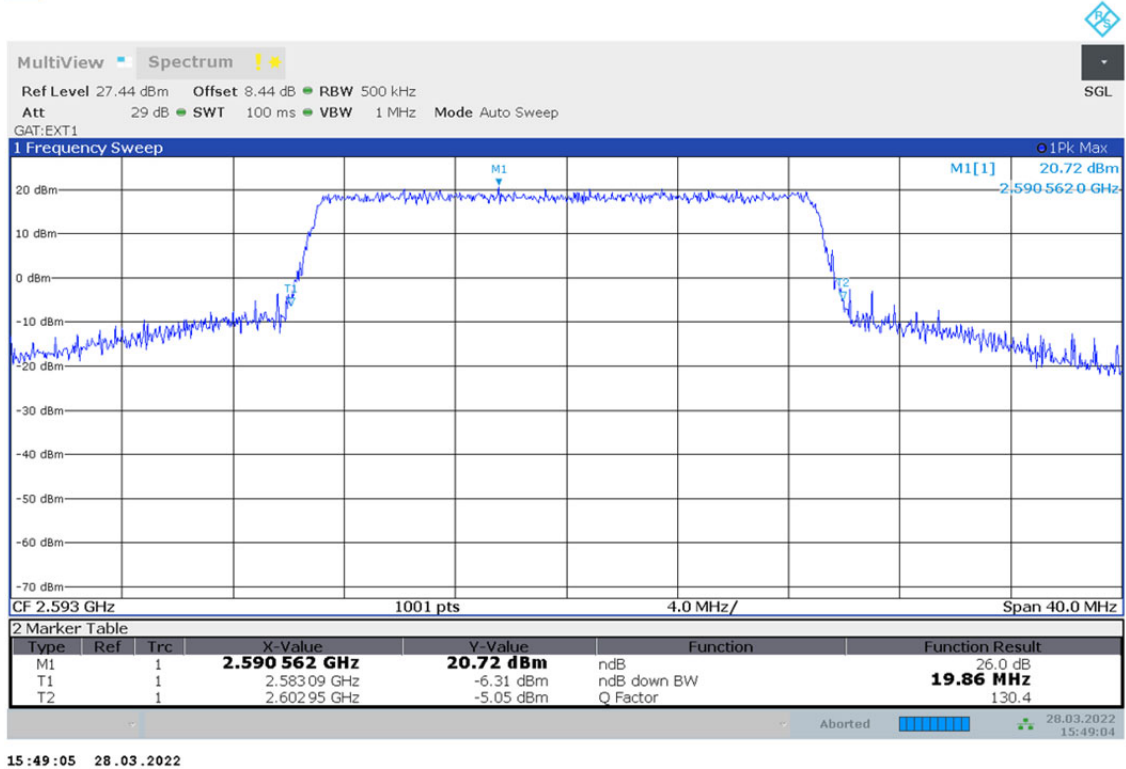


15:48:37 28.03.2022

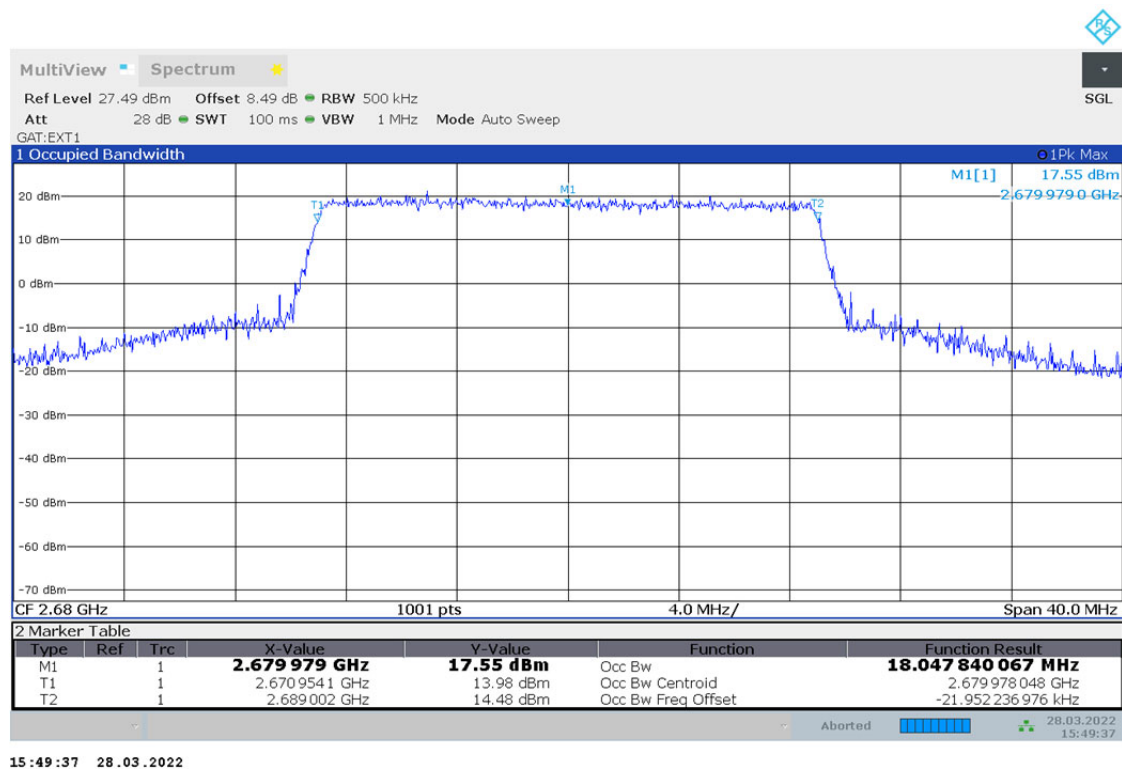
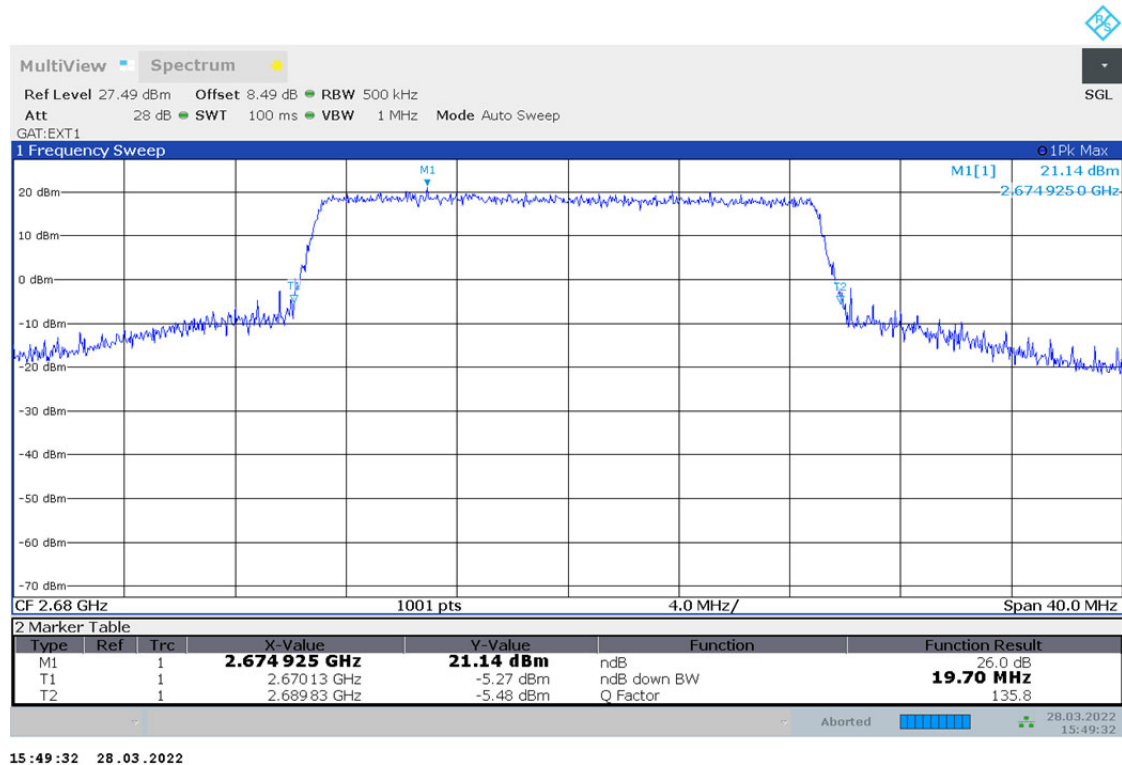


15:48:42 28.03.2022

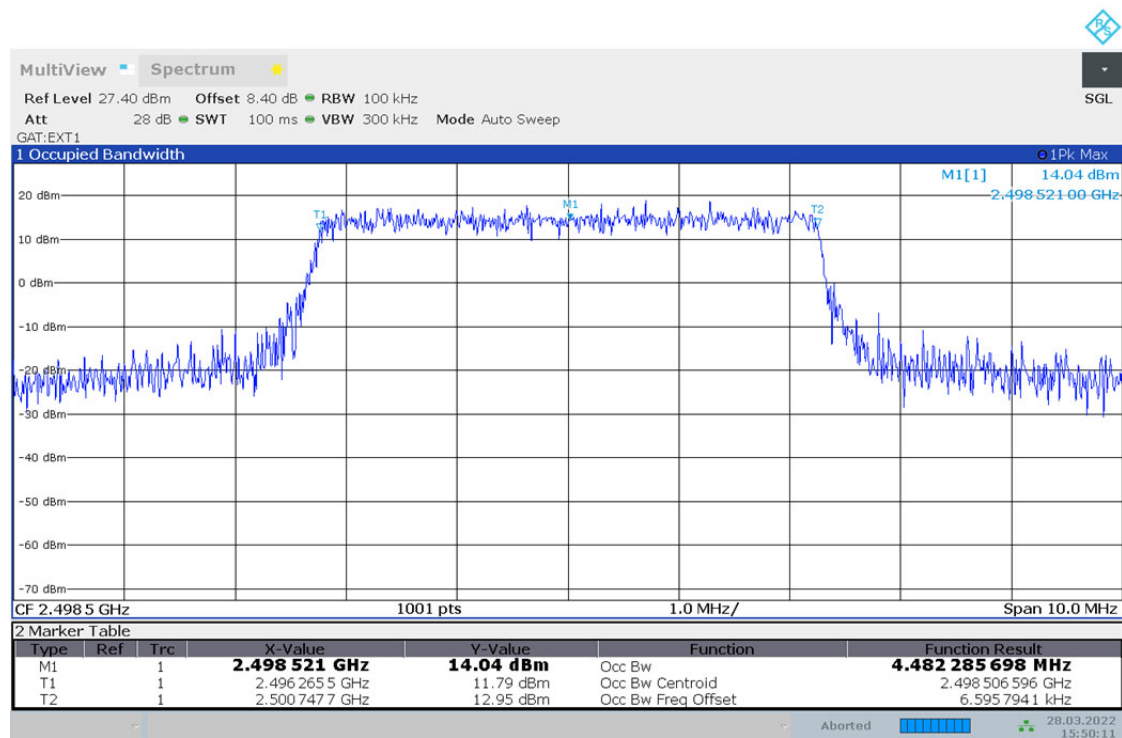
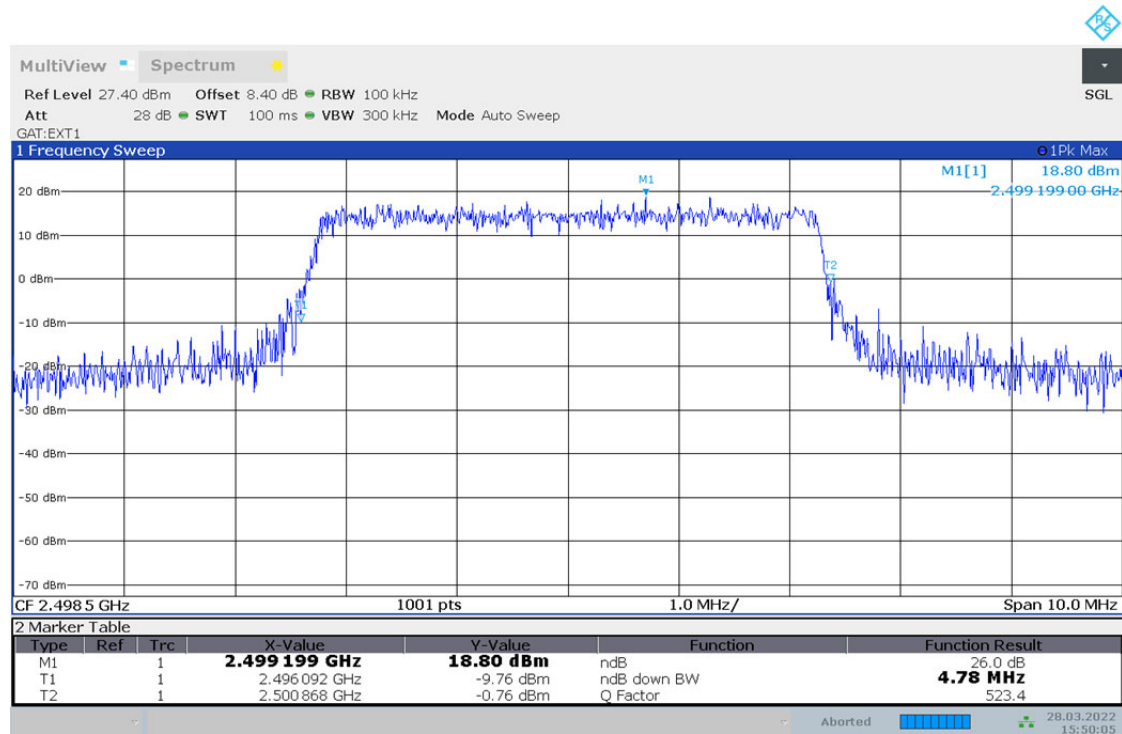
## 4.2.23 TM2\_20MHz\_MCH\_RB100#0



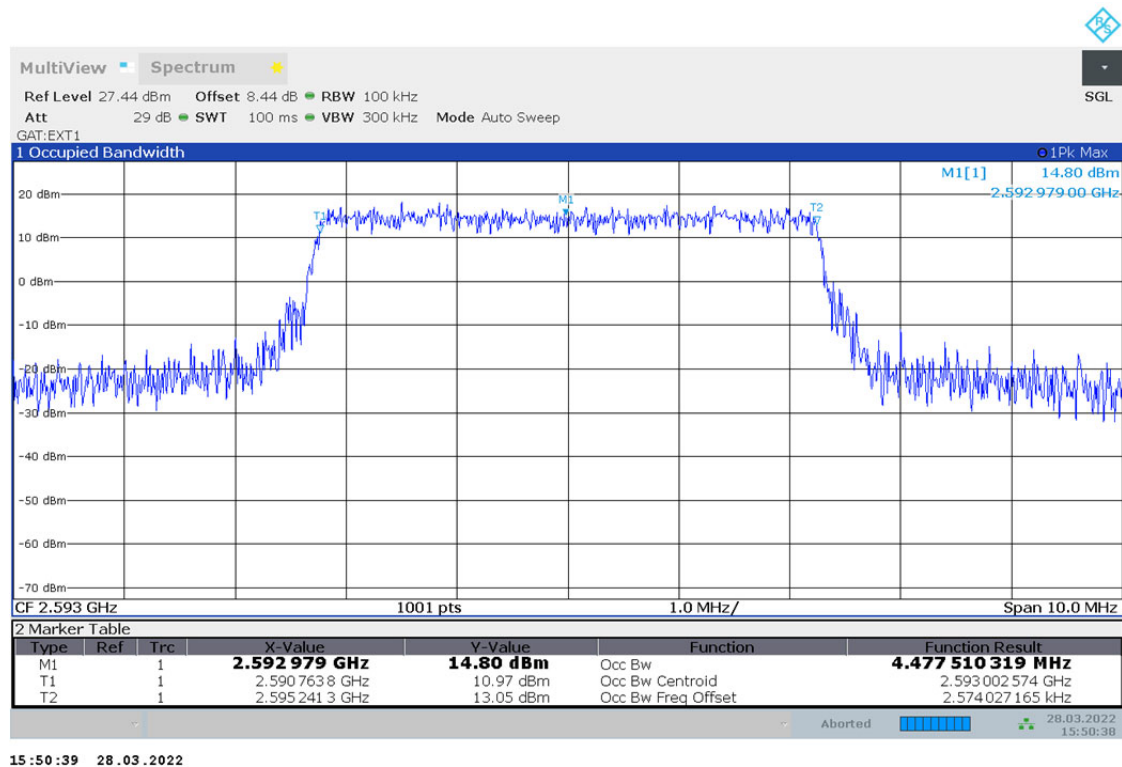
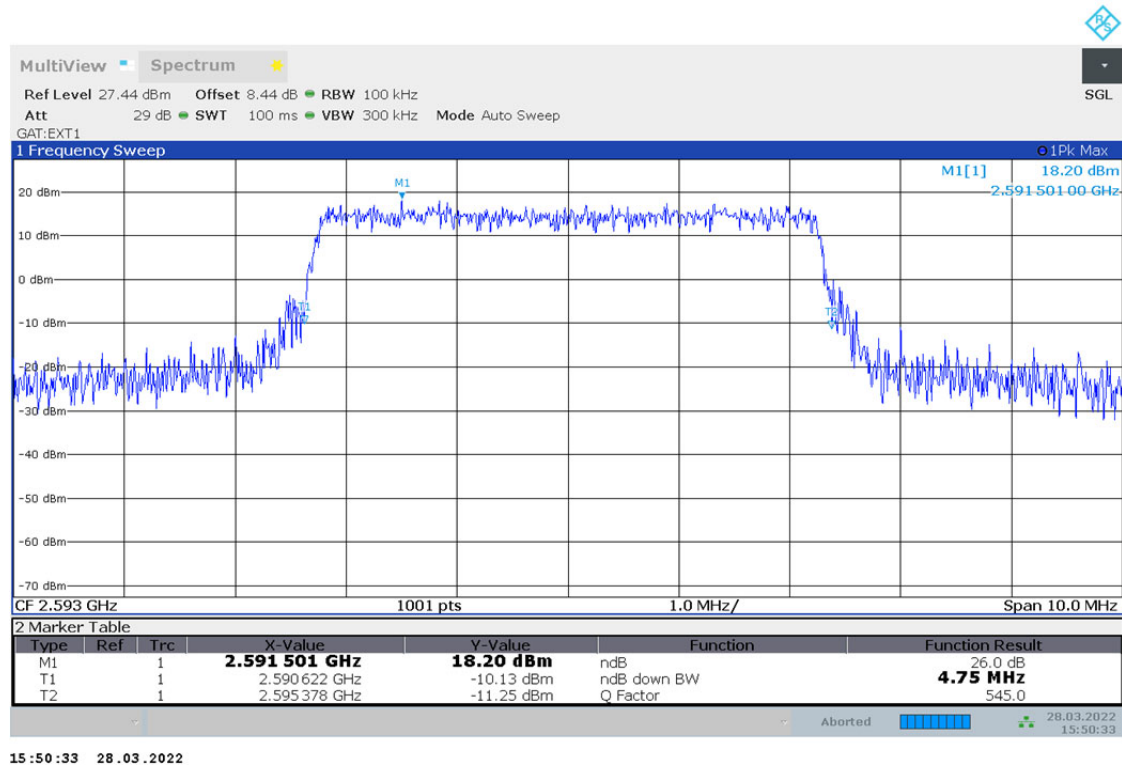
## 4.2.24 TM2\_20MHz\_HCH\_RB100#0



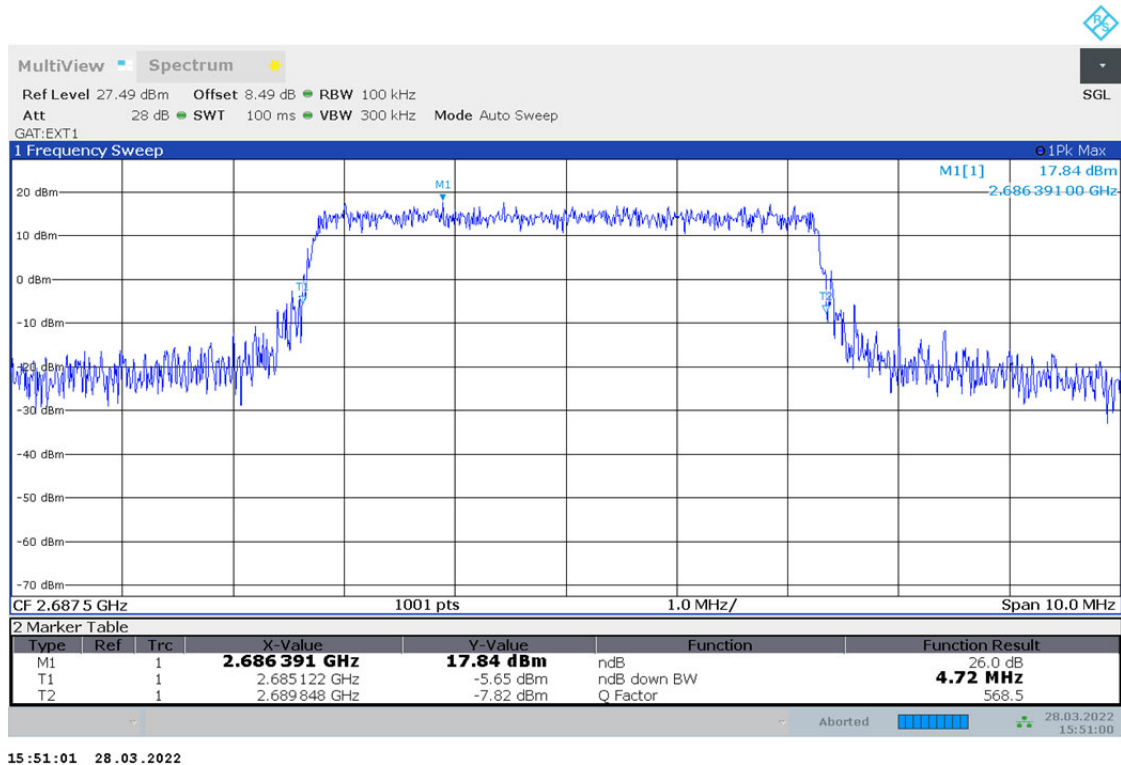
## 4.2.25 TM3\_5MHz\_LCH\_RB25#0



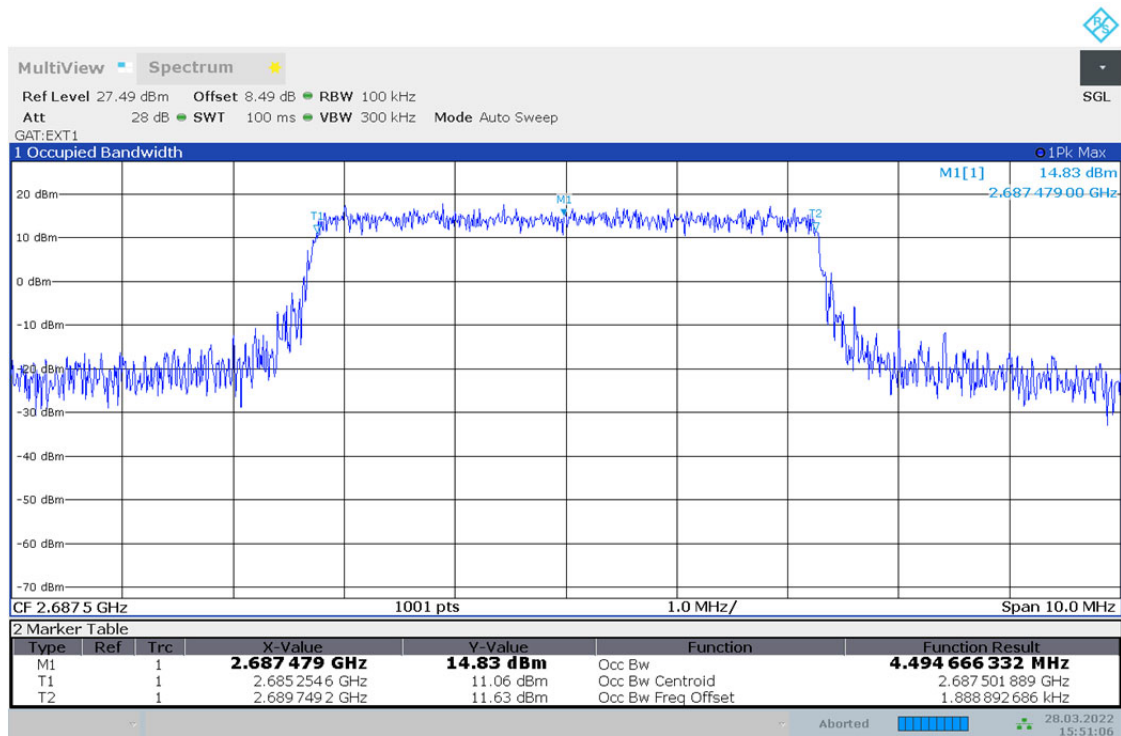
## 4.2.26 TM3\_5MHz\_MCH\_RB25#0



## 4.2.27 TM3\_5MHz\_HCH\_RB25#0

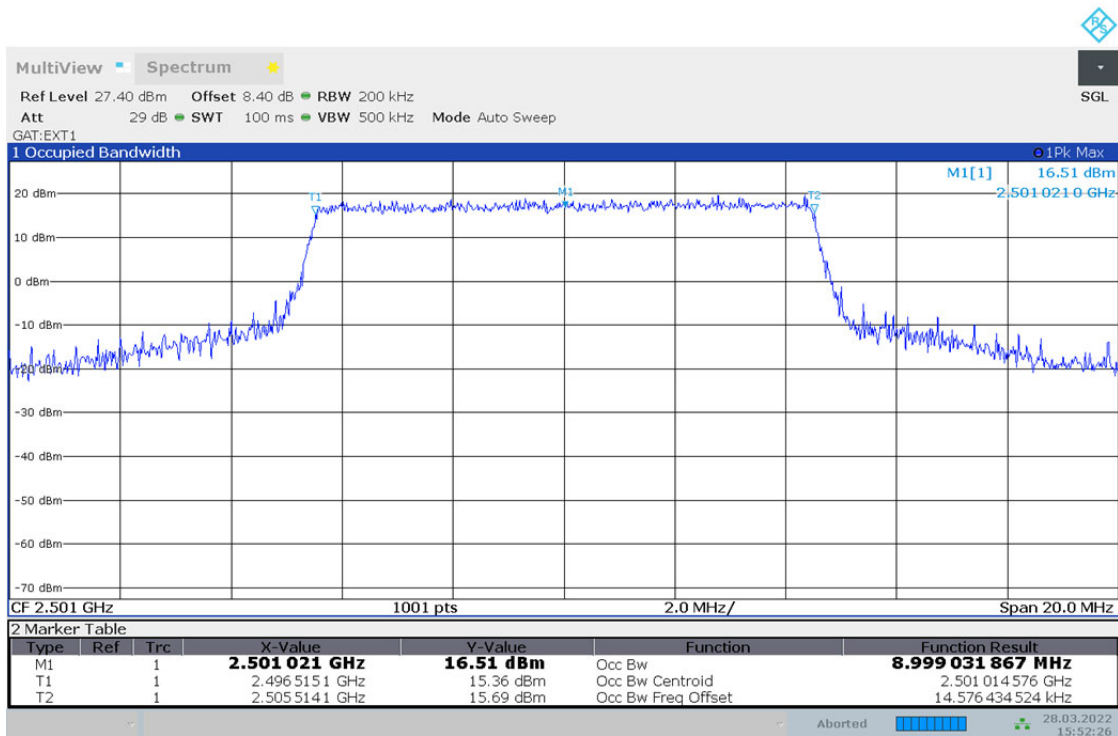
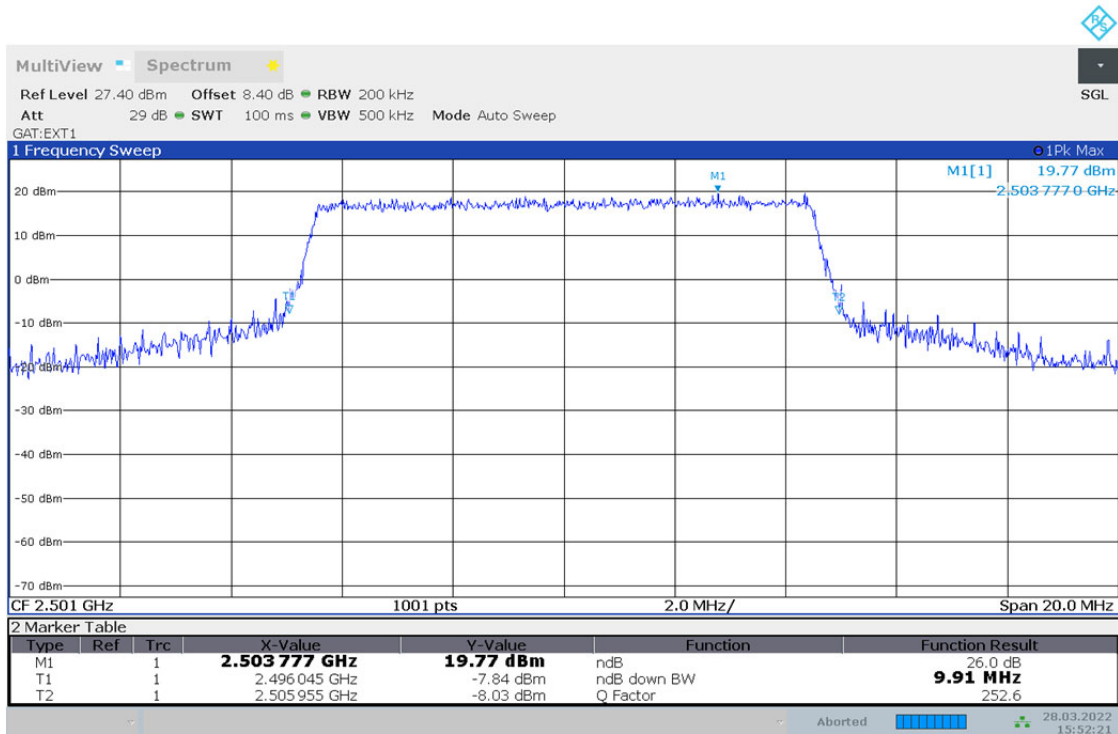


15:51:01 28.03.2022

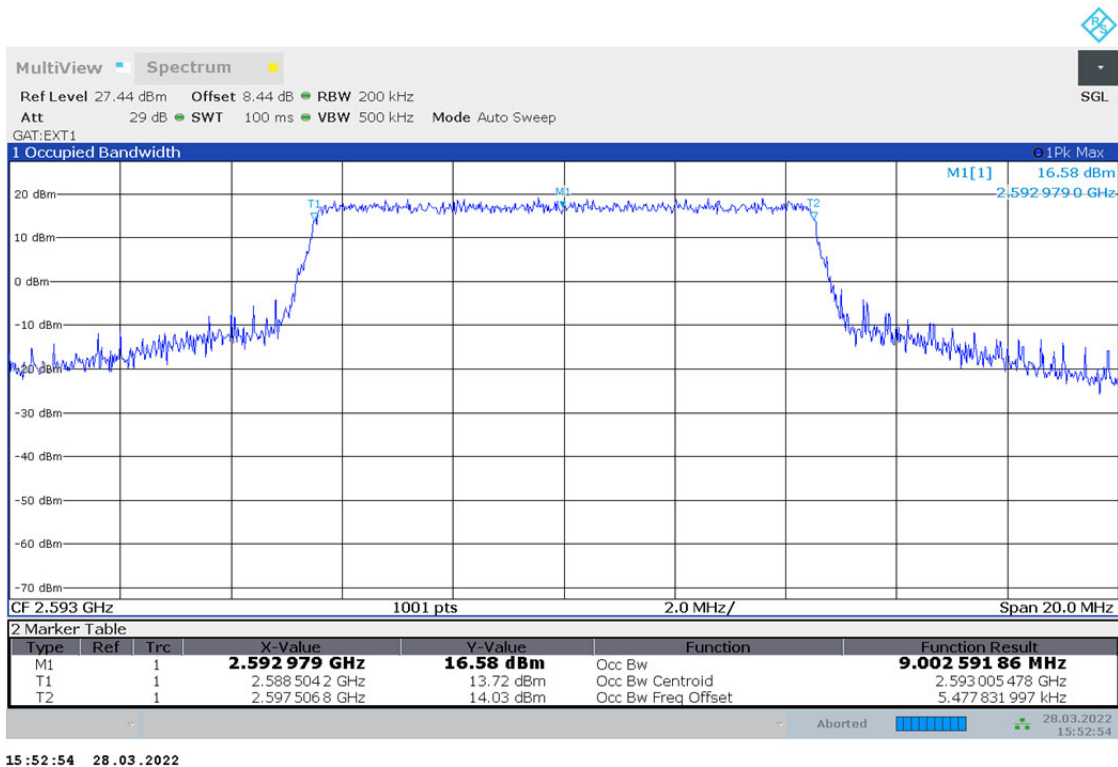
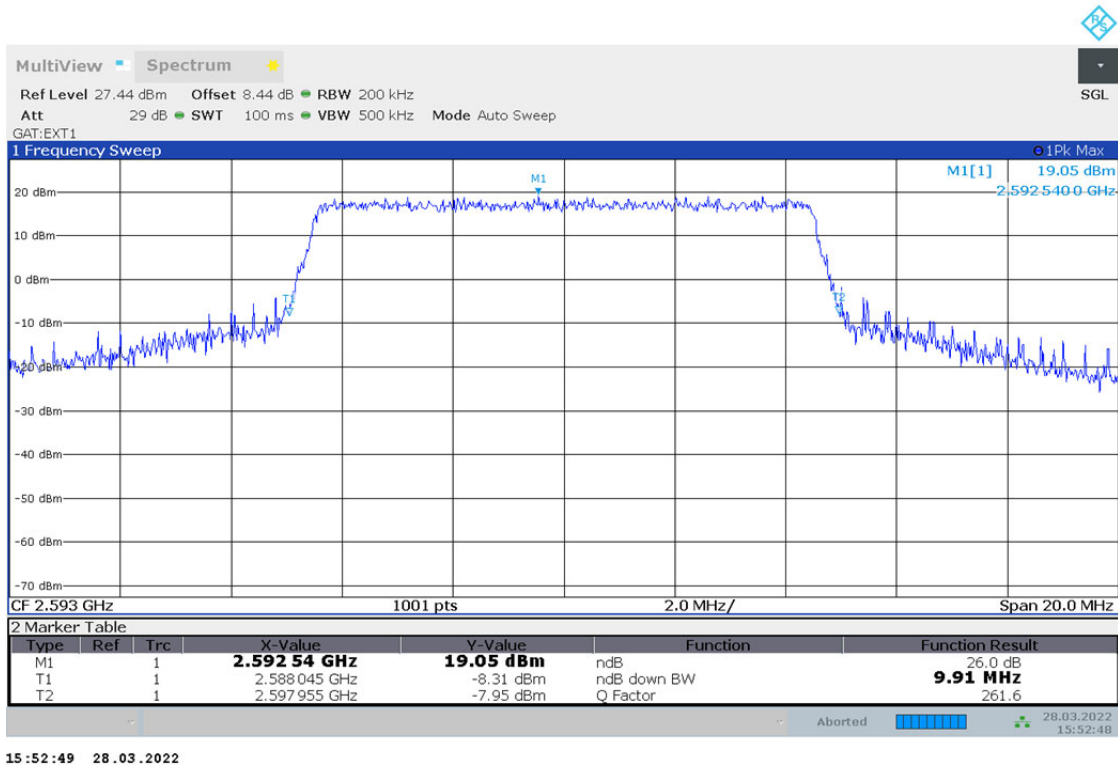


15:51:06 28.03.2022

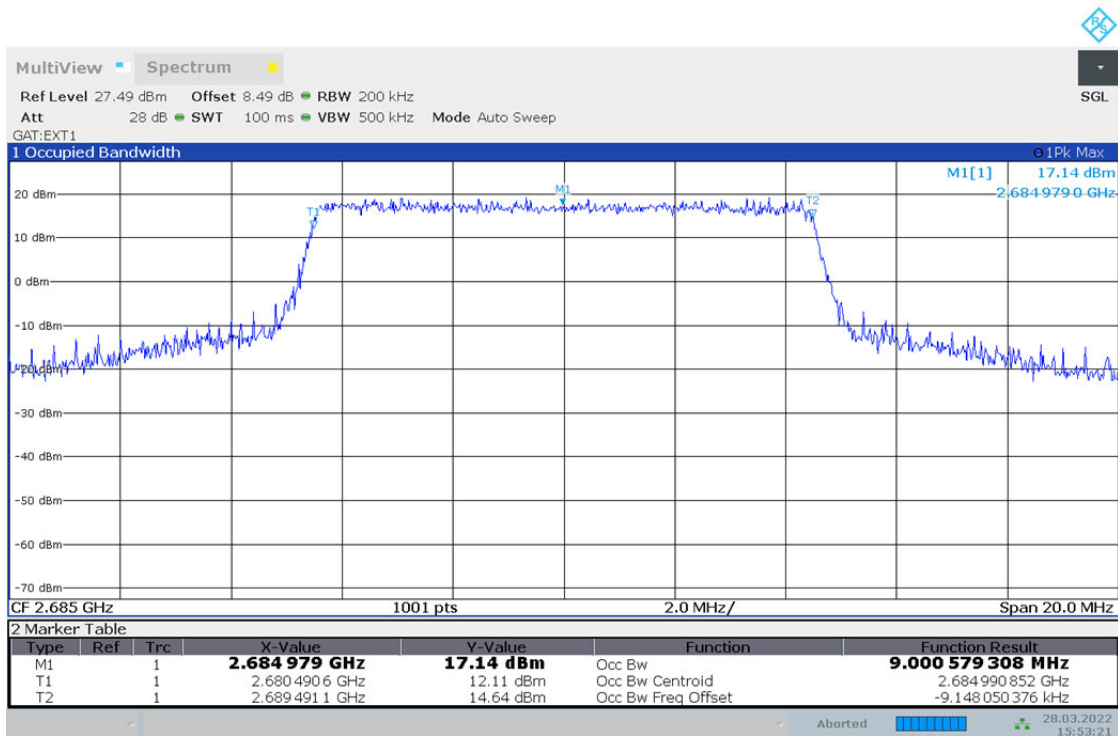
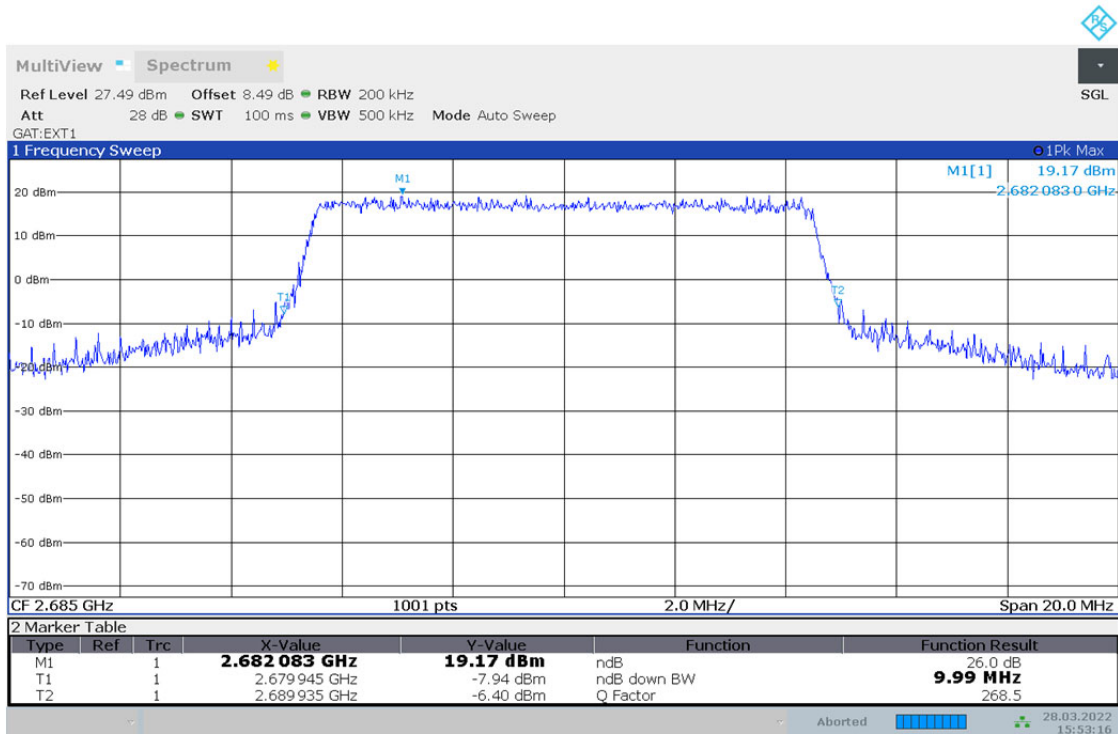
## 4.2.28 TM3\_10MHz\_LCH\_RB50#0



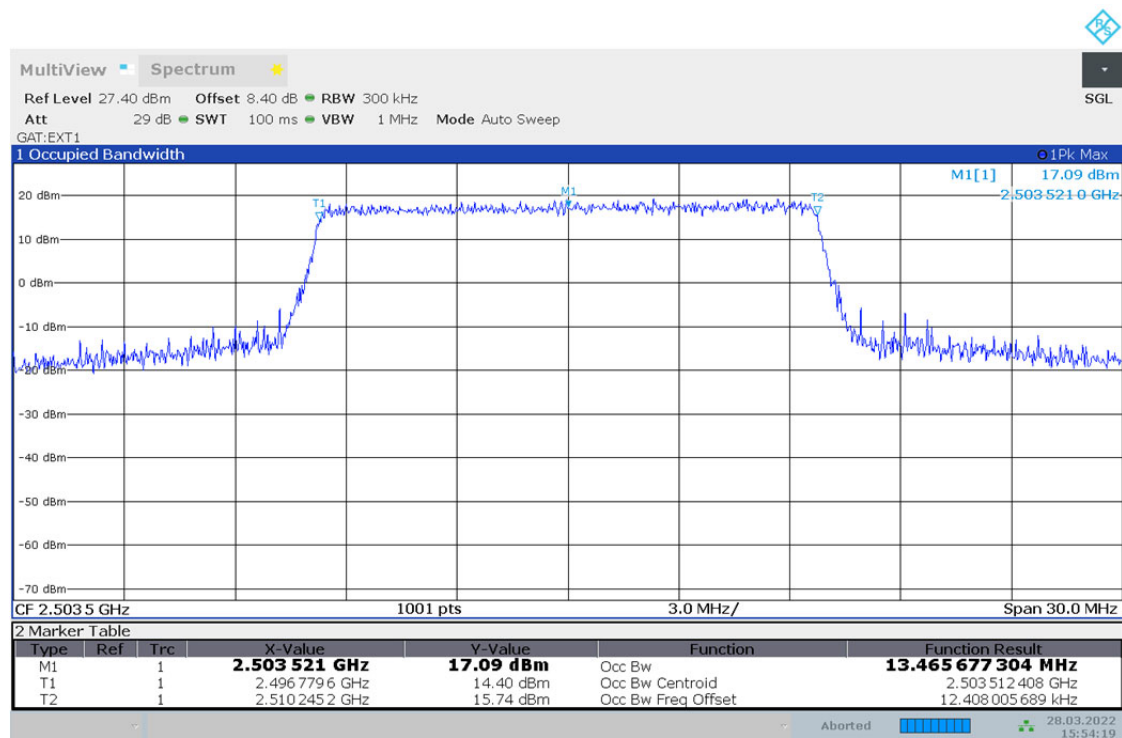
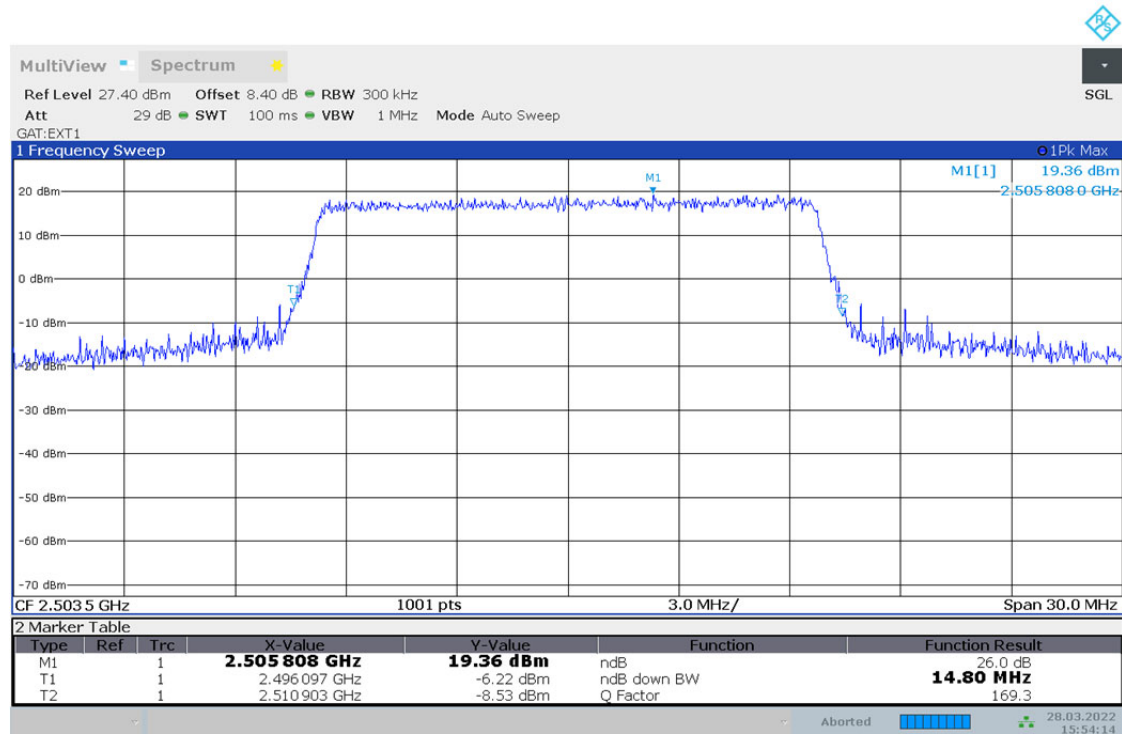
## 4.2.29 TM3\_10MHz\_MCH\_RB50#0



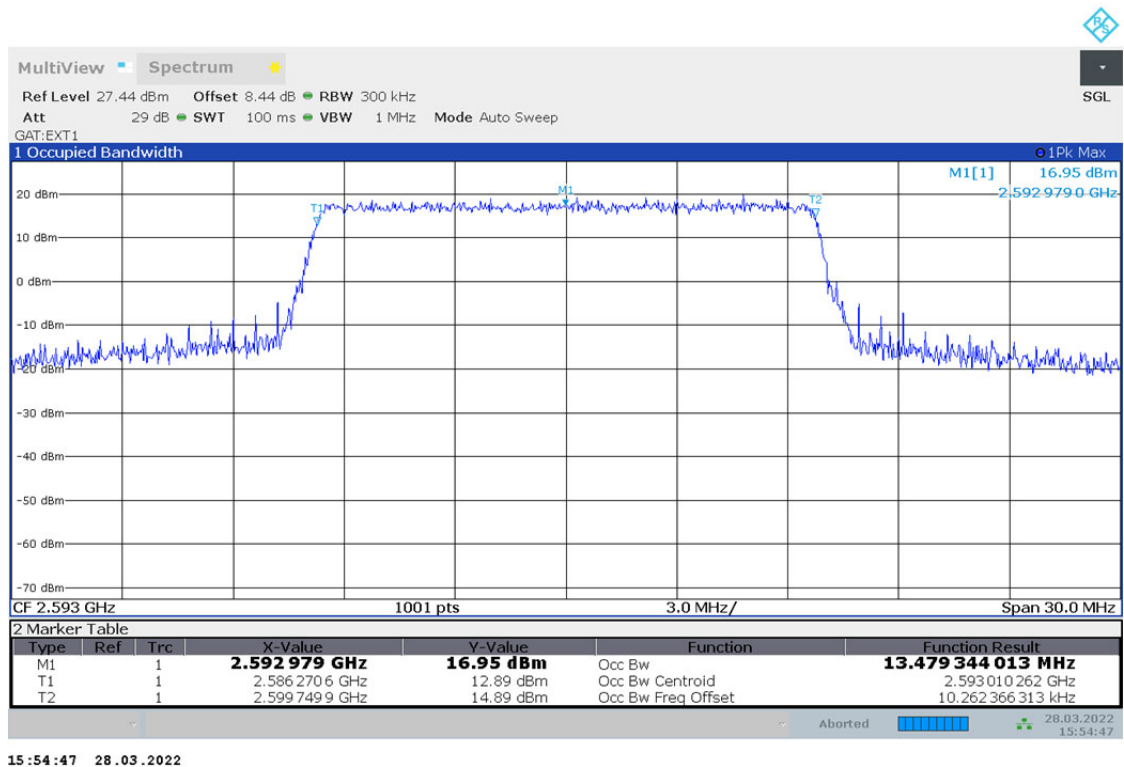
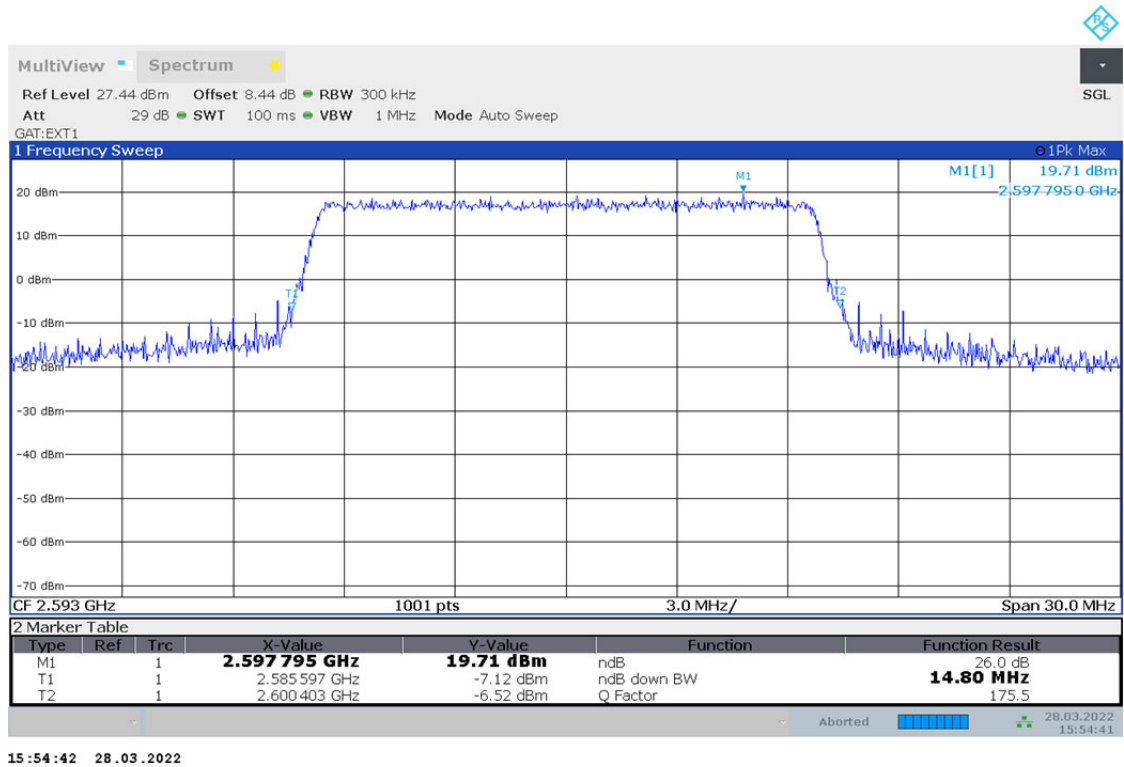
## 4.2.30 TM3\_10MHz\_HCH\_RB50#0



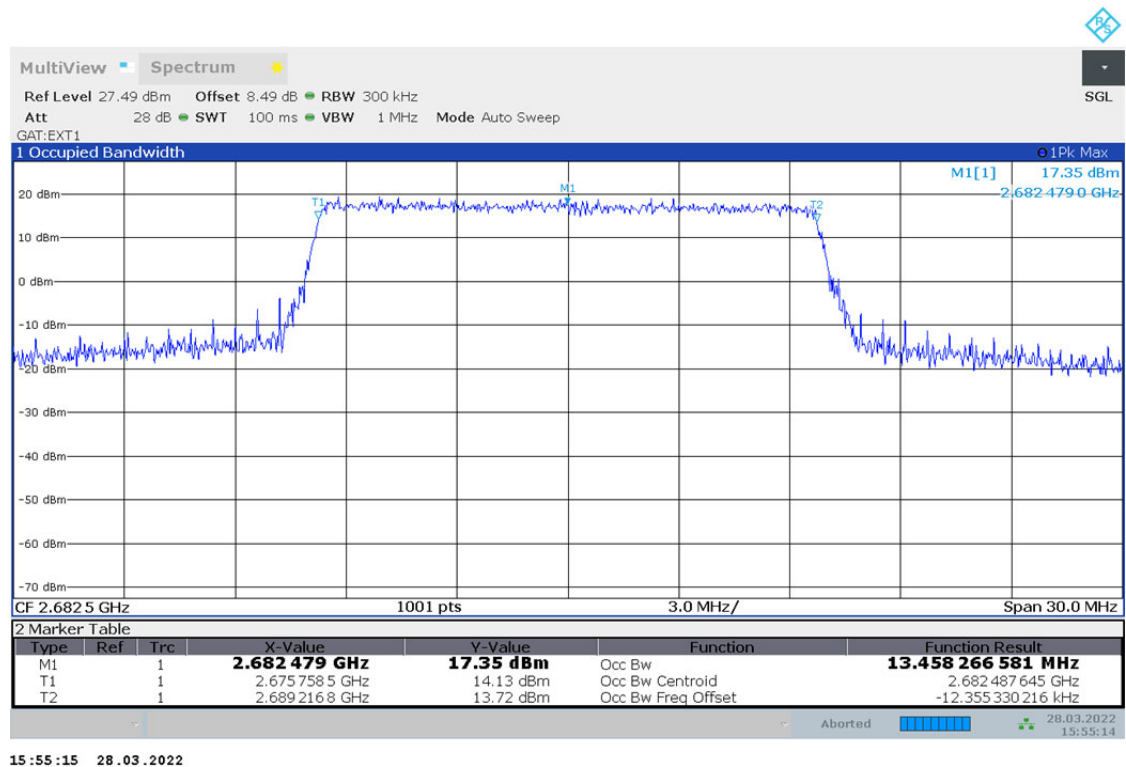
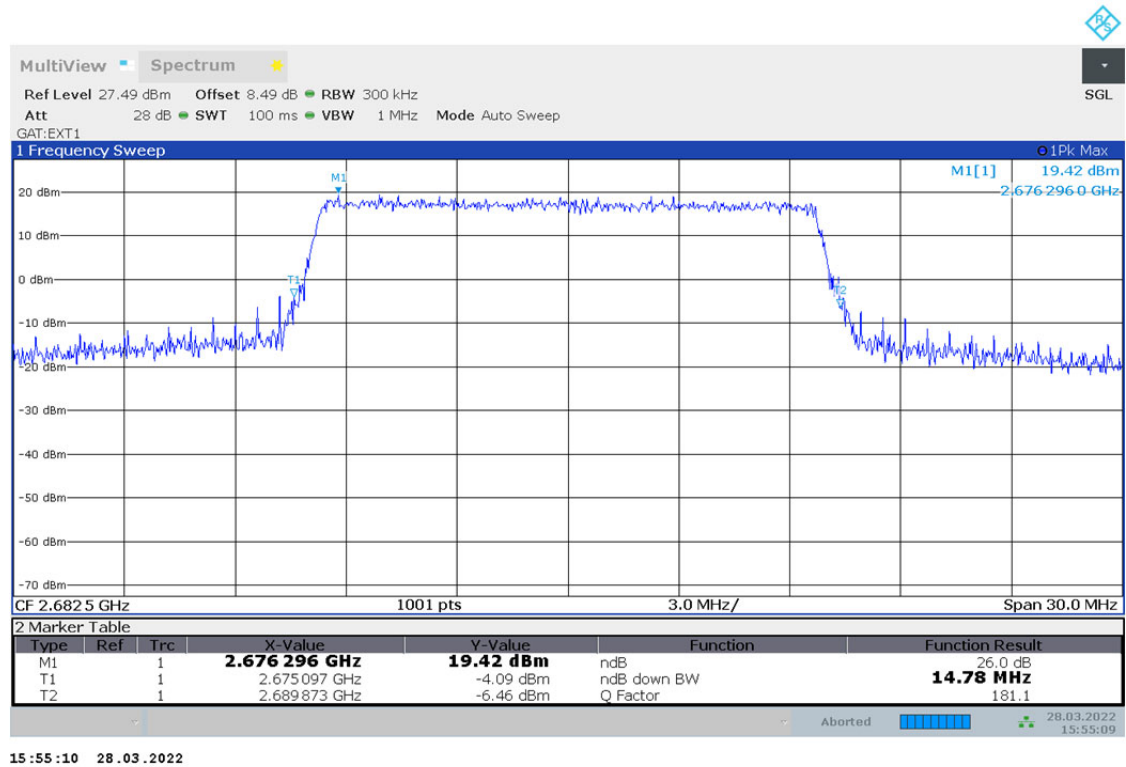
## 4.2.31 TM3\_15MHz\_LCH\_RB75#0



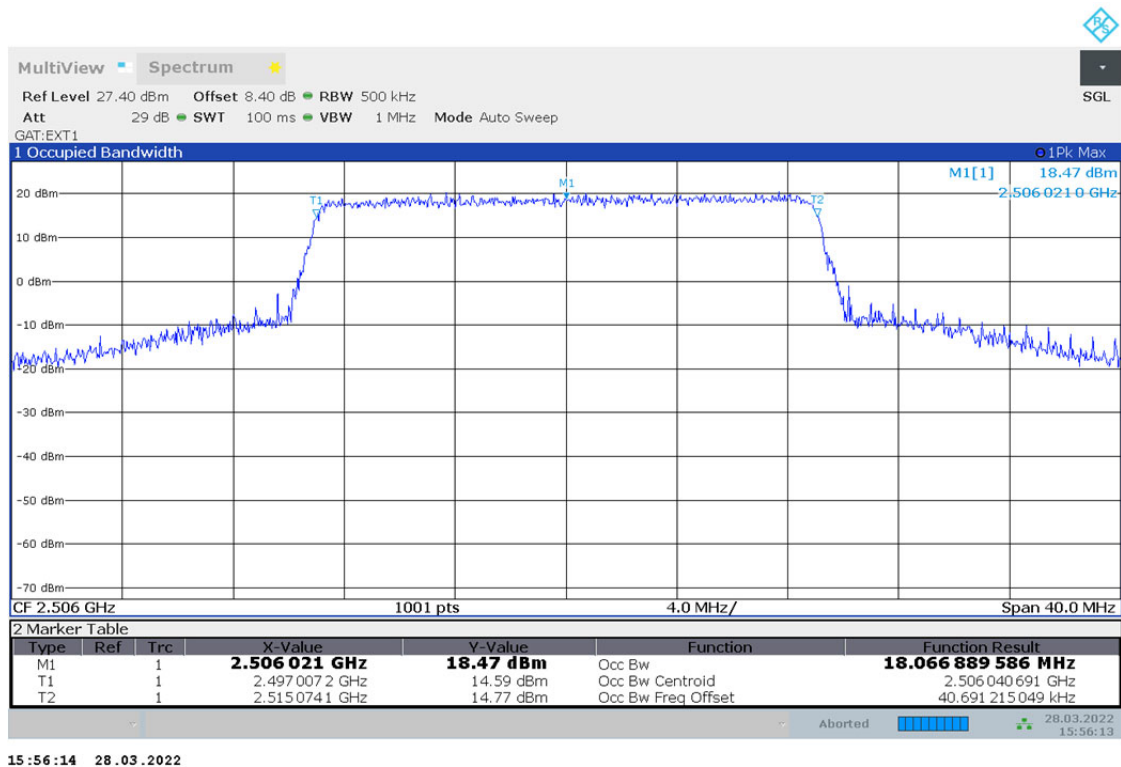
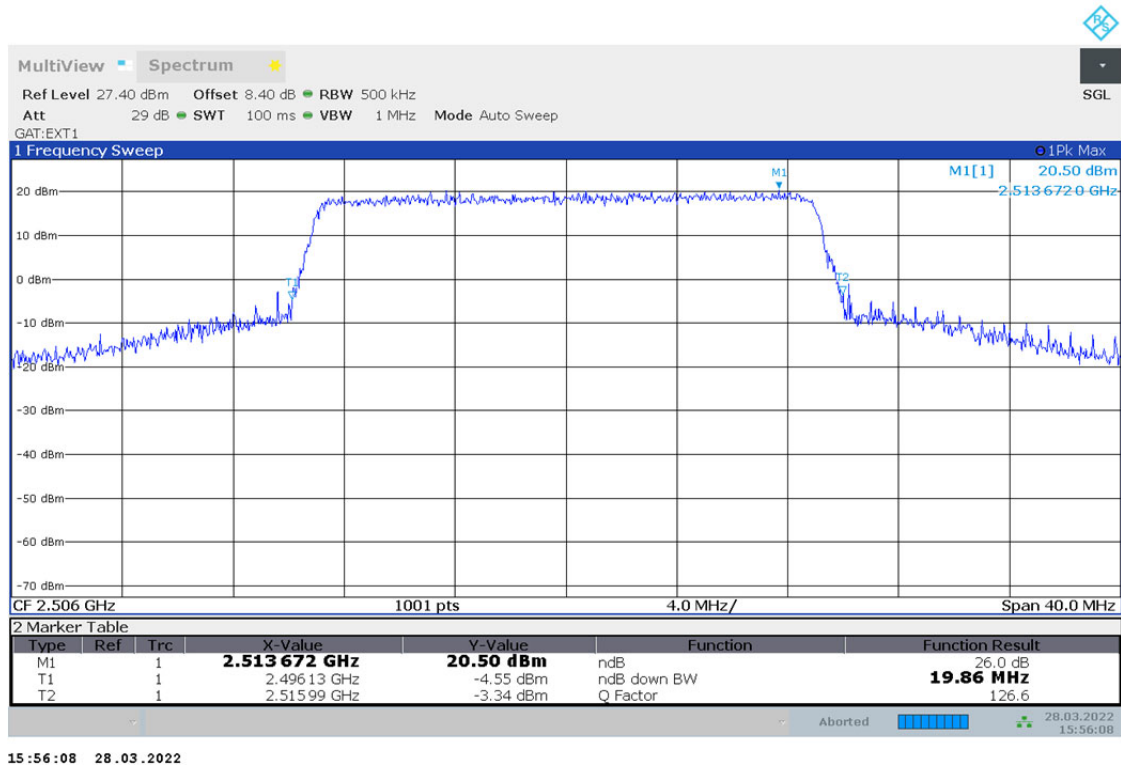
## 4.2.32 TM3\_15MHz\_MCH\_RB75#0



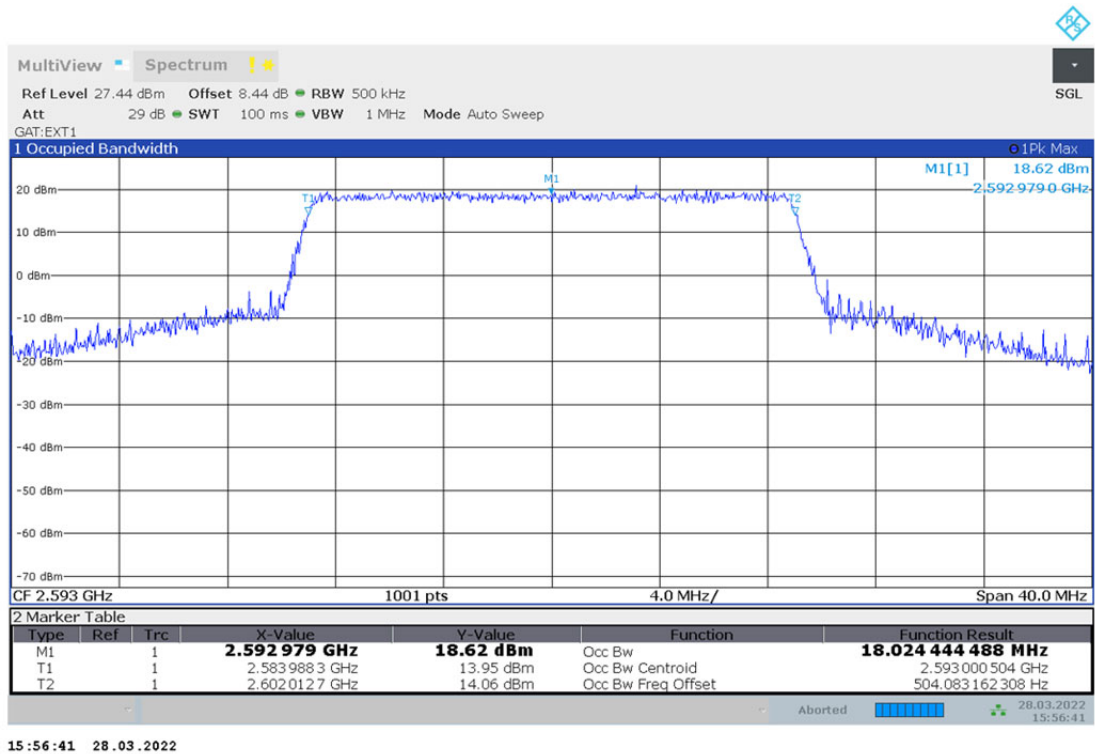
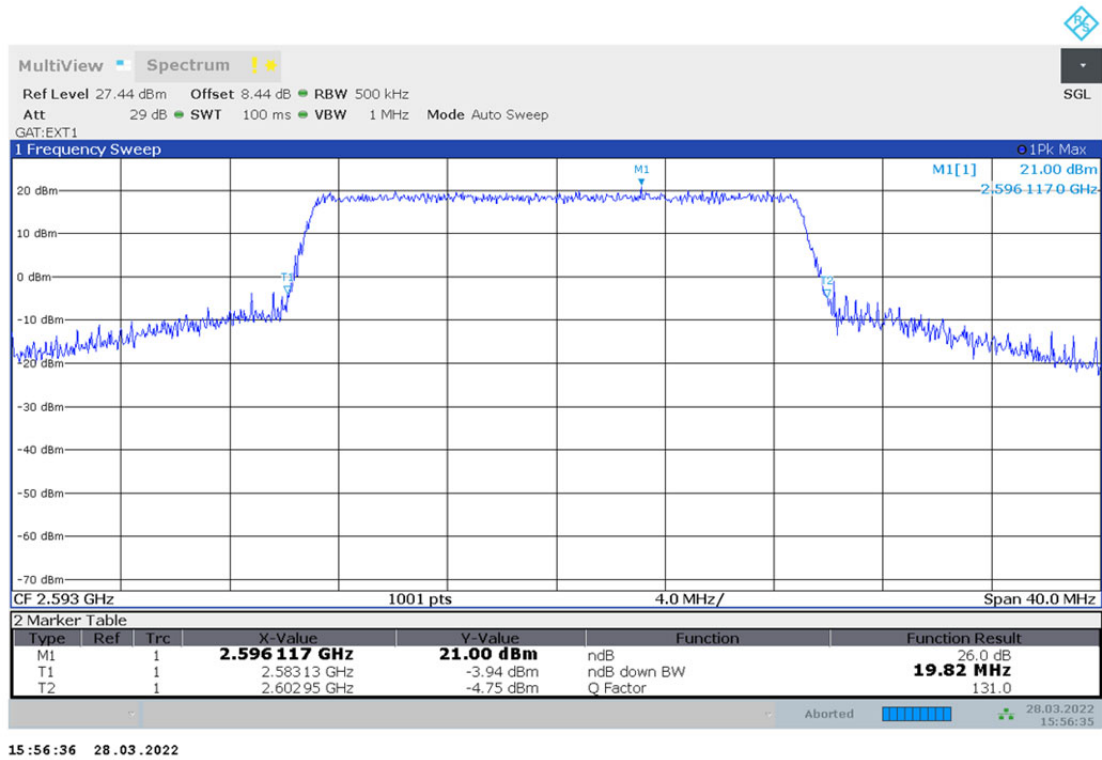
## 4.2.33 TM3\_15MHz\_HCH\_RB75#0



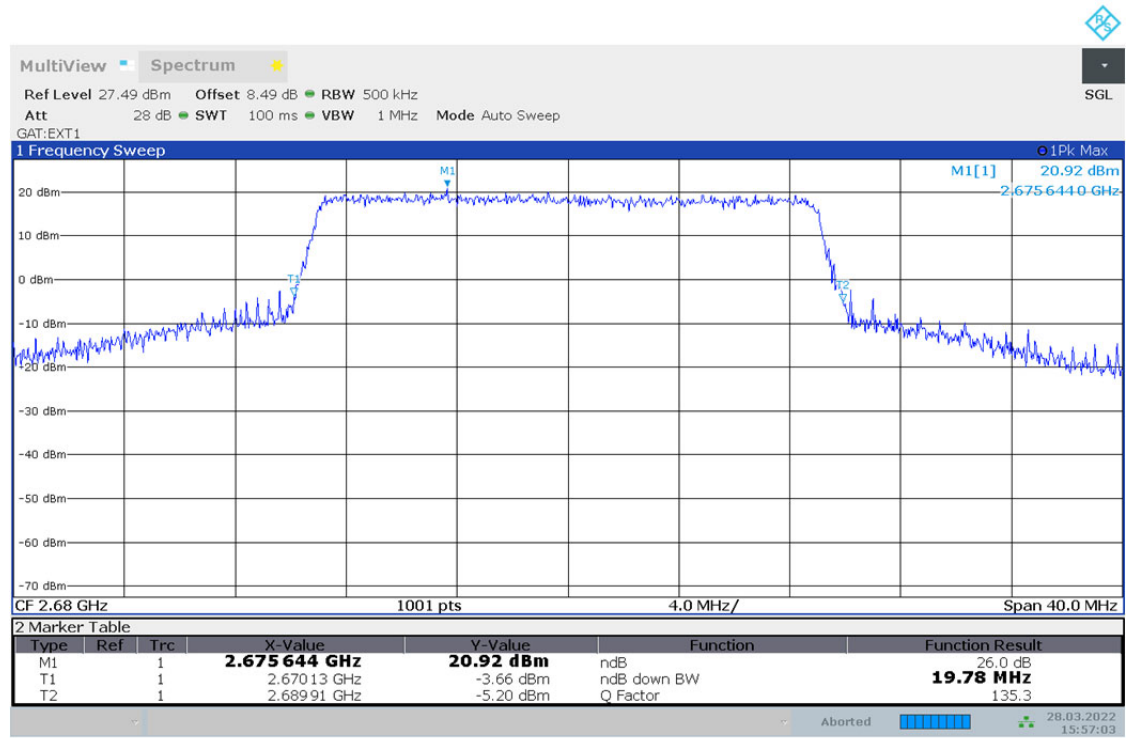
## 4.2.34 TM3\_20MHz\_LCH\_RB100#0



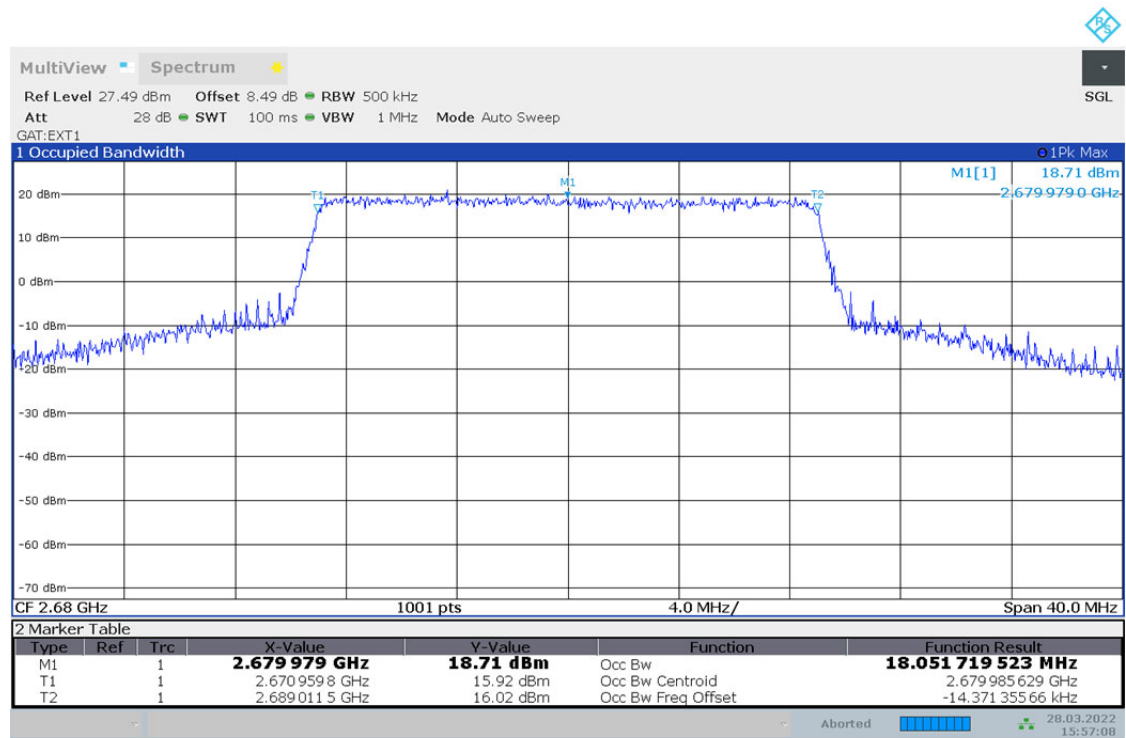
## 4.2.35 TM3\_20MHz\_MCH\_RB100#0



## 4.2.36 TM3\_20MHz\_HCH\_RB100#0



15:57:04 28.03.2022



15:57:09 28.03.2022

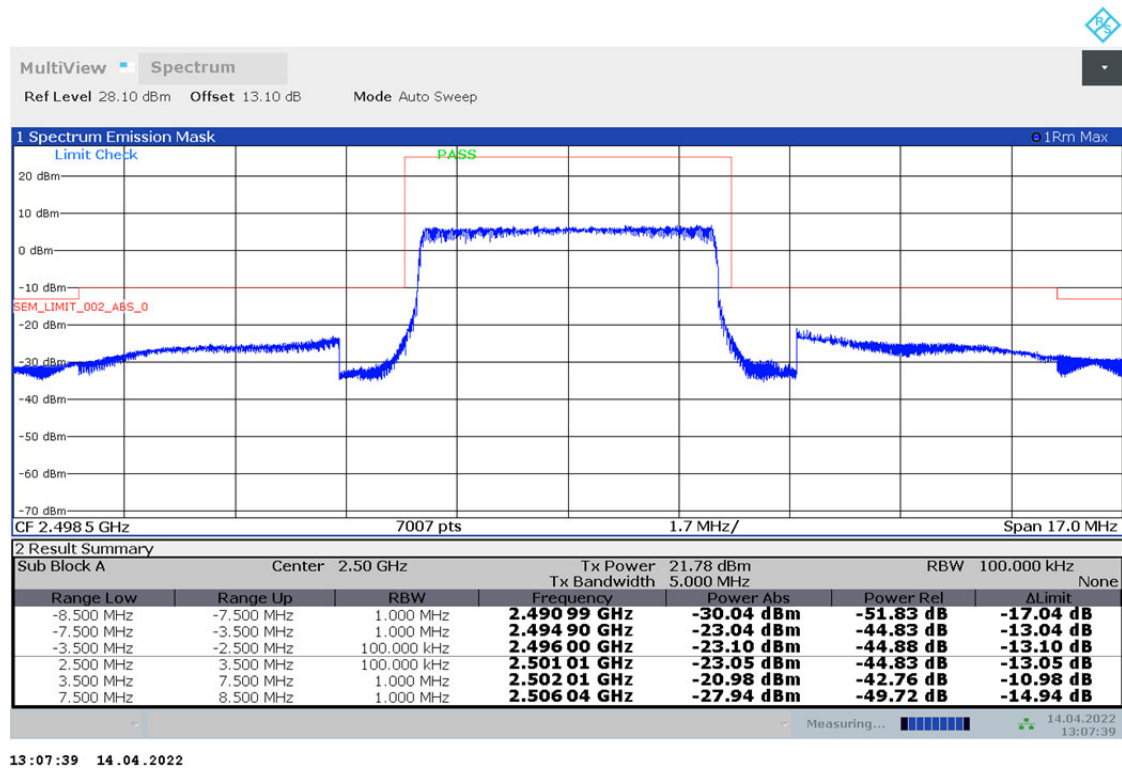
## 5 Appendix E: Band Edges Compliance

### 5.1 Test Results

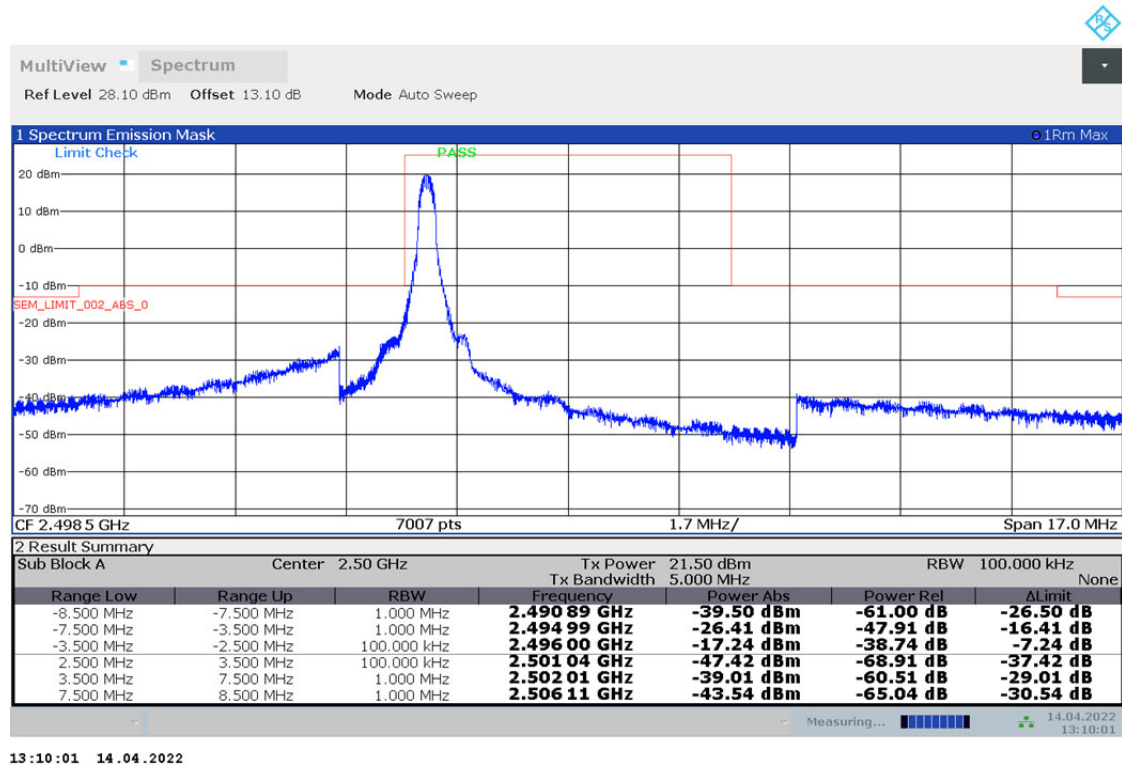
| Band Edges Compliance |                      |              |         |            |         |
|-----------------------|----------------------|--------------|---------|------------|---------|
| Test Mode             | Test Bandwidth [MHz] | Test Channel | Test RB | Modulation | Verdict |
| TM1                   | 5                    | LCH          | RB25#0  | QPSK       | PASS    |
|                       |                      |              | RB1#0   | QPSK       | PASS    |
|                       |                      |              | RB1#24  | QPSK       | PASS    |
|                       |                      | HCH          | RB25#0  | QPSK       | PASS    |
|                       |                      |              | RB1#0   | QPSK       | PASS    |
|                       |                      |              | RB1#24  | QPSK       | PASS    |
|                       | 20                   | LCH          | RB100#0 | QPSK       | PASS    |
|                       |                      |              | RB1#0   | QPSK       | PASS    |
|                       |                      |              | RB1#99  | QPSK       | PASS    |
|                       |                      | HCH          | RB100#0 | QPSK       | PASS    |
|                       |                      |              | RB1#0   | QPSK       | PASS    |
|                       |                      |              | RB1#99  | QPSK       | PASS    |
| TM2                   | 5                    | LCH          | RB25#0  | 16QAM      | PASS    |
|                       |                      |              | RB1#0   | 16QAM      | PASS    |
|                       |                      |              | RB1#24  | 16QAM      | PASS    |
|                       |                      | HCH          | RB25#0  | 16QAM      | PASS    |
|                       |                      |              | RB1#0   | 16QAM      | PASS    |
|                       |                      |              | RB1#24  | 16QAM      | PASS    |
|                       | 20                   | LCH          | RB100#0 | 16QAM      | PASS    |
|                       |                      |              | RB1#0   | 16QAM      | PASS    |
|                       |                      |              | RB1#99  | 16QAM      | PASS    |
|                       |                      | HCH          | RB100#0 | 16QAM      | PASS    |
|                       |                      |              | RB1#0   | 16QAM      | PASS    |
|                       |                      |              | RB1#99  | 16QAM      | PASS    |
| TM3                   | 5                    | LCH          | RB25#0  | 64QAM      | PASS    |
|                       |                      |              | RB1#0   | 64QAM      | PASS    |
|                       |                      |              | RB1#24  | 64QAM      | PASS    |
|                       |                      | HCH          | RB25#0  | 64QAM      | PASS    |
|                       |                      |              | RB1#0   | 64QAM      | PASS    |
|                       |                      |              | RB1#24  | 64QAM      | PASS    |
|                       | 20                   | LCH          | RB100#0 | 64QAM      | PASS    |
|                       |                      |              | RB1#0   | 64QAM      | PASS    |
|                       |                      |              | RB1#99  | 64QAM      | PASS    |
|                       |                      | HCH          | RB100#0 | 64QAM      | PASS    |
|                       |                      |              | RB1#0   | 64QAM      | PASS    |
|                       |                      |              | RB1#99  | 64QAM      | PASS    |

## 5.2 Test Plots

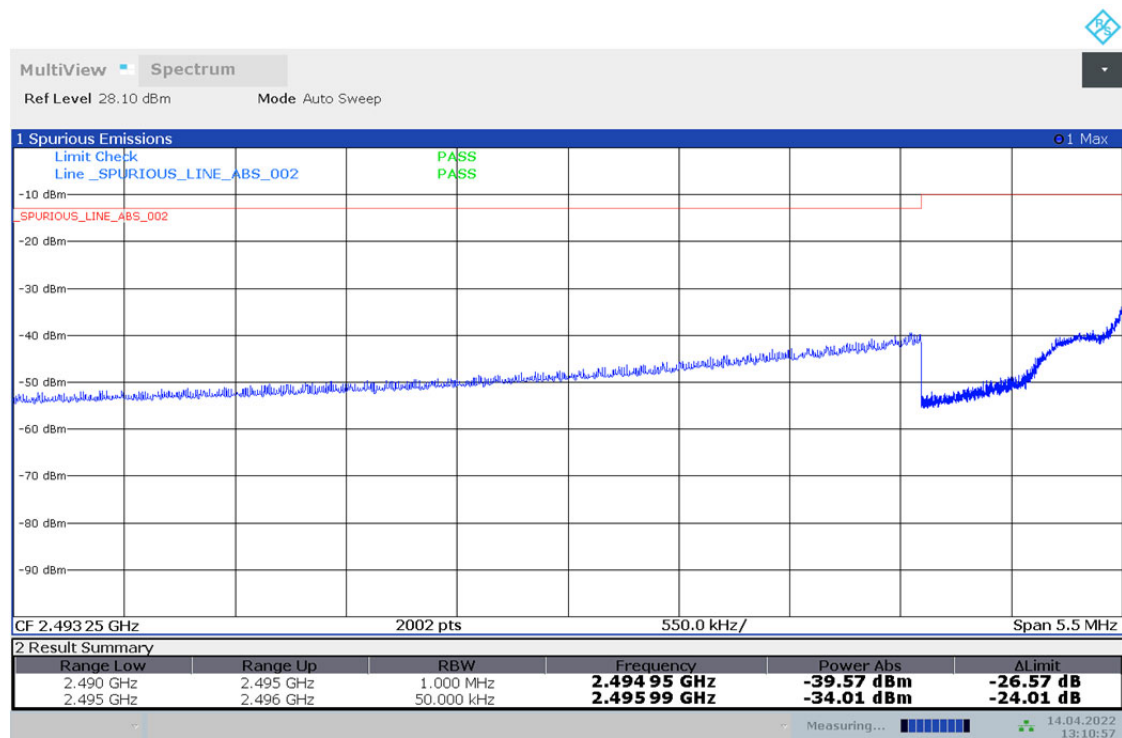
### 5.2.1 TM1\_5MHz\_LCH\_RB25#0



## 5.2.2 TM1\_5MHz\_LCH\_RB1#0

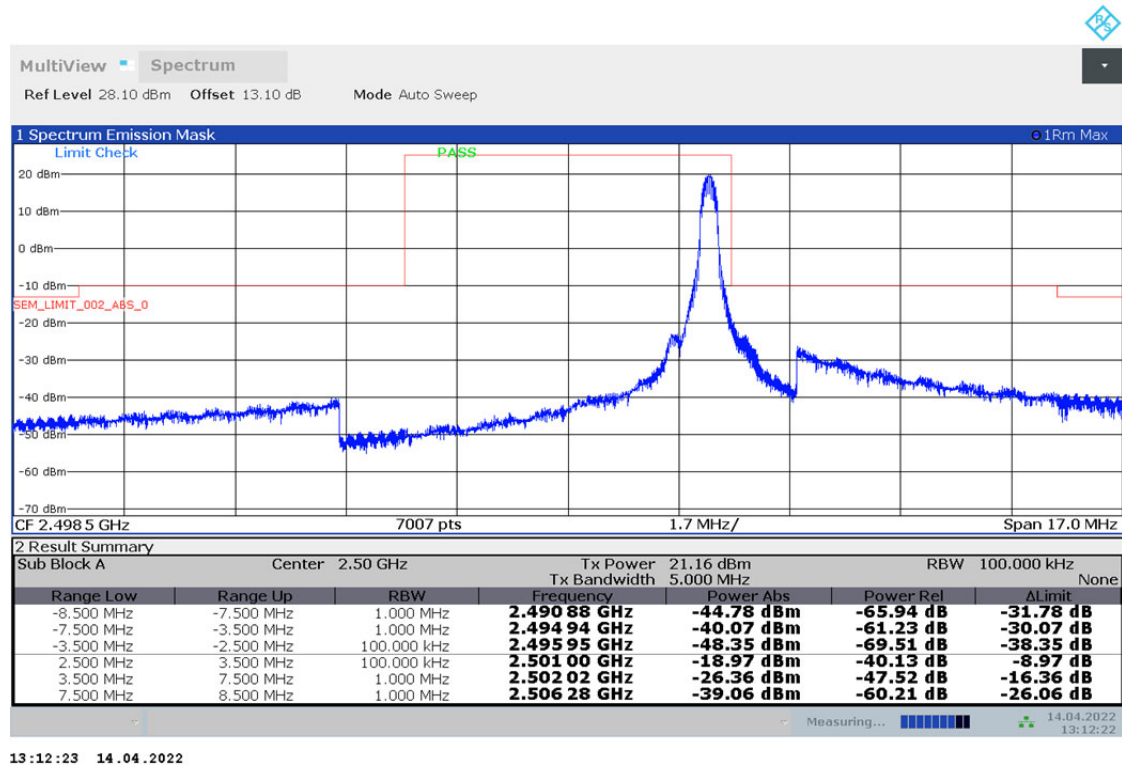


13:10:01 14.04.2022



13:10:58 14.04.2022

## 5.2.3 TM1\_5MHz\_LCH\_RB1#24

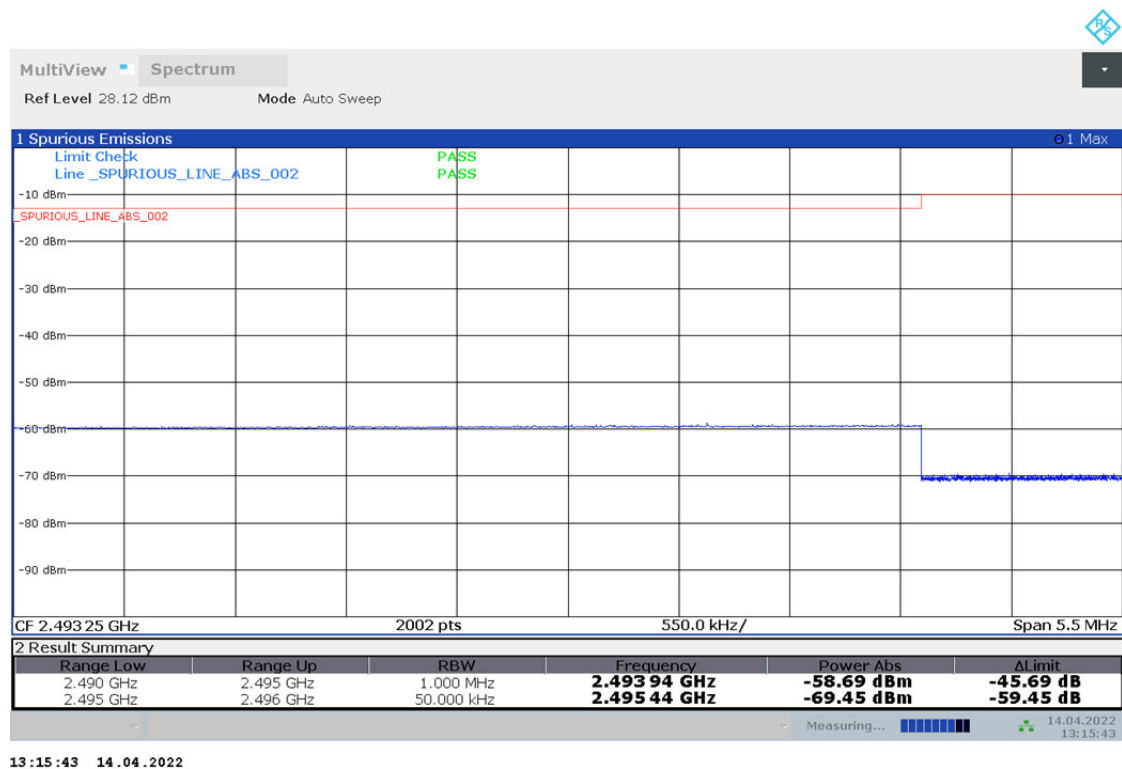
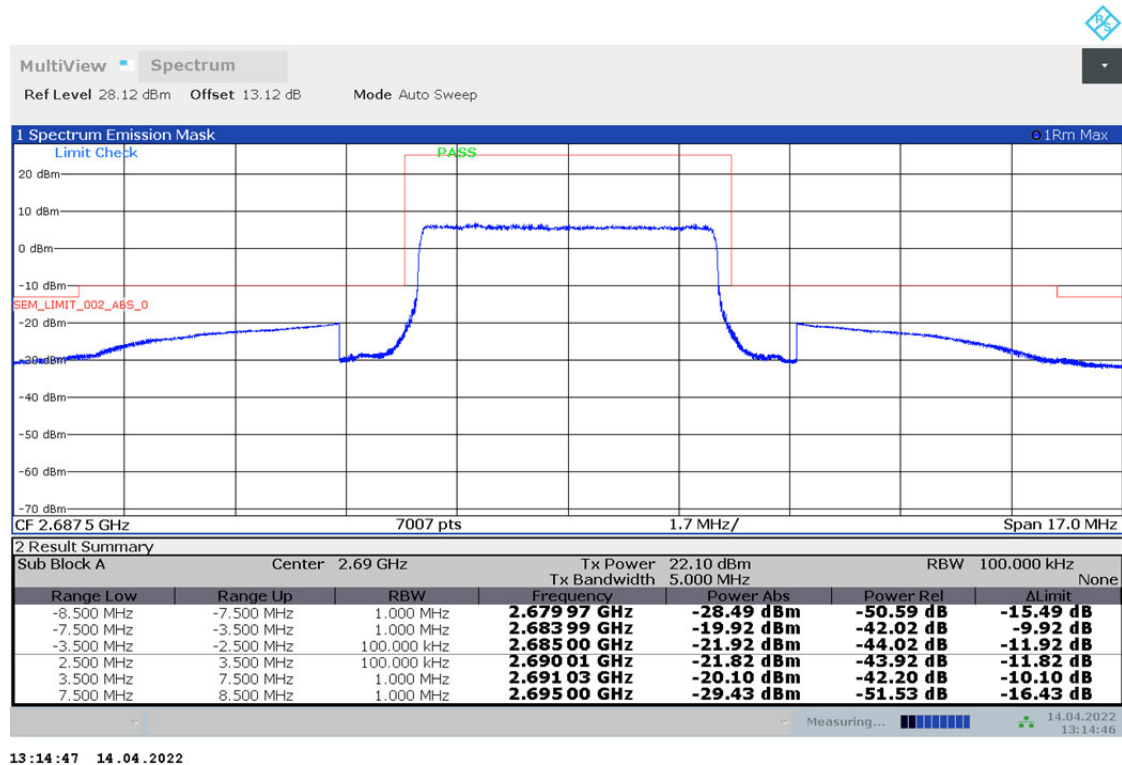


13:12:23 14.04.2022

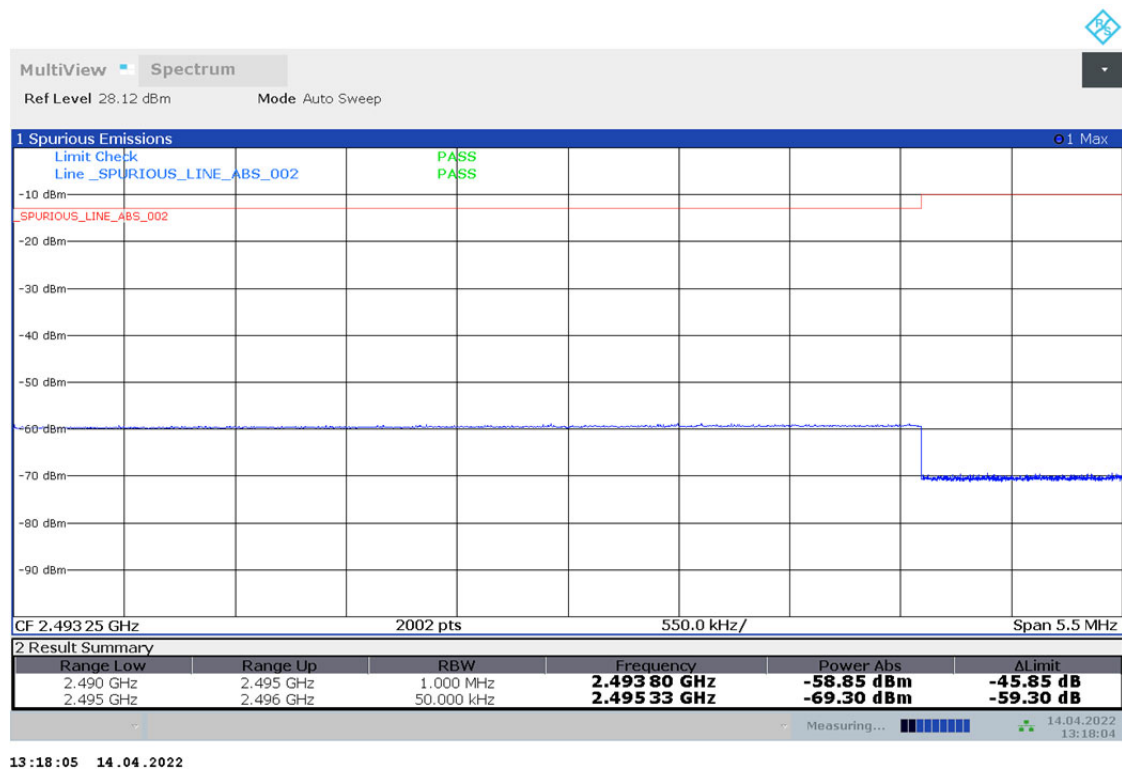
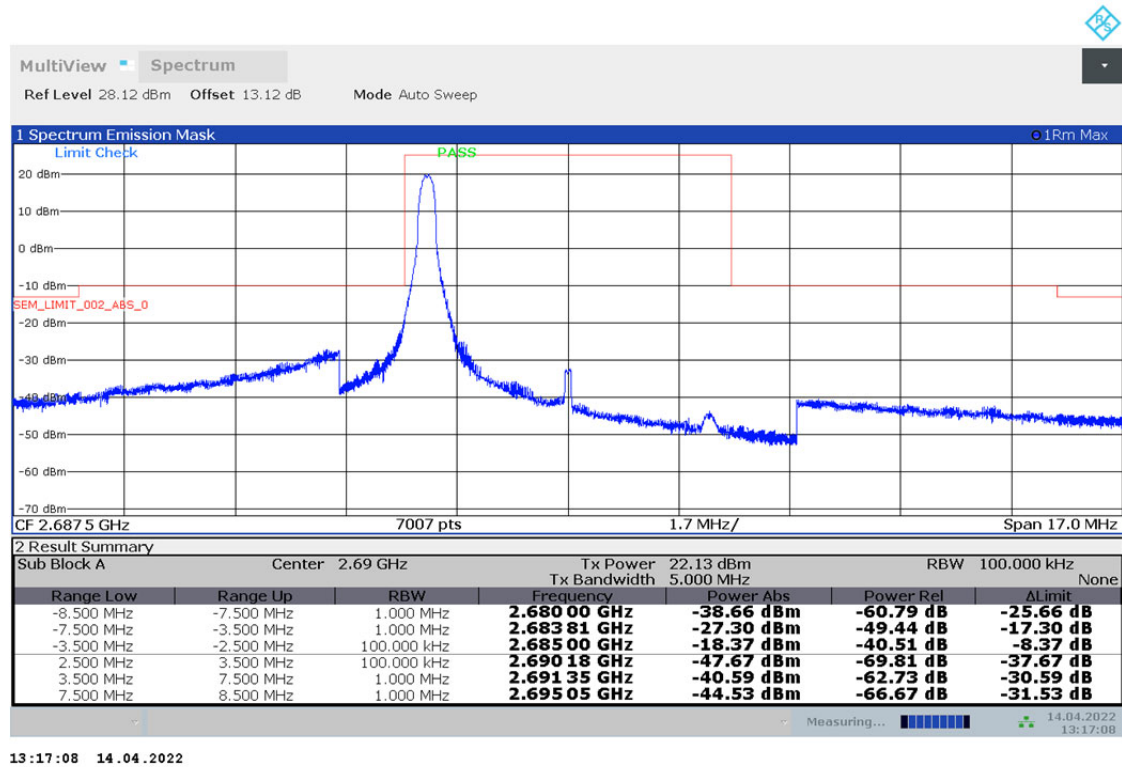


13:13:19 14.04.2022

## 5.2.4 TM1\_5MHz\_HCH\_RB25#0



## 5.2.5 TM1\_5MHz\_HCH\_RB1#0



## 5.2.6 TM1\_5MHz\_HCH\_RB1#24

