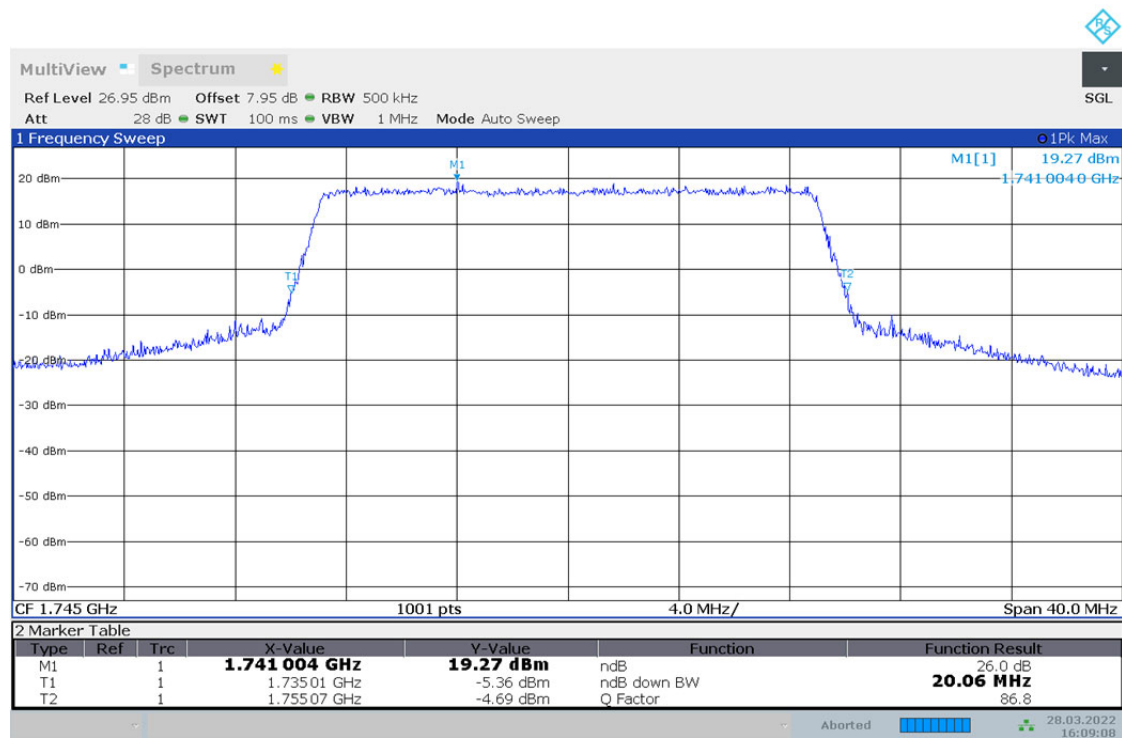
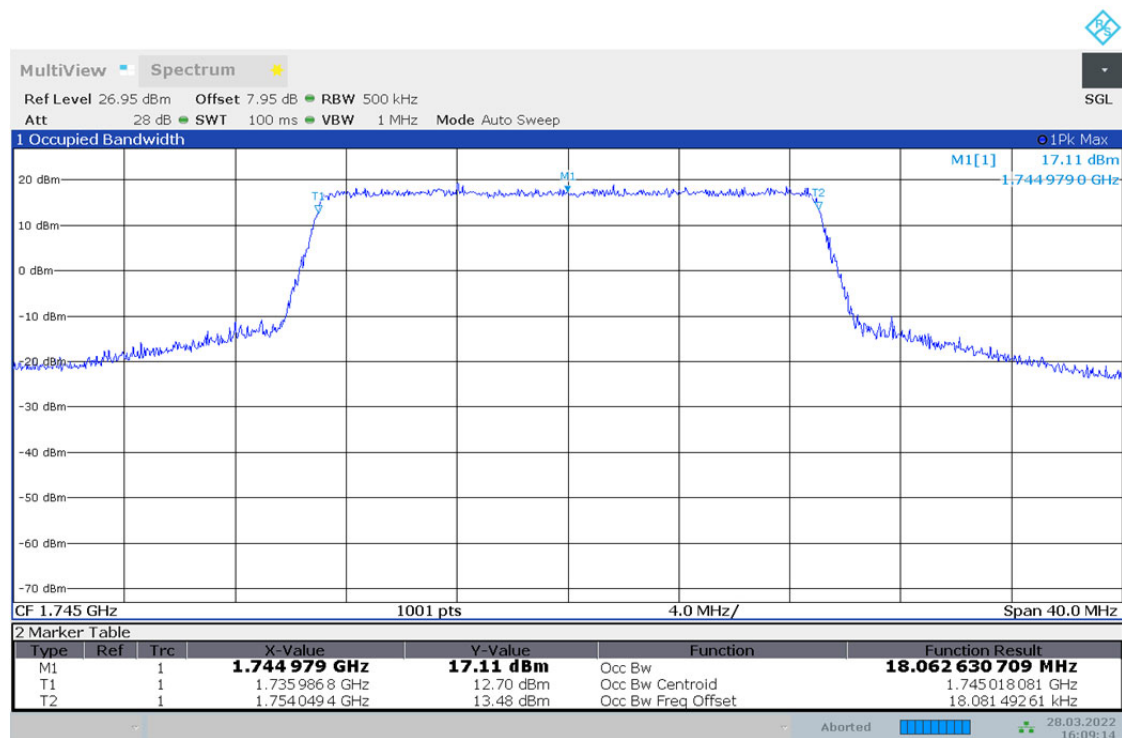


4.2.17 TM1_20MHZ_MCH_RB100#0

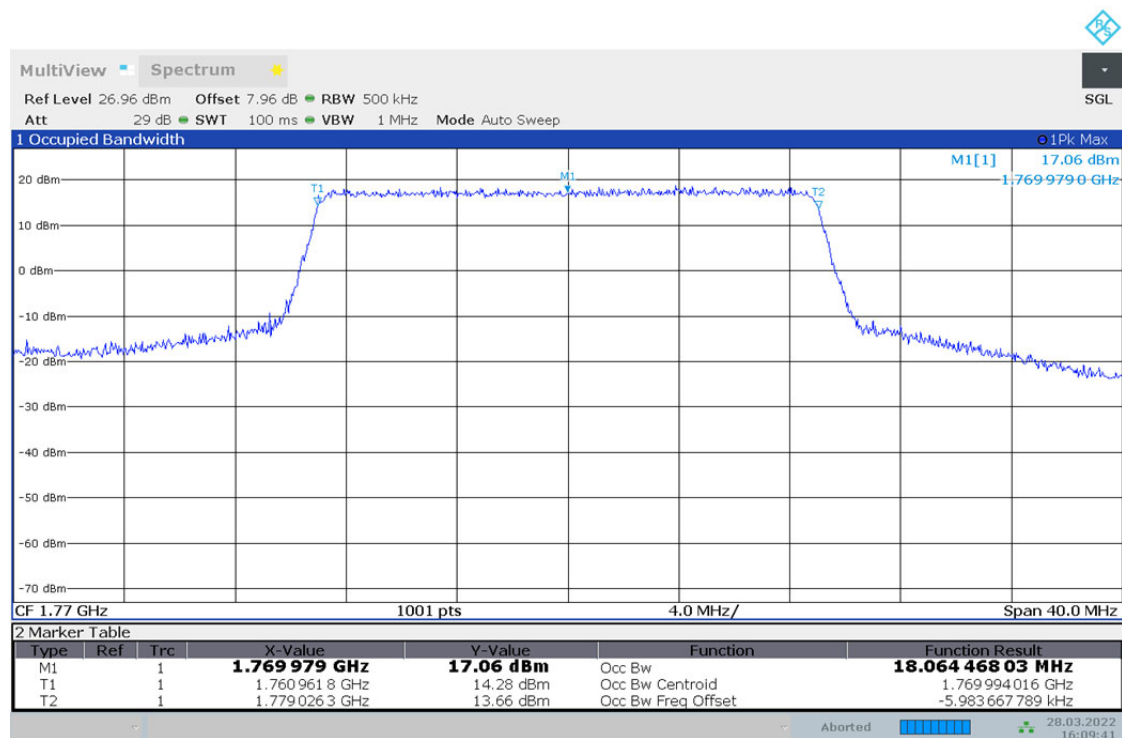
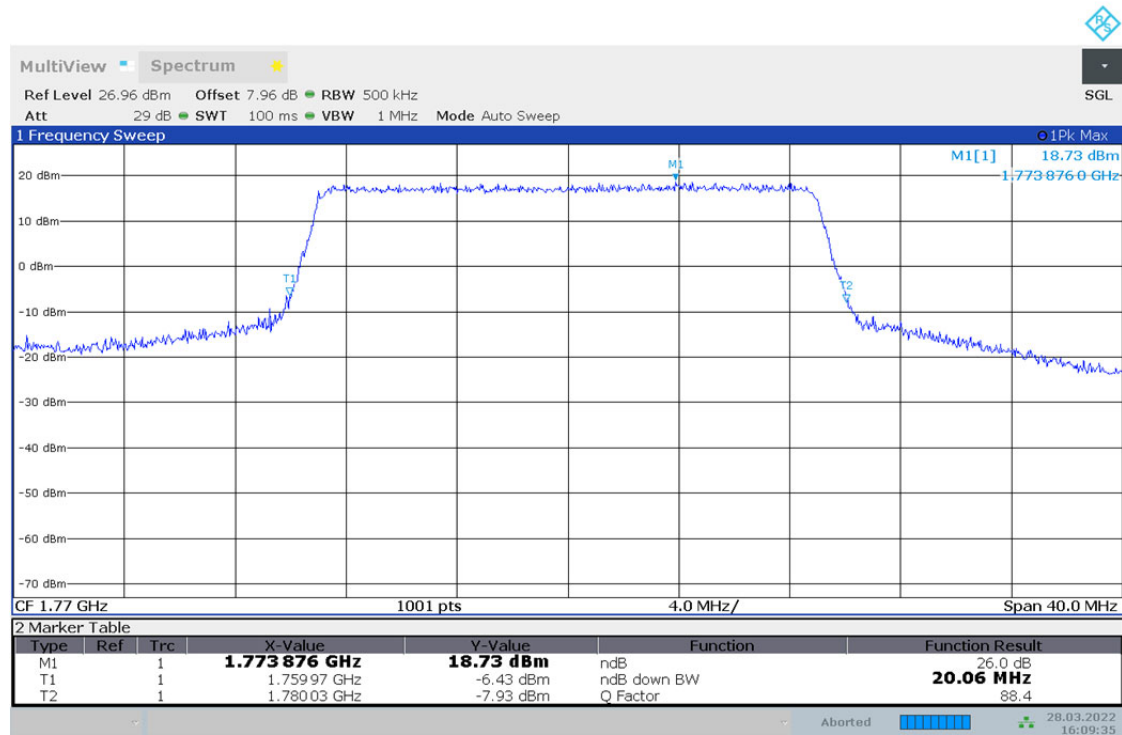


16:09:09 28.03.2022

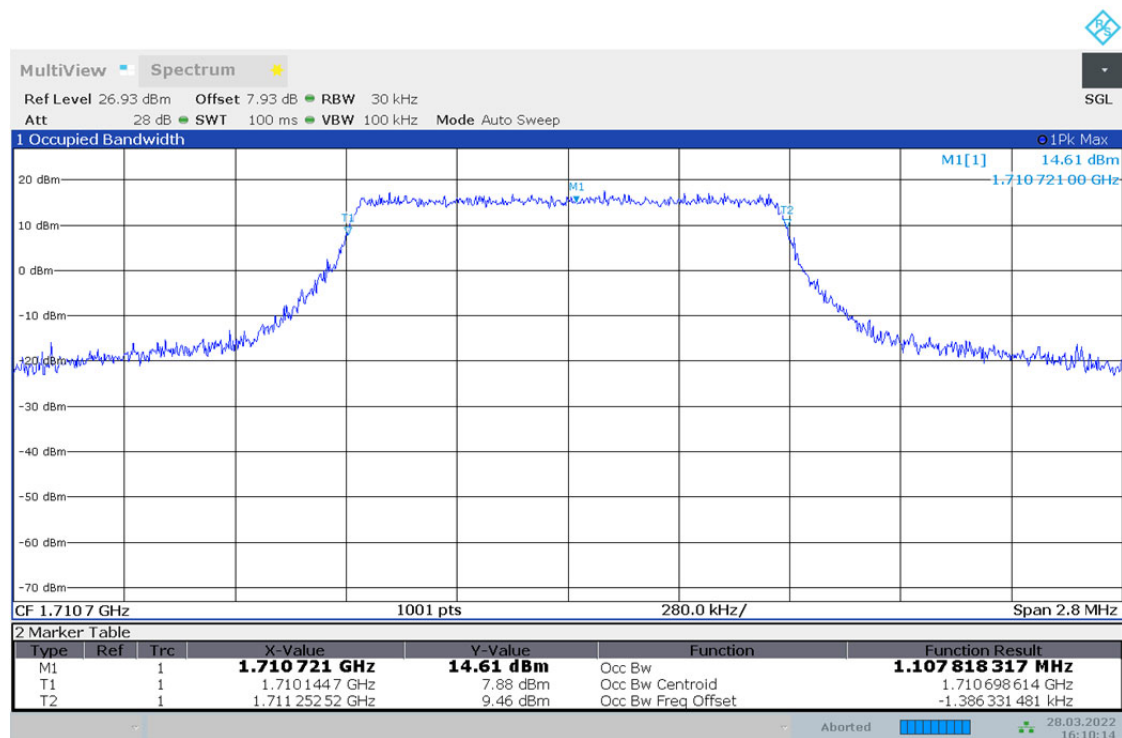
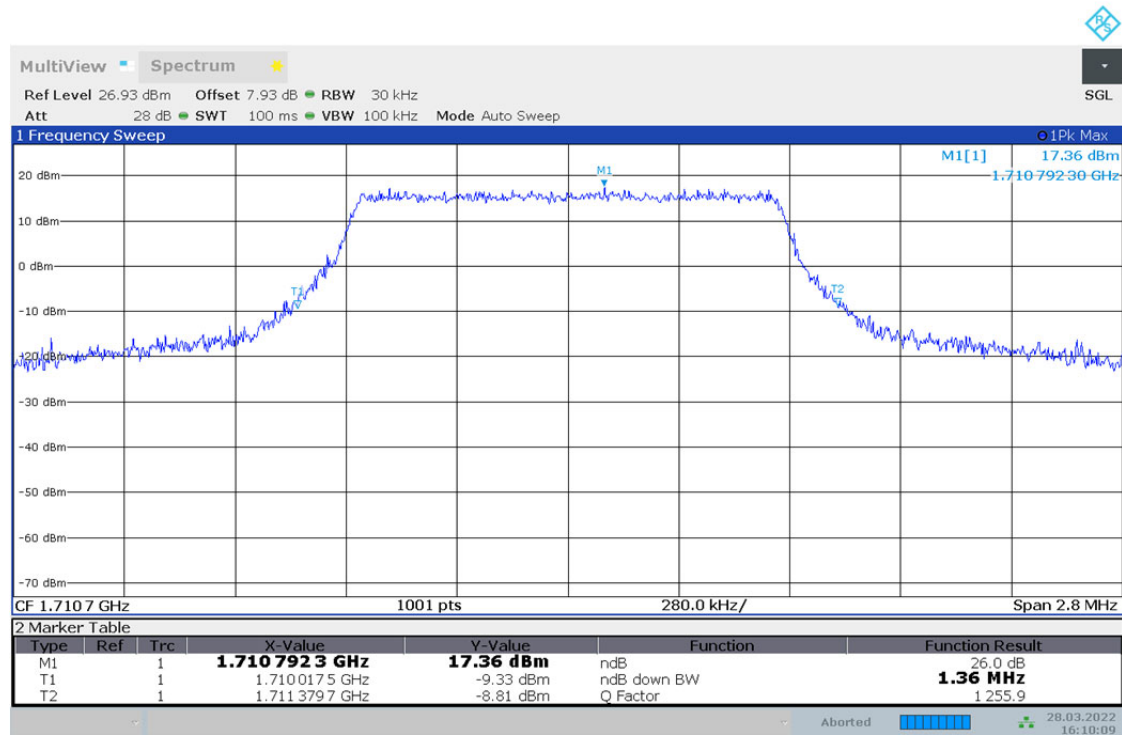


16:09:14 28.03.2022

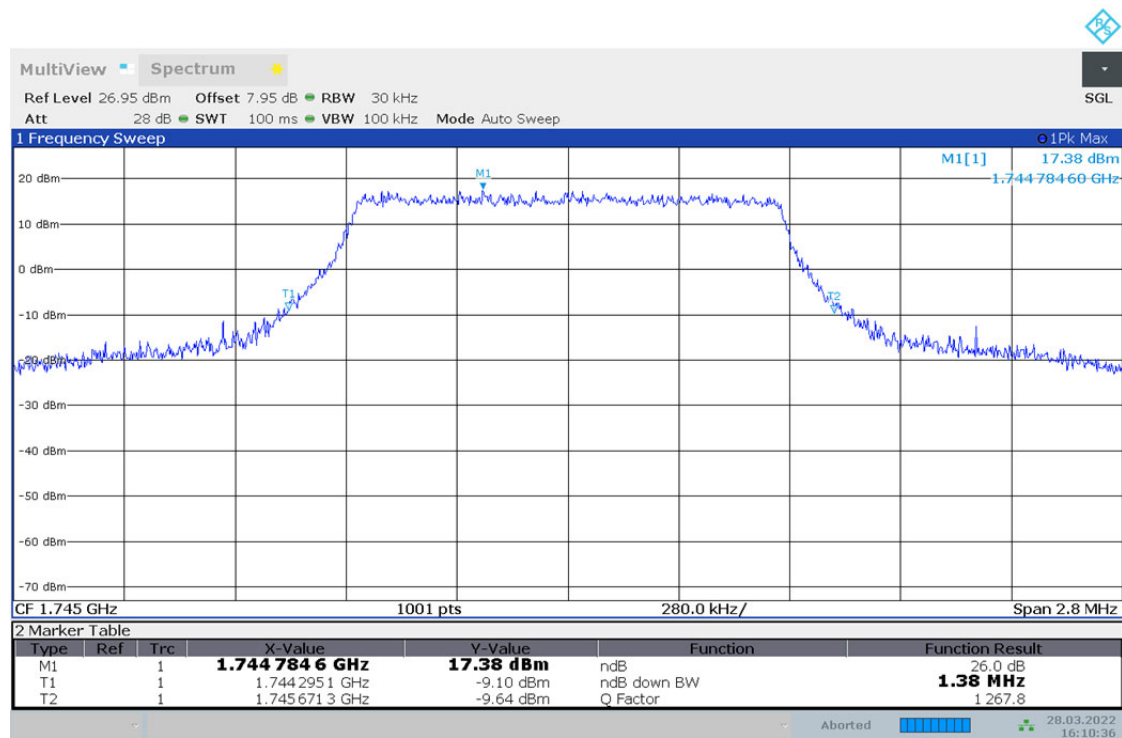
4.2.18 TM1_20MHZ_HCH_RB100#0



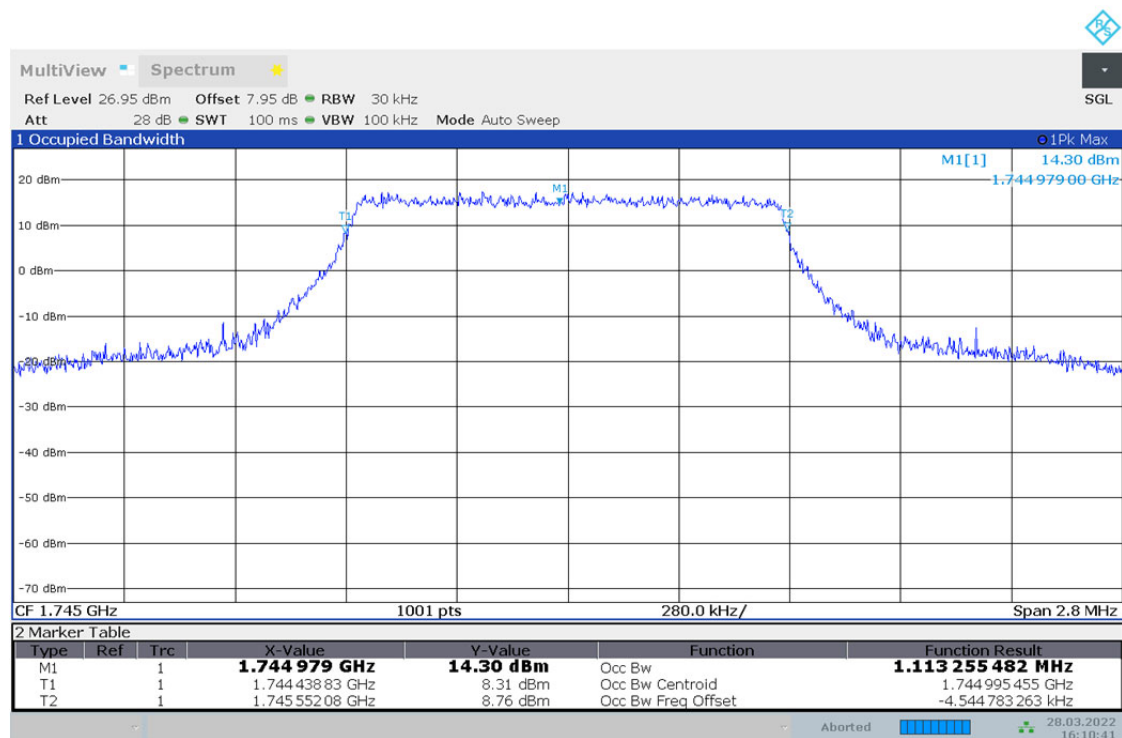
4.2.19 TM2_1.4MHZ_LCH_RB6#0



4.2.20 TM2_1.4MHZ_MCH_RB6#0

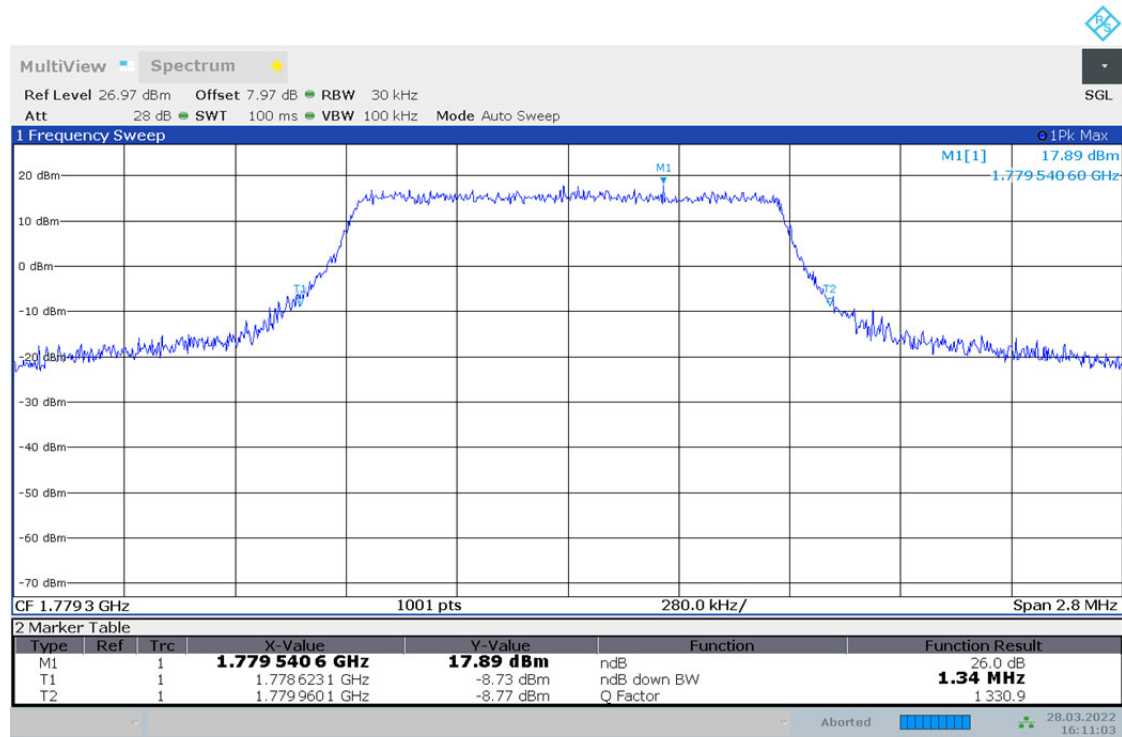


16:10:36 28.03.2022

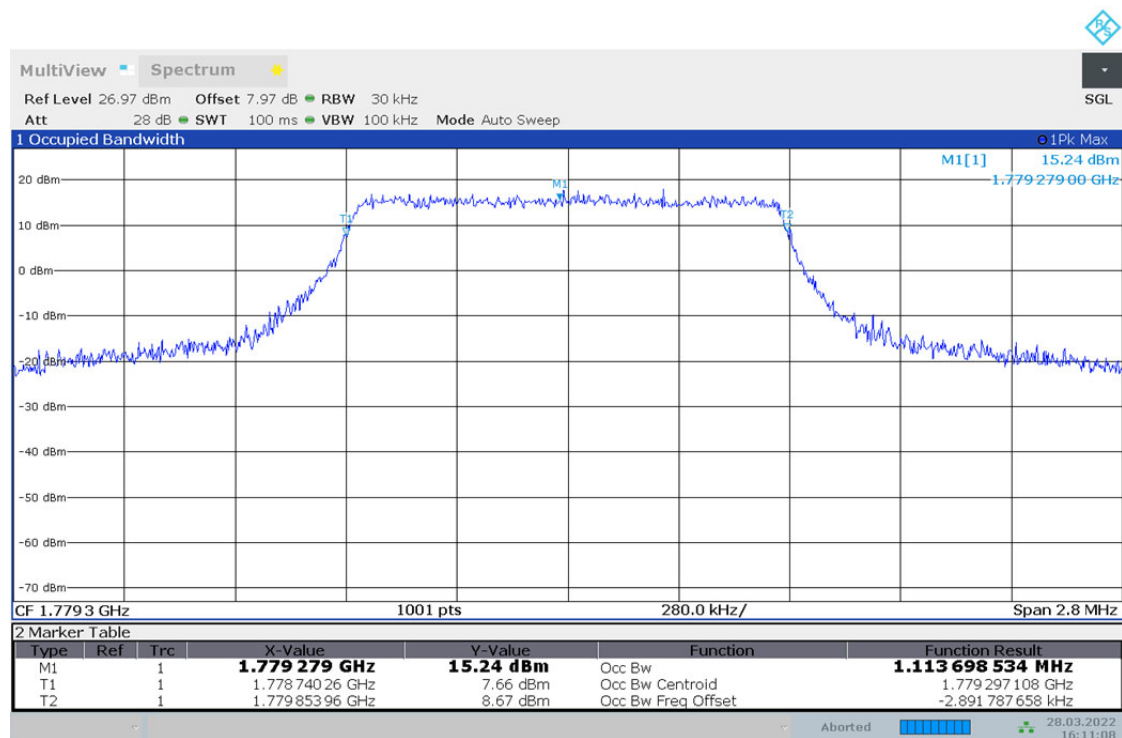


16:10:42 28.03.2022

4.2.21 TM2_1.4MHZ_HCH_RB6#0

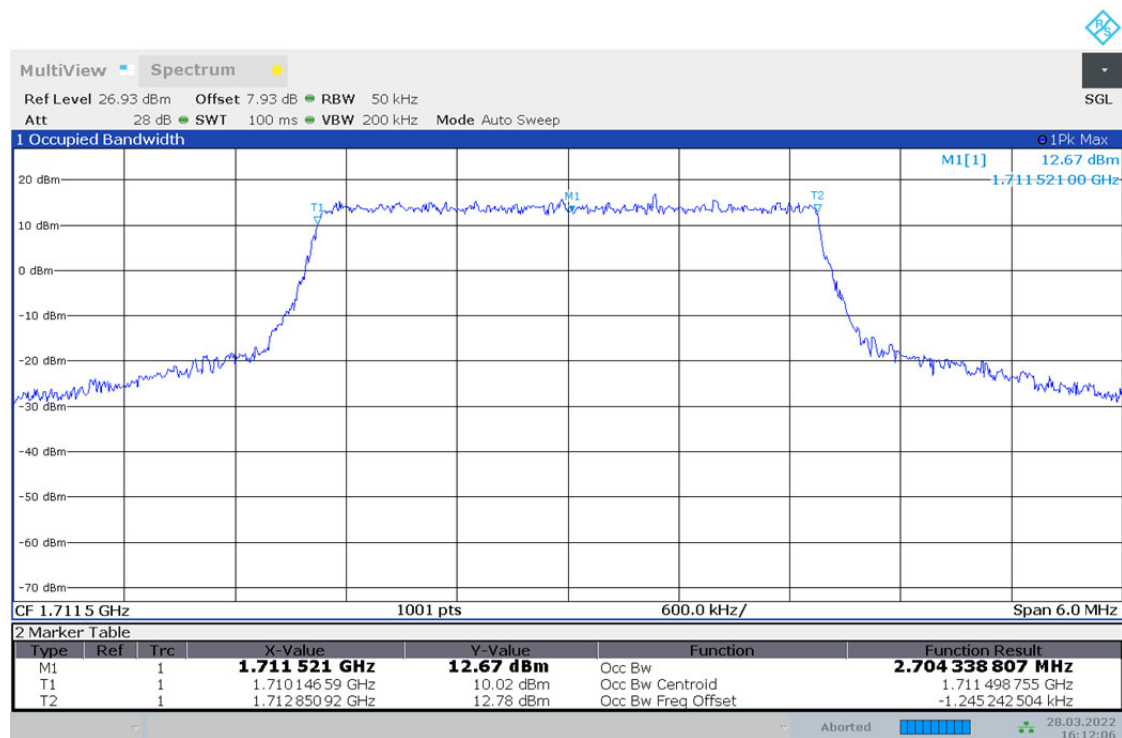
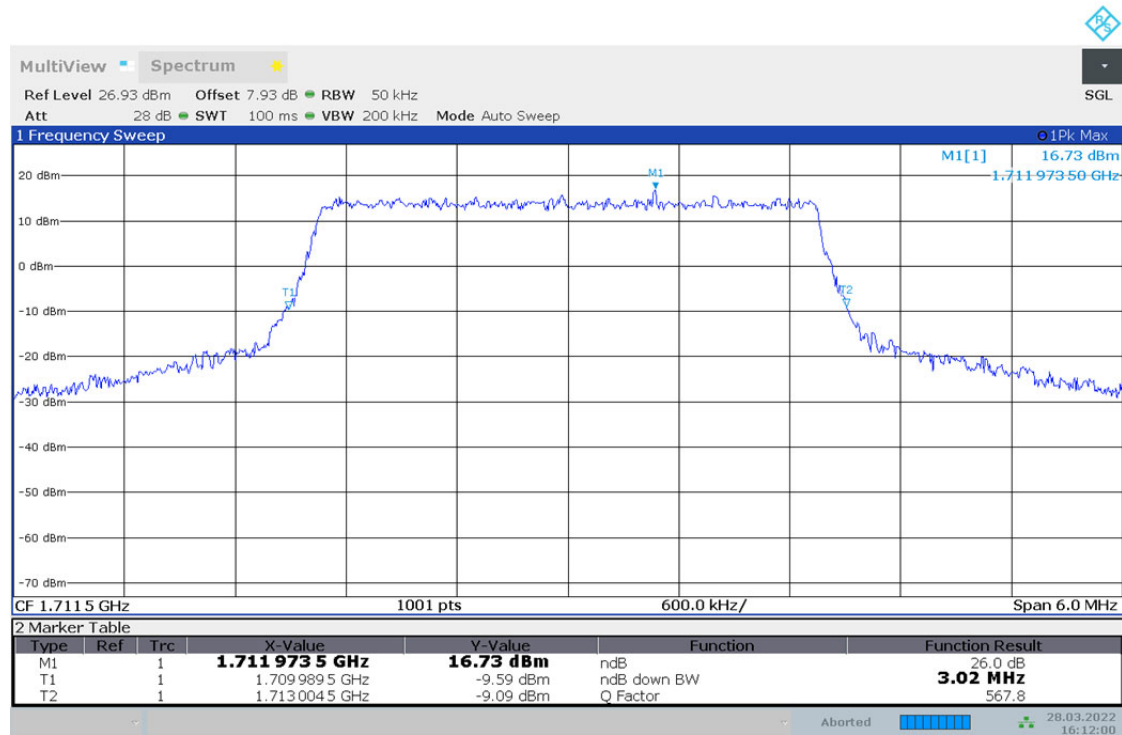


16:11:04 28.03.2022

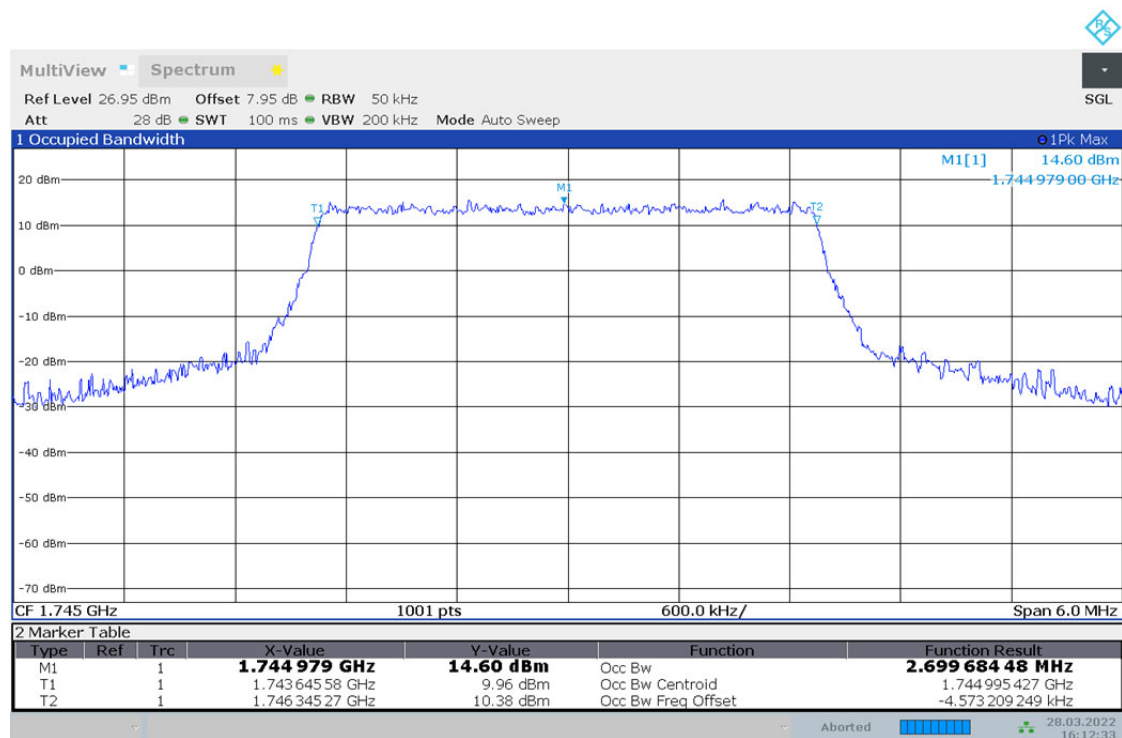
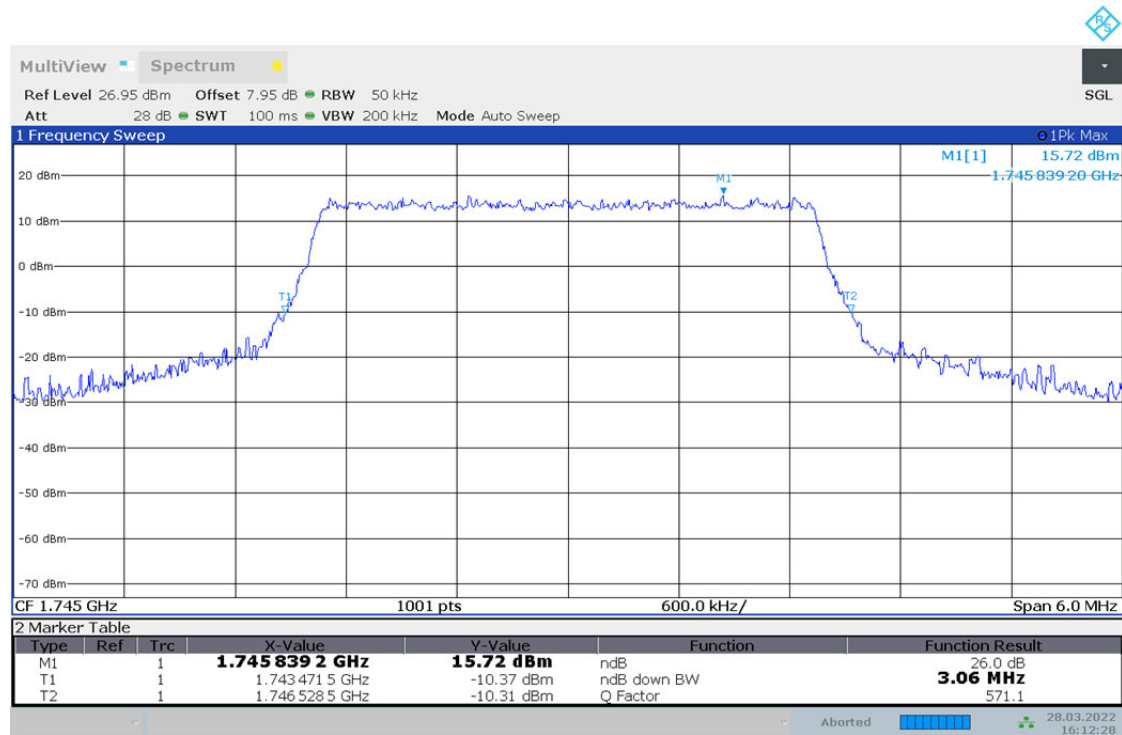


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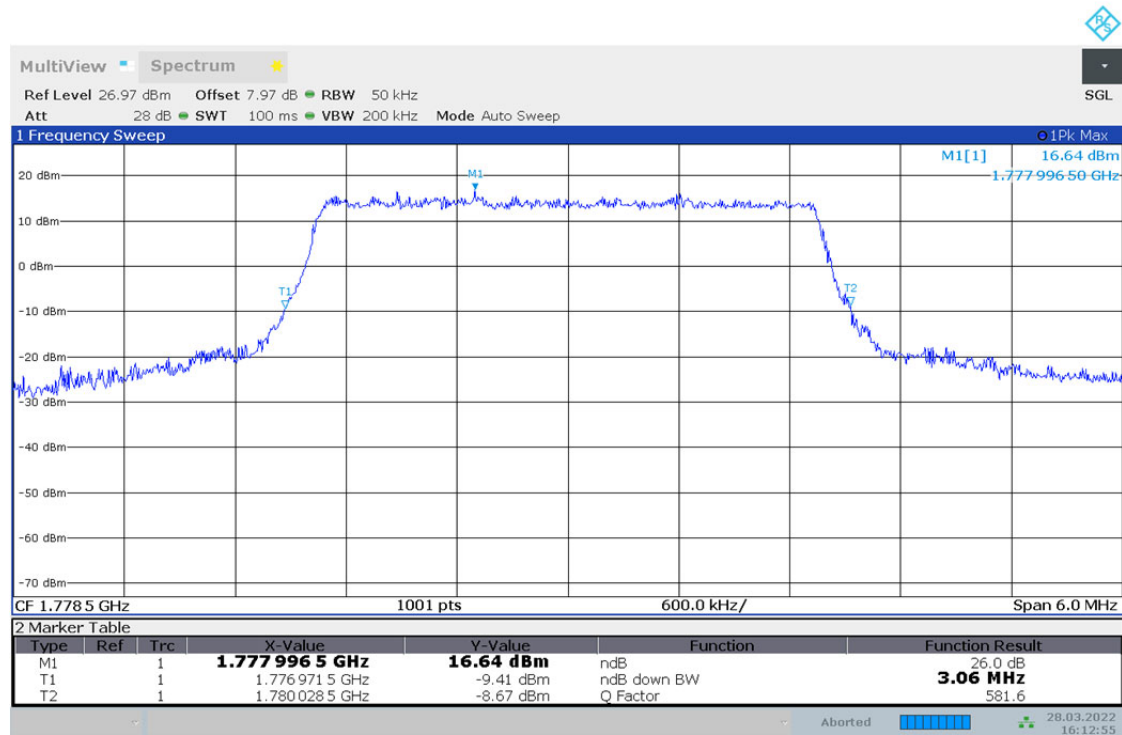
4.2.22 TM2_3MHZ_LCH_RB15#0



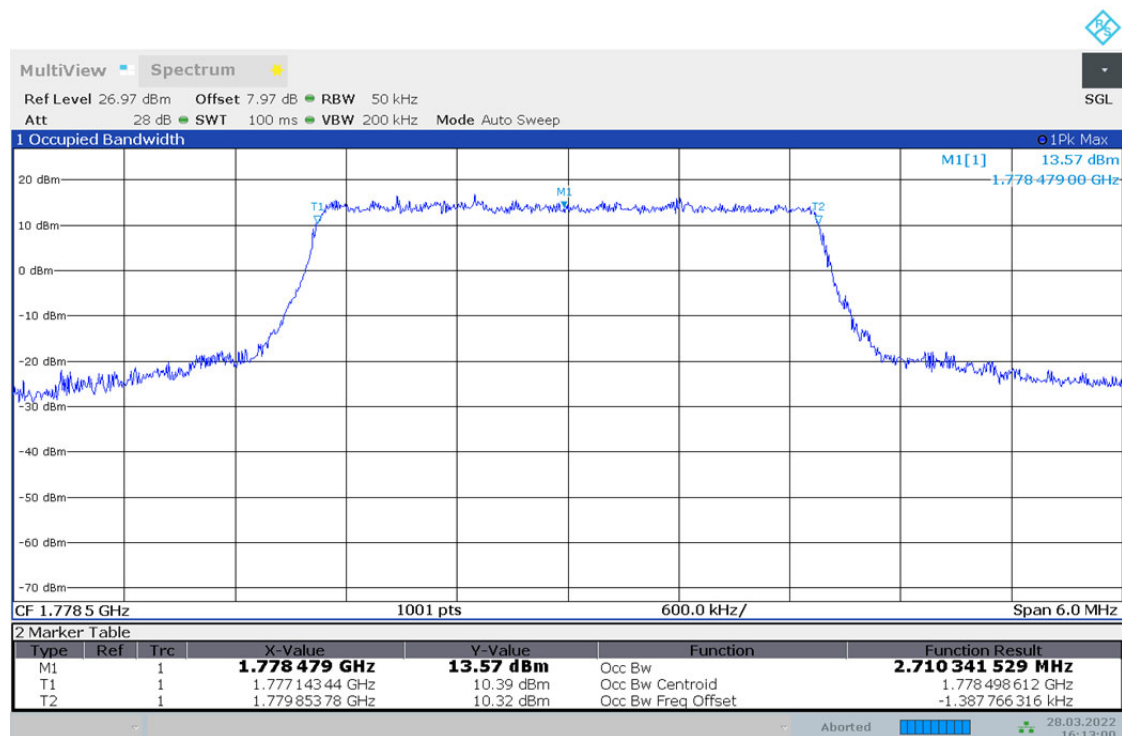
4.2.23 TM2_3MHZ_MCH_RB15#0



4.2.24 TM2_3MHZ_HCH_RB15#0

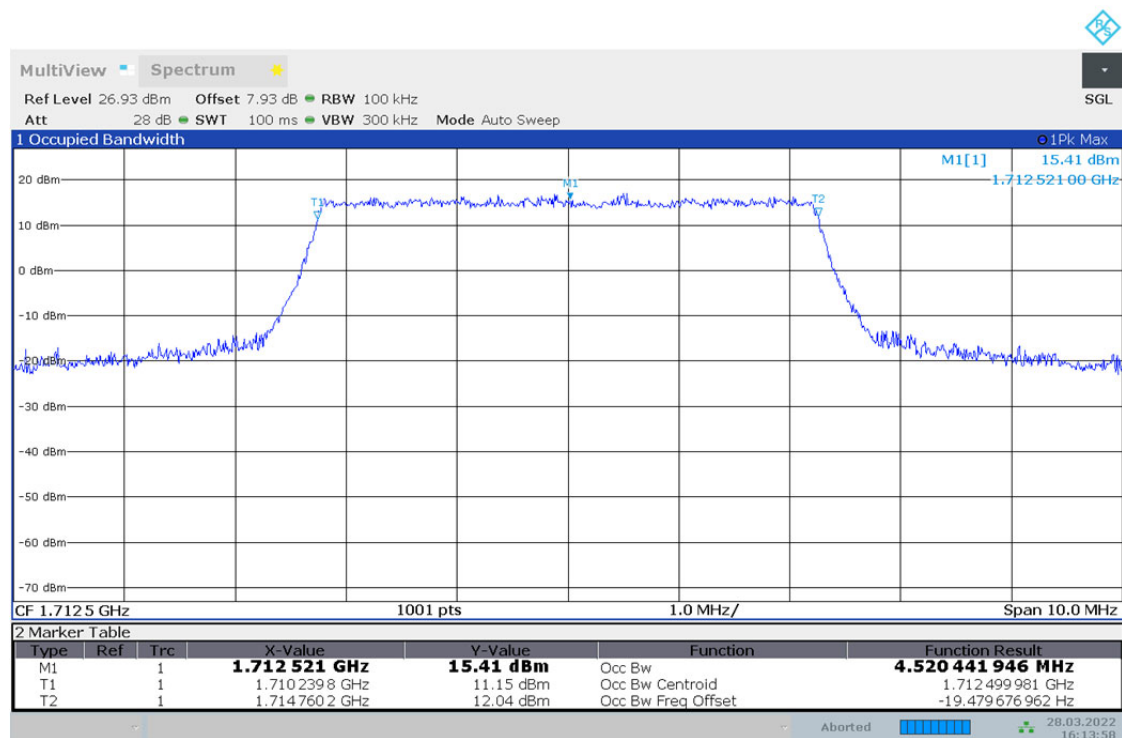
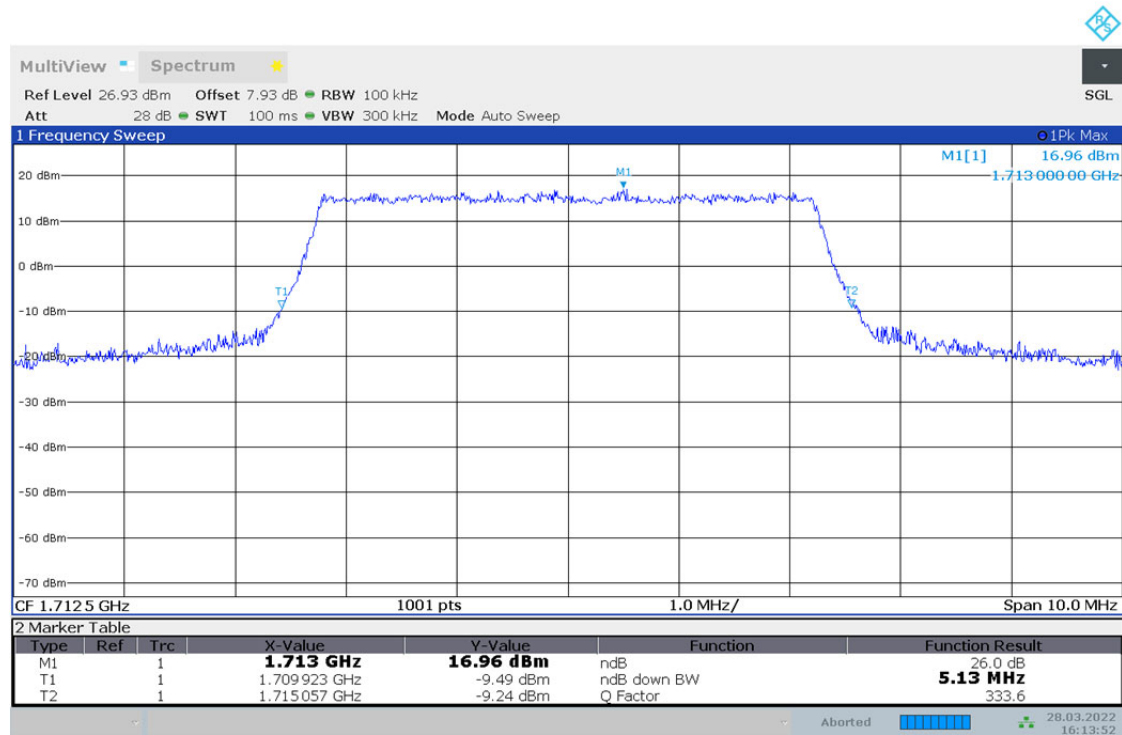


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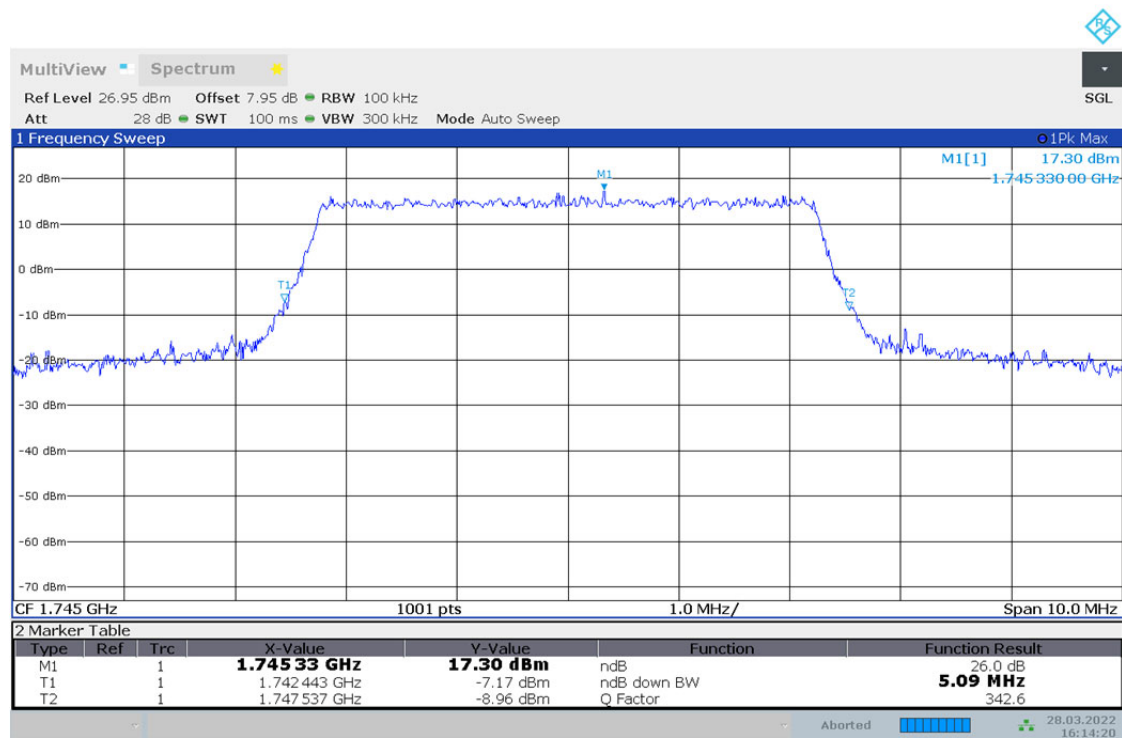


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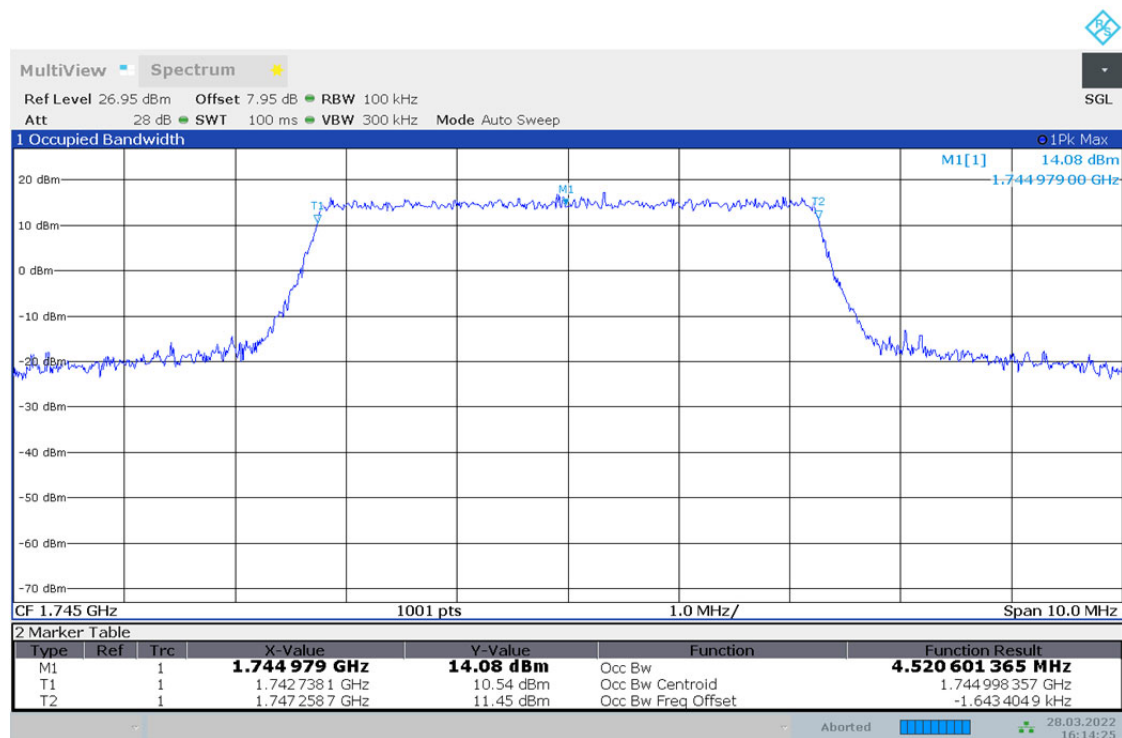
4.2.25 TM2_5MHZ_LCH_RB25#0



4.2.26 TM2_5MHZ_MCH_RB25#0

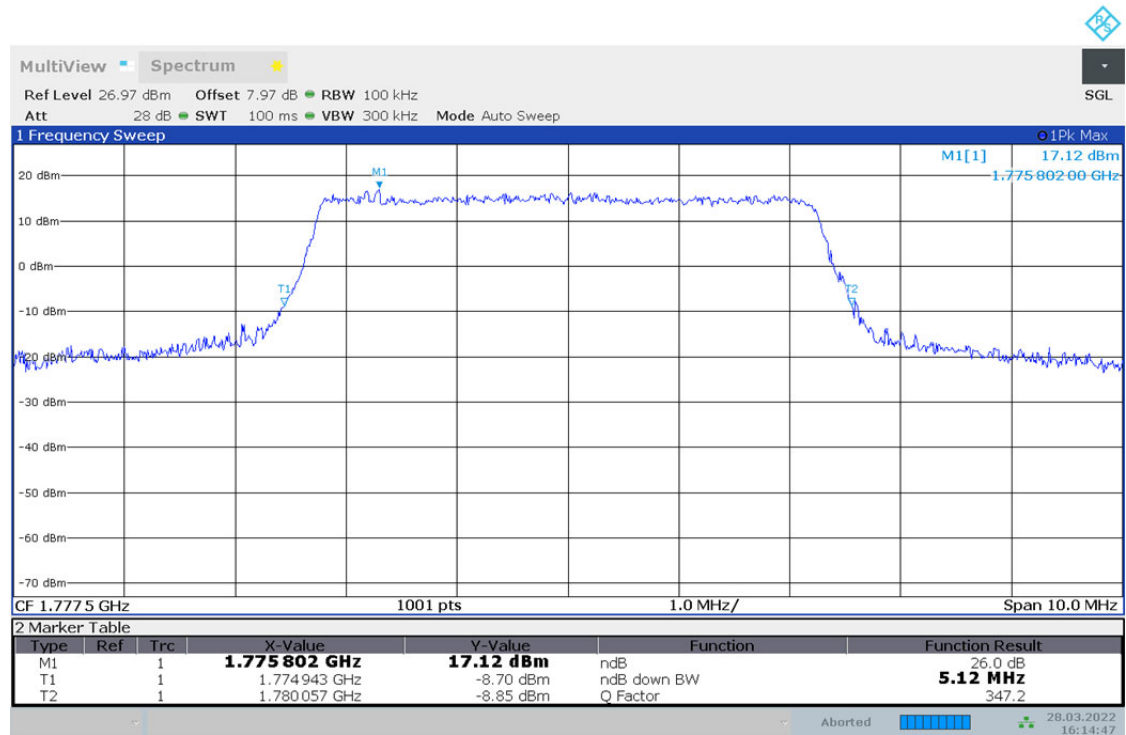


16:14:20 28.03.2022

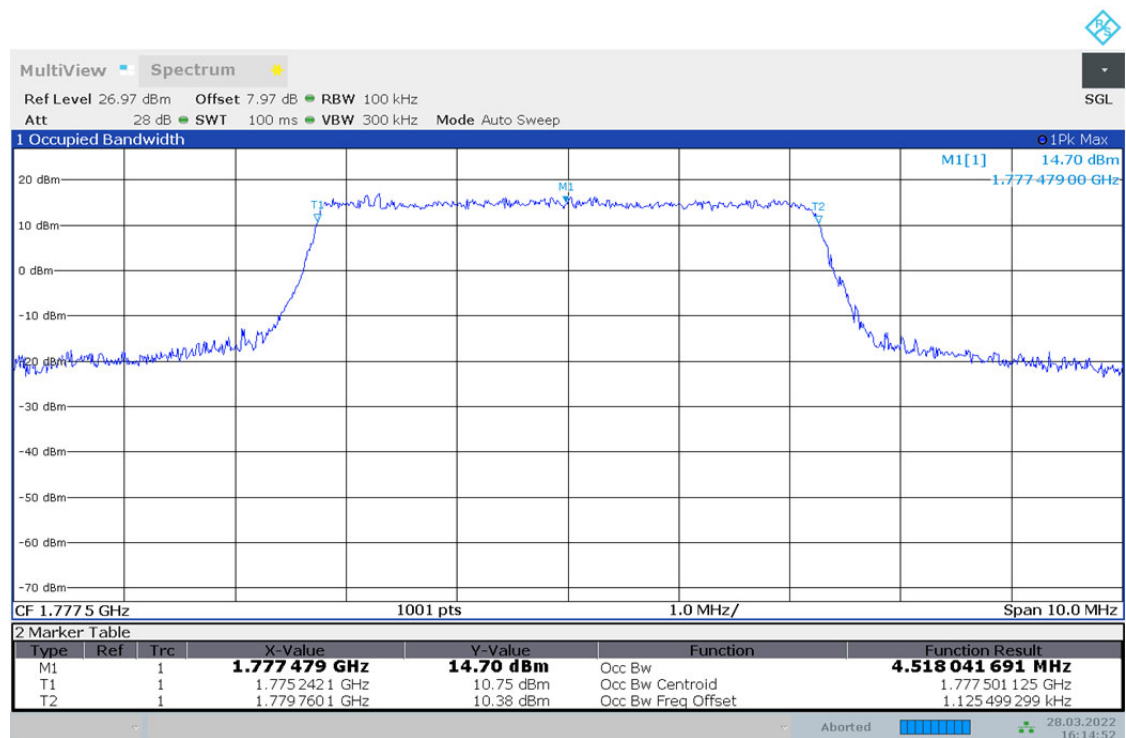


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4.2.27 TM2_5MHZ_HCH_RB25#0

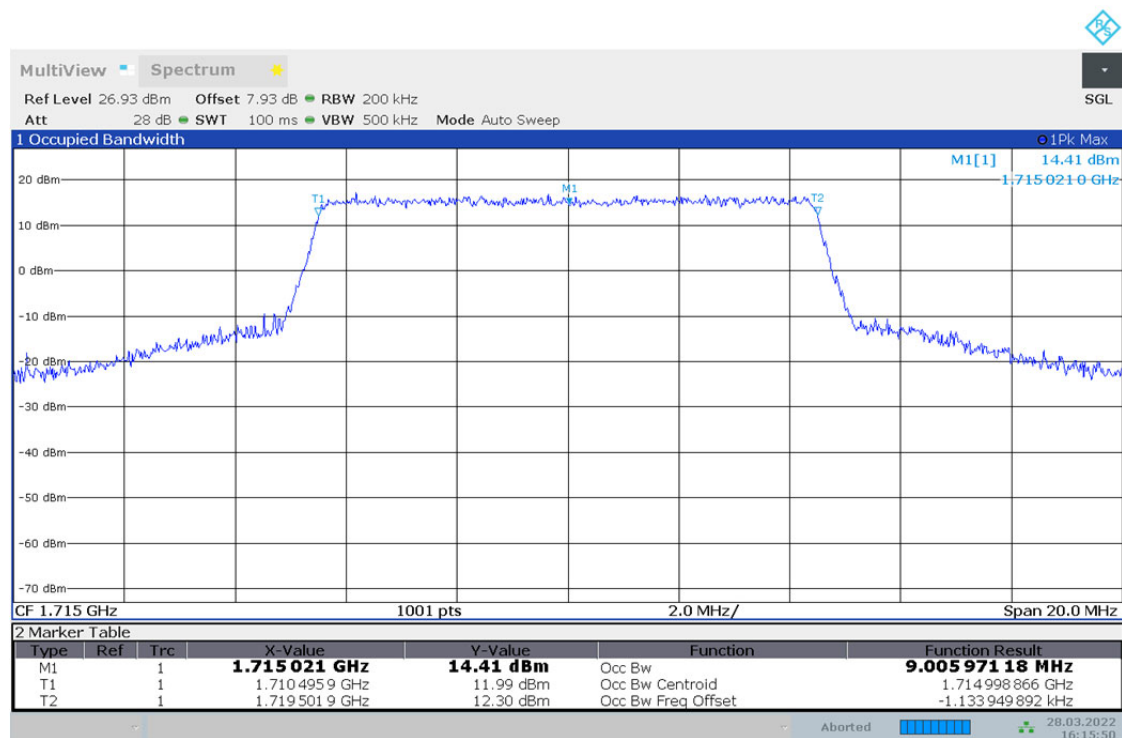
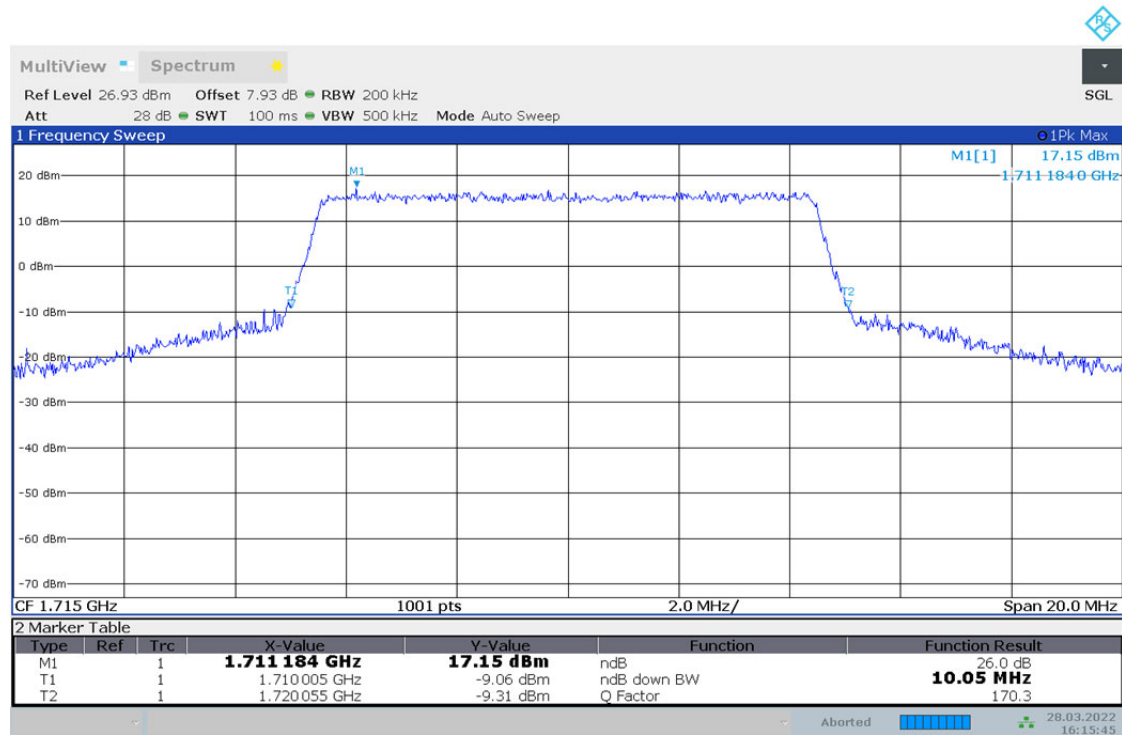


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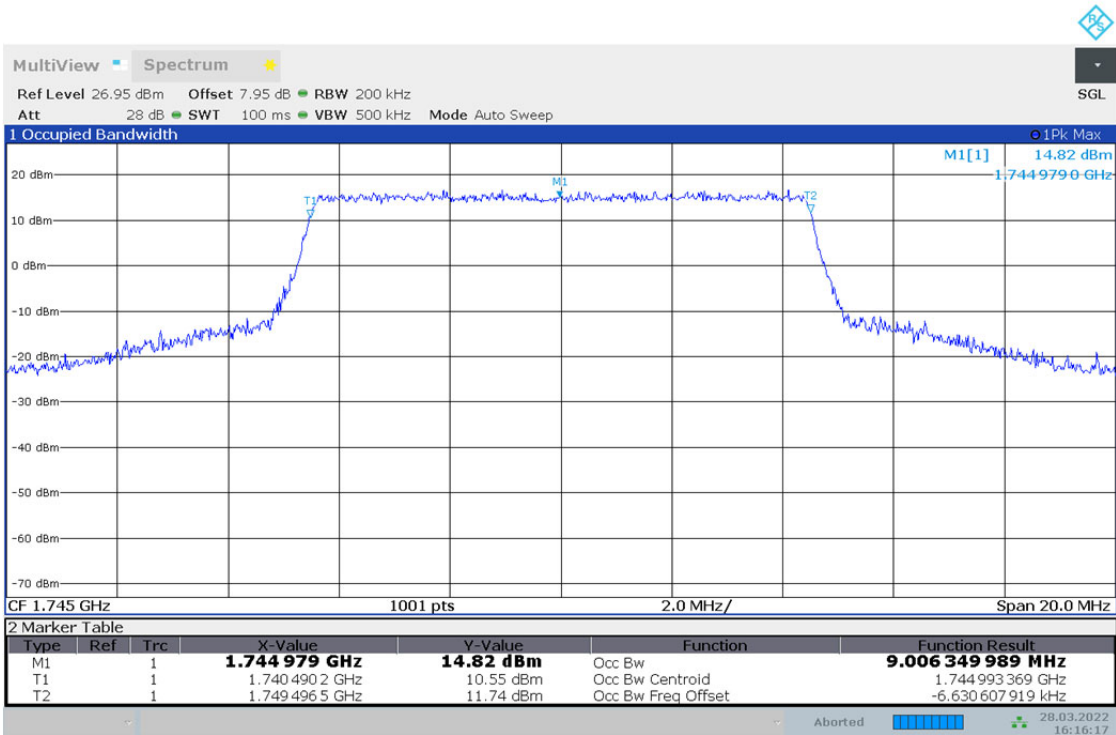
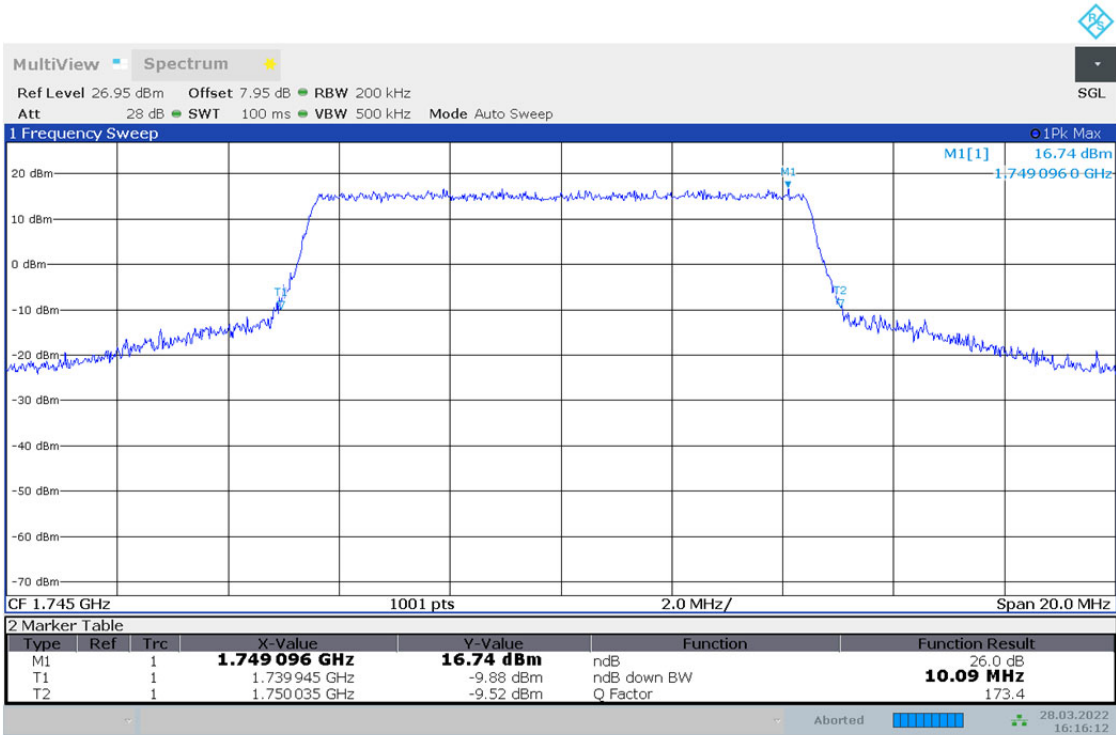


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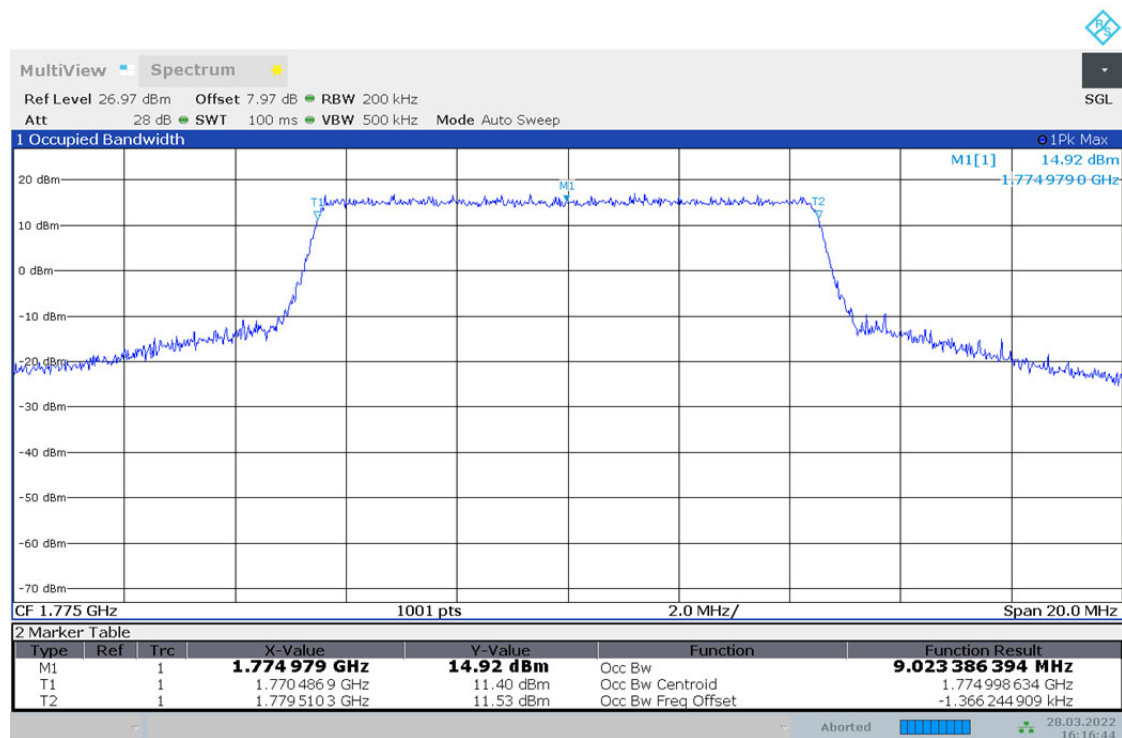
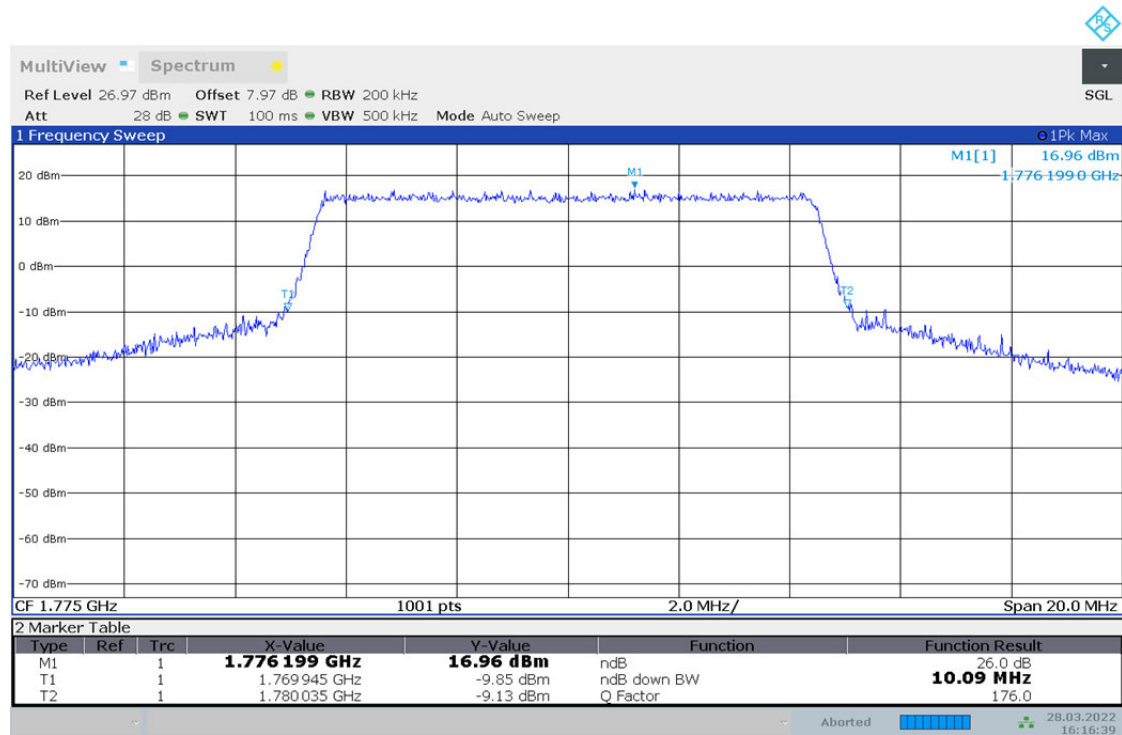
4.2.28 TM2_10MHZ_LCH_RB50#0



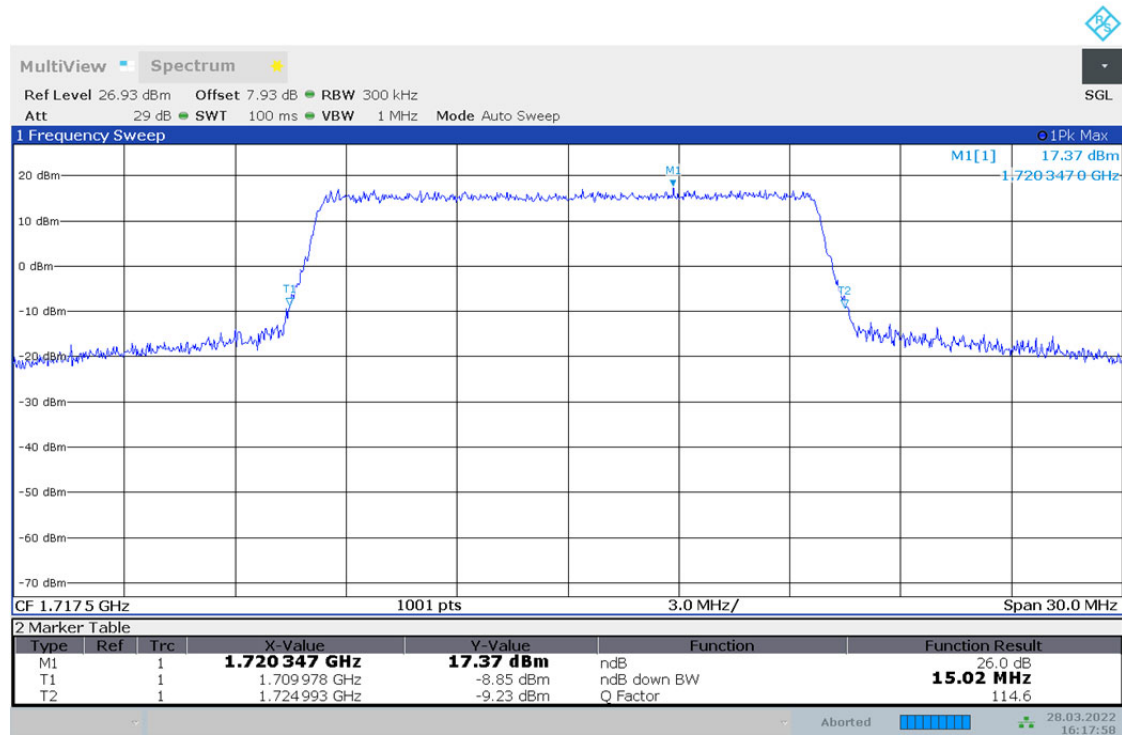
4.2.29 TM2_10MHZ_MCH_RB50#0



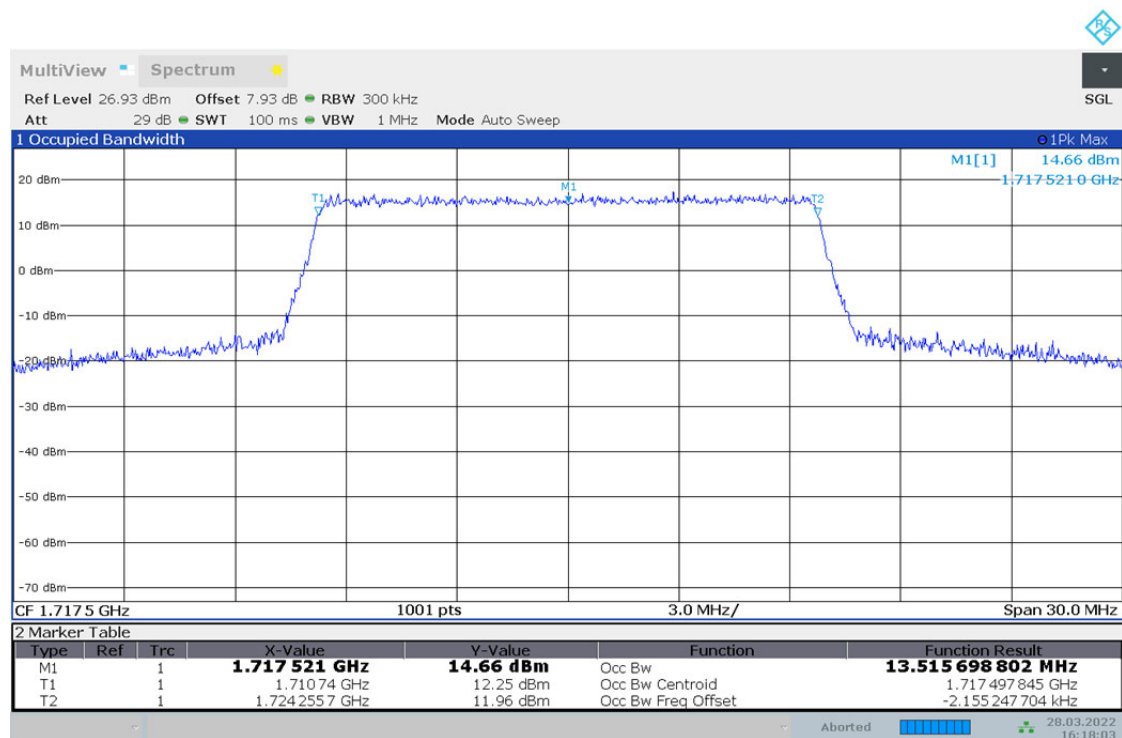
4.2.30 TM2_10MHZ_HCH_RB50#0



4.2.31 TM2_15MHZ_LCH_RB75#0

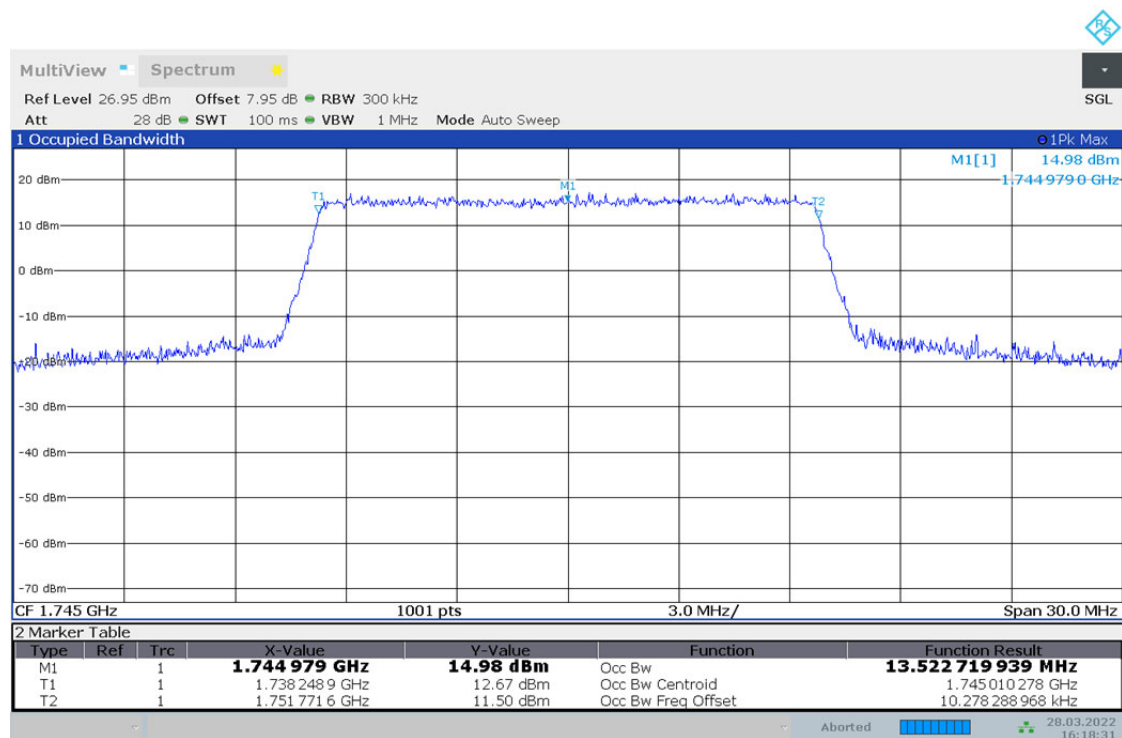
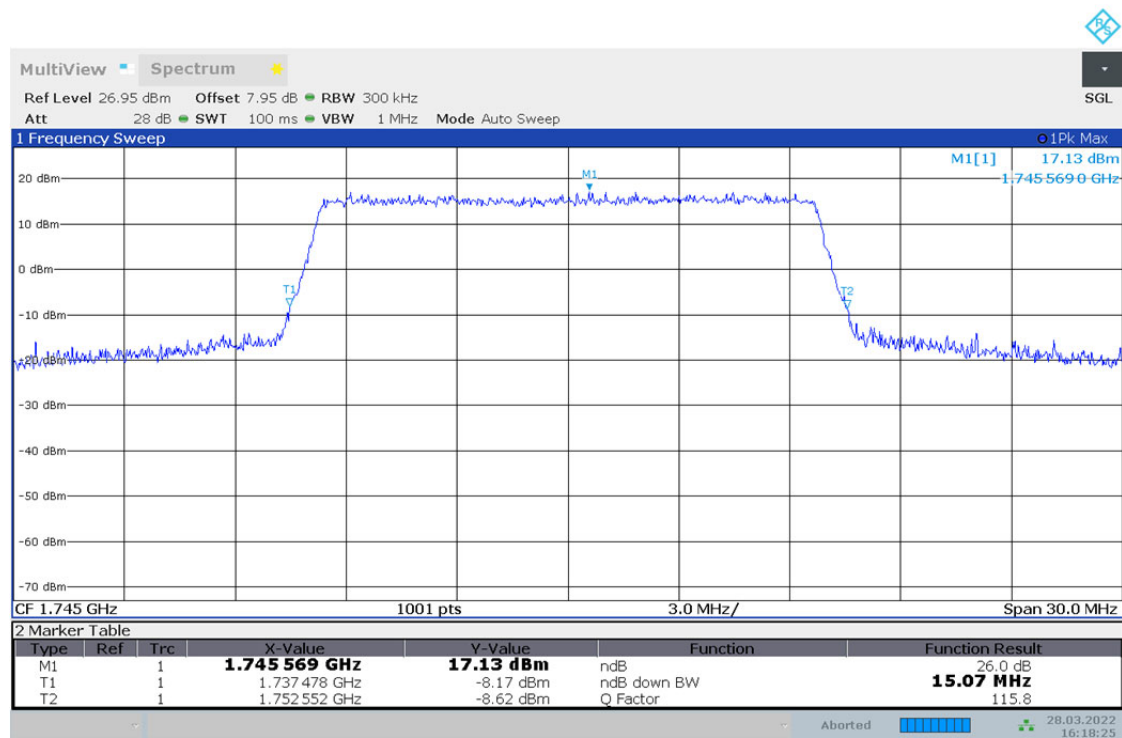


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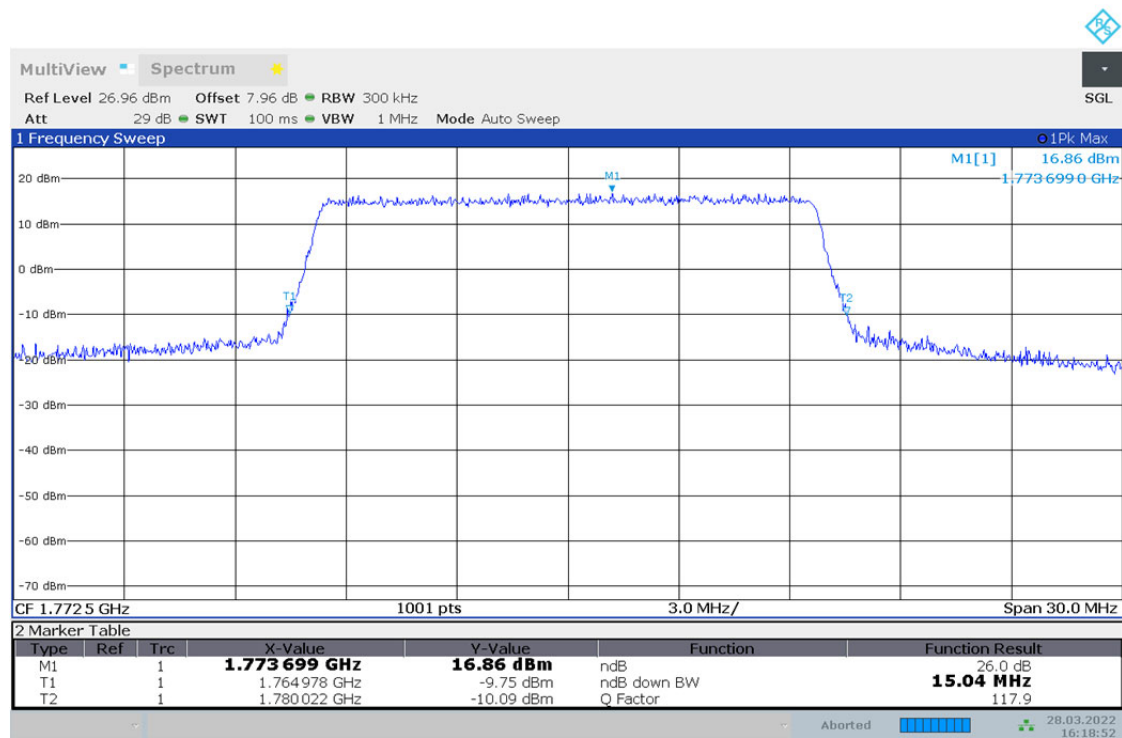


16:18:04 28.03.2022

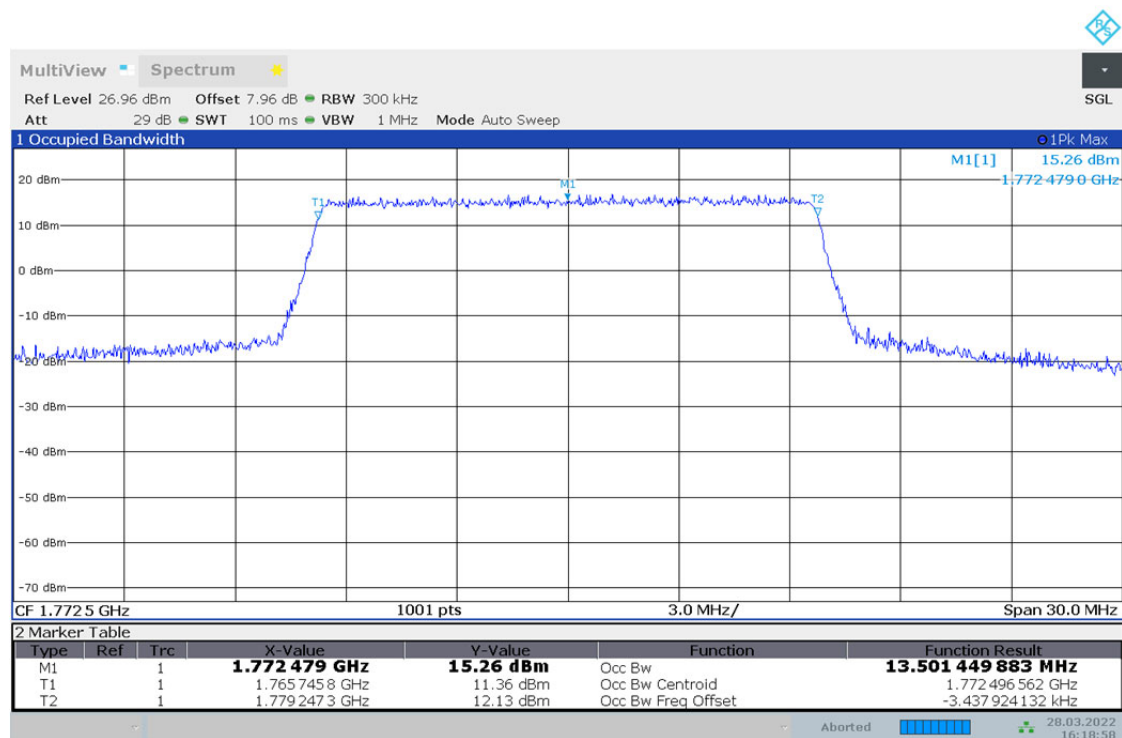
4.2.32 TM2_15MHZ_MCH_RB75#0



4.2.33 TM2_15MHZ_HCH_RB75#0

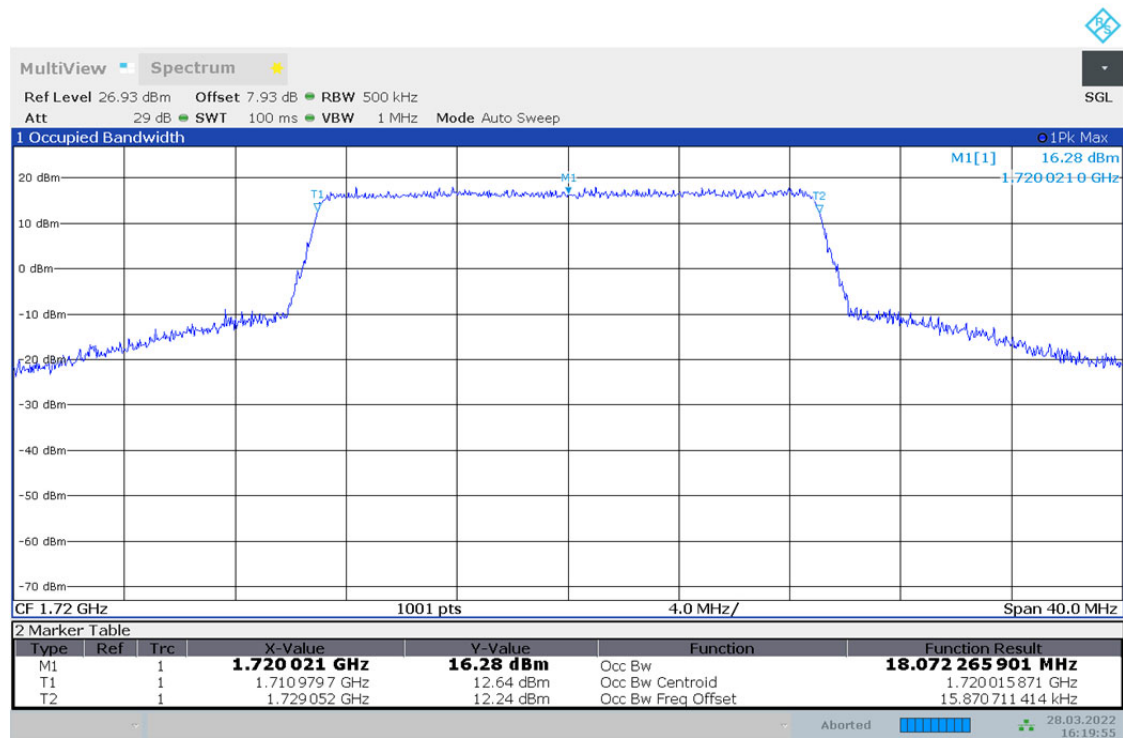
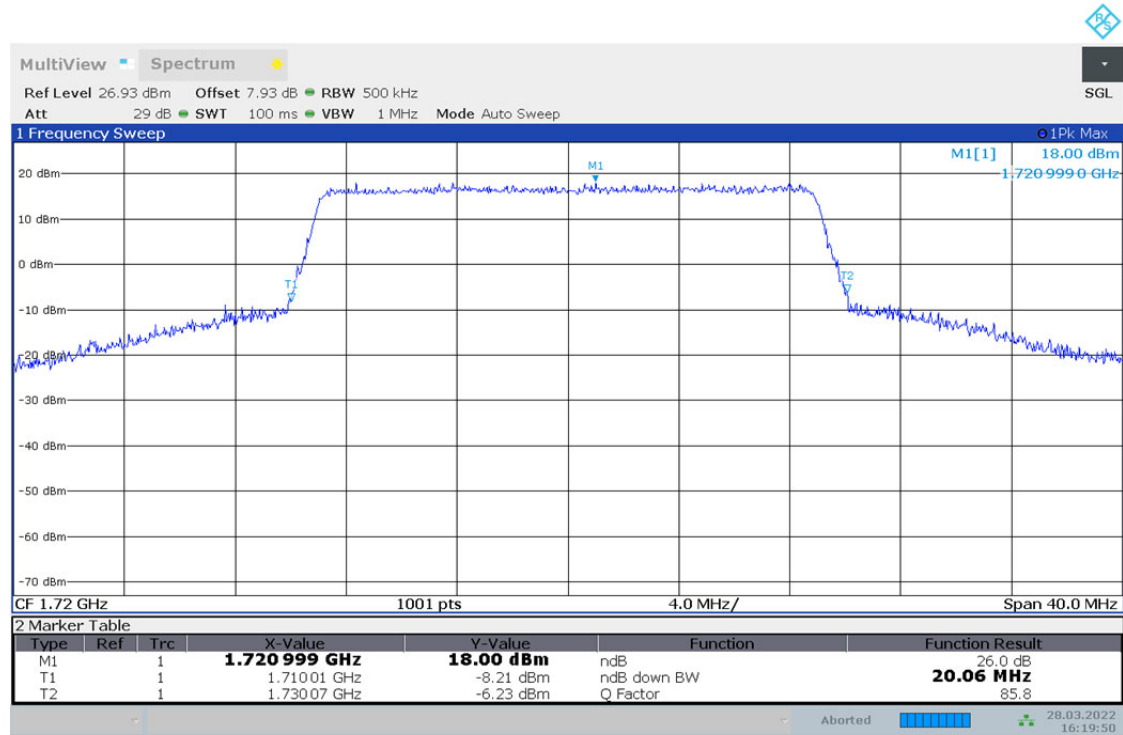


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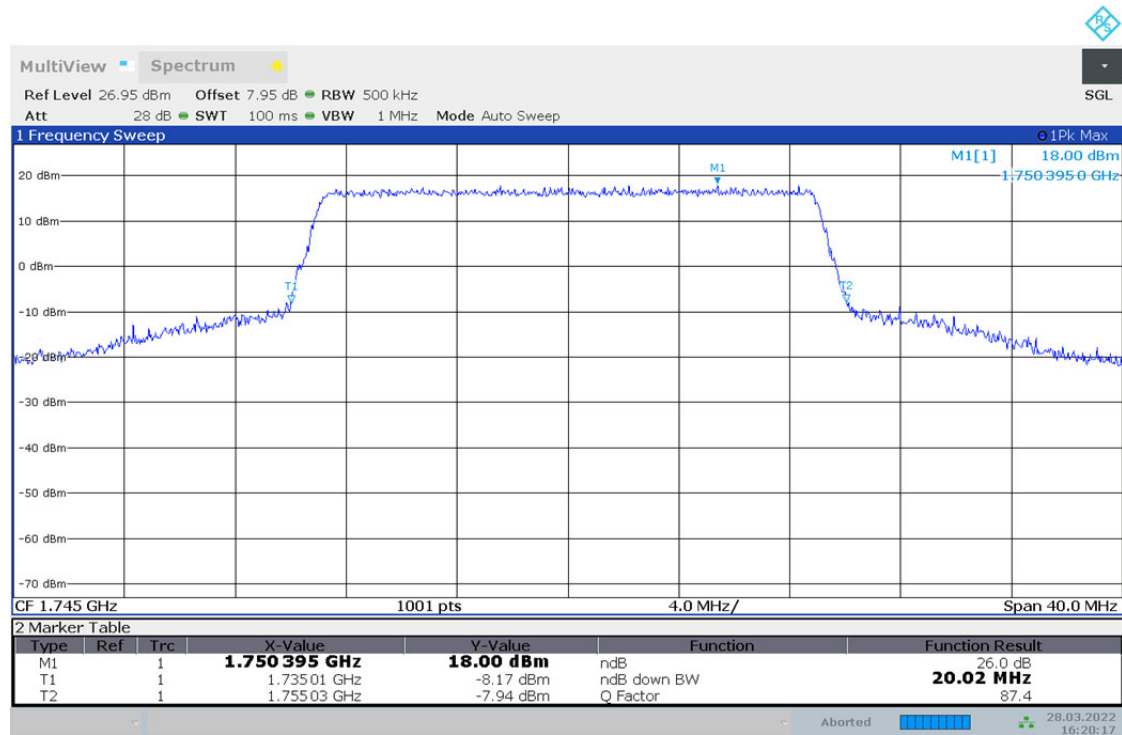


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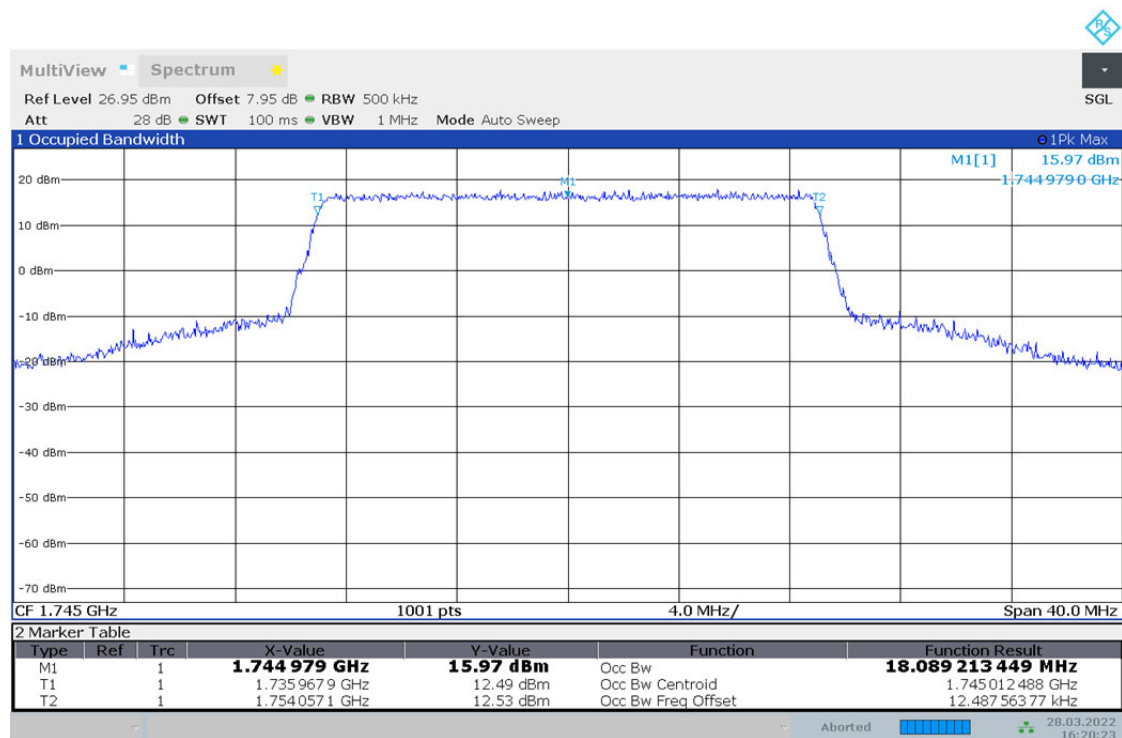
4.2.34 TM2_20MHZ_LCH_RB100#0



4.2.35 TM2_20MHZ_MCH_RB100#0

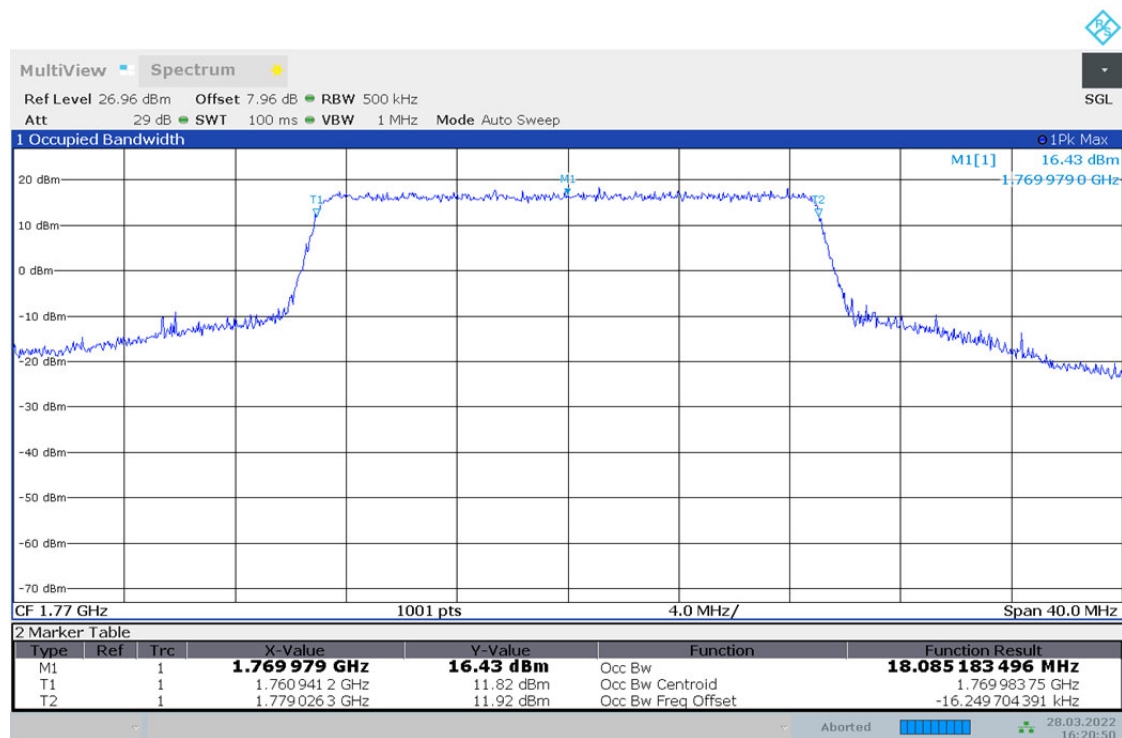
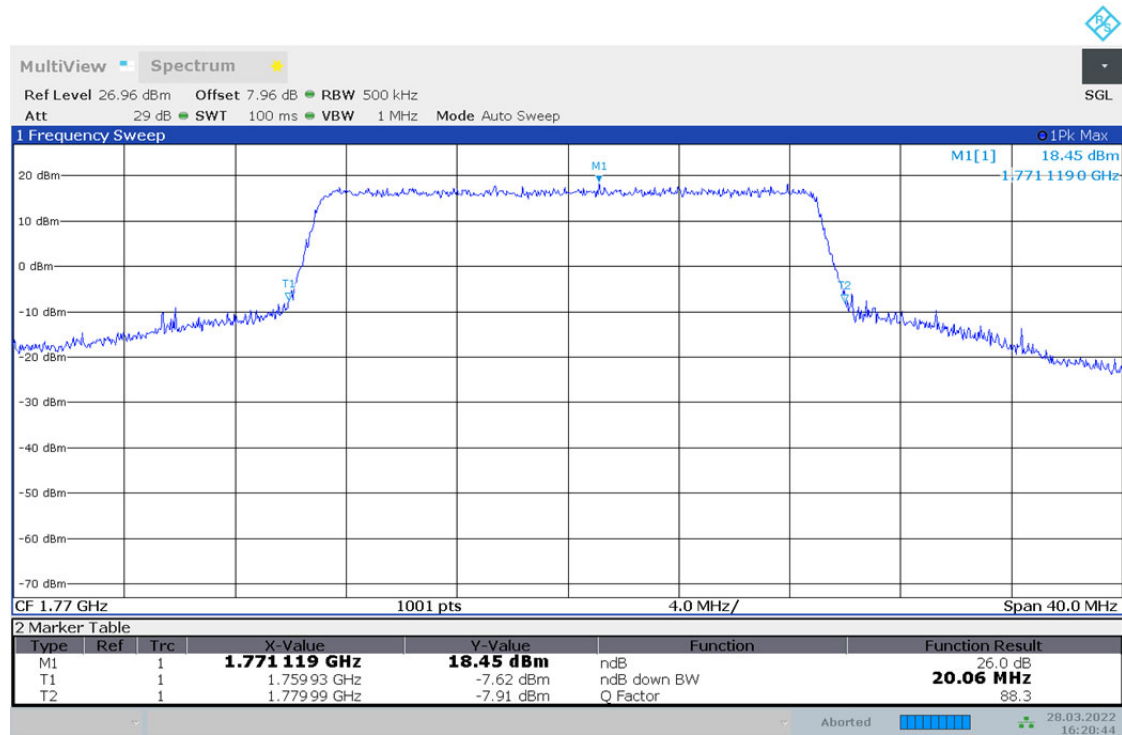


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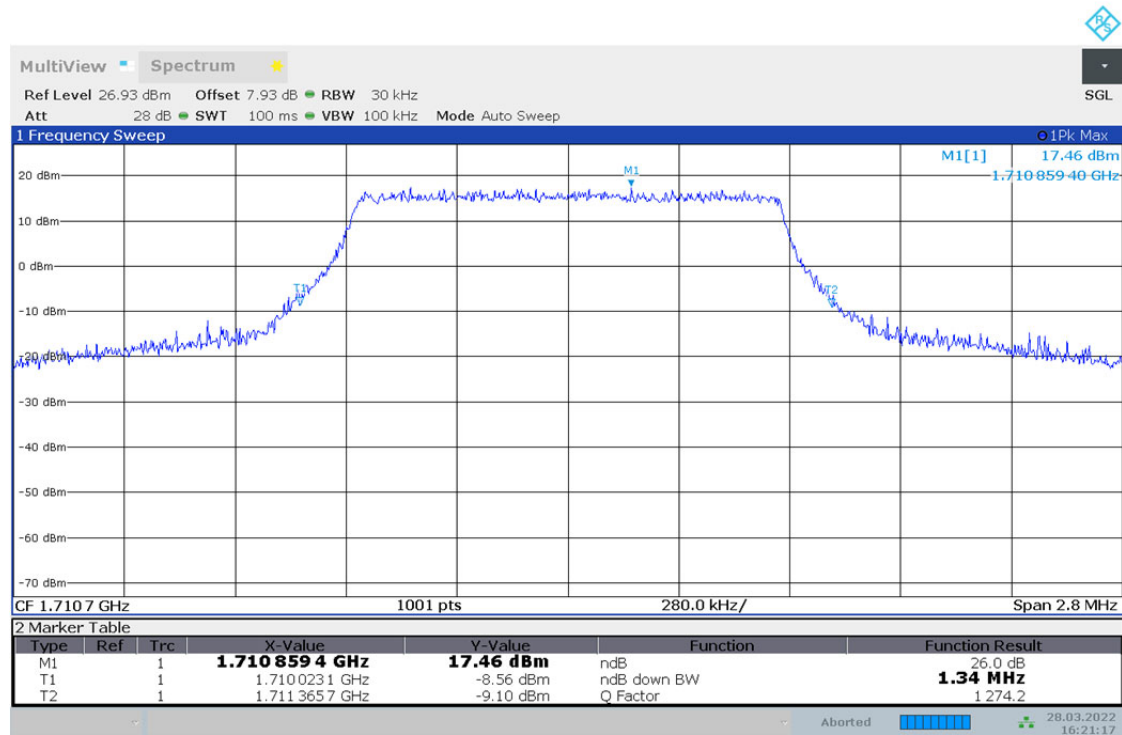


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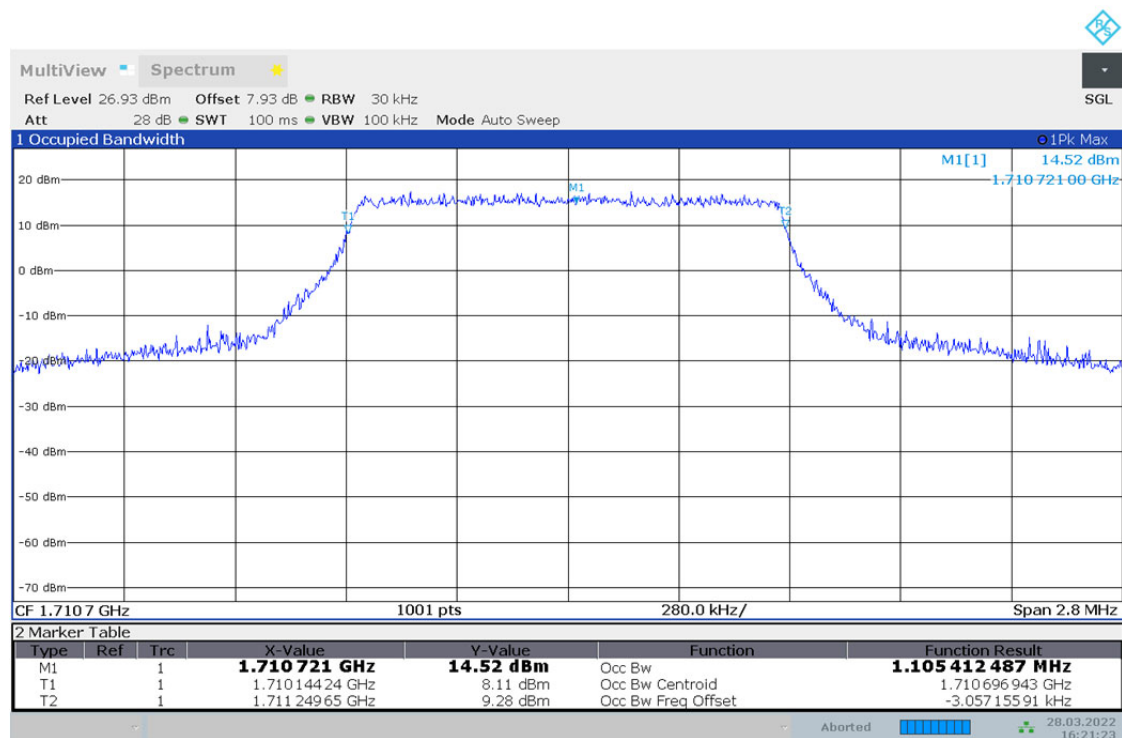
4.2.36 TM2_20MHZ_HCH_RB100#0



4.2.37 TM3_1.4MHZ_LCH_RB6#0

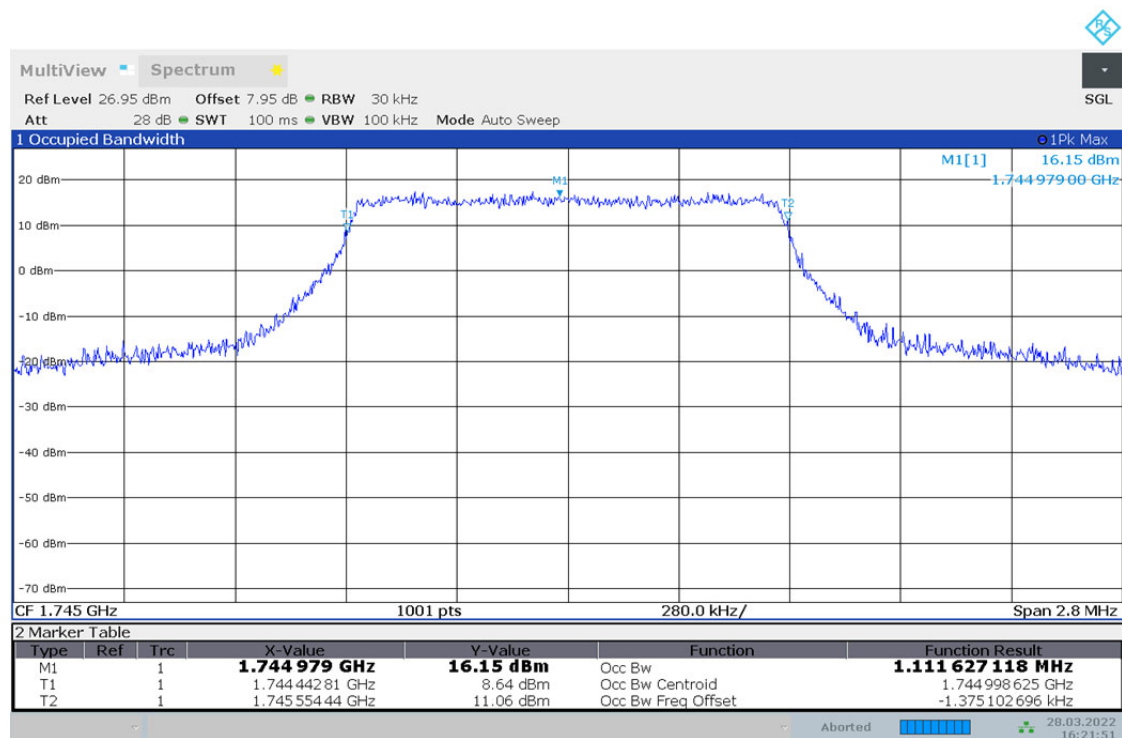
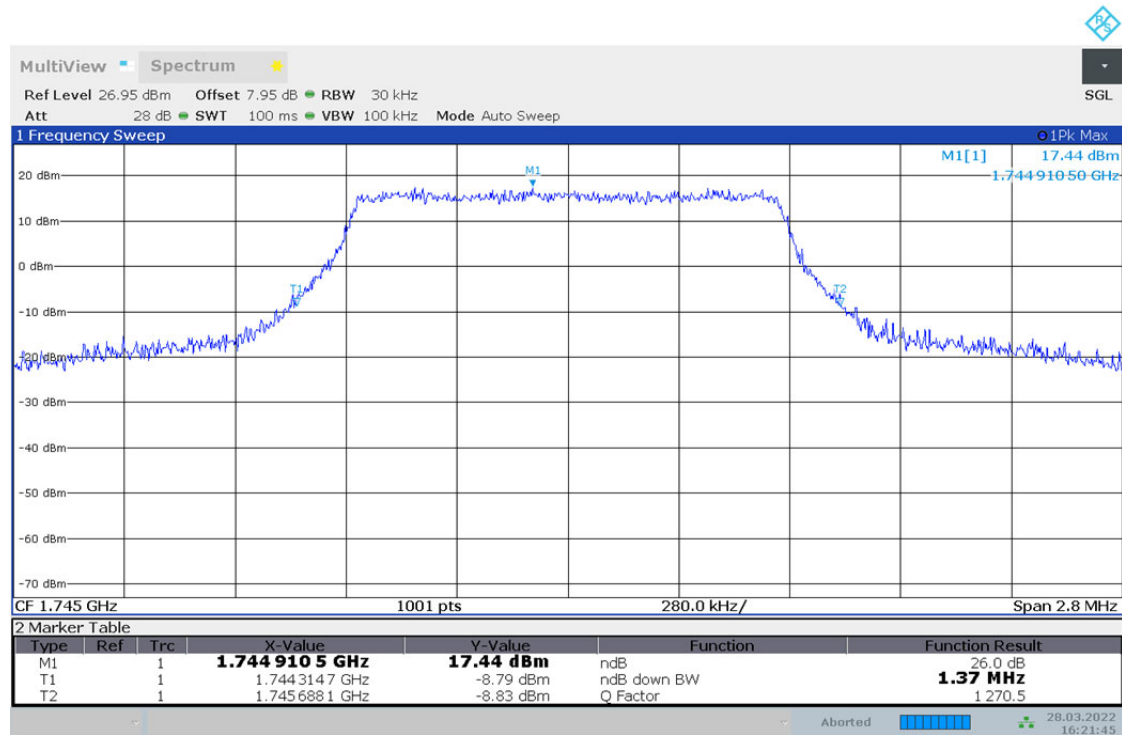


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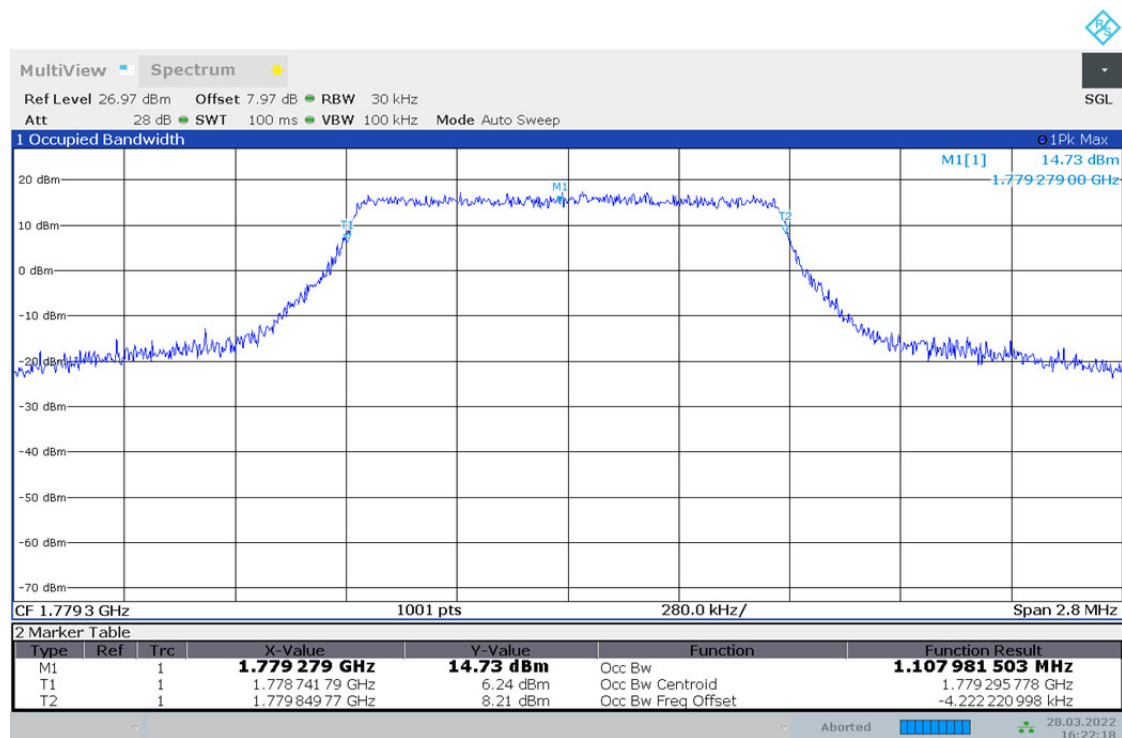
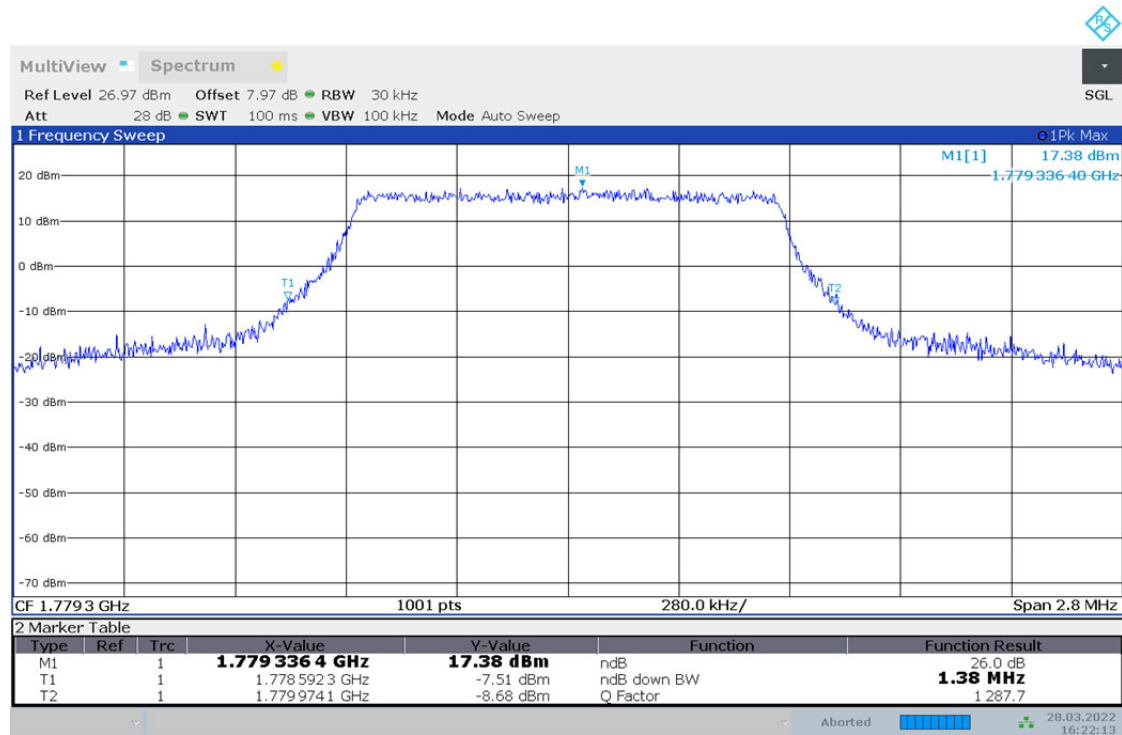


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4.2.38 TM3_1.4MHZ_MCH_RB6#0



4.2.39 TM3_1.4MHZ_HCH_RB6#0



4.2.40 TM3_3MHZ_LCH_RB15#0

