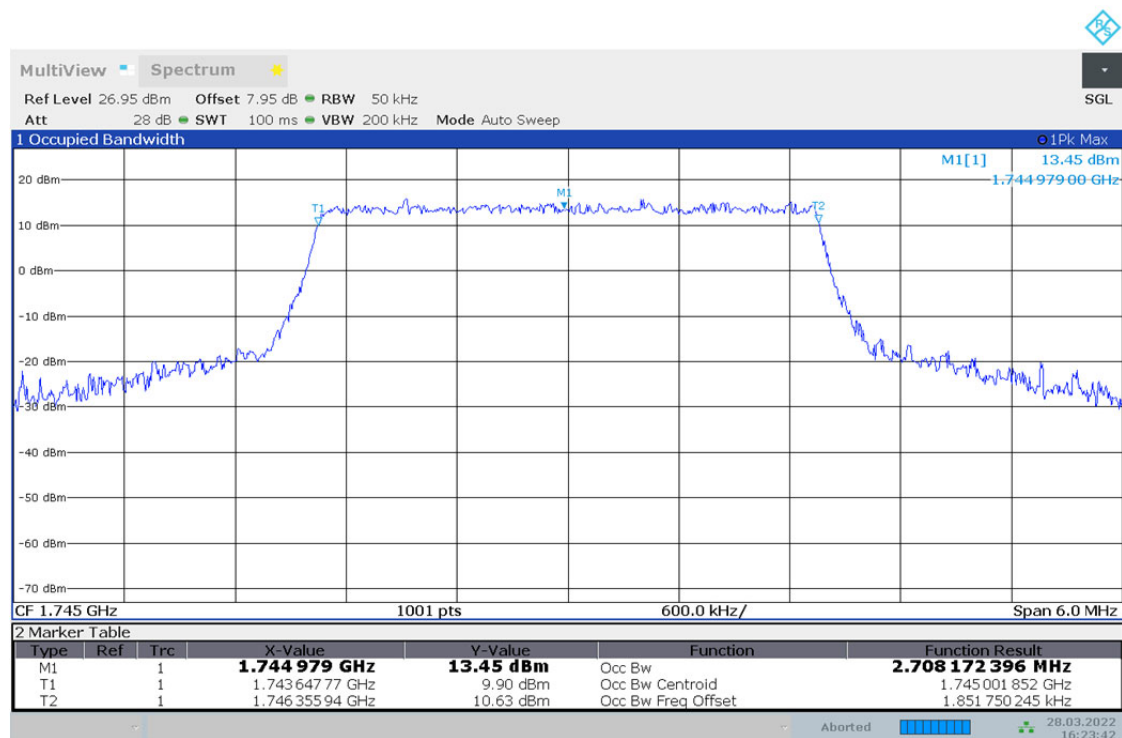
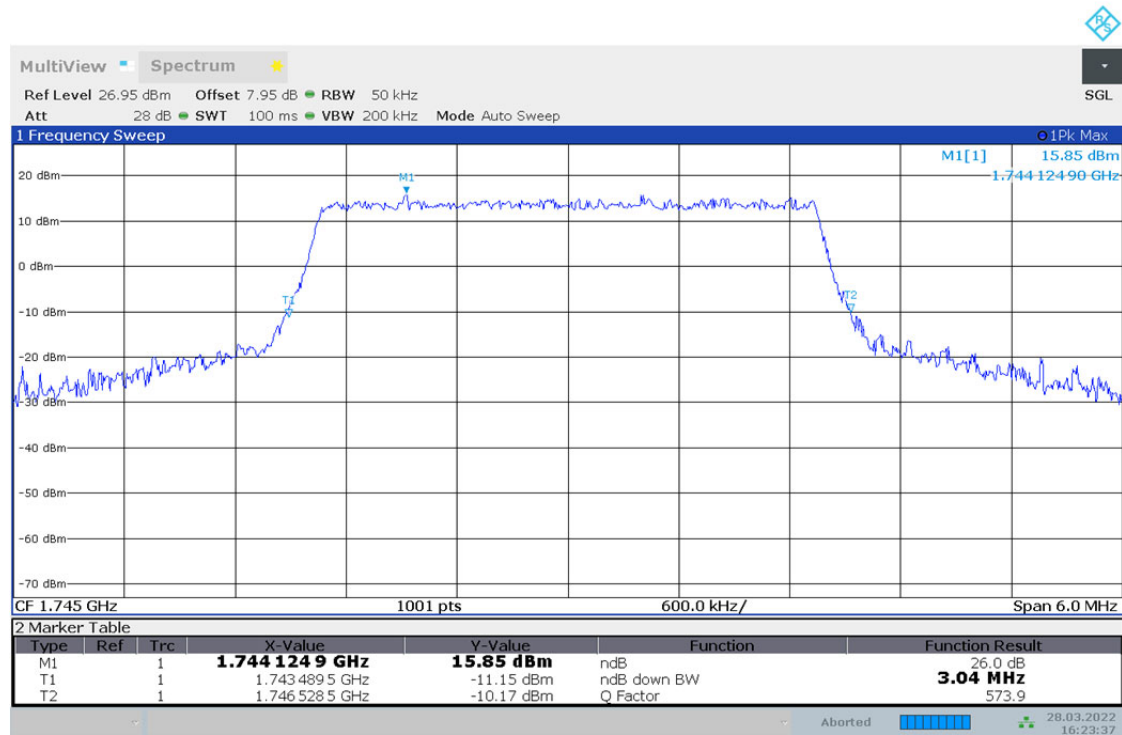
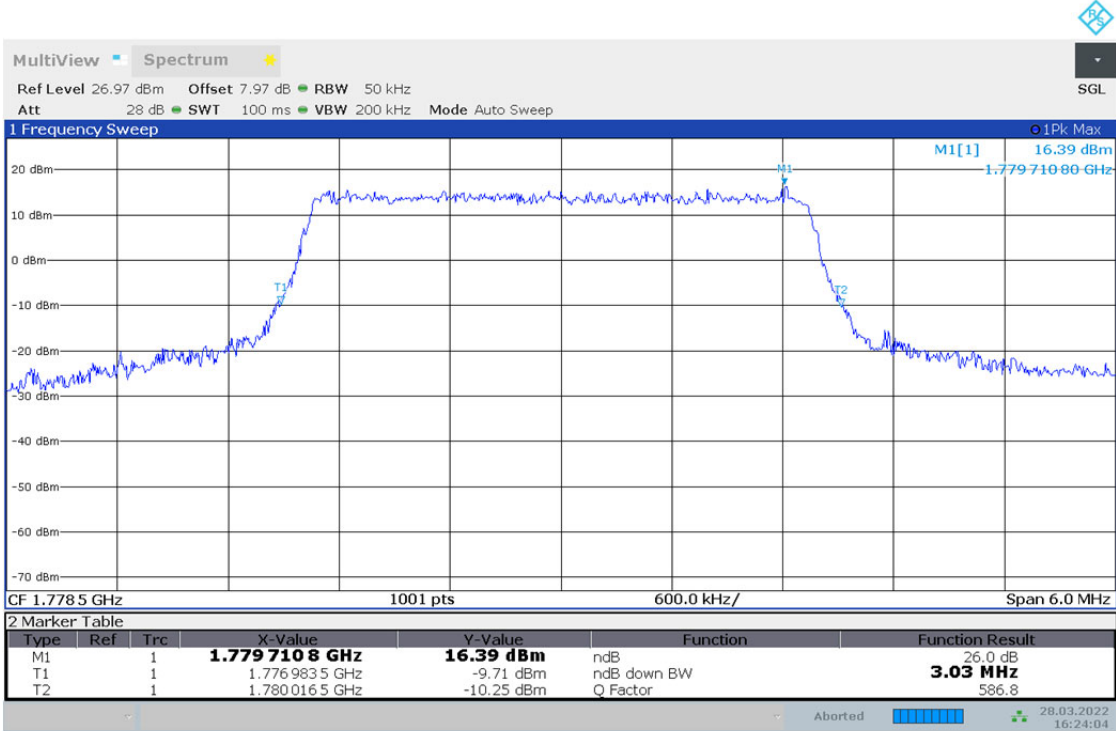


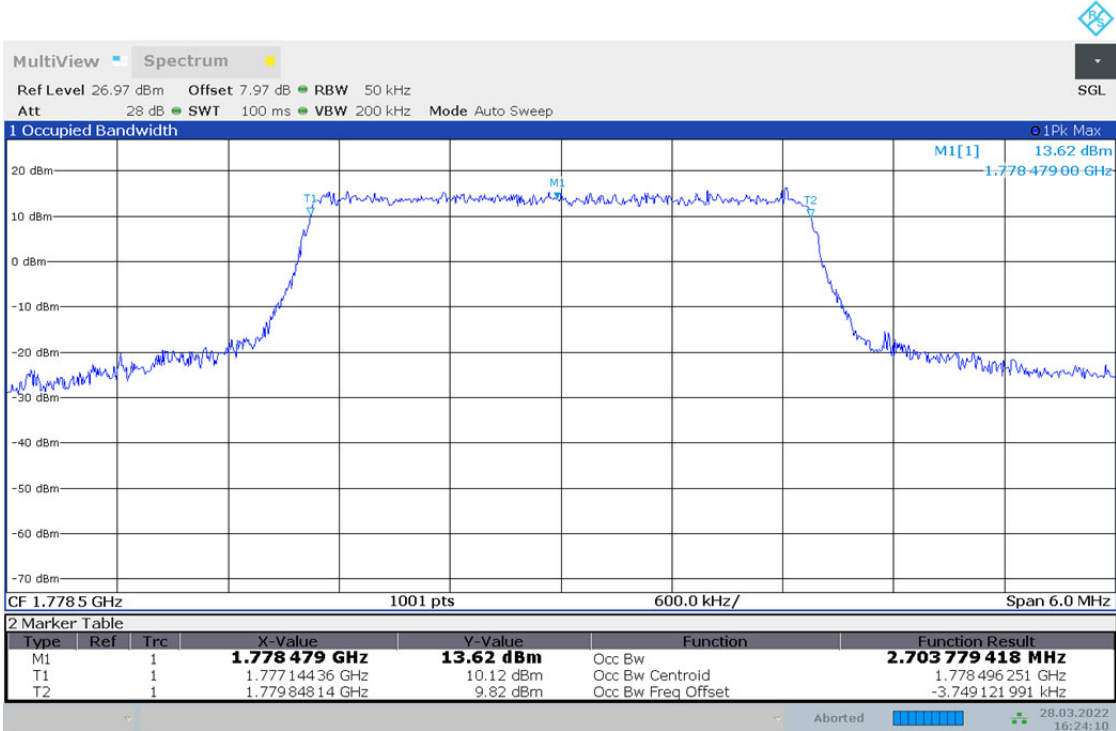
4.2.41 TM3_3MHZ_MCH_RB15#0



4.2.42 TM3_3MHZ_HCH_RB15#0

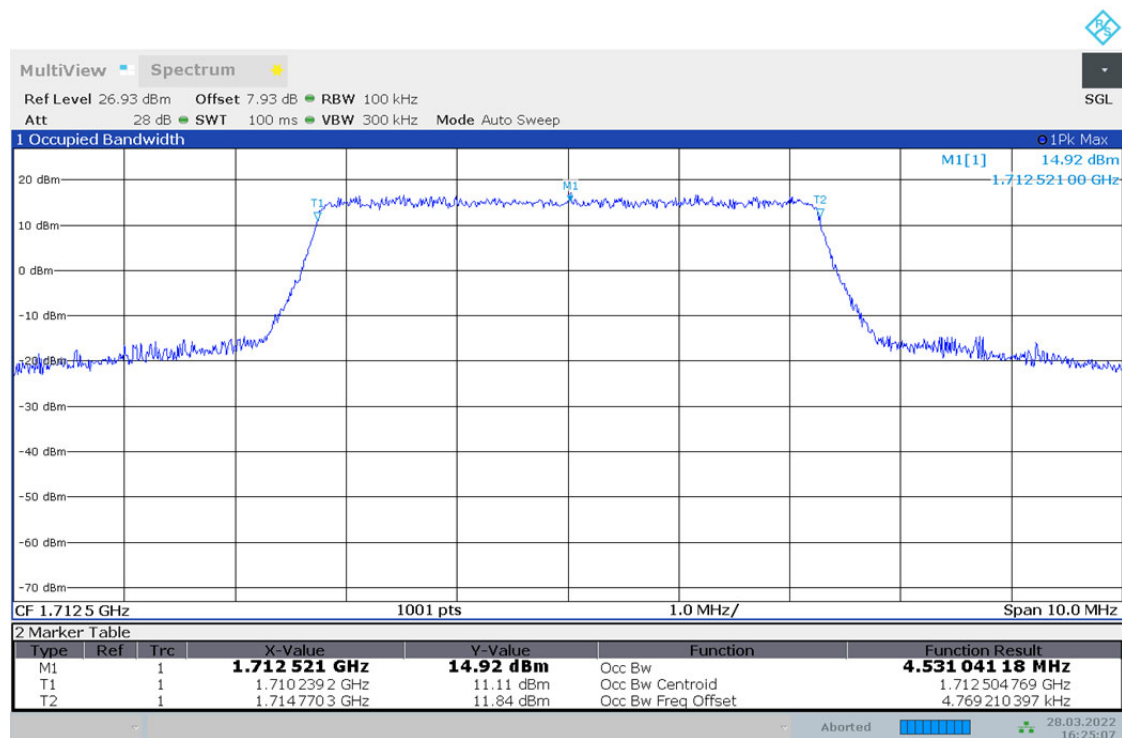
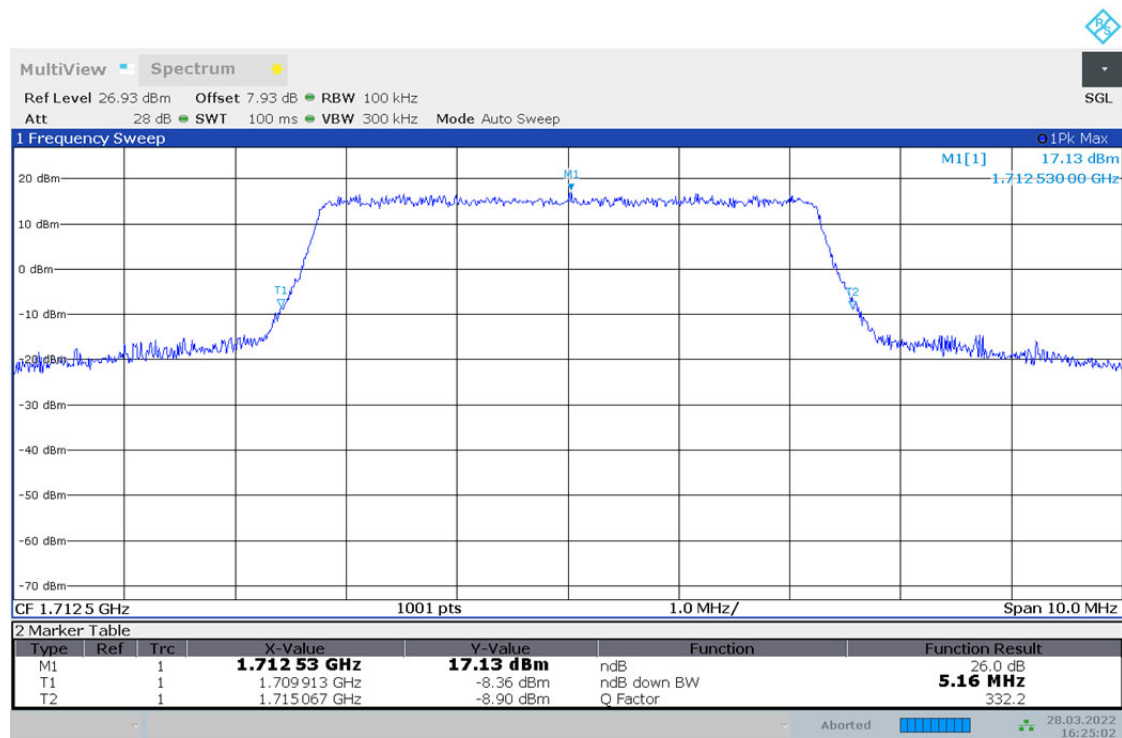


16:24:04 28.03.2022

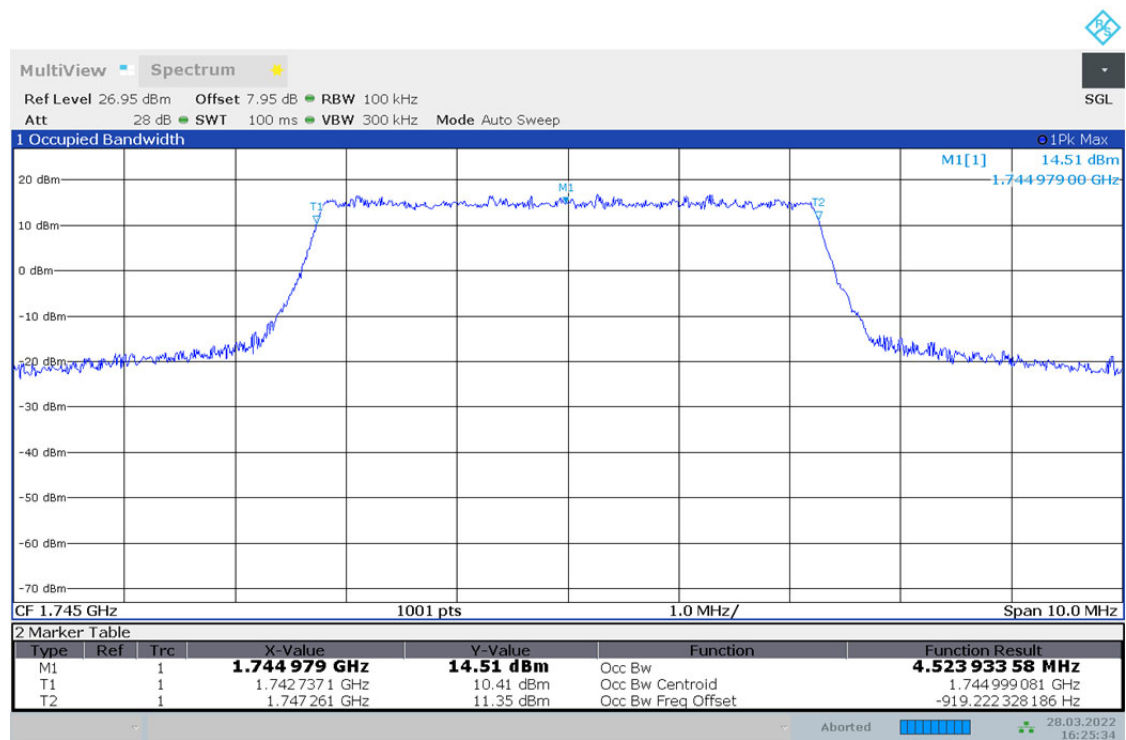
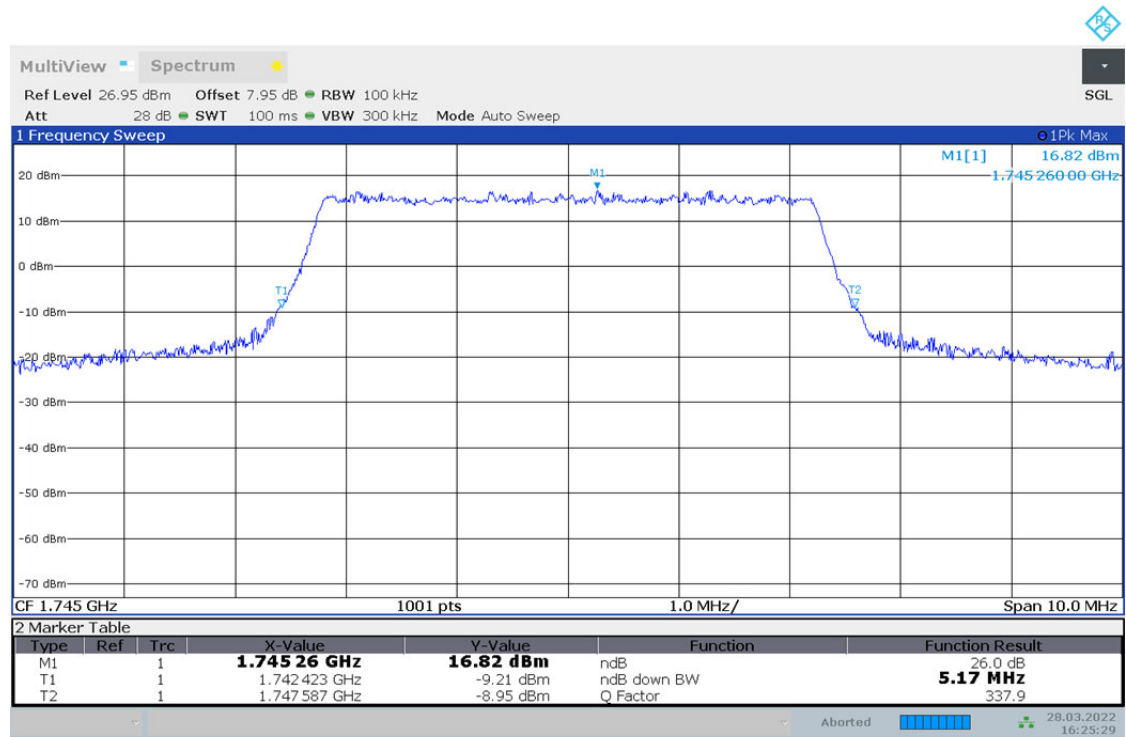


16:24:10 28.03.2022

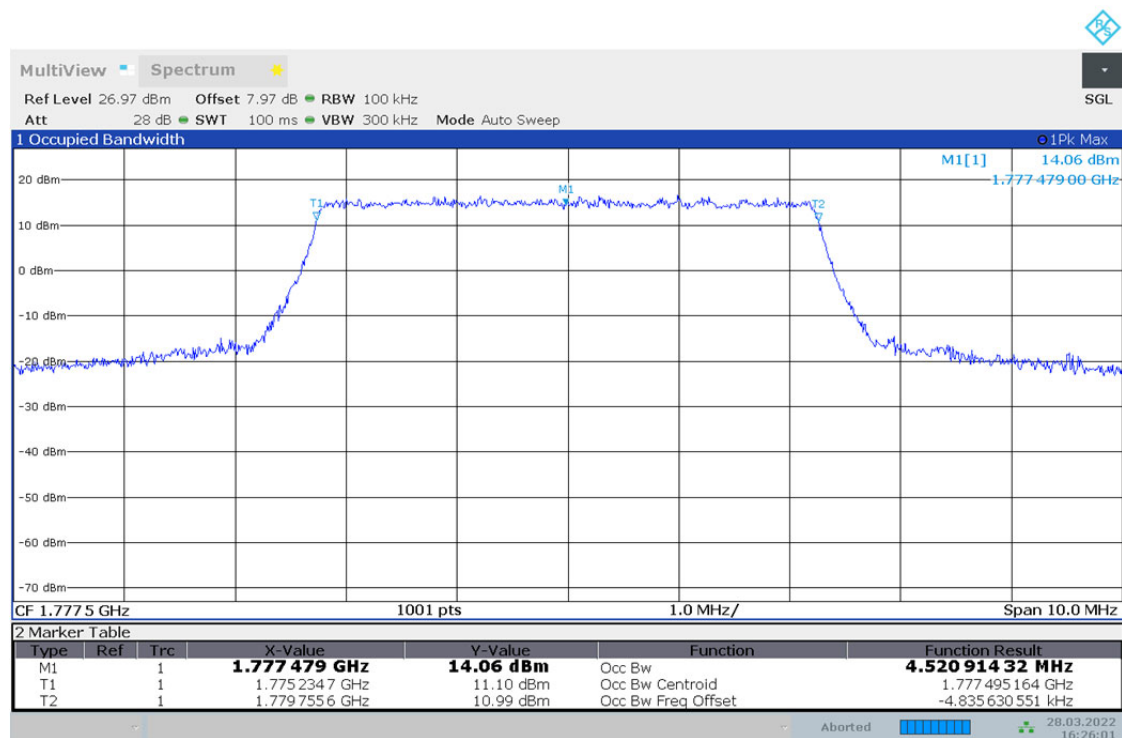
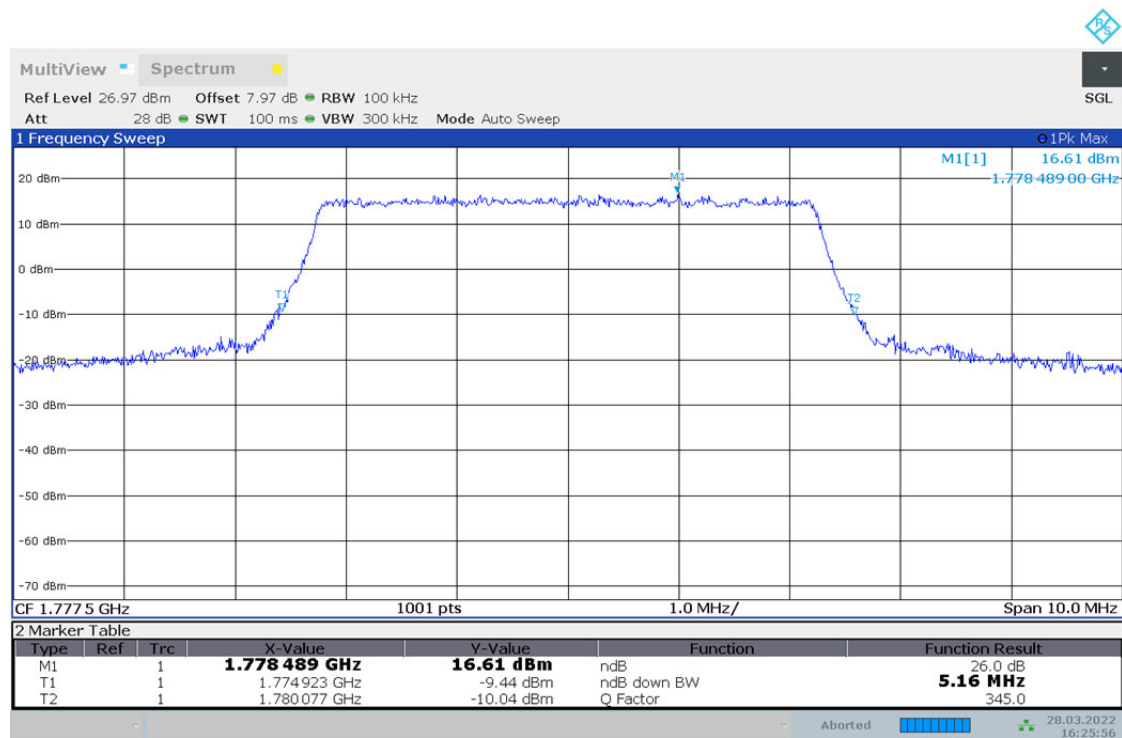
4.2.43 TM3_5MHZ_LCH_RB25#0



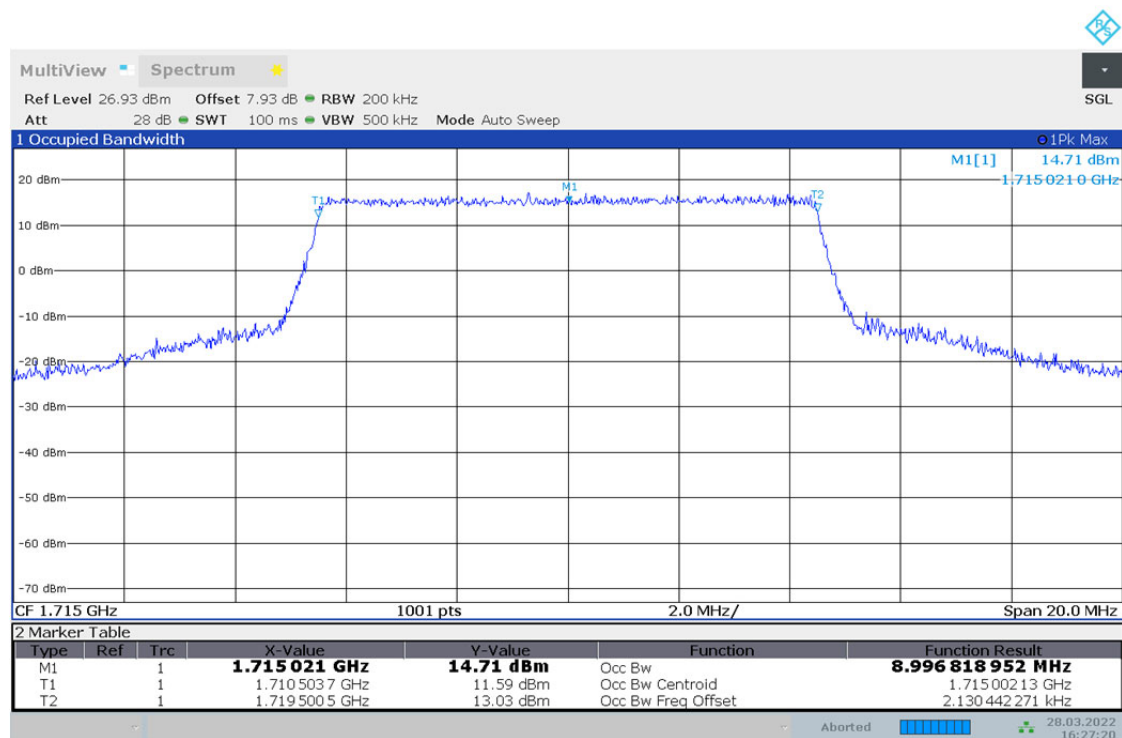
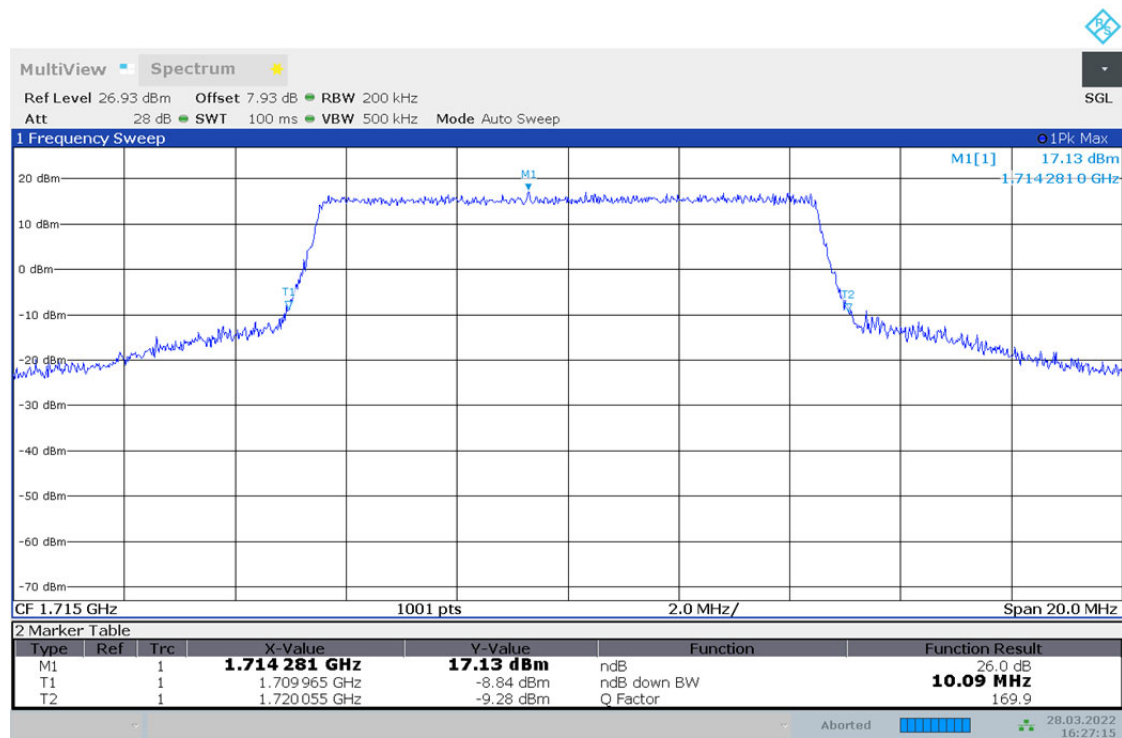
4.2.44 TM3_5MHZ_MCH_RB25#0



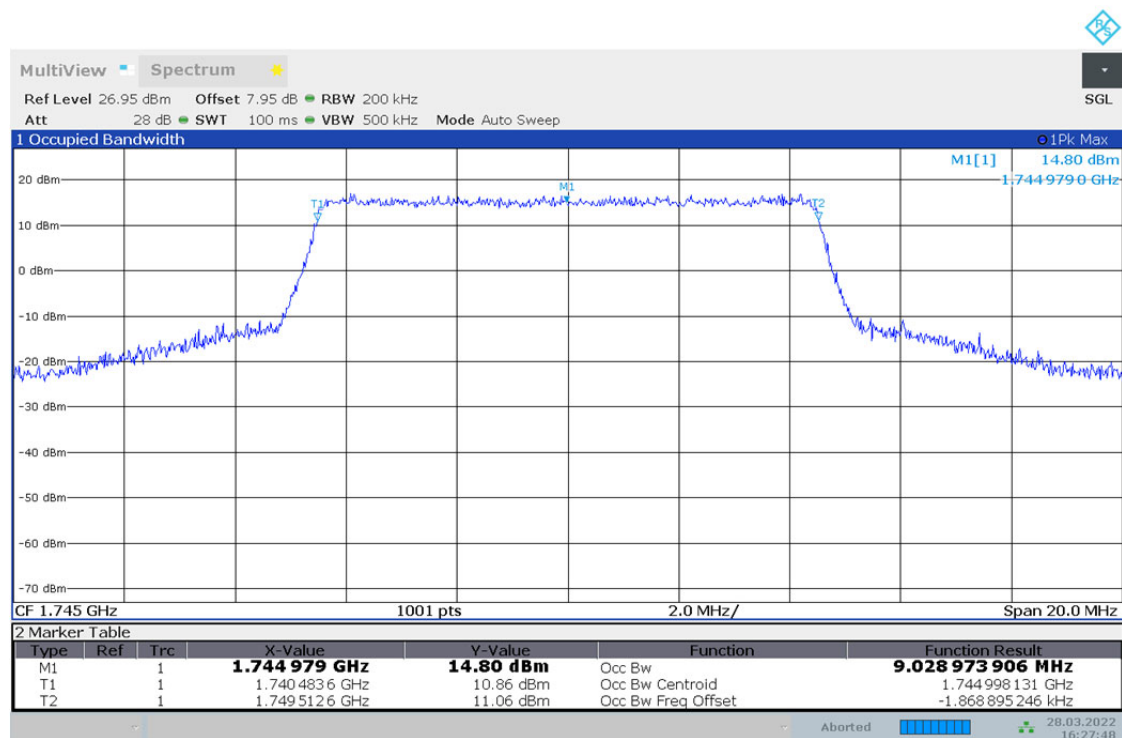
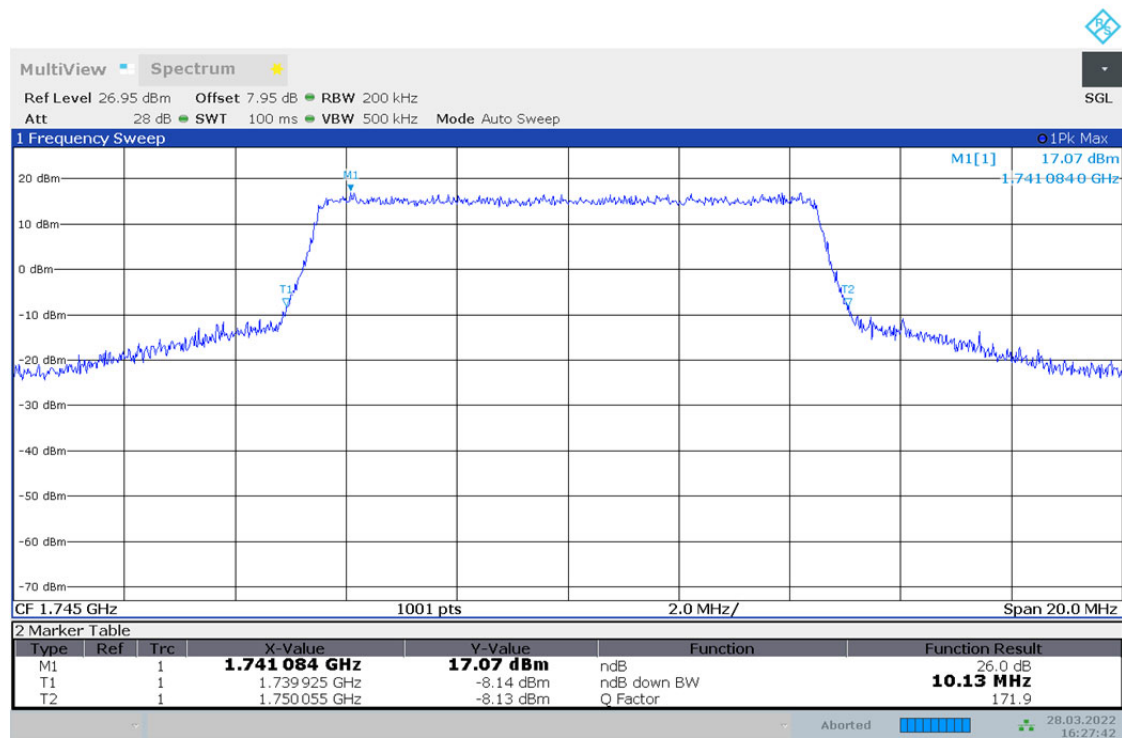
4.2.45 TM3_5MHZ_HCH_RB25#0



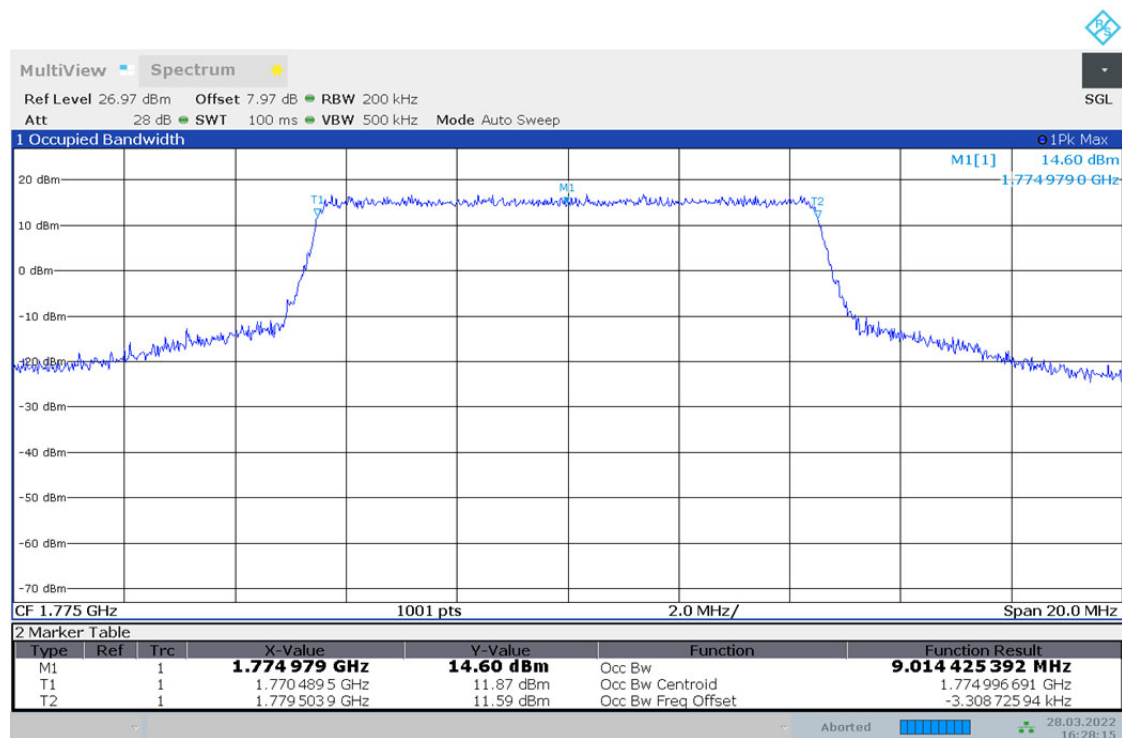
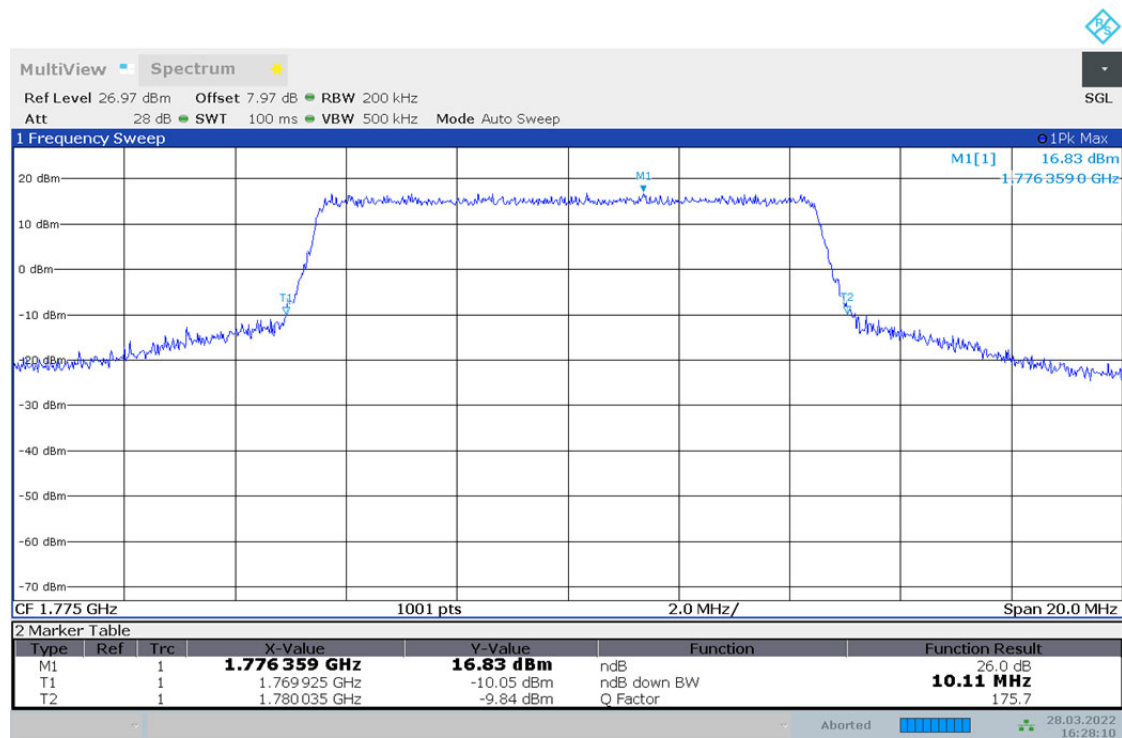
4.2.46 TM3_10MHZ_LCH_RB50#0



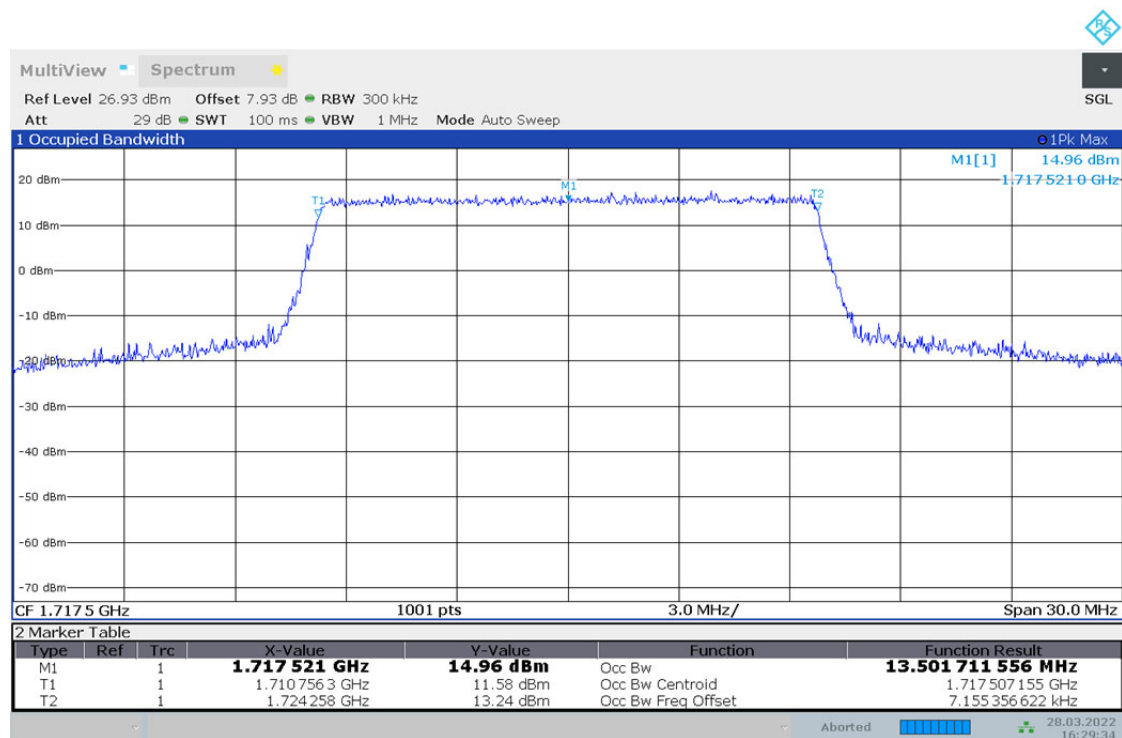
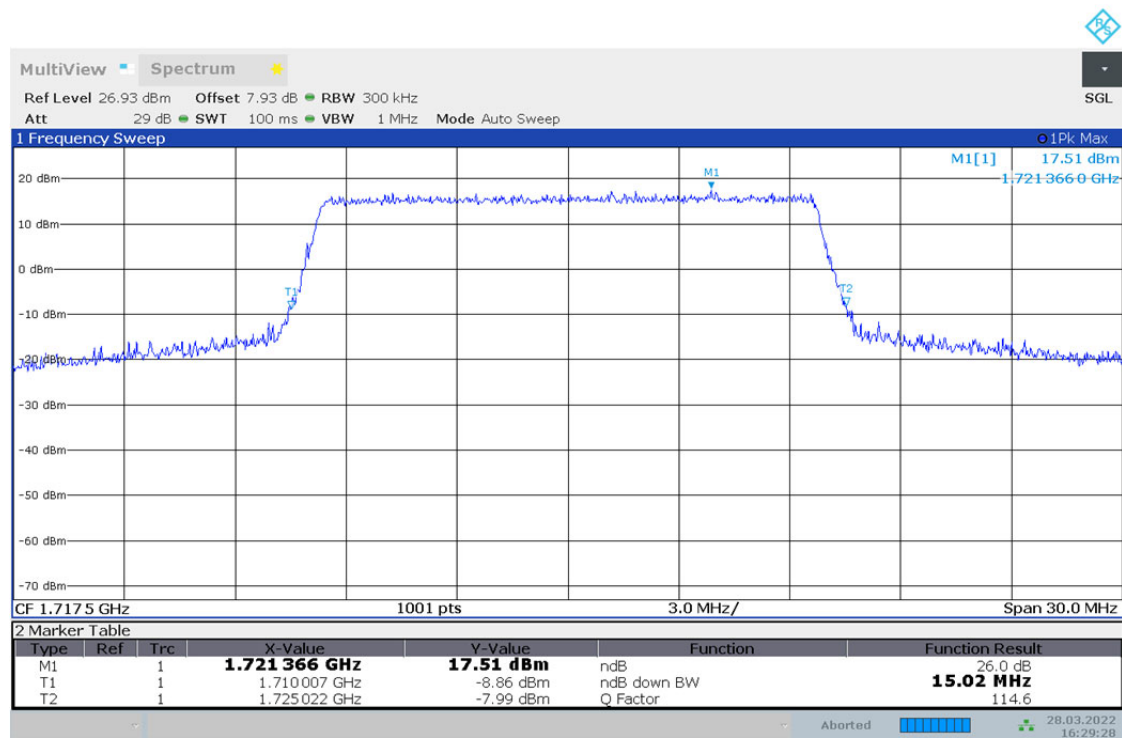
4.2.47 TM3_10MHZ_MCH_RB50#0



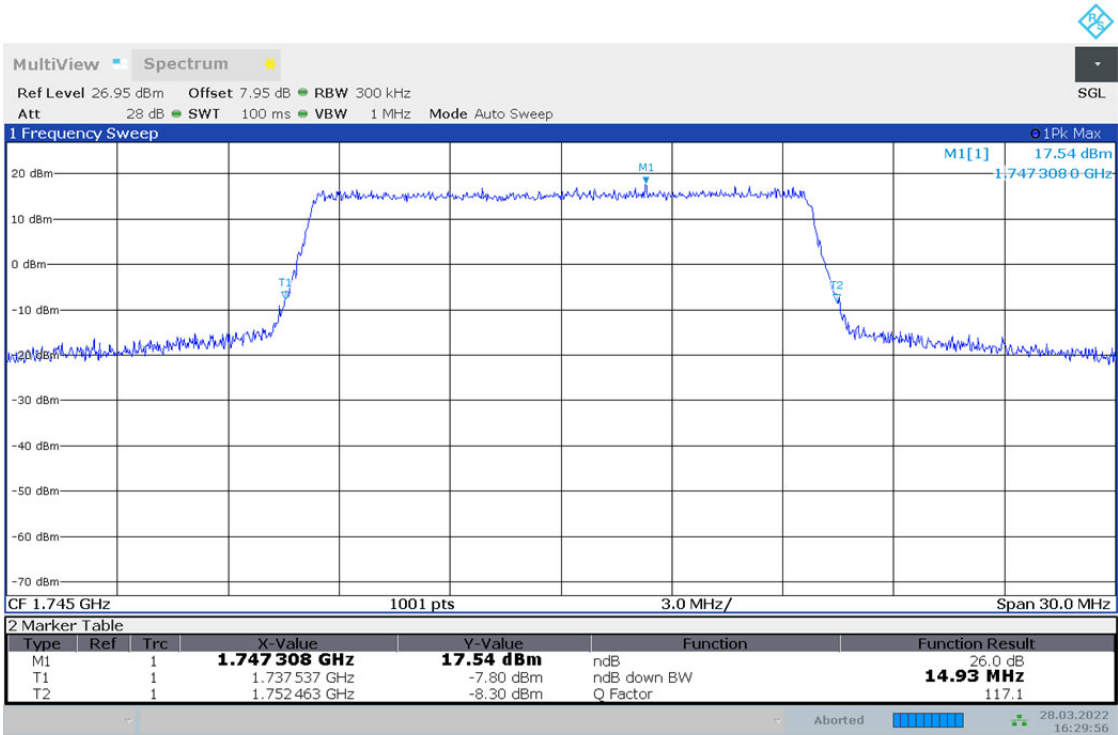
4.2.48 TM3_10MHZ_HCH_RB50#0



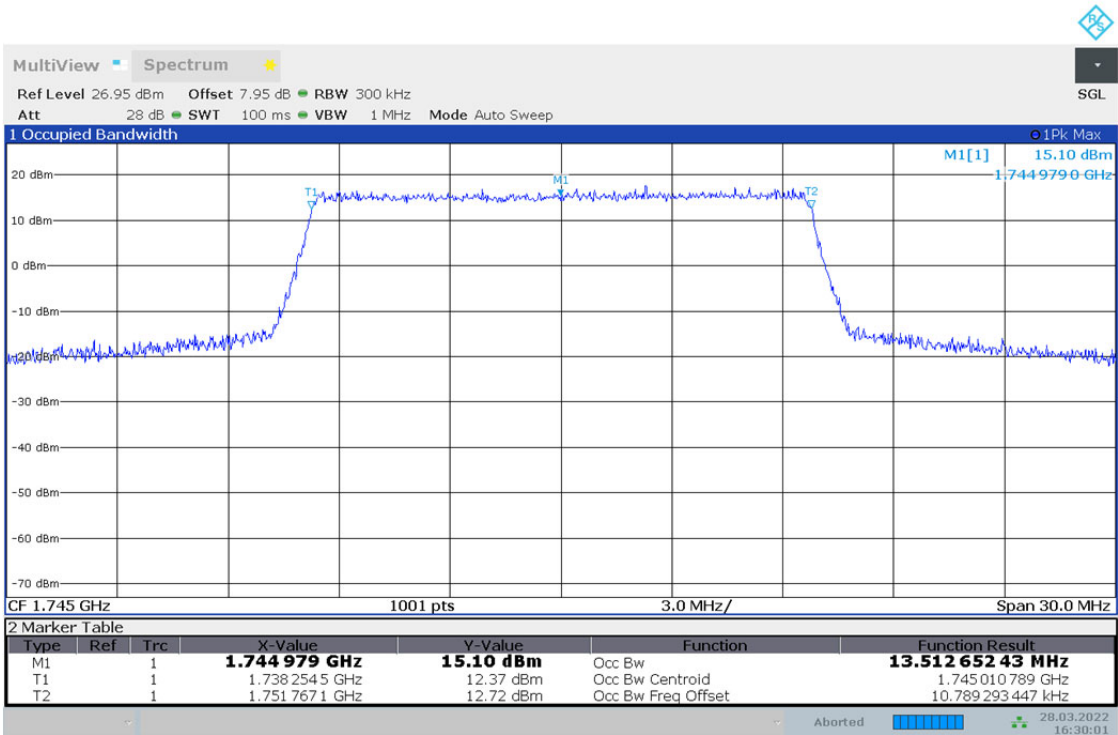
4.2.49 TM3_15MHZ_LCH_RB75#0



4.2.50 TM3_15MHZ_MCH_RB75#0

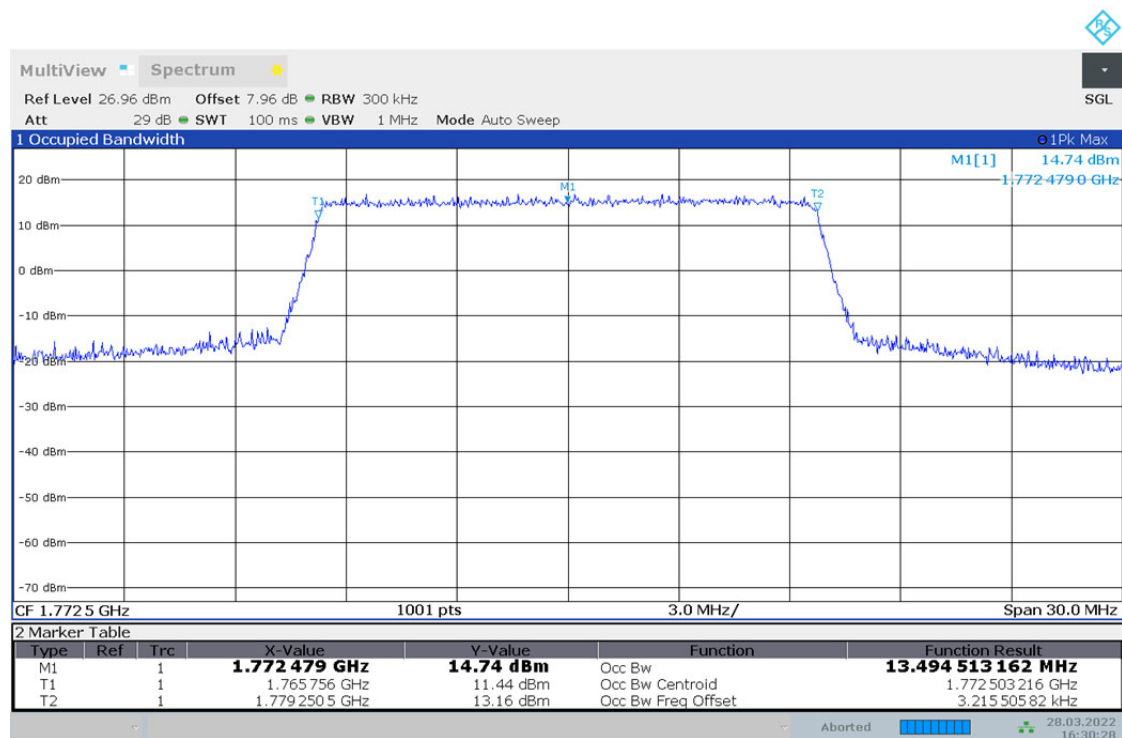
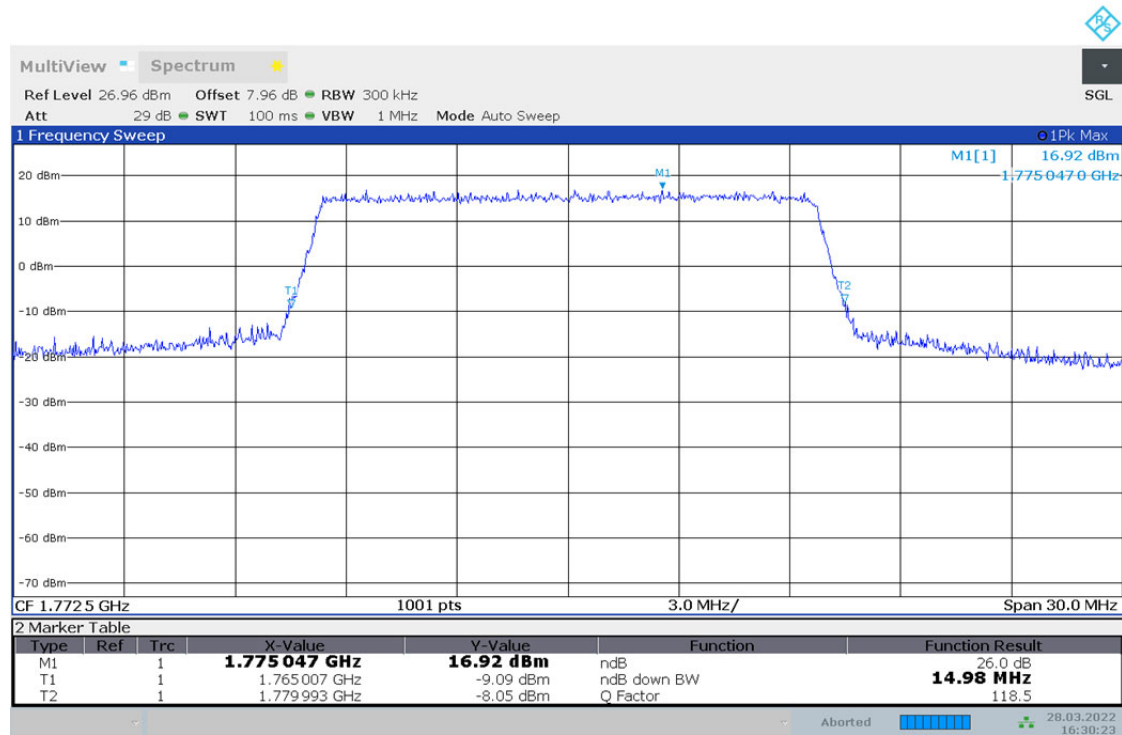


16:29:56 28.03.2022

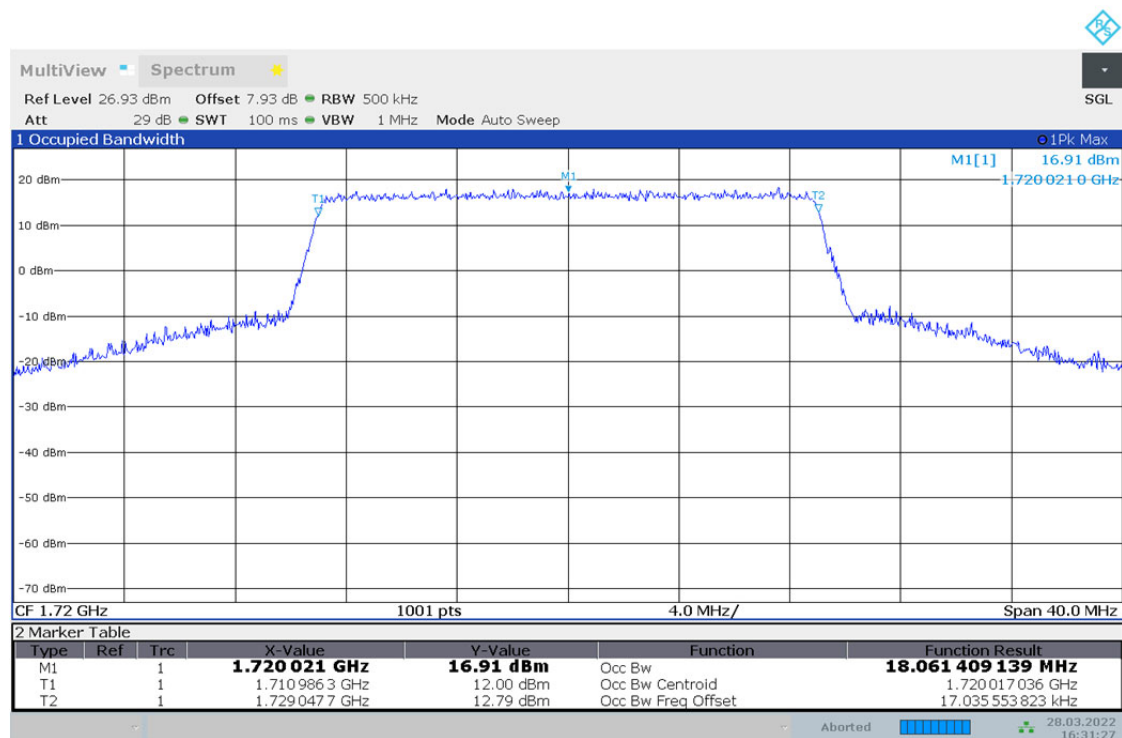
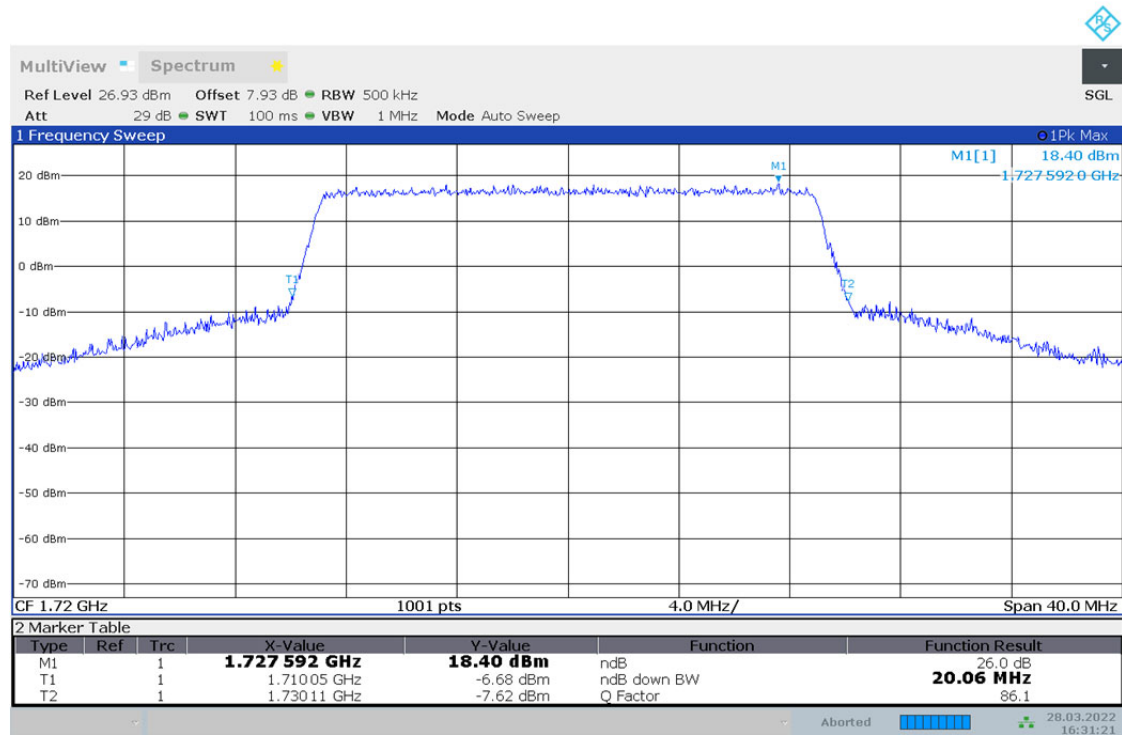


16:30:01 28.03.2022

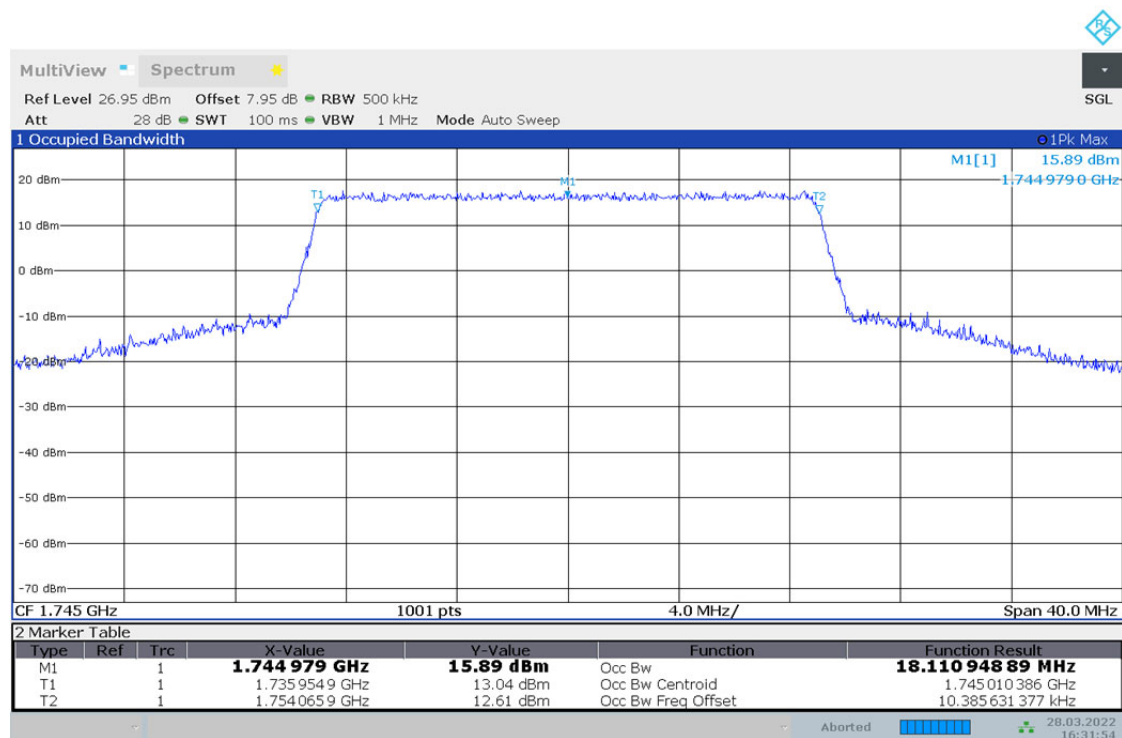
4.2.51 TM3_15MHZ_HCH_RB75#0



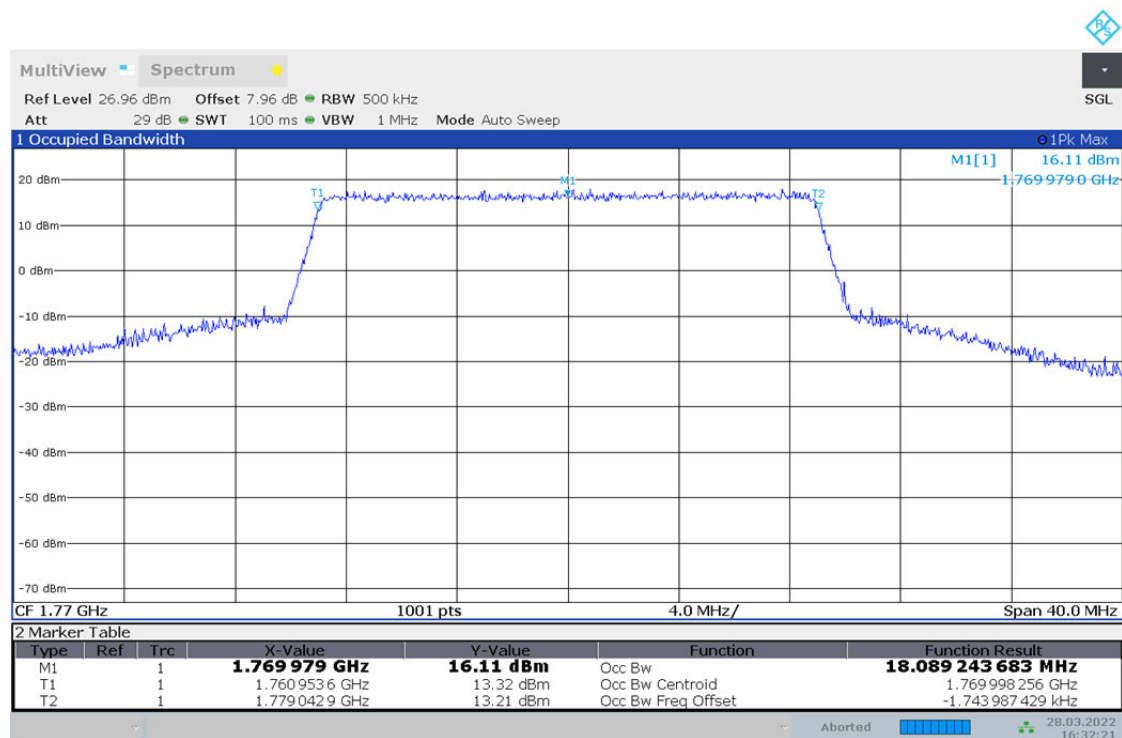
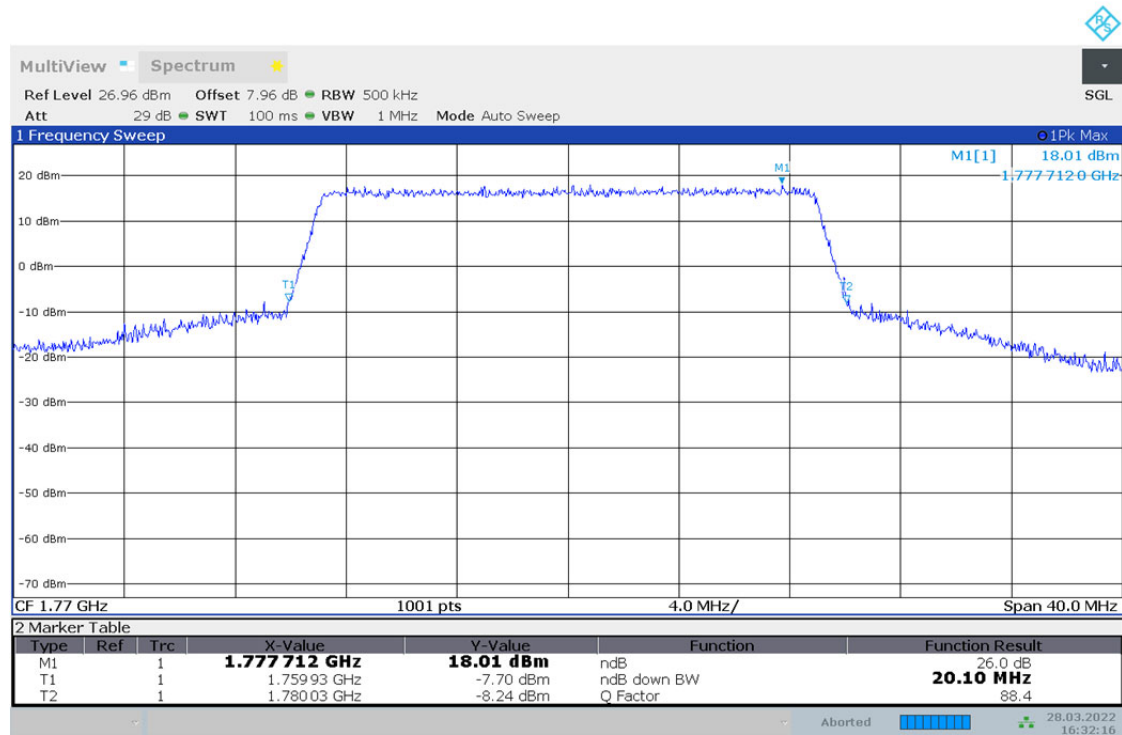
4.2.52 TM3_20MHZ_LCH_RB100#0



4.2.53 TM3_20MHZ_MCH_RB100#0



4.2.54 TM3_20MHZ_HCH_RB100#0



5 APPENDIX E: BAND EDGES COMPLIANCE

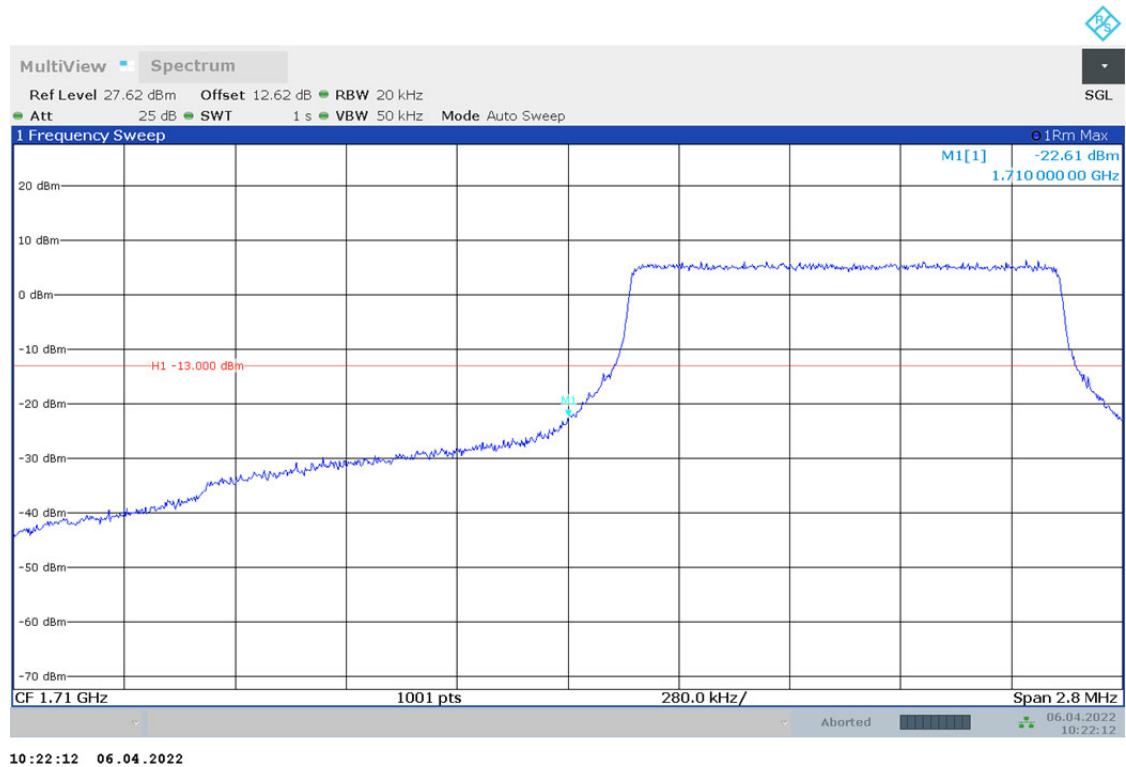
5.1 TEST RESULTS

Band Edges Compliance					
Test Mode	Test Bandwidth [MHz]	Test Channel	Test RB	Modulation	Verdict
TM1	1.4	LCH	RB6#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#5	QPSK	PASS
		HCH	RB6#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#5	QPSK	PASS
	5	LCH	RB25#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#24	QPSK	PASS
		HCH	RB25#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#24	QPSK	PASS
	20	LCH	RB100#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#99	QPSK	PASS
		HCH	RB100#0	QPSK	PASS
			RB1#0	QPSK	PASS
			RB1#99	QPSK	PASS
TM2	1.4	LCH	RB6#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#5	16QAM	PASS
		HCH	RB6#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#5	16QAM	PASS
	5	LCH	RB25#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#24	16QAM	PASS
		HCH	RB25#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#24	16QAM	PASS
	20	LCH	RB100#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#99	16QAM	PASS
		HCH	RB100#0	16QAM	PASS
			RB1#0	16QAM	PASS
			RB1#99	16QAM	PASS

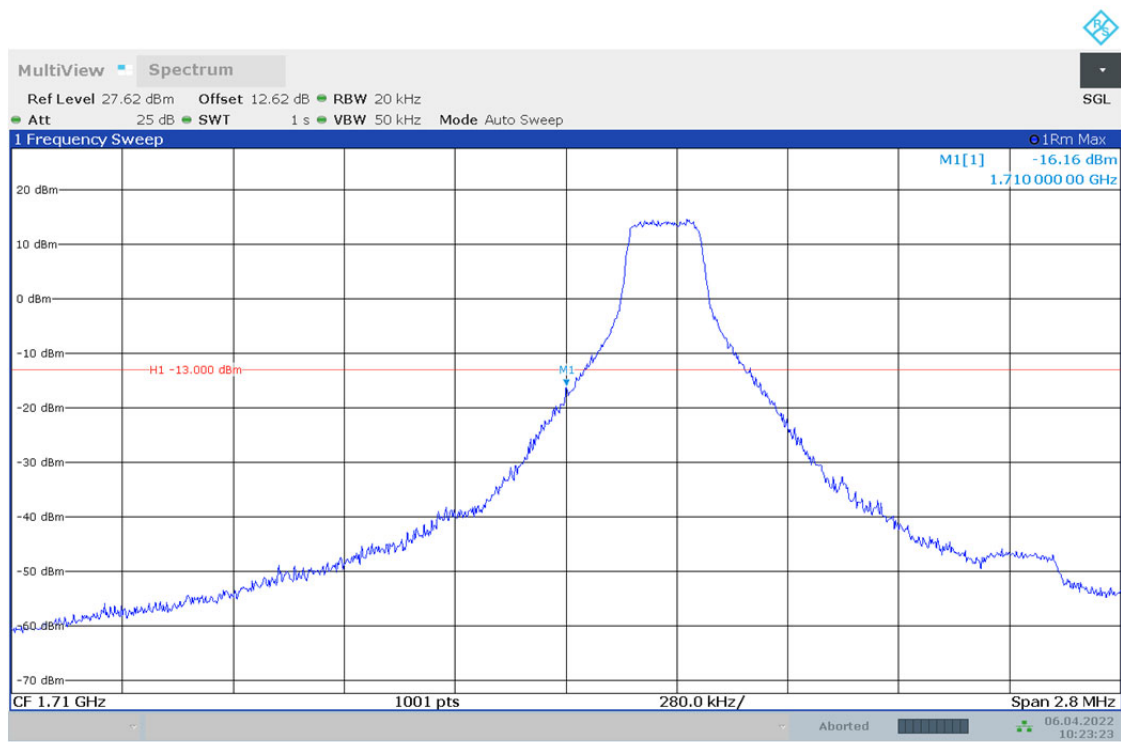
Band Edges Compliance					
Test Mode	Test Bandwidth [MHz]	Test Channel	Test RB	Modulation	Verdict
TM3	1.4	LCH	RB6#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#5	64QAM	PASS
		HCH	RB6#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#5	64QAM	PASS
	5	LCH	RB25#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#24	64QAM	PASS
		HCH	RB25#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#24	64QAM	PASS
	20	LCH	RB100#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#99	64QAM	PASS
		HCH	RB100#0	64QAM	PASS
			RB1#0	64QAM	PASS
			RB1#99	64QAM	PASS

5.2 TEST PLOTS

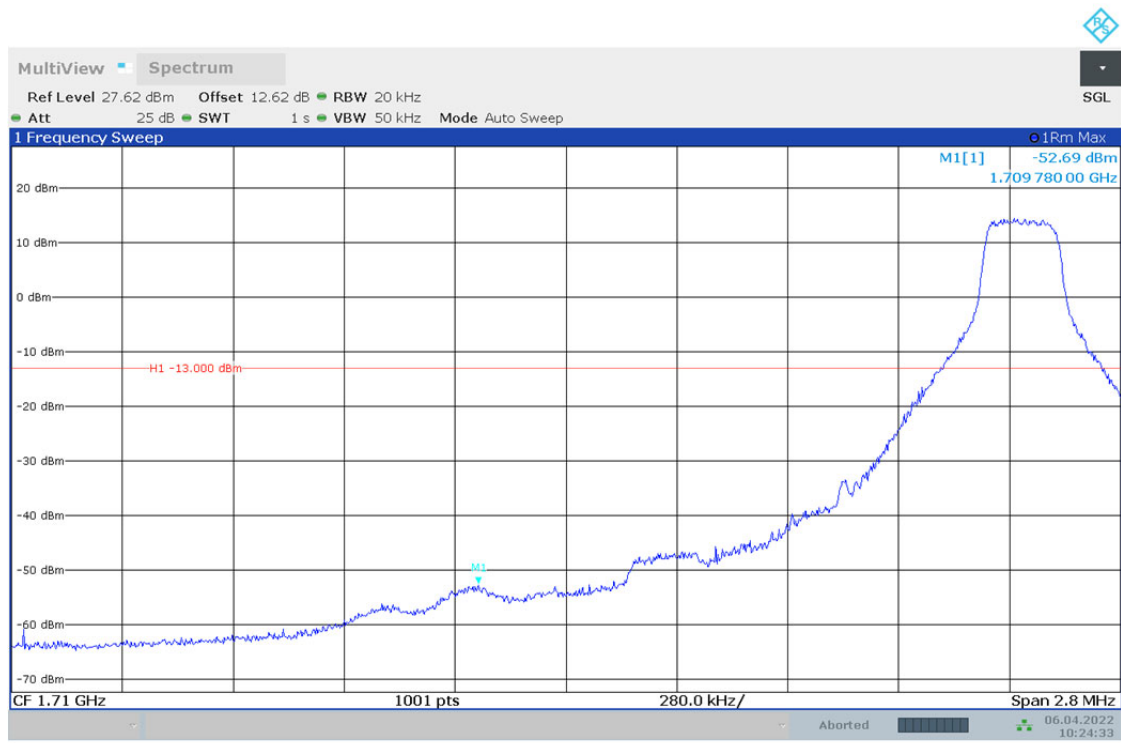
5.2.1 TM1_1.4MHZ_LCH_RB6#0



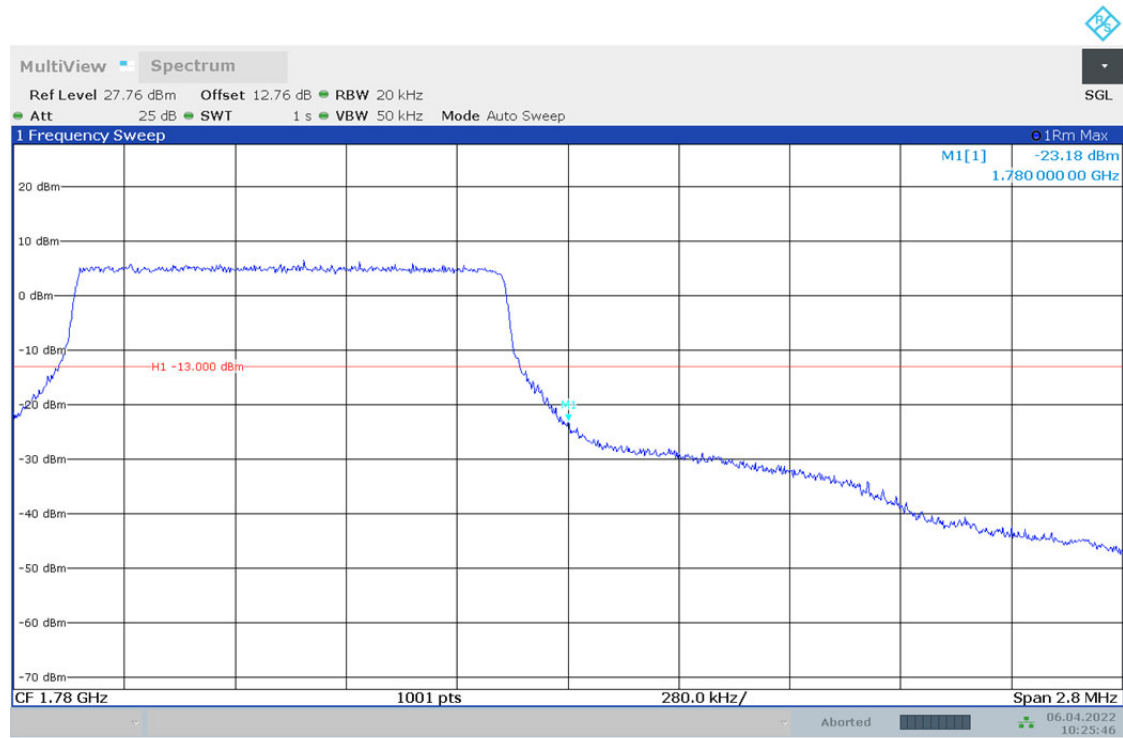
5.2.2 TM1_1.4MHZ_LCH_RB1#0



5.2.3 TM1_1.4MHZ_LCH_RB1#5



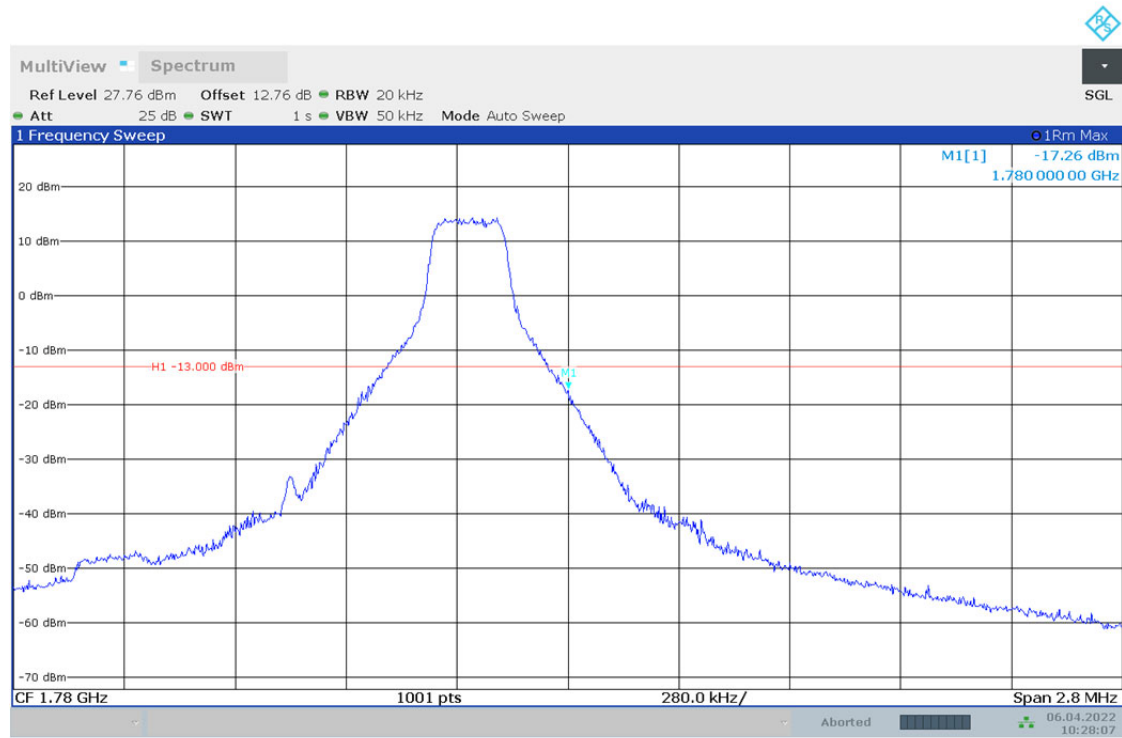
5.2.4 TM1_1.4MHZ_HCH_RB6#0



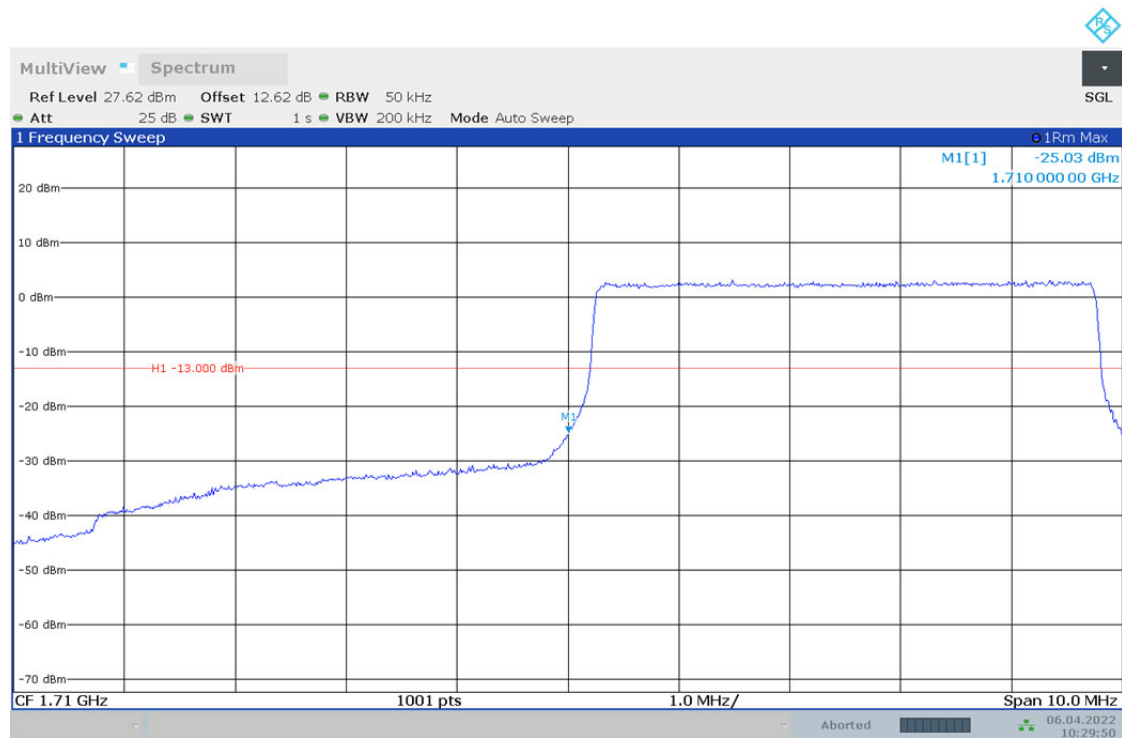
5.2.5 TM1_1.4MHZ_HCH_RB1#0



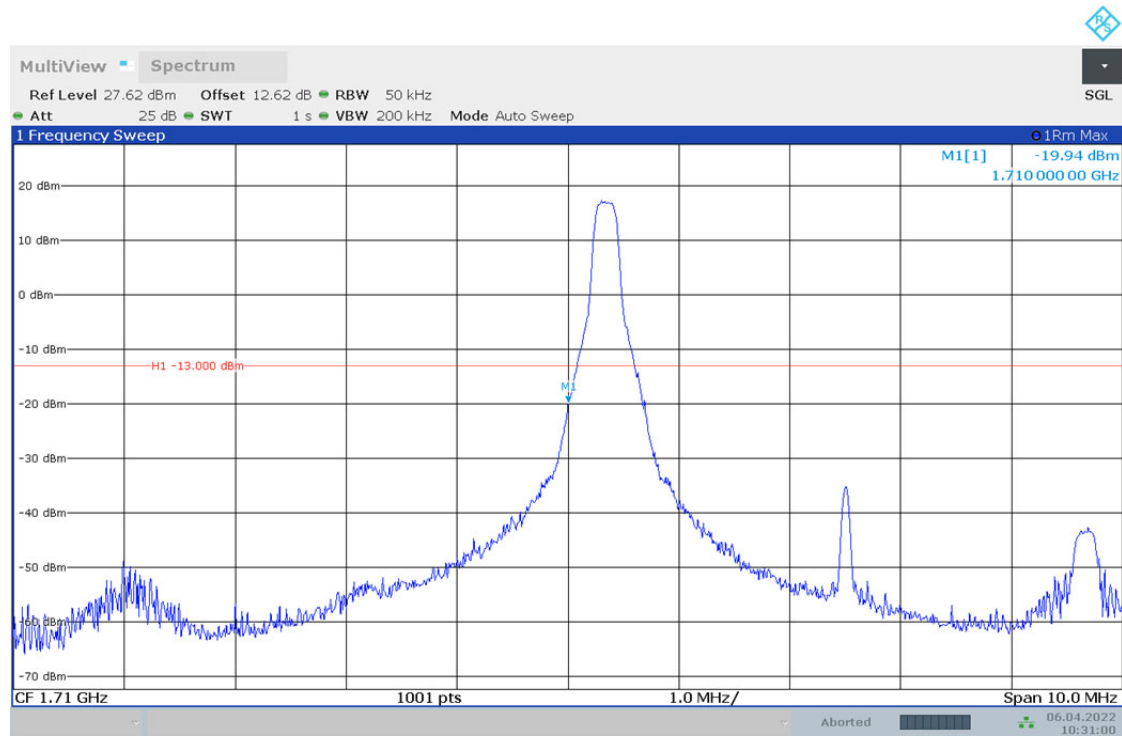
5.2.6 TM1_1.4MHZ_HCH_RB1#5



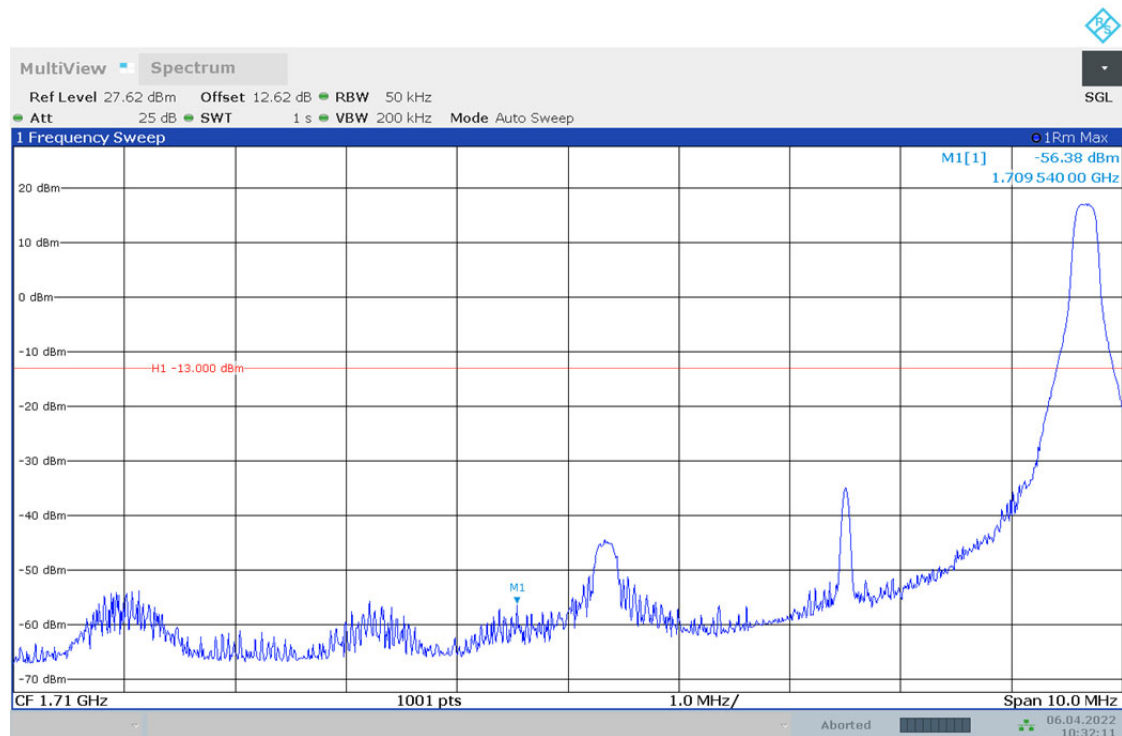
5.2.7 TM1_5MHZ_LCH_RB25#0



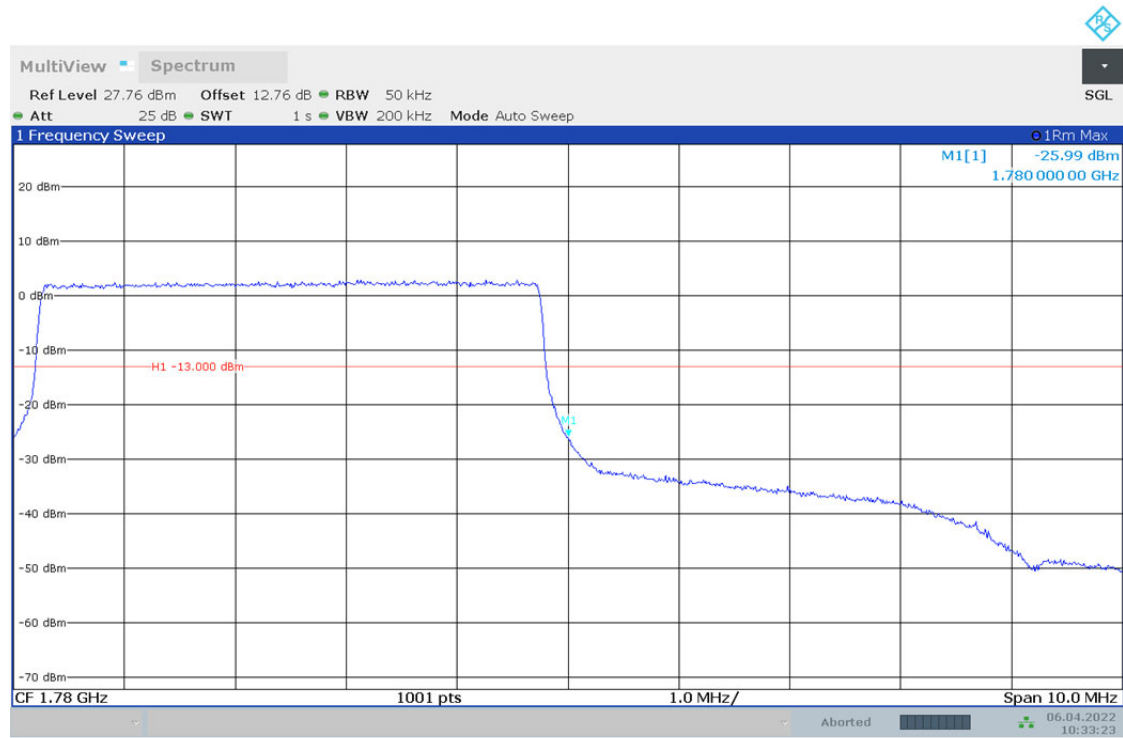
5.2.8 TM1_5MHZ_LCH_RB1#0



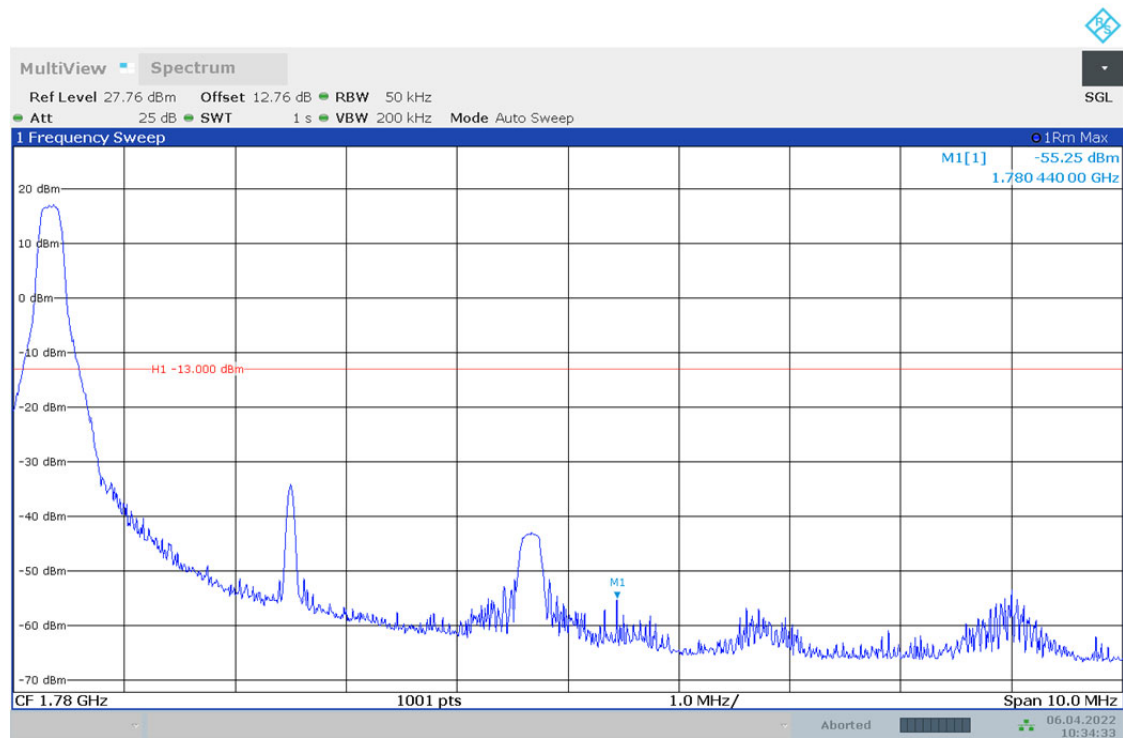
5.2.9 TM1_5MHZ_LCH_RB1#24



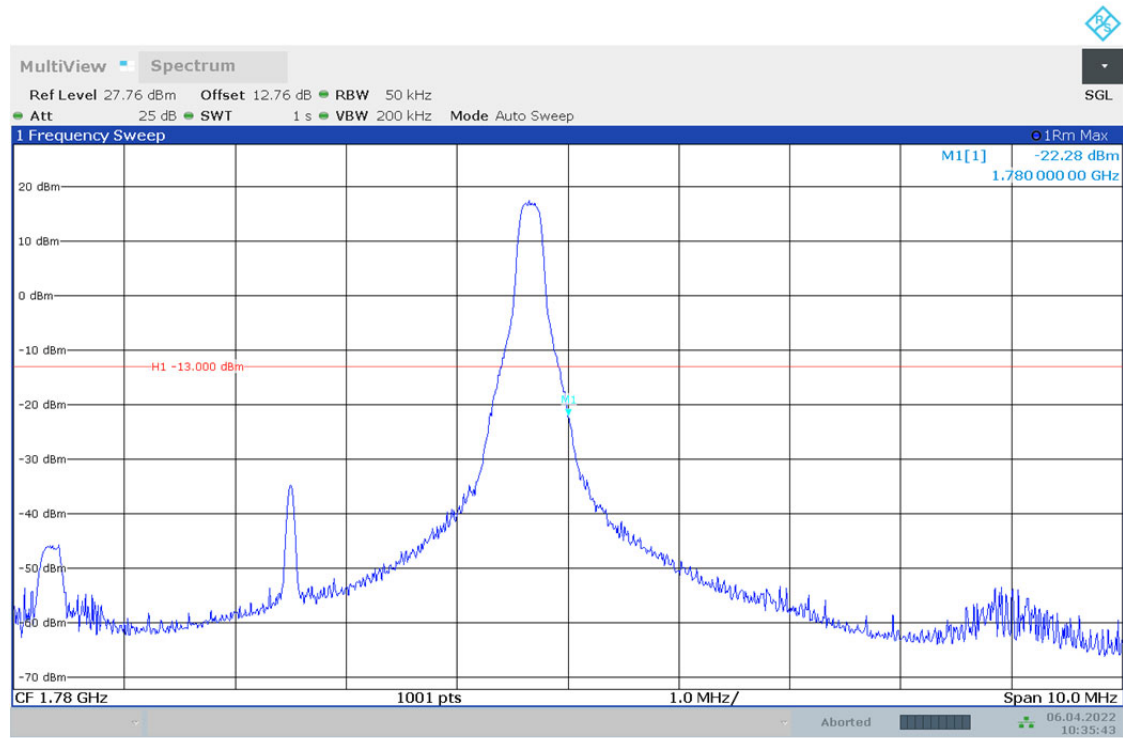
5.2.10 TM1_5MHZ_HCH_RB25#0



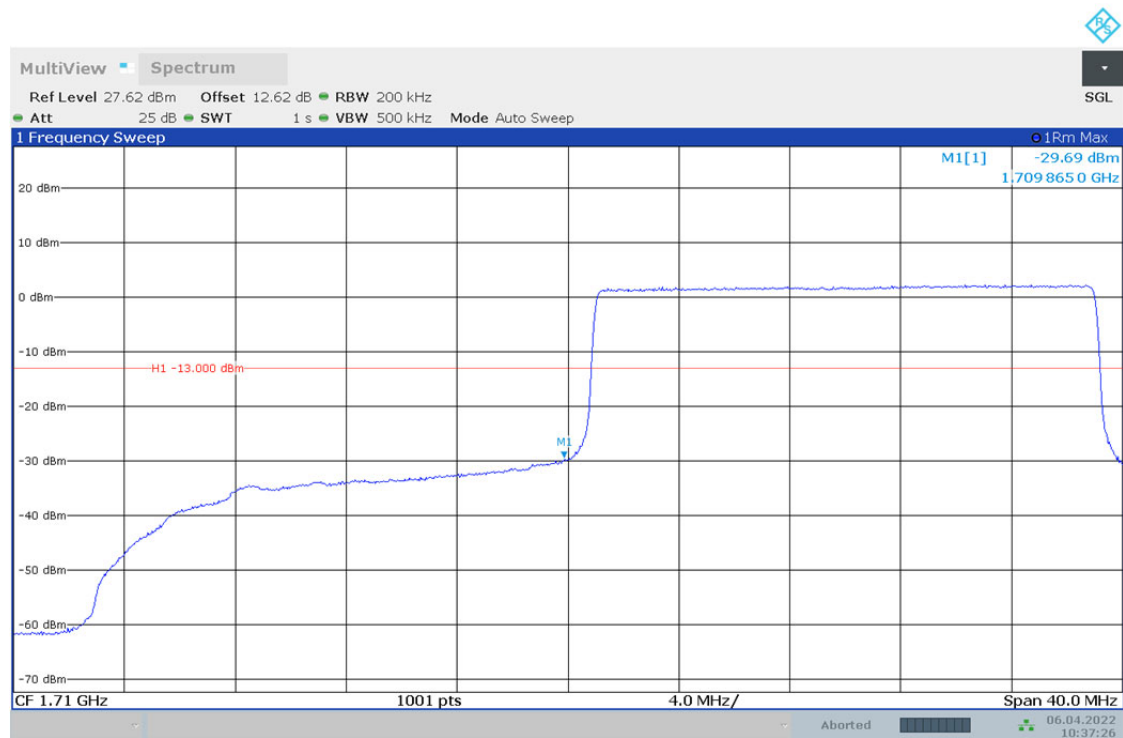
5.2.11 TM1_5MHZ_HCH_RB1#0



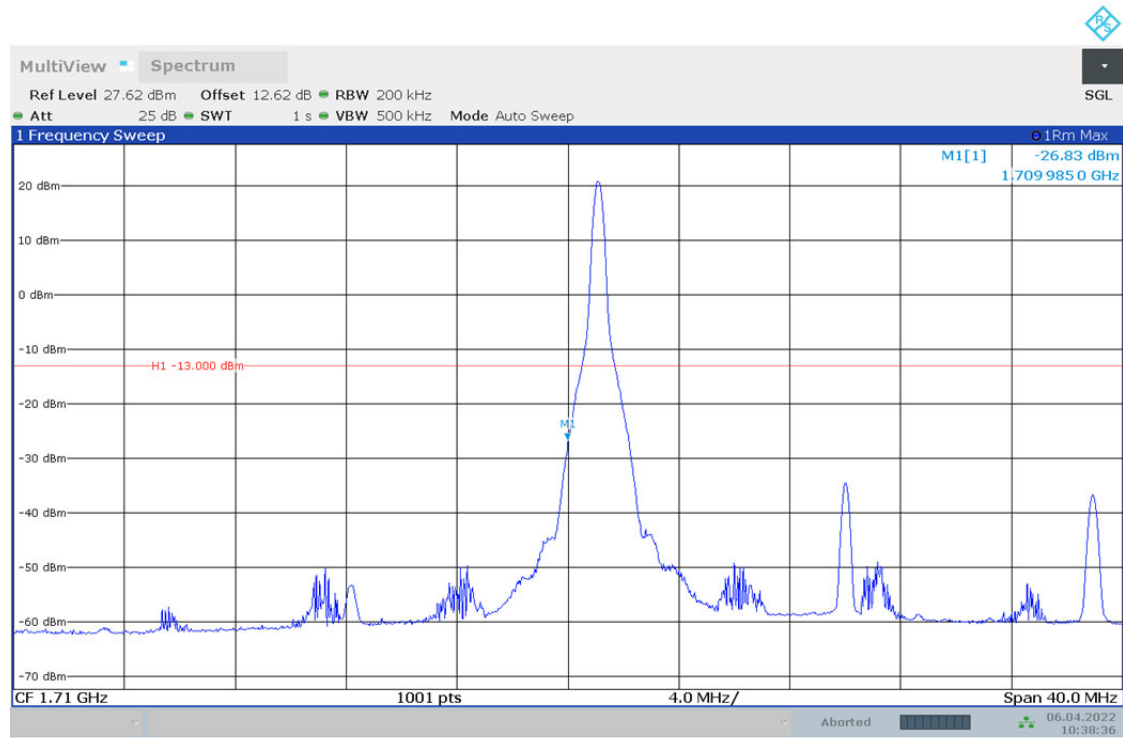
5.2.12 TM1_5MHZ_HCH_RB1#24



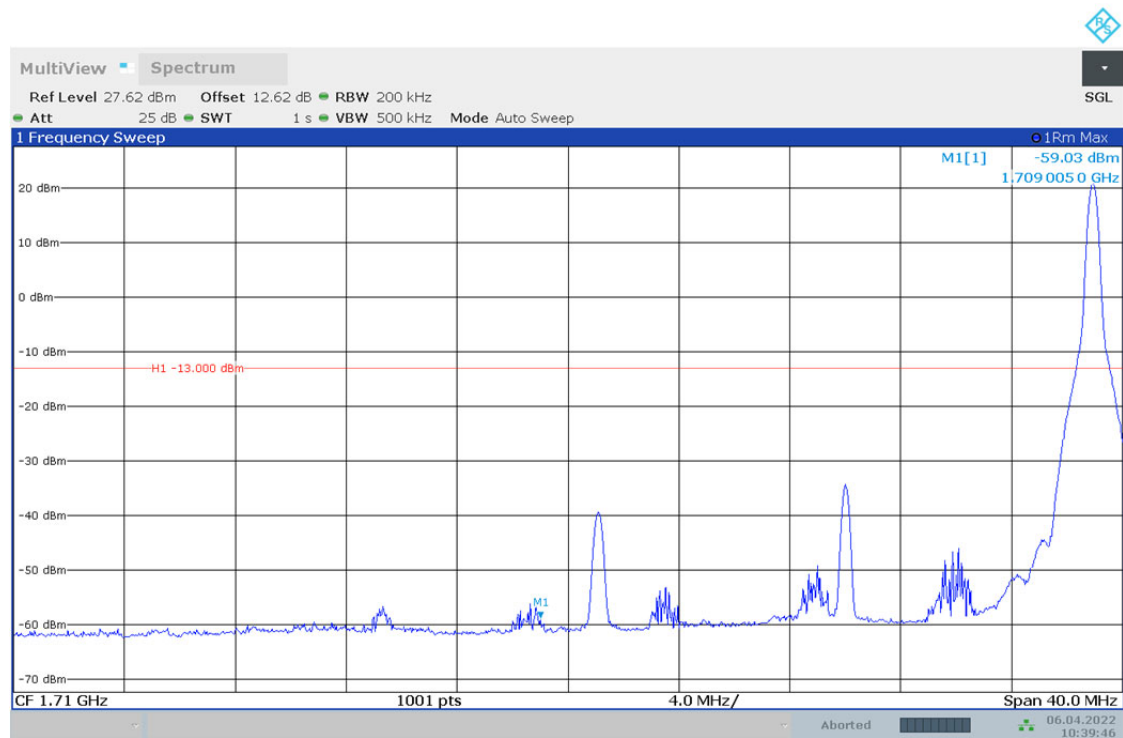
5.2.13 TM1_20MHZ_LCH_RB100#0



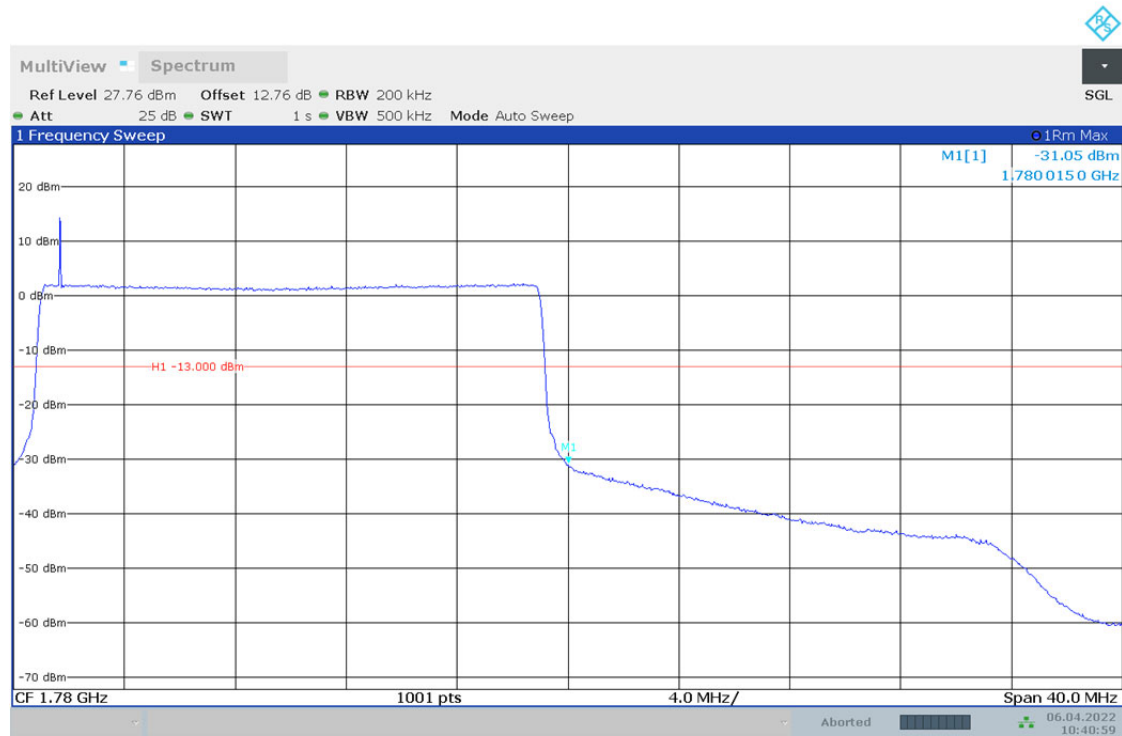
5.2.14 TM1_20MHZ_LCH_RB1#0



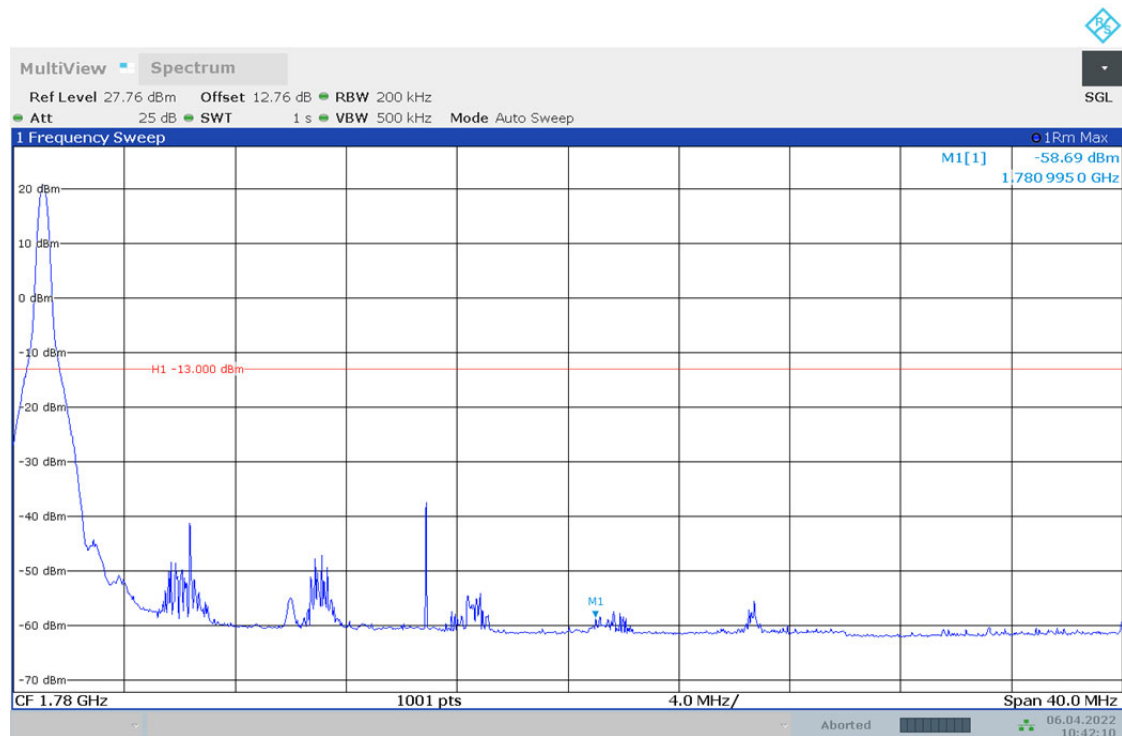
5.2.15 TM1_20MHZ_LCH_RB1#99



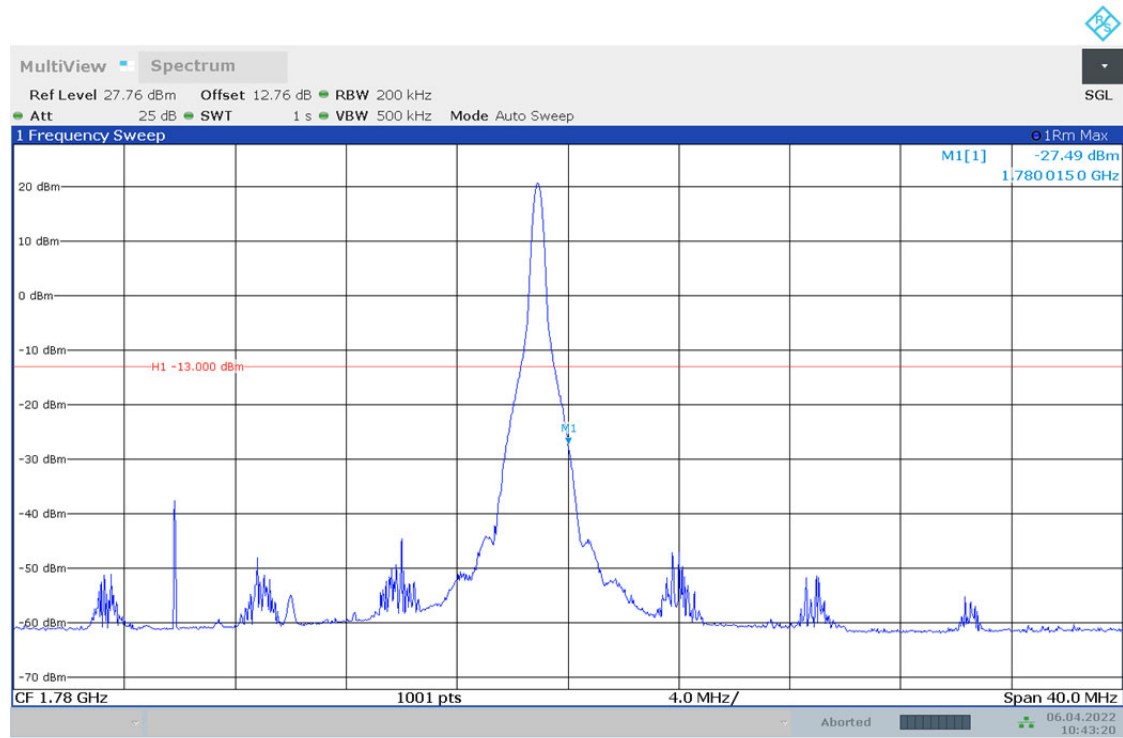
5.2.16 TM1_20MHZ_HCH_RB100#0



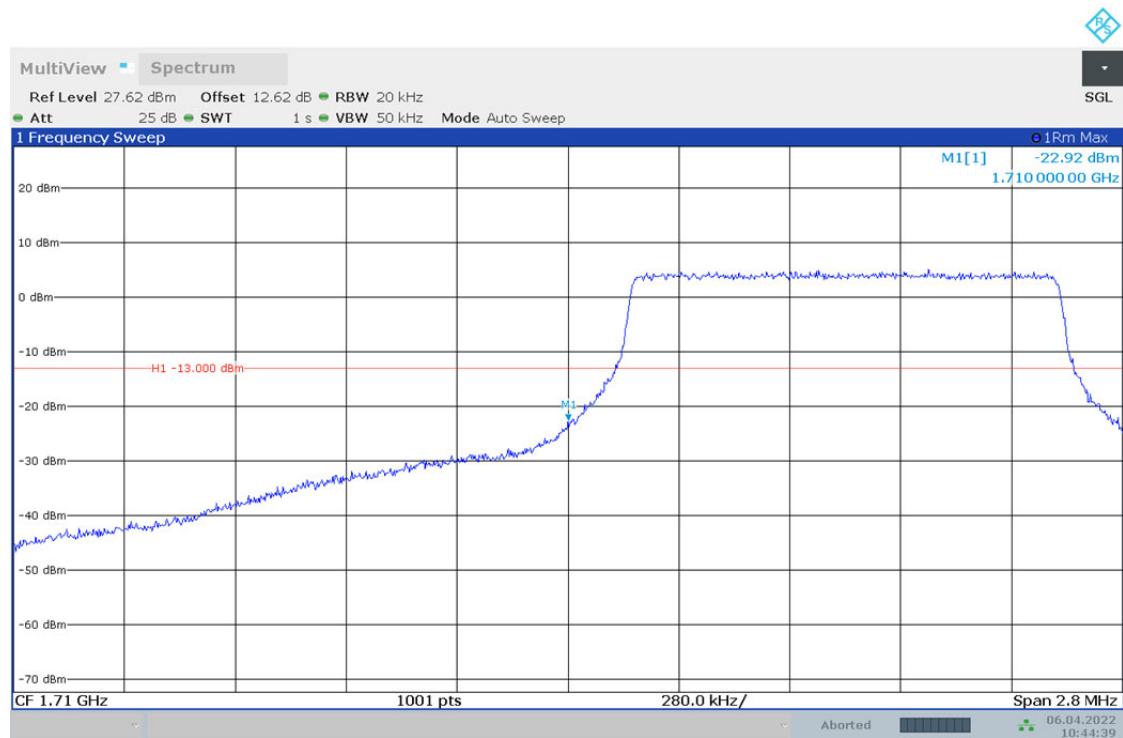
5.2.17 TM1_20MHZ_HCH_RB1#0



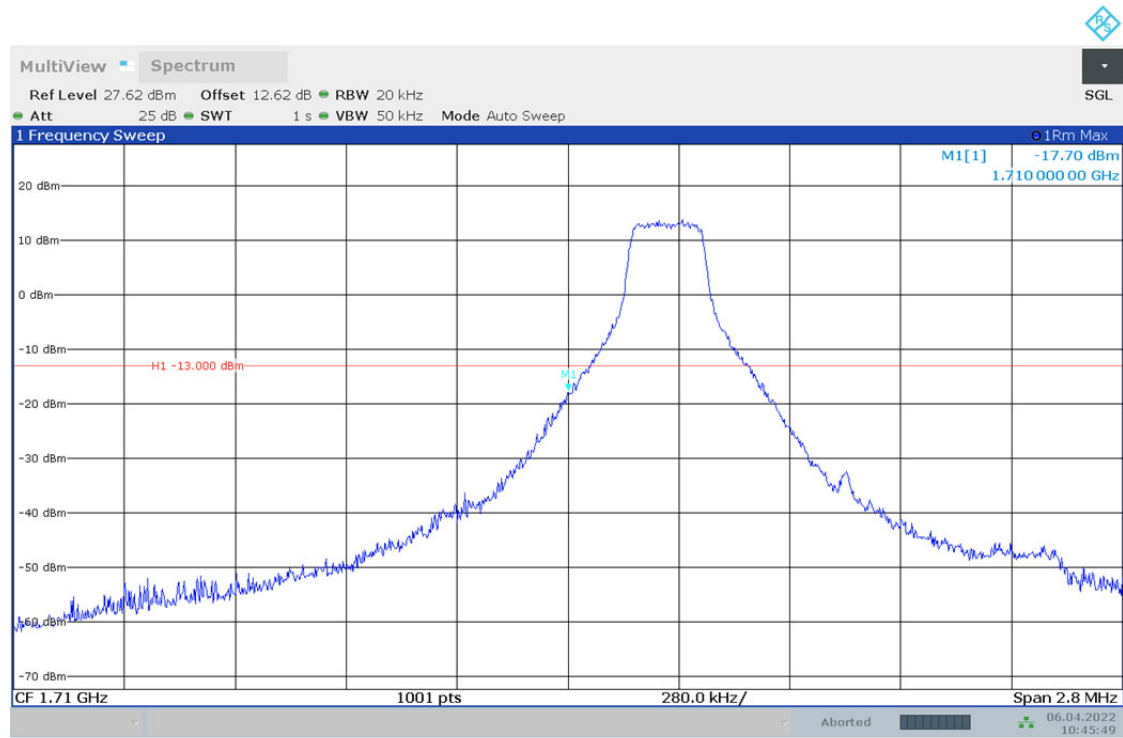
5.2.18 TM1_20MHZ_HCH_RB1#99



5.2.19 TM2_1.4MHZ_LCH_RB6#0



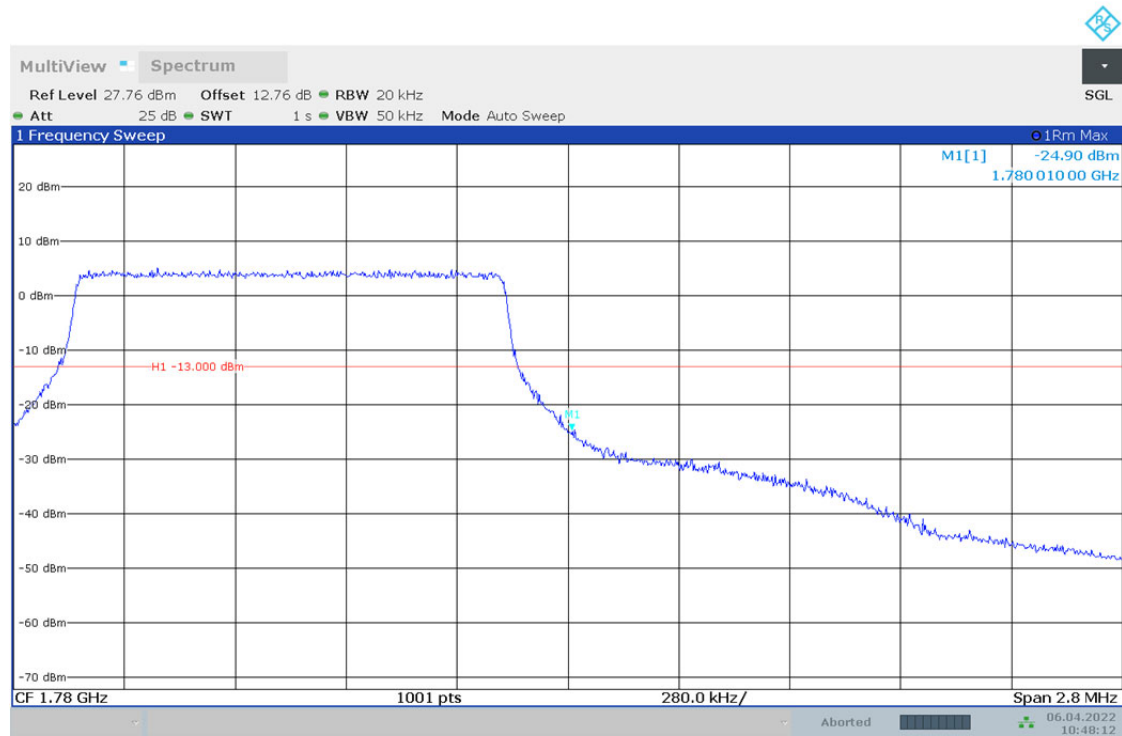
5.2.20 TM2_1.4MHZ_LCH_RB1#0



5.2.21 TM2_1.4MHZ_LCH_RB1#5

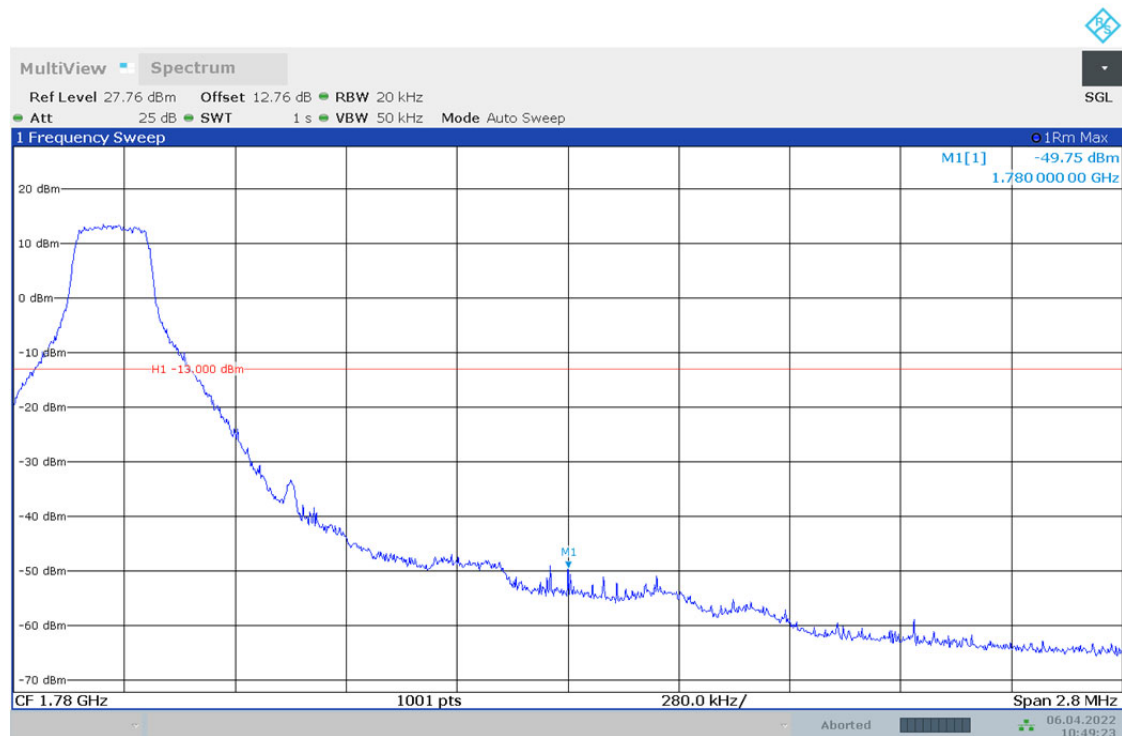


5.2.22 TM2_1.4MHZ_HCH_RB6#0



10:48:12 06.04.2022

5.2.23 TM2_1.4MHZ_HCH_RB1#0



10:49:23 06.04.2022