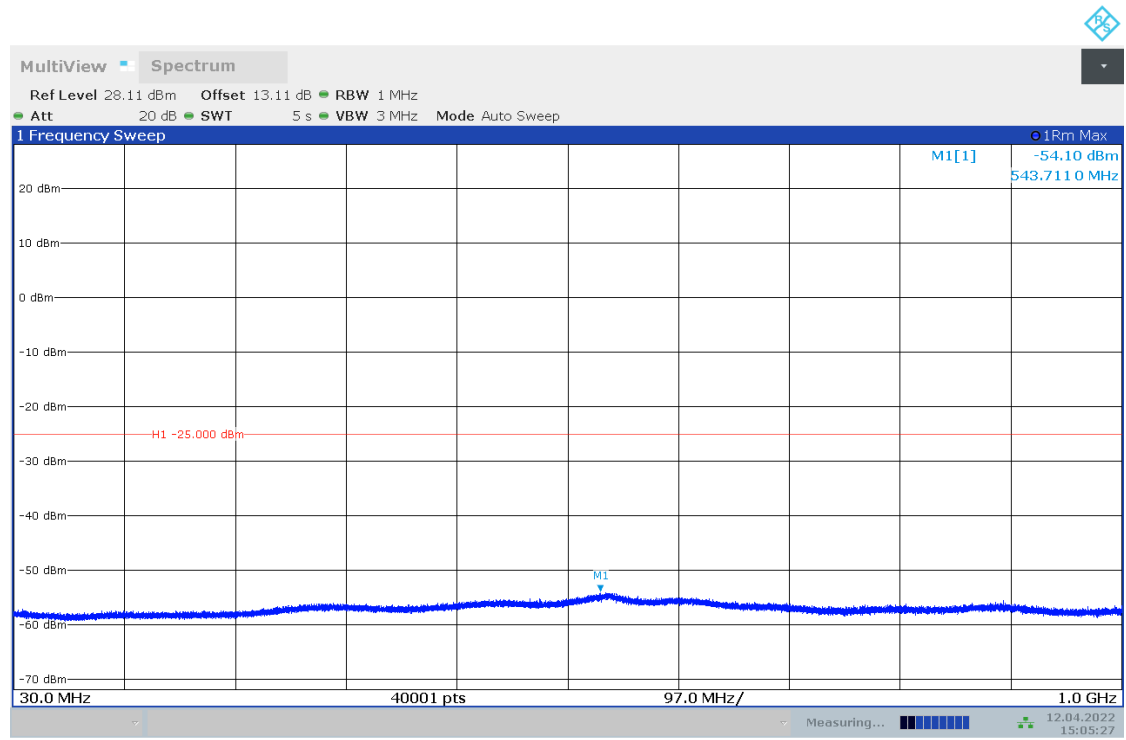


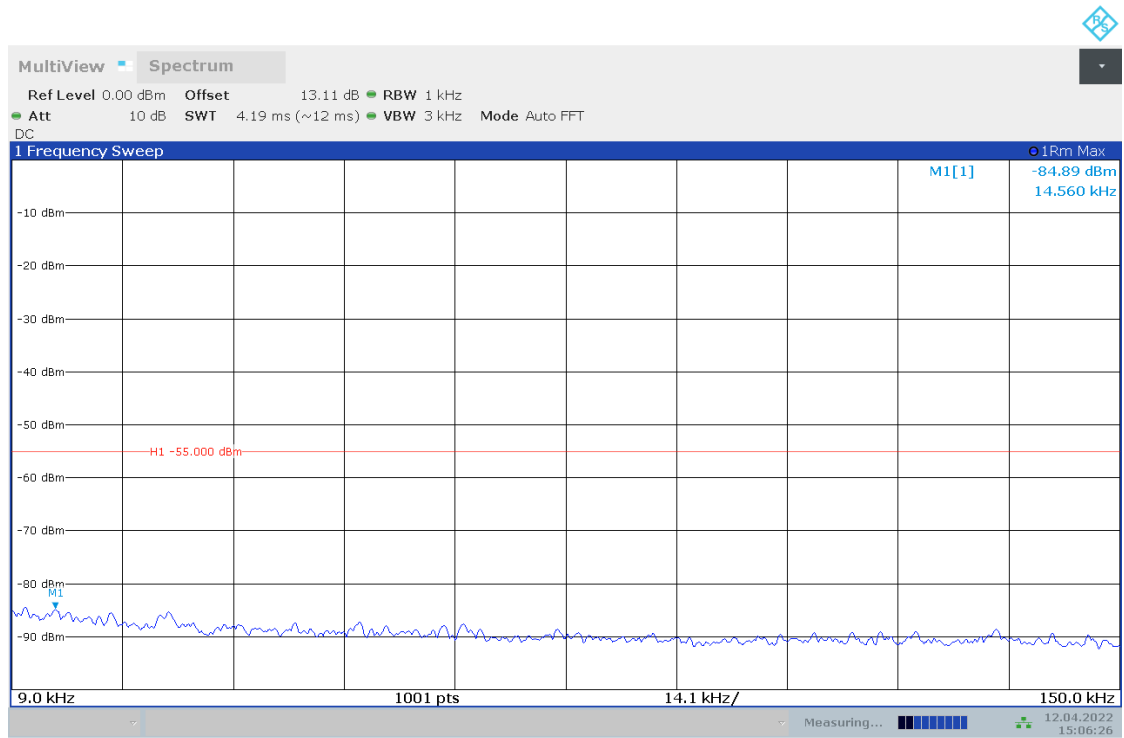


15:05:28 12.04.2022

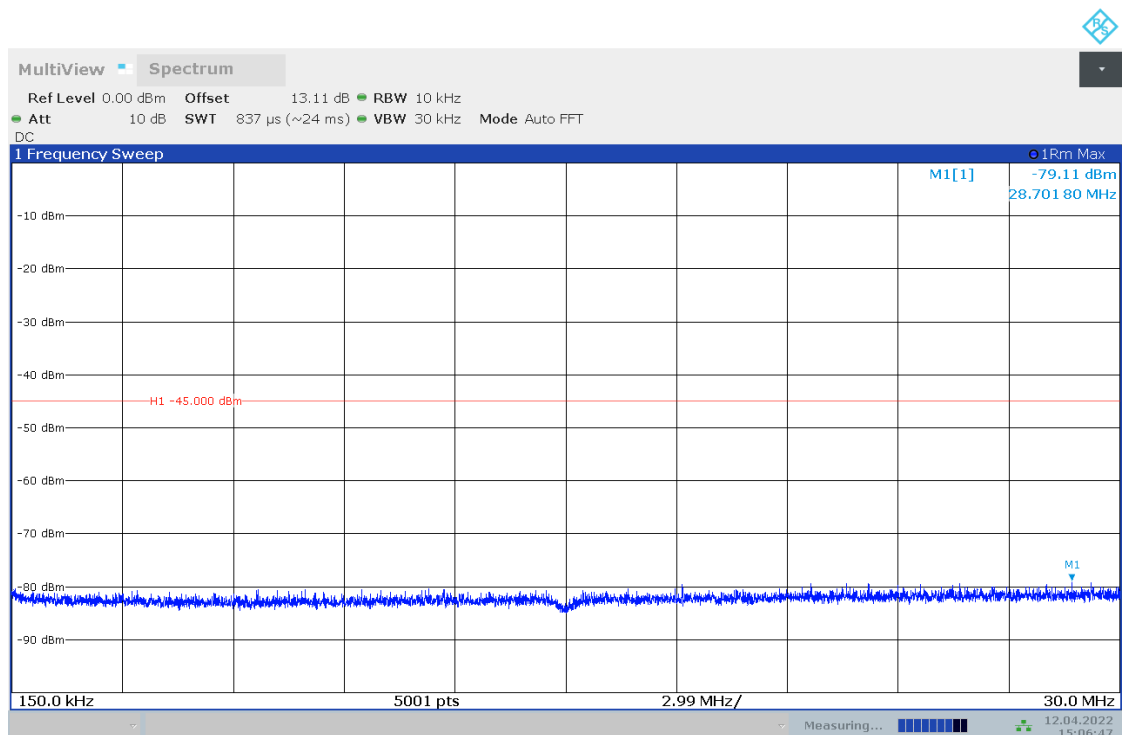


15:05:50 12.04.2022

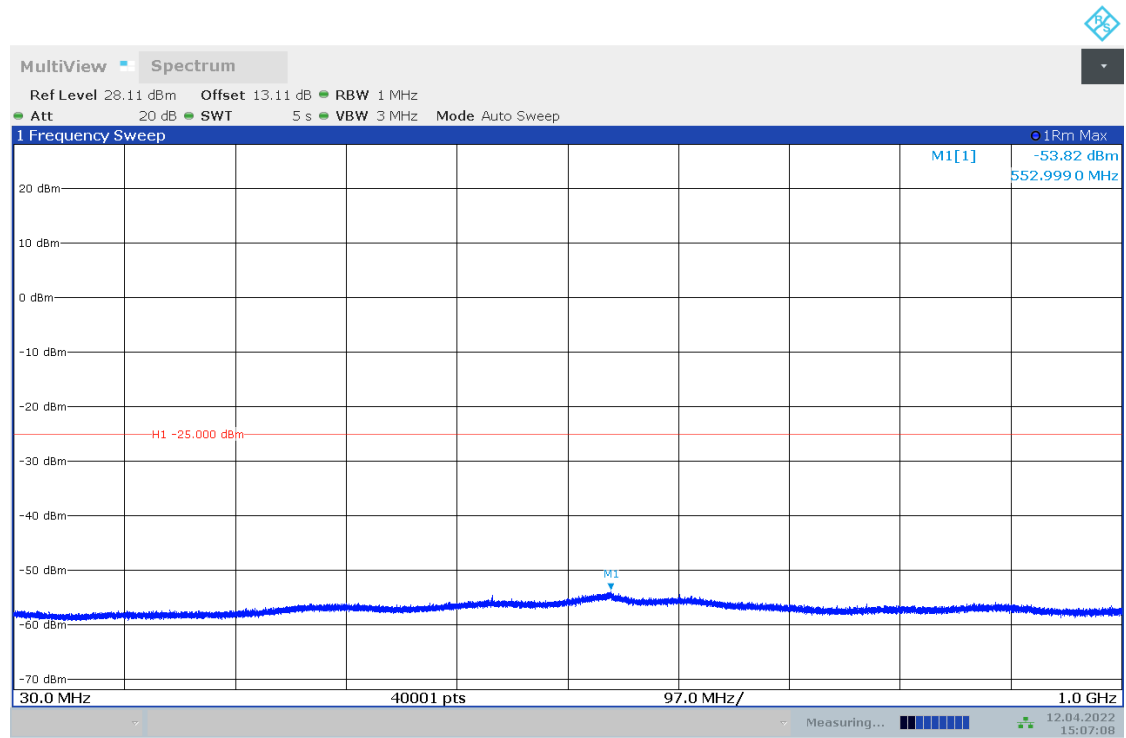
4.2.17 TM2_HCH_15MHZ+15MHZ_PCCRB75#0_SCCRB75#0



15:06:27 12.04.2022



15:06:48 12.04.2022



15:07:09 12.04.2022

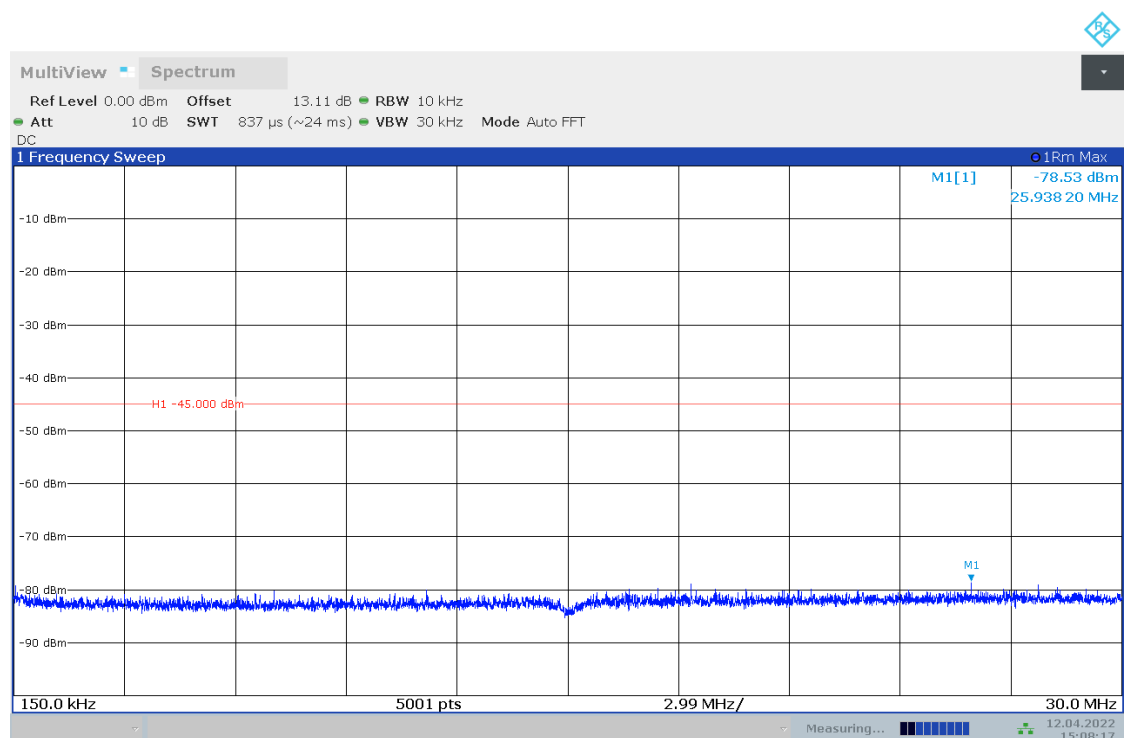


15:07:30 12.04.2022

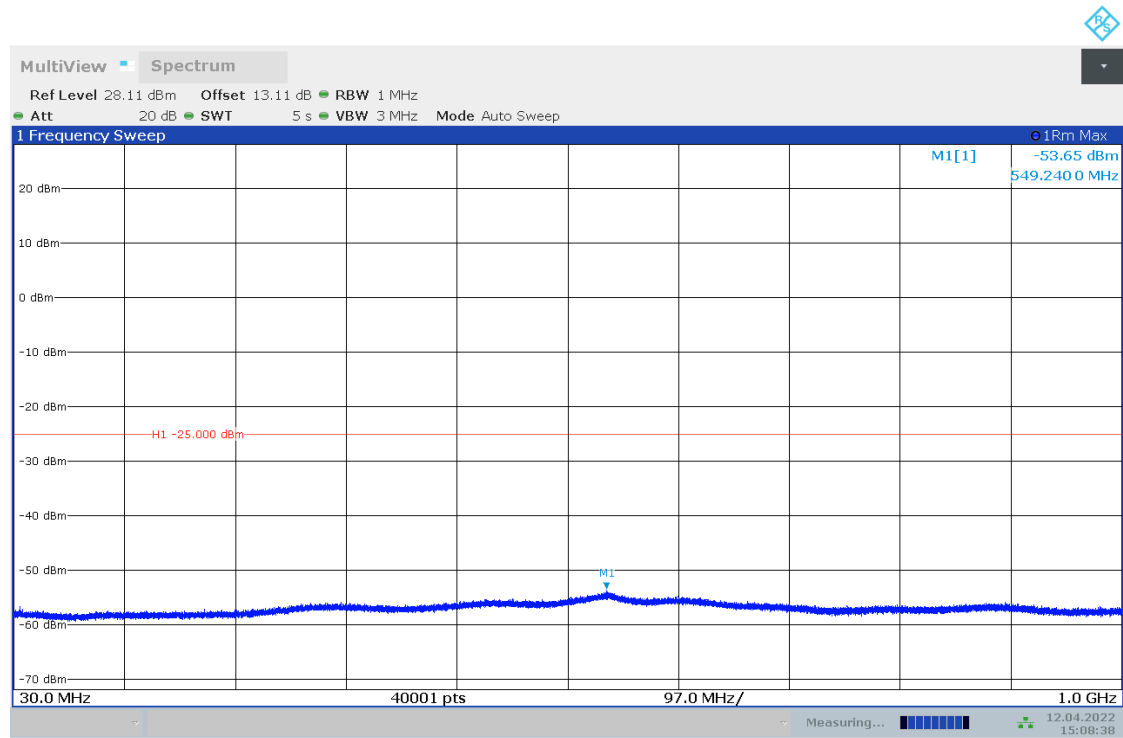
4.2.18 TM2_HCH_15MHZ+15MHZ_PCCRB1#0_SCCRB1#74



15:07:56 12.04.2022



15:08:17 12.04.2022

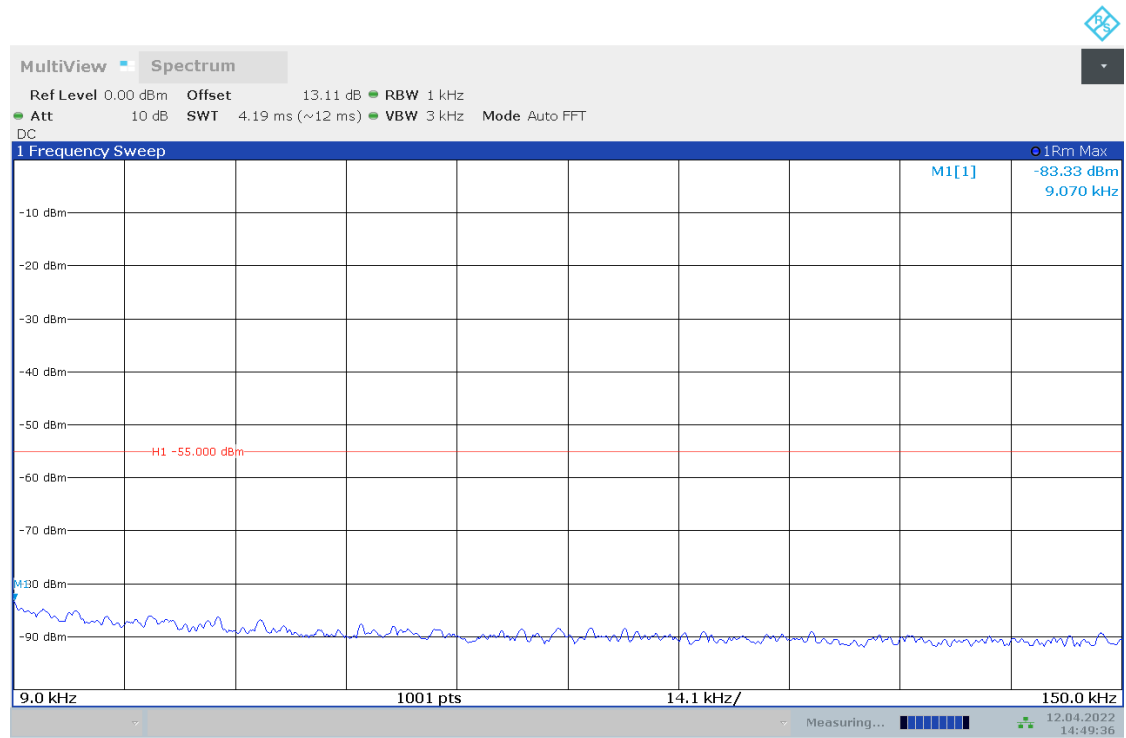


15:08:39 12.04.2022

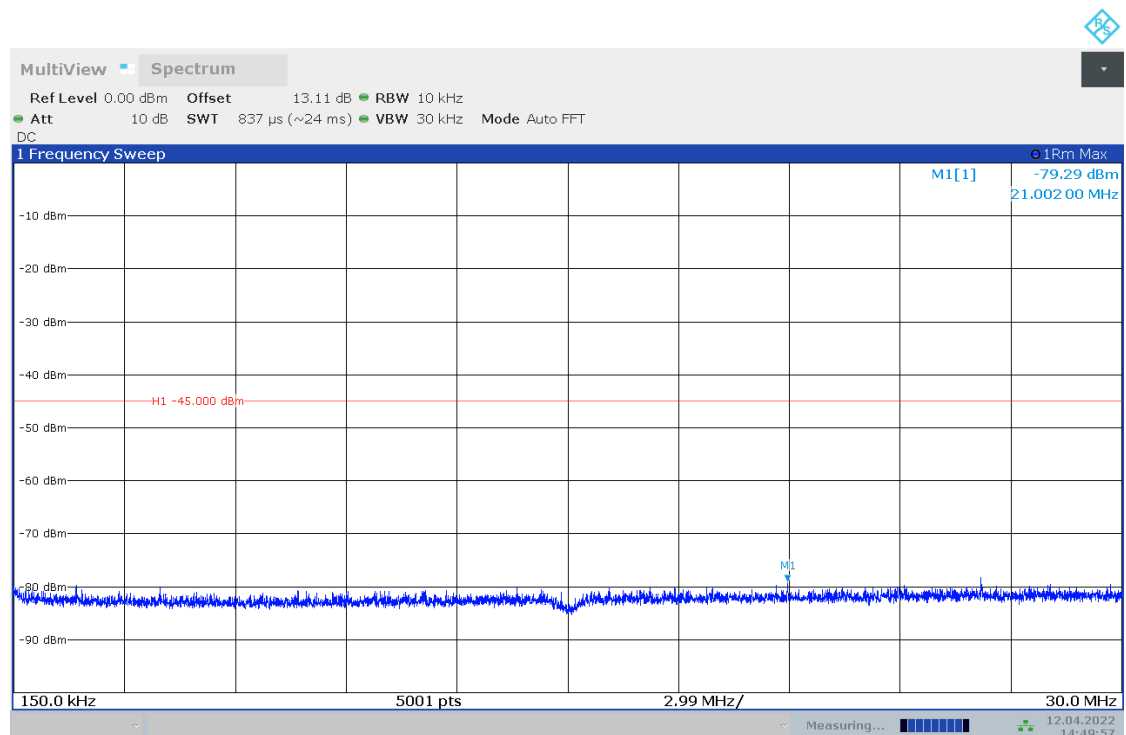


15:09:00 12.04.2022

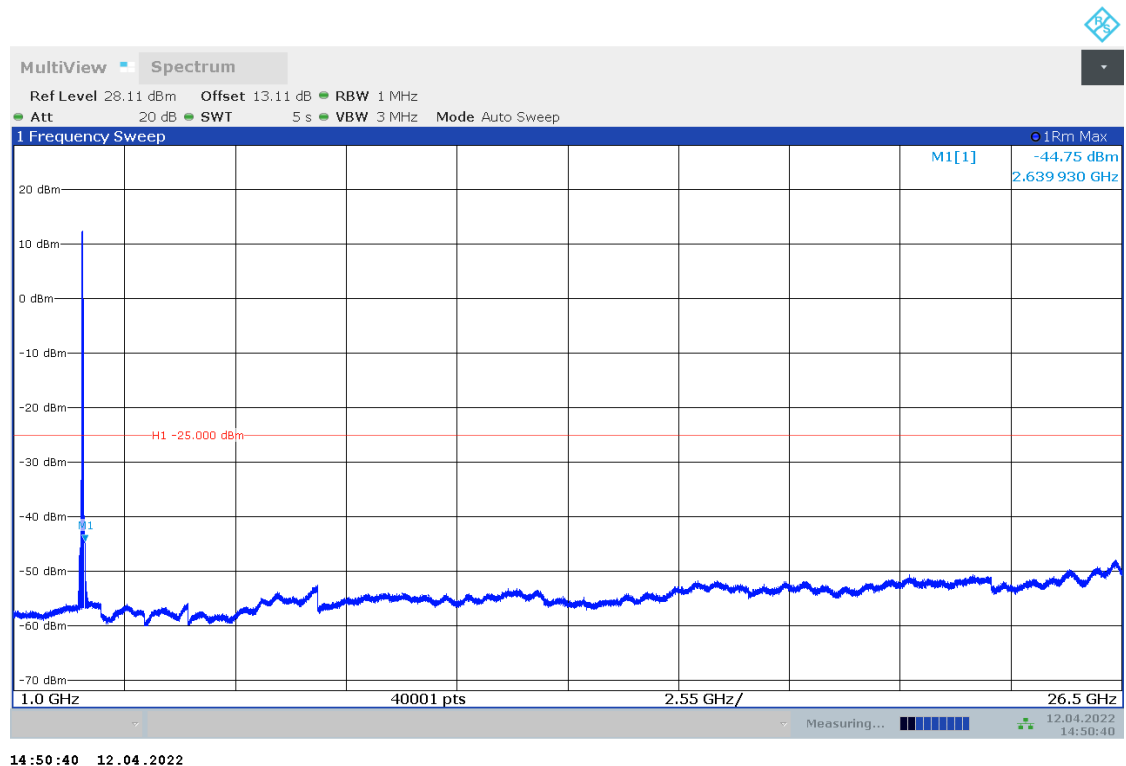
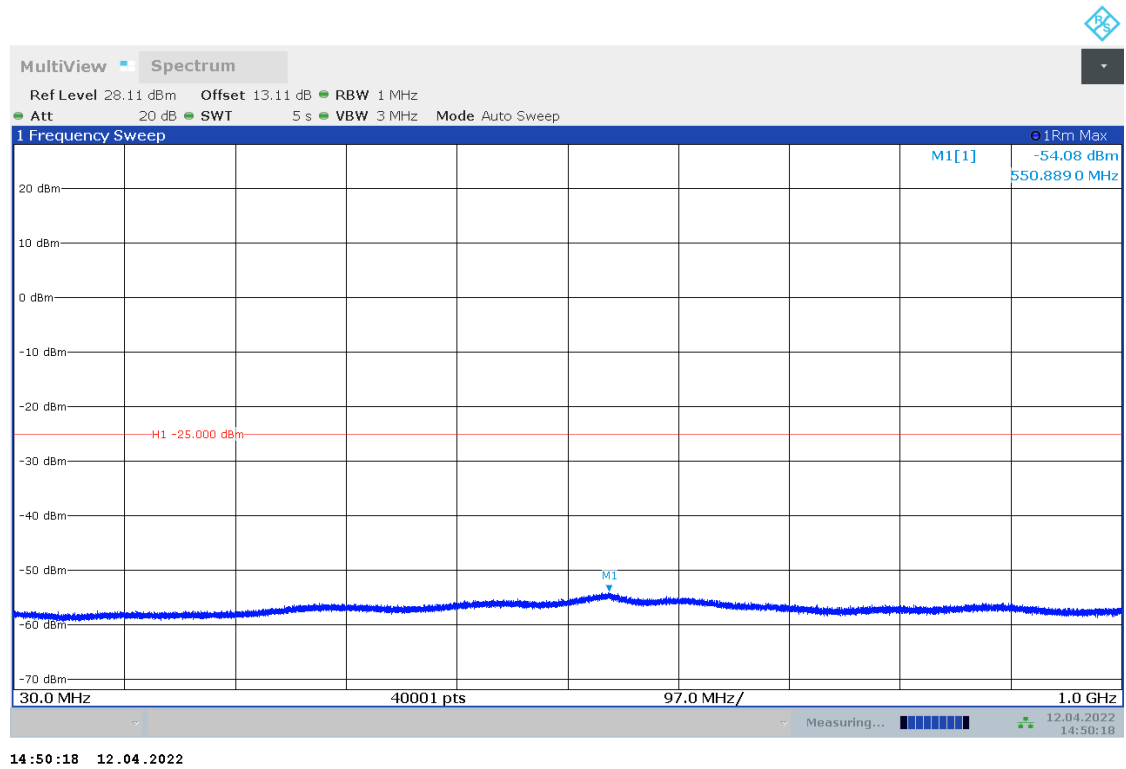
4.2.19 TM2_LCH_20MHZ+20MHZ_PCCRB100#0_SCCRB100#0



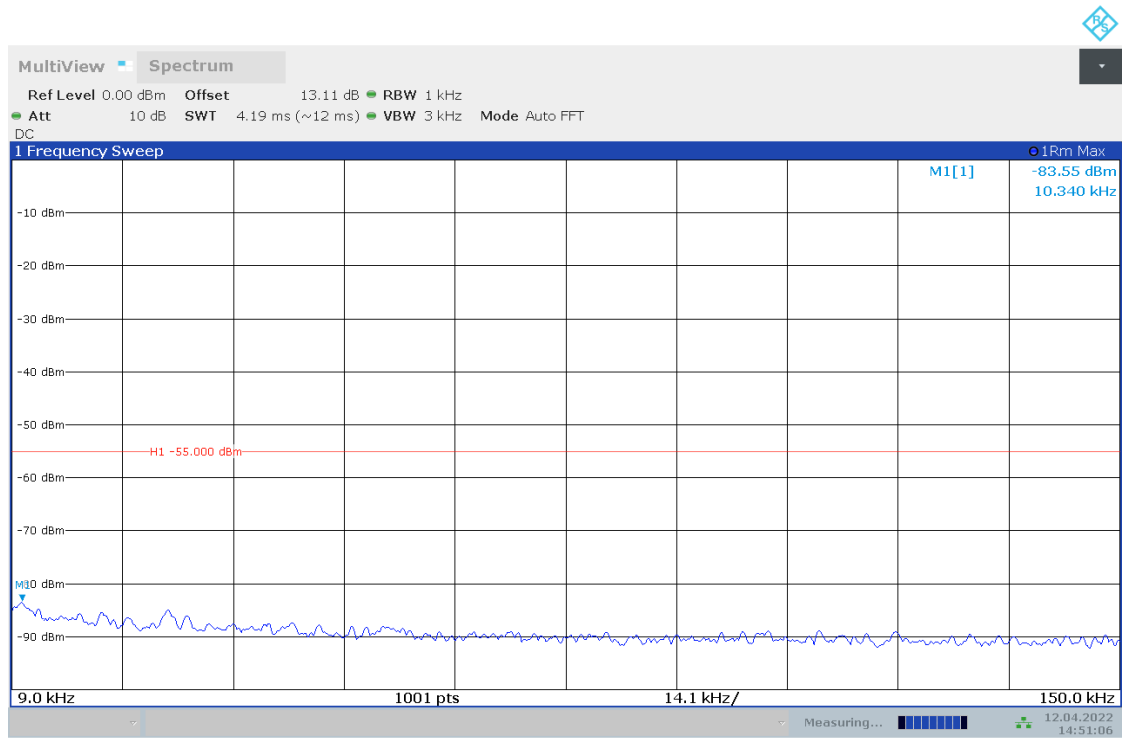
14:49:36 12.04.2022



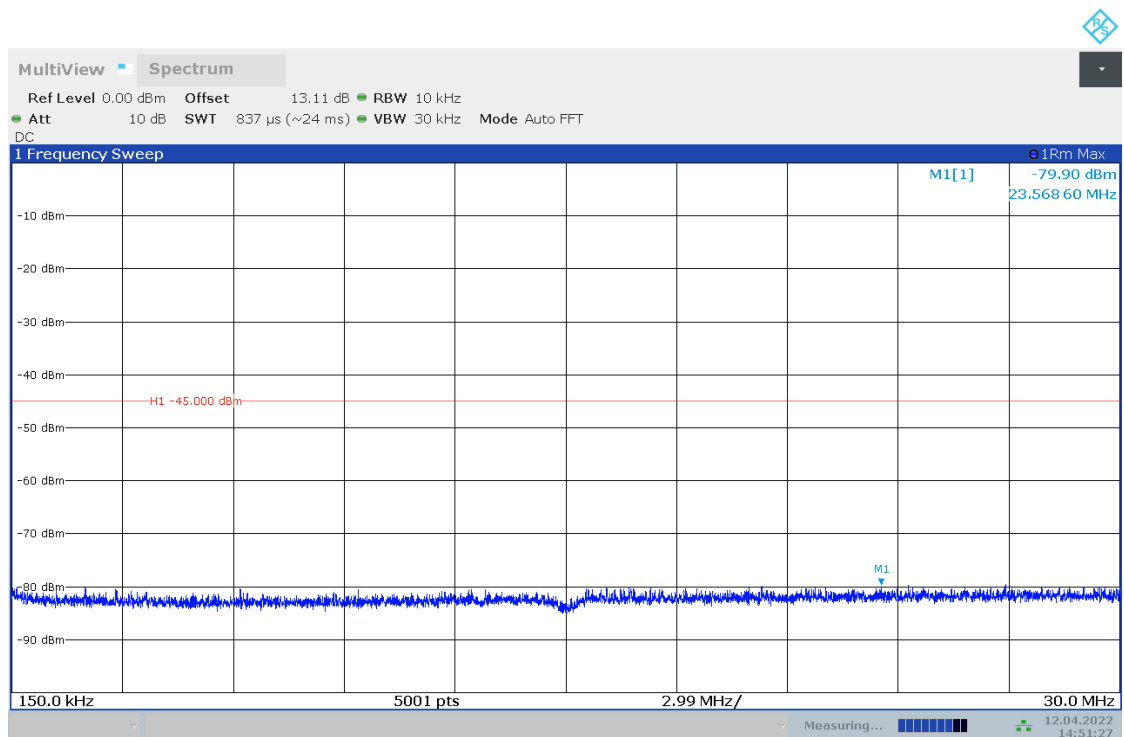
14:49:57 12.04.2022



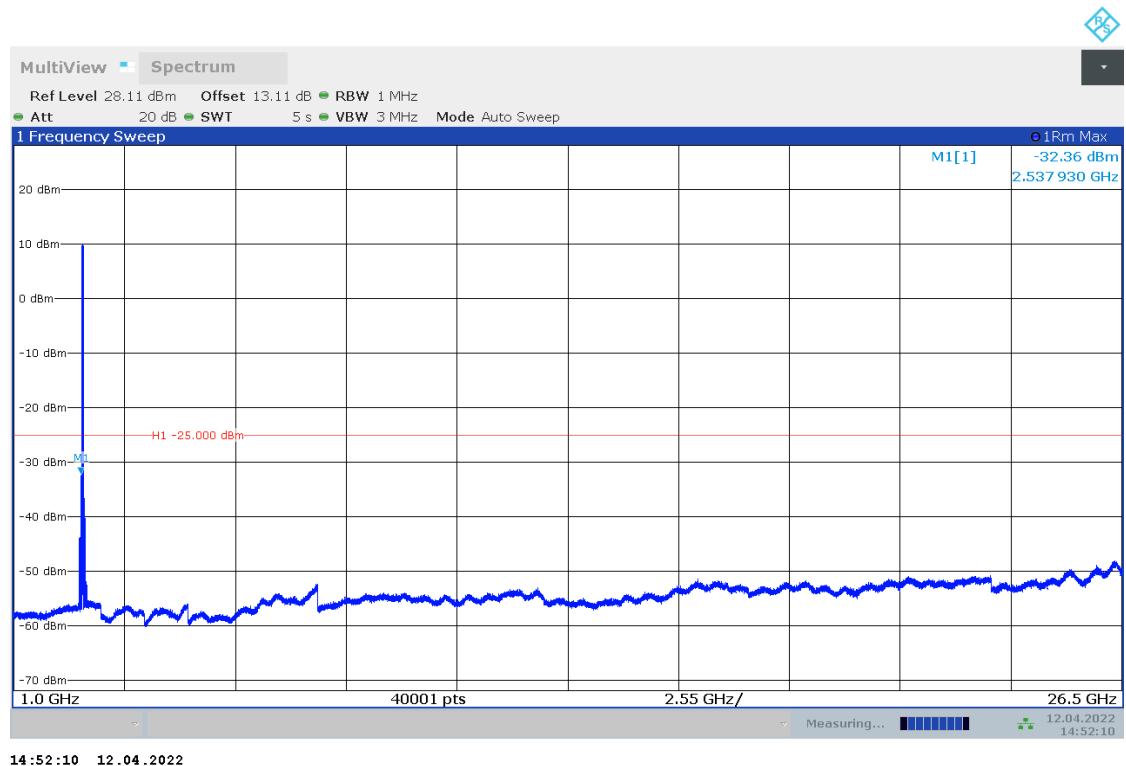
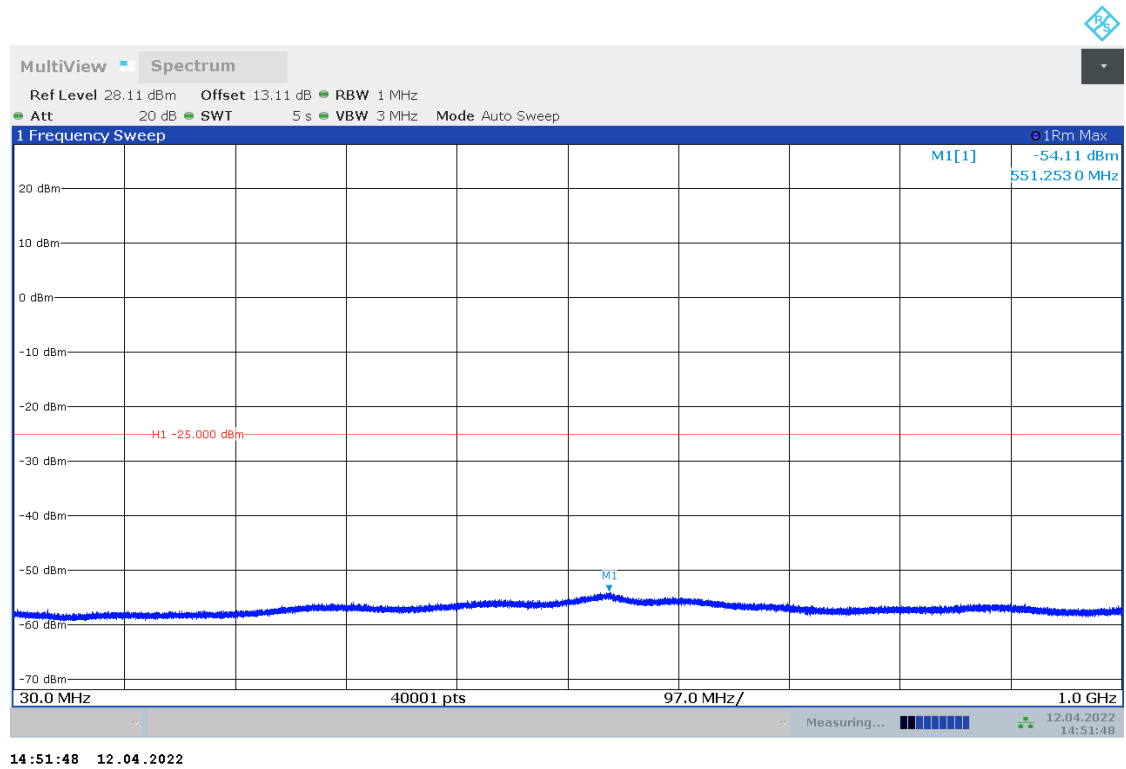
4.2.20 TM2_LCH_20MHZ+20MHZ_PCCRB1#0_SCCRB1#99



14:51:06 12.04.2022



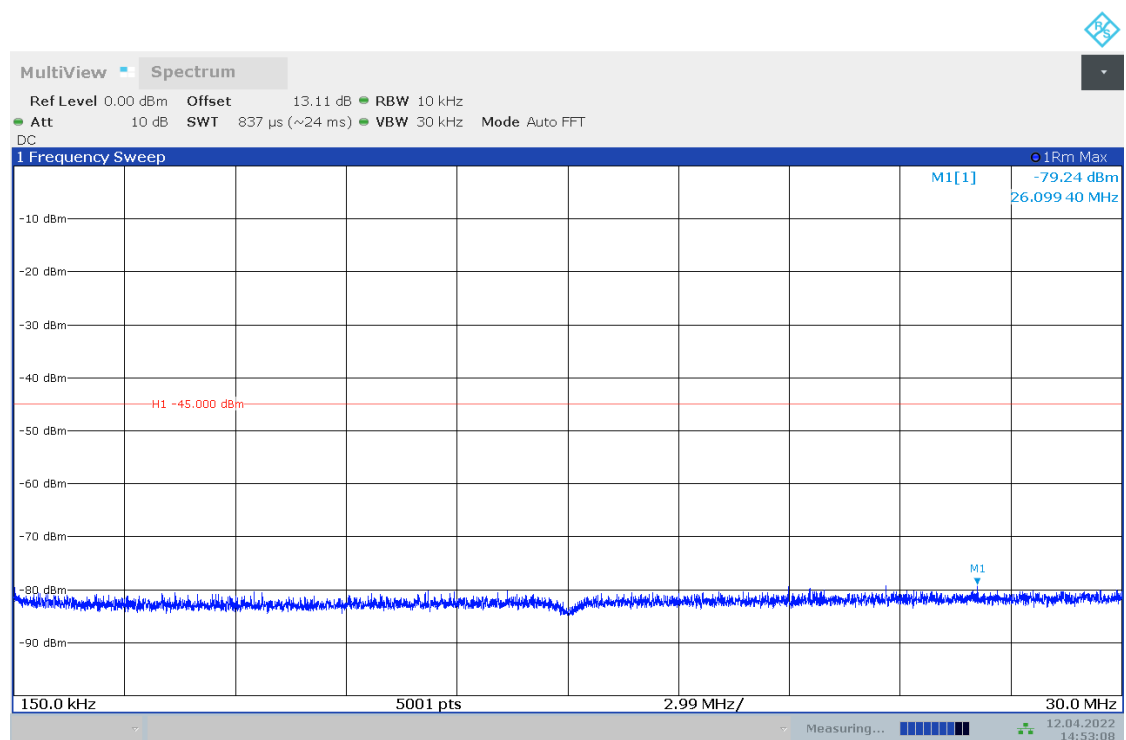
14:51:27 12.04.2022



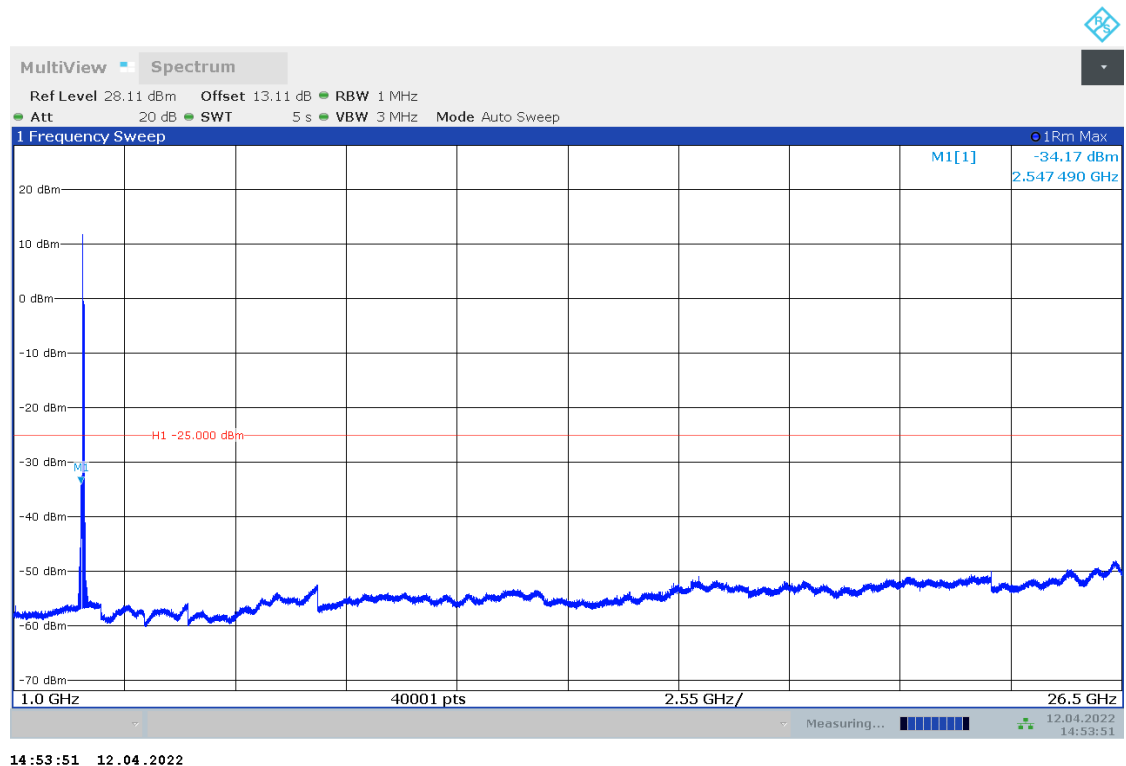
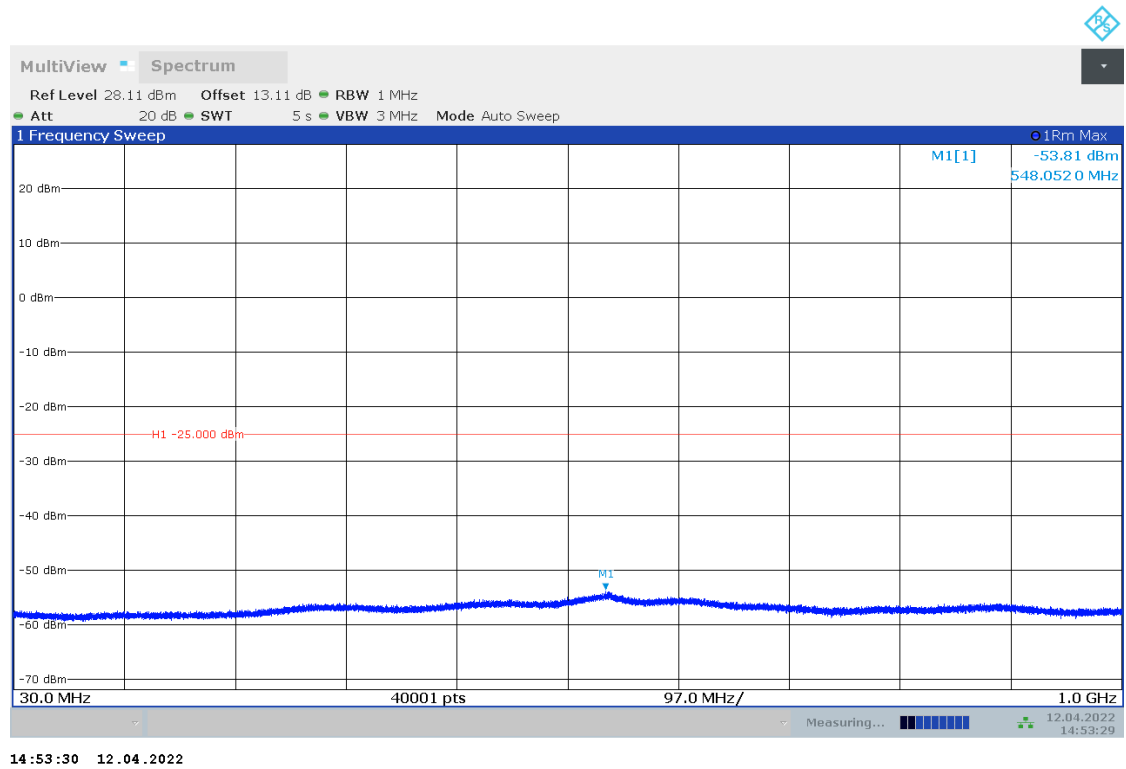
4.2.21 TM2_MCH_20MHZ+20MHZ_PCCRB100#0_SCCRB100#0



14:52:47 12.04.2022



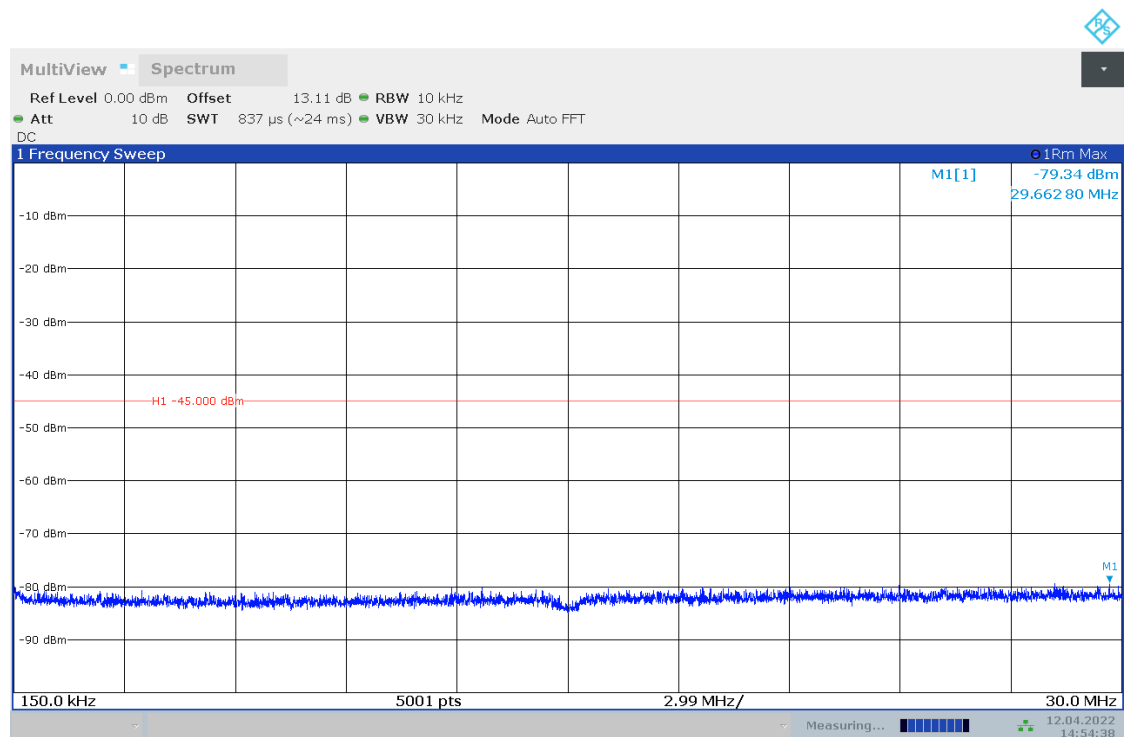
14:53:08 12.04.2022



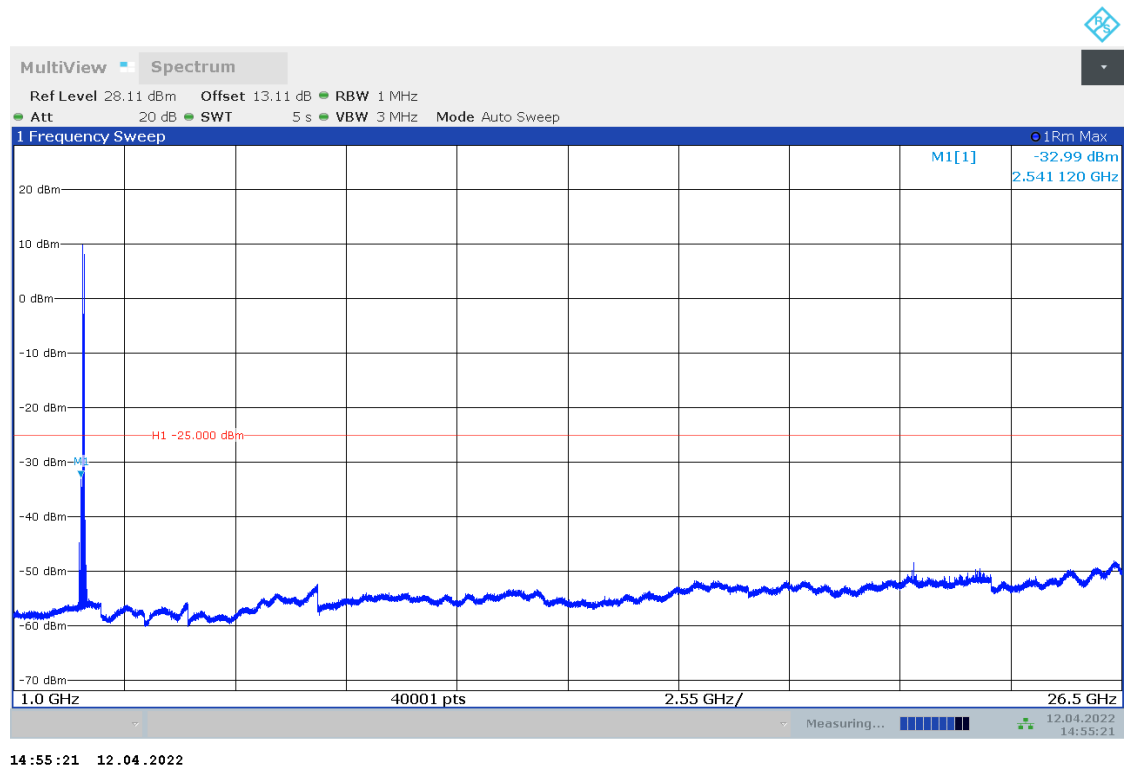
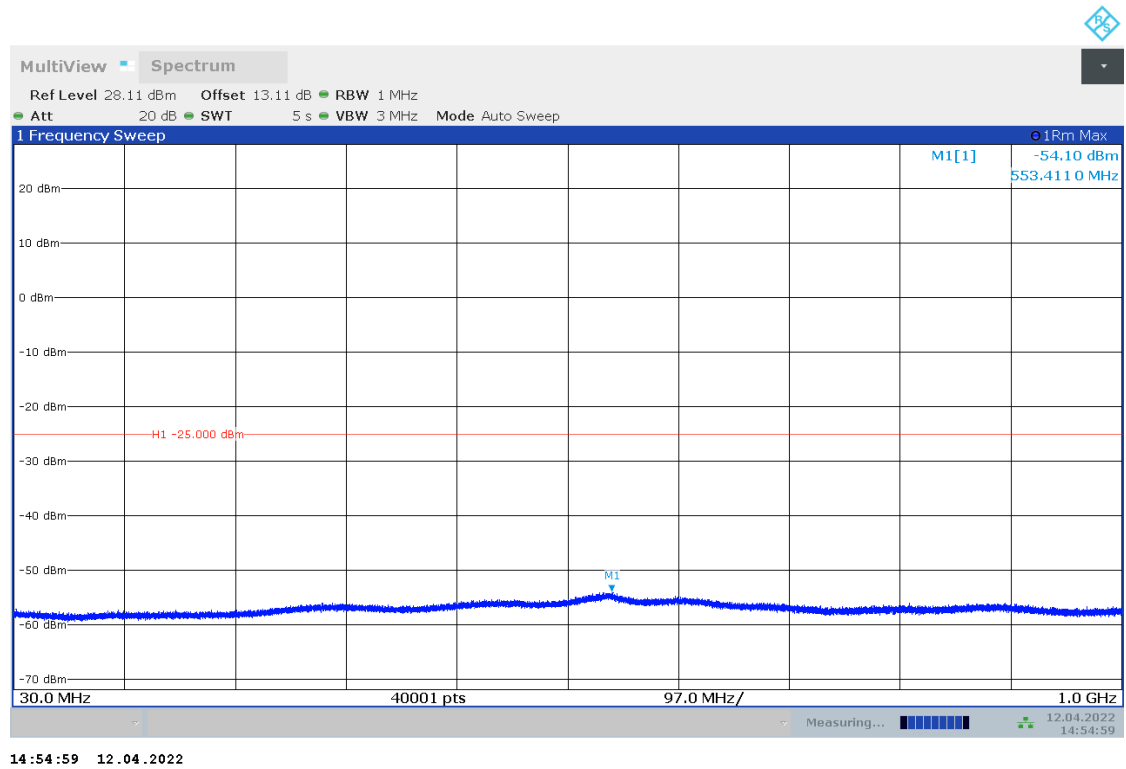
4.2.22 TM2_MCH_20MHZ+20MHZ_PCCRB1#0_SCCRB1#99



14:54:17 12.04.2022



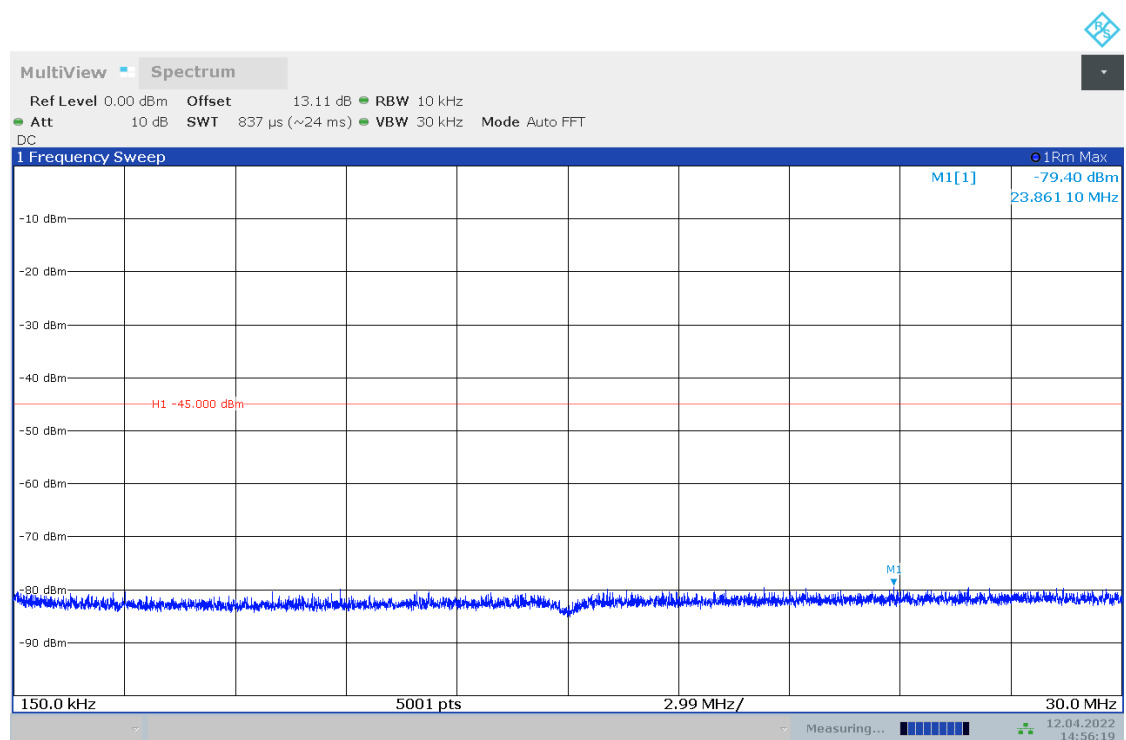
14:54:38 12.04.2022



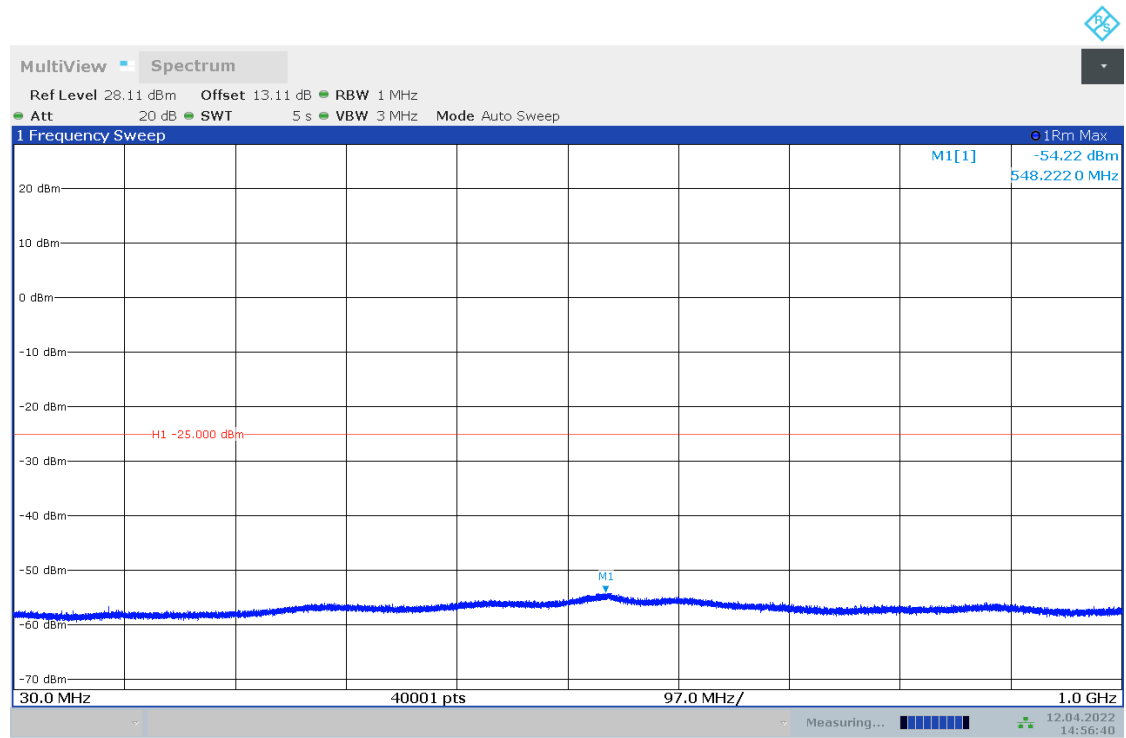
4.2.23 TM2_HCH_20MHZ+20MHZ_PCCRB100#0_SCCRB100#0



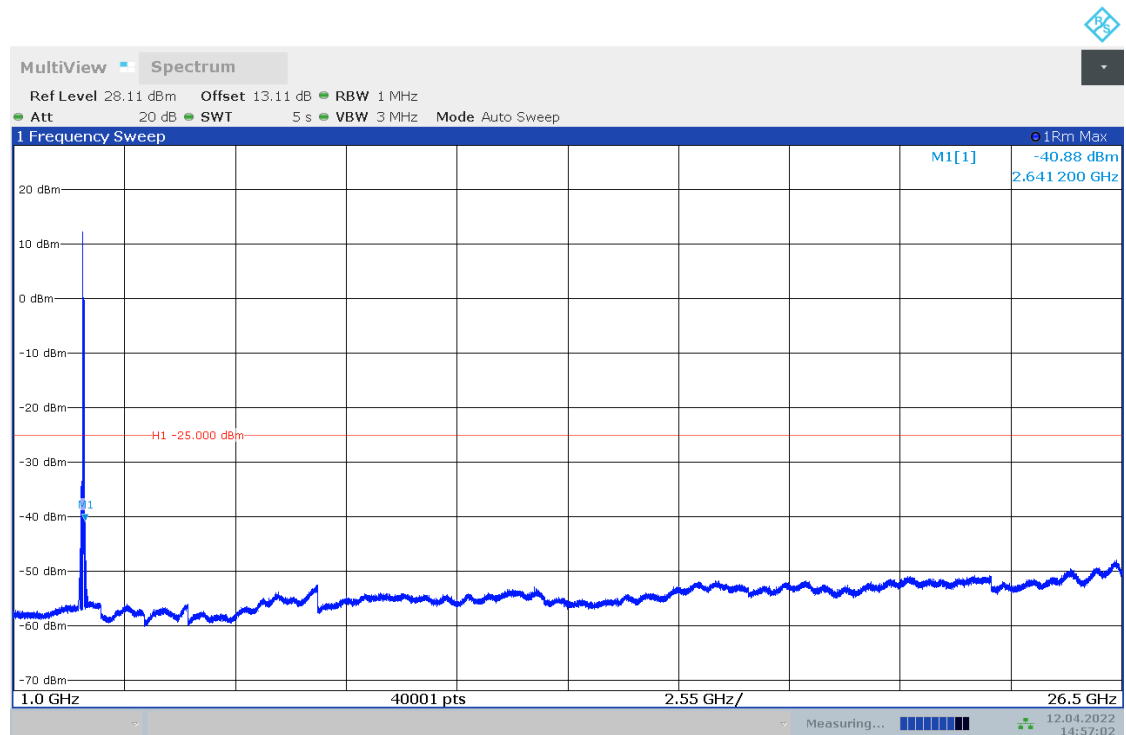
14:55:58 12.04.2022



14:56:19 12.04.2022



14:56:40 12.04.2022

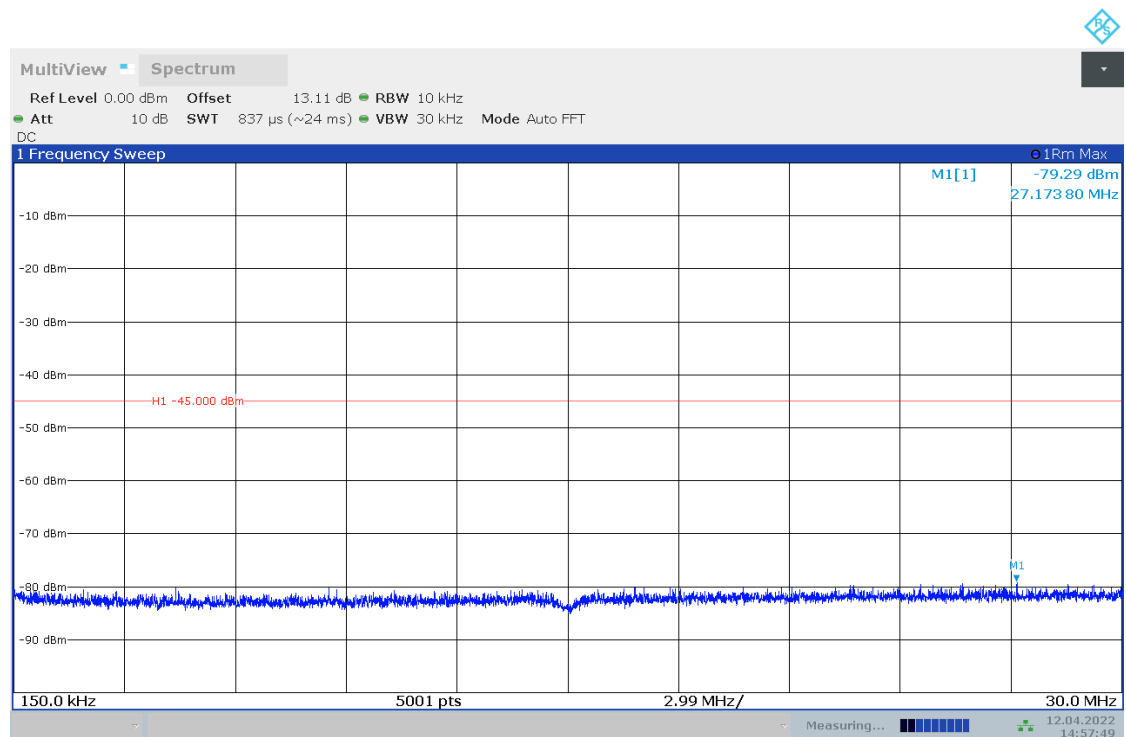


14:57:02 12.04.2022

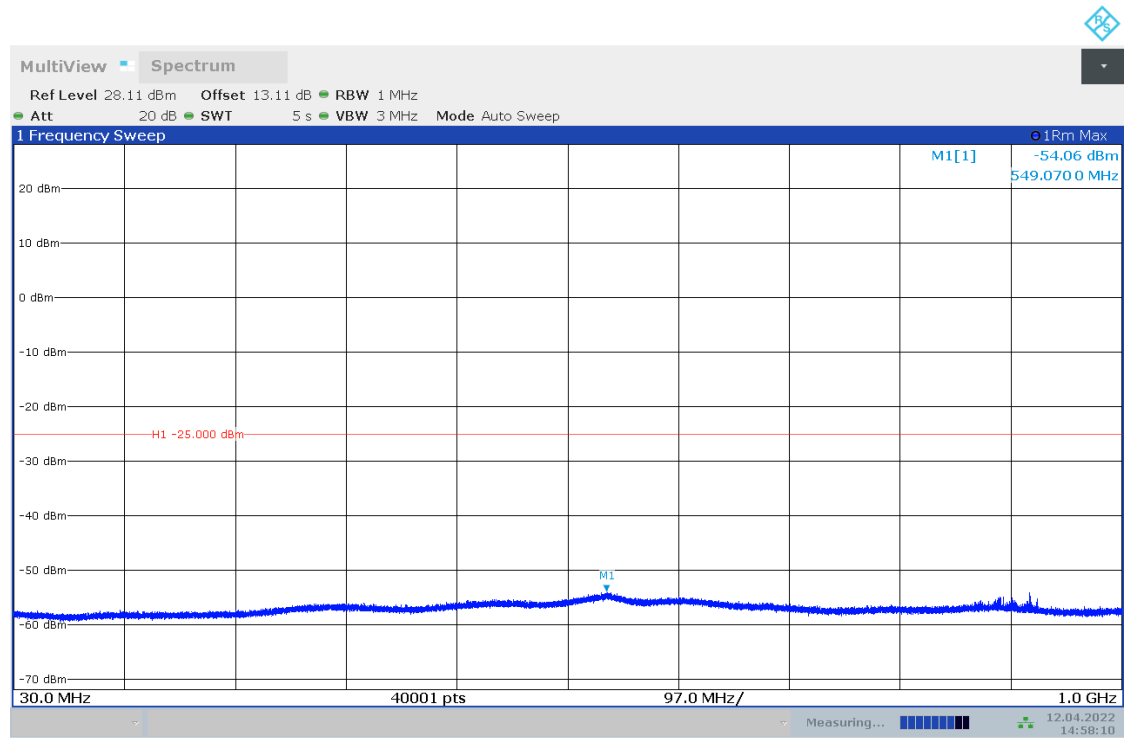
4.2.24 TM2_HCH_20MHZ+20MHZ_PCCRB1#0_SCCRB1#99



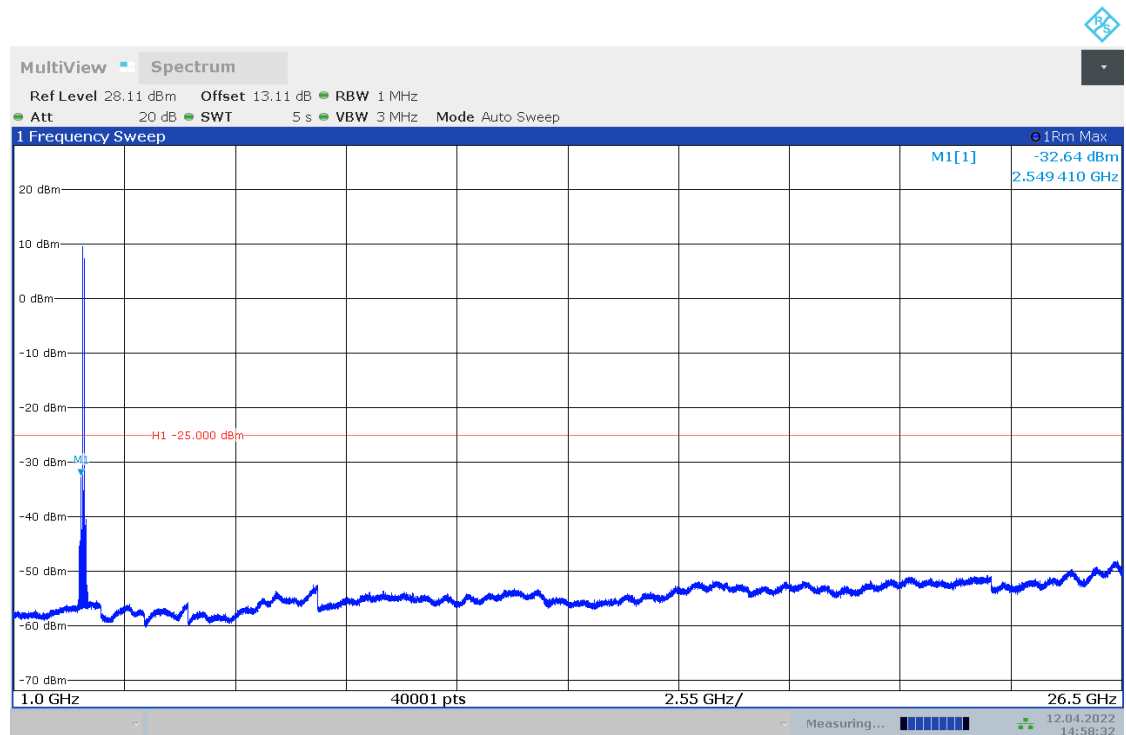
14:57:28 12.04.2022



14:57:49 12.04.2022



14:58:11 12.04.2022



14:58:32 12.04.2022

End of Test Report