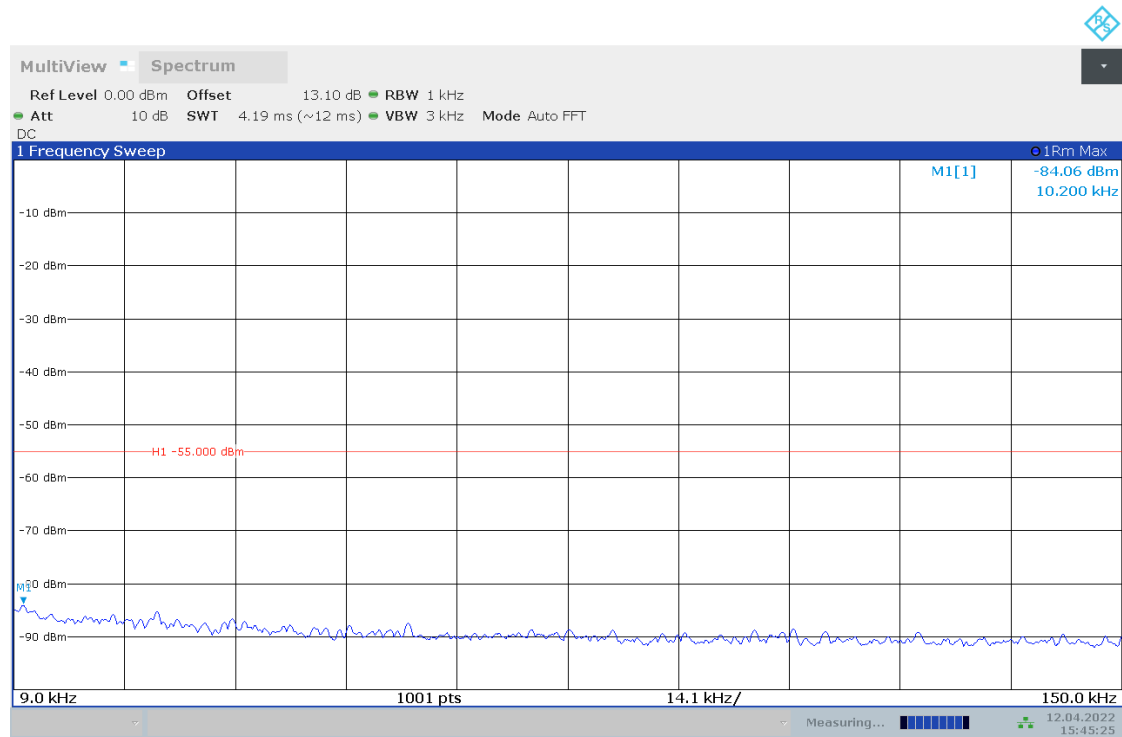
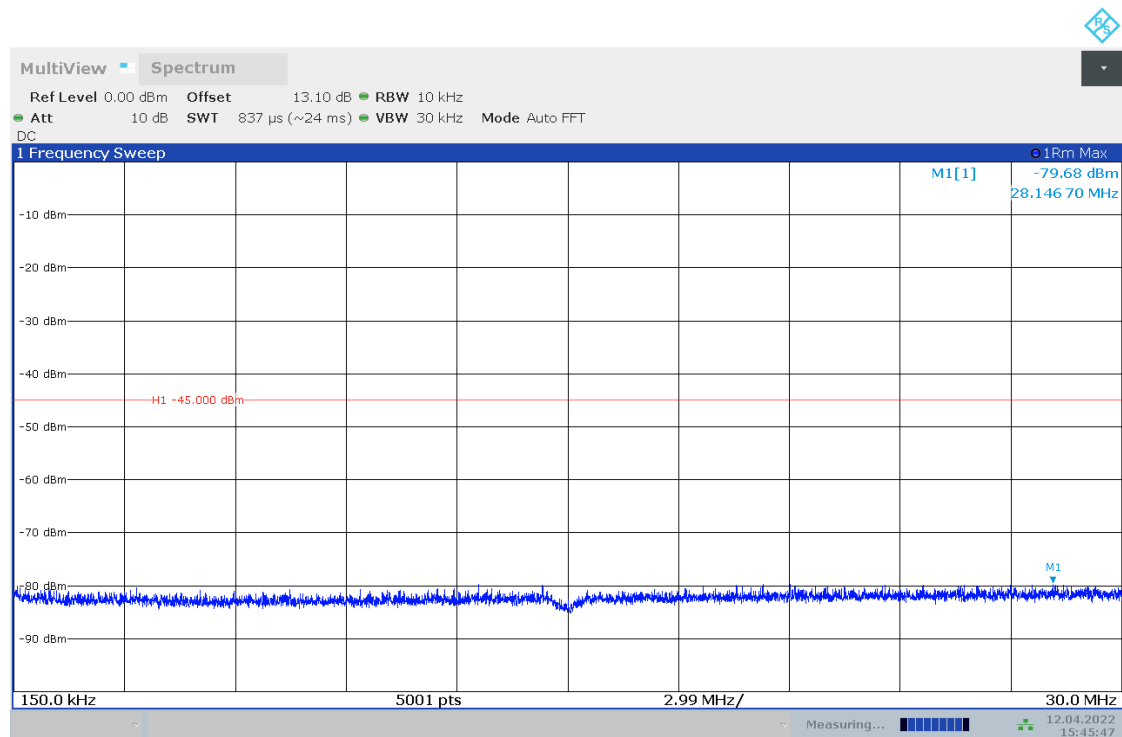


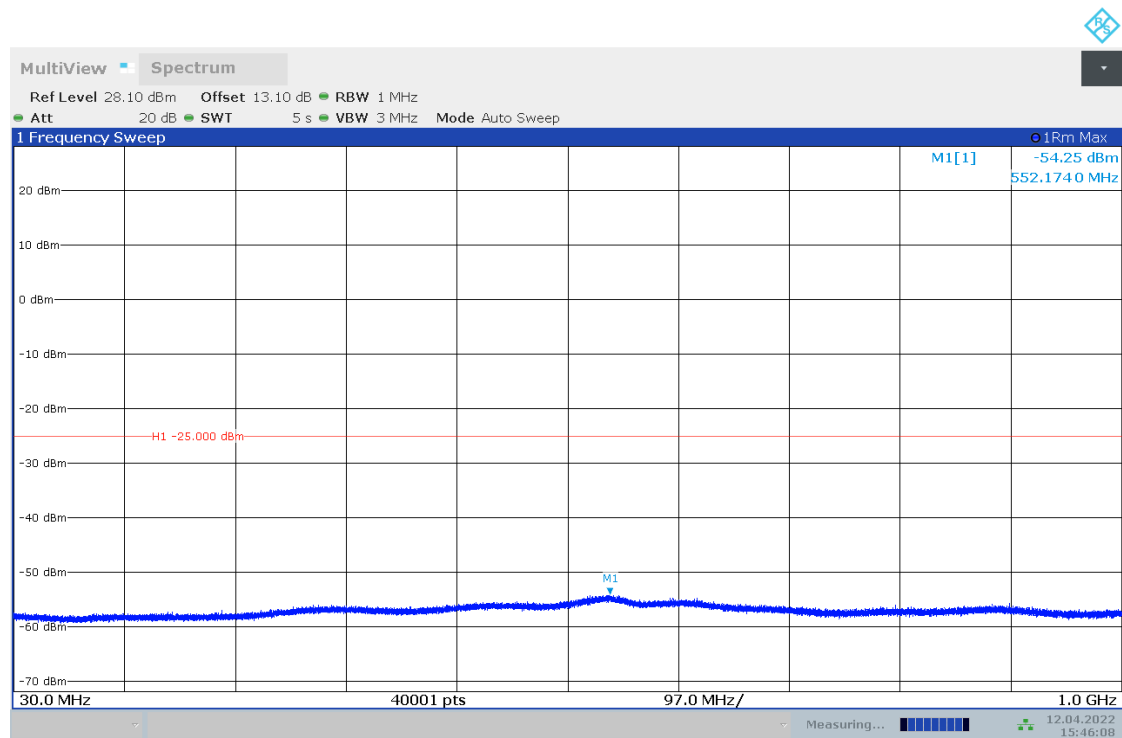
4.2.14 TM2_LCH_15MHz+15MHz_PCCRB1#0_SCCRB1#74



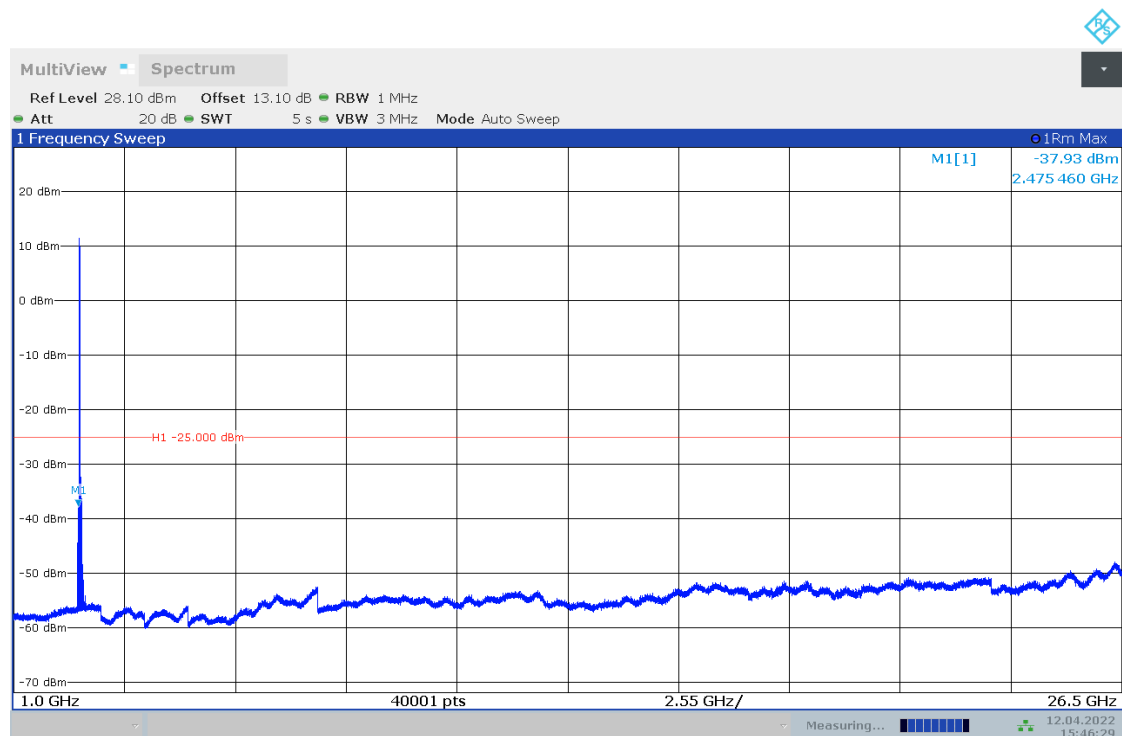
15:45:26 12.04.2022



15:45:47 12.04.2022



15:46:09 12.04.2022

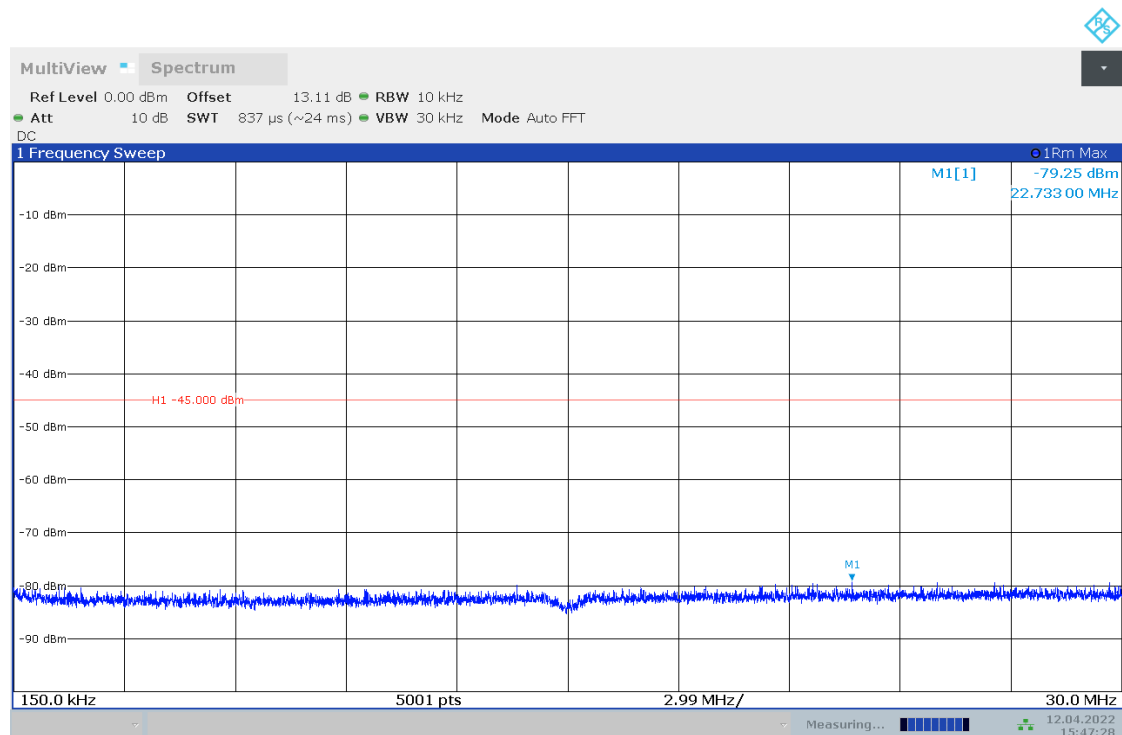


15:46:30 12.04.2022

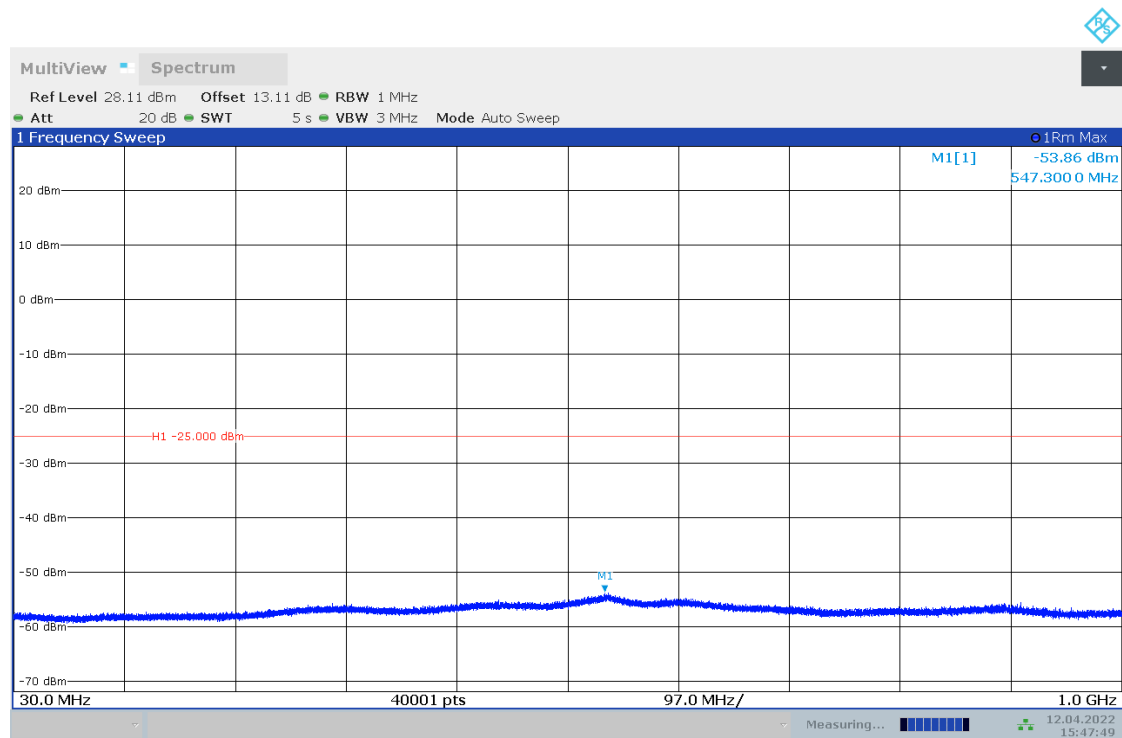
4.2.15 TM2_MCH_15MHz+15MHz_PCCRB75#0_SCCRB75#0



15:47:07 12.04.2022



15:47:28 12.04.2022

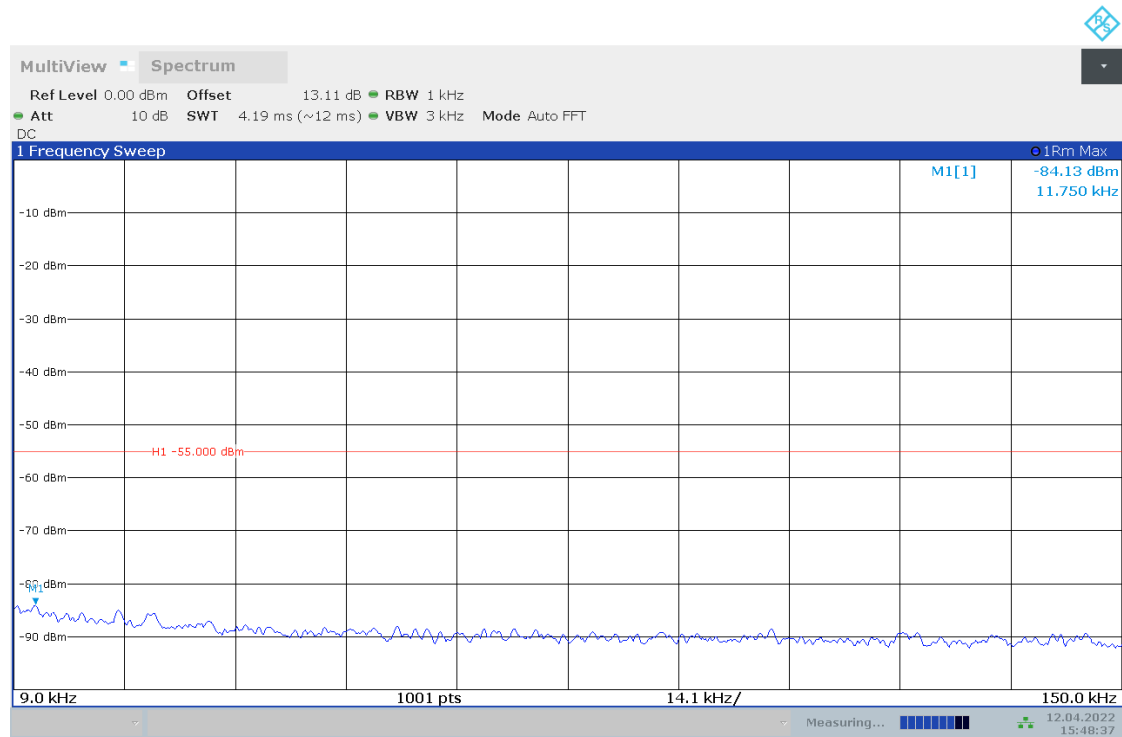


15:47:50 12.04.2022

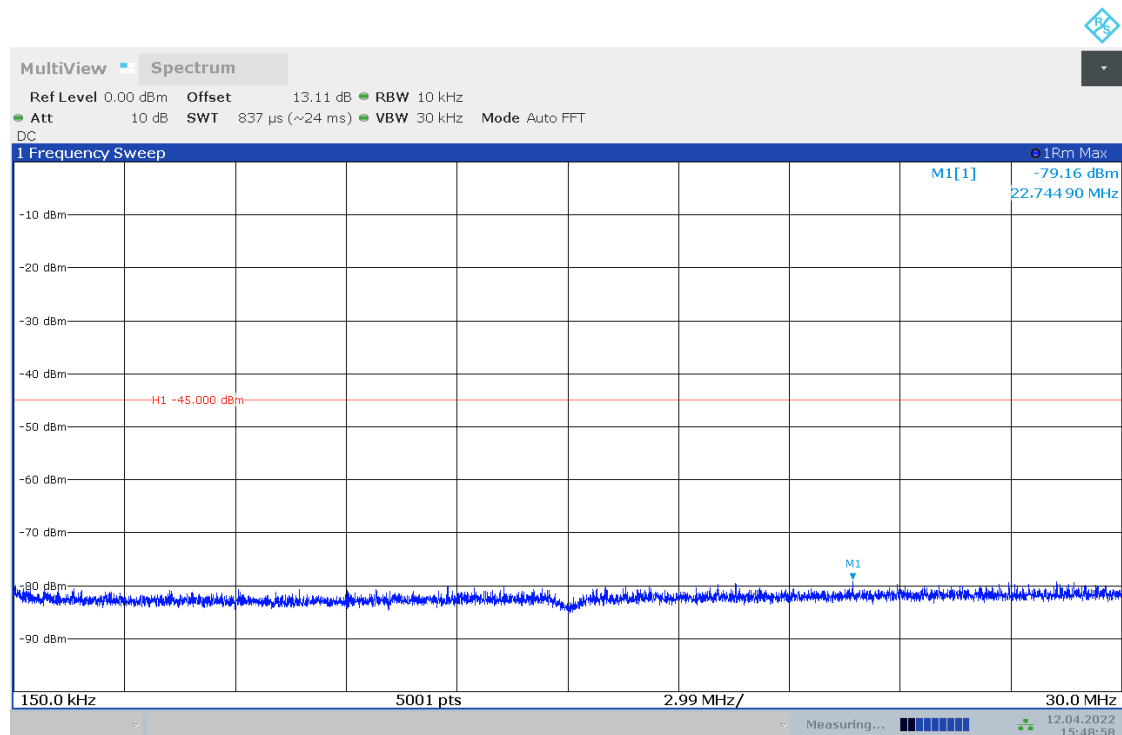


15:48:11 12.04.2022

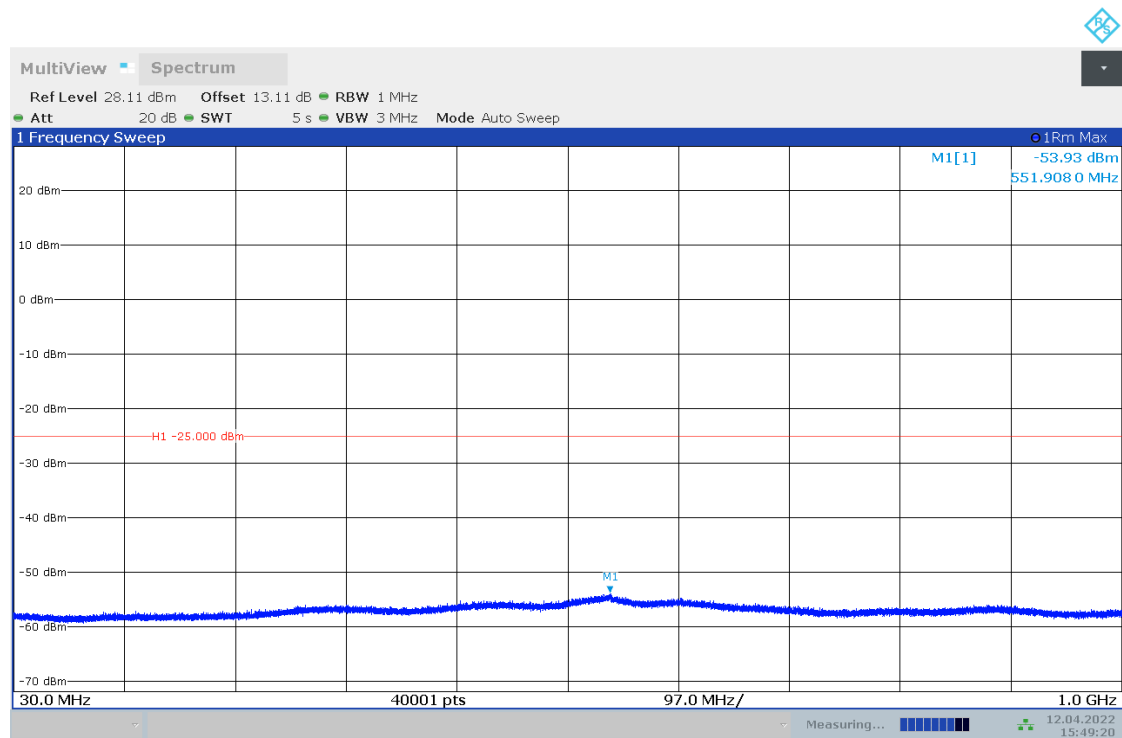
4.2.16 TM2_MCH_15MHz+15MHz_PCCRB1#0_SCCRB1#74



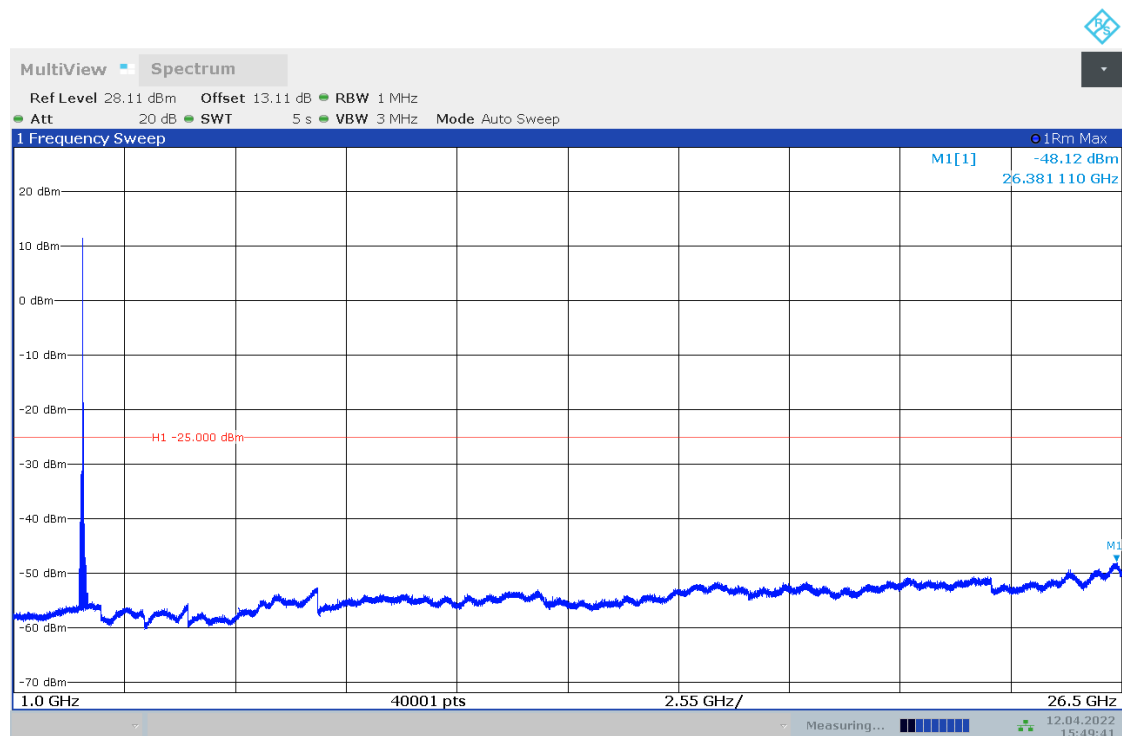
15:48:37 12.04.2022



15:48:58 12.04.2022

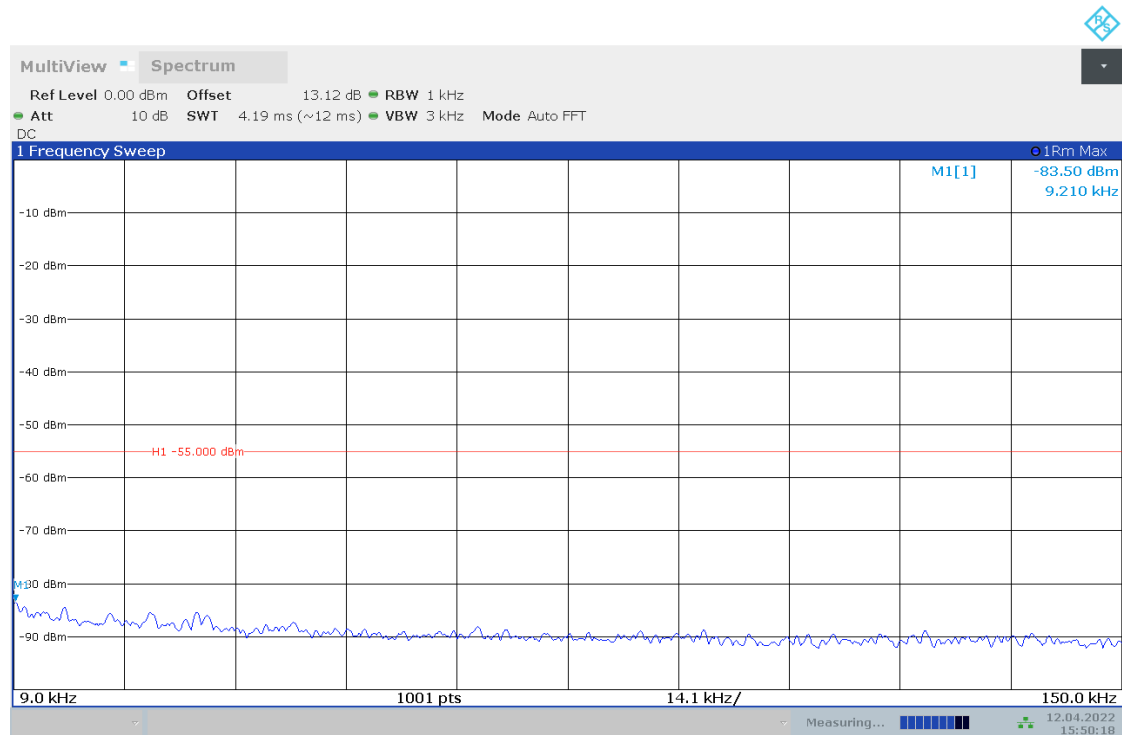


15:49:20 12.04.2022

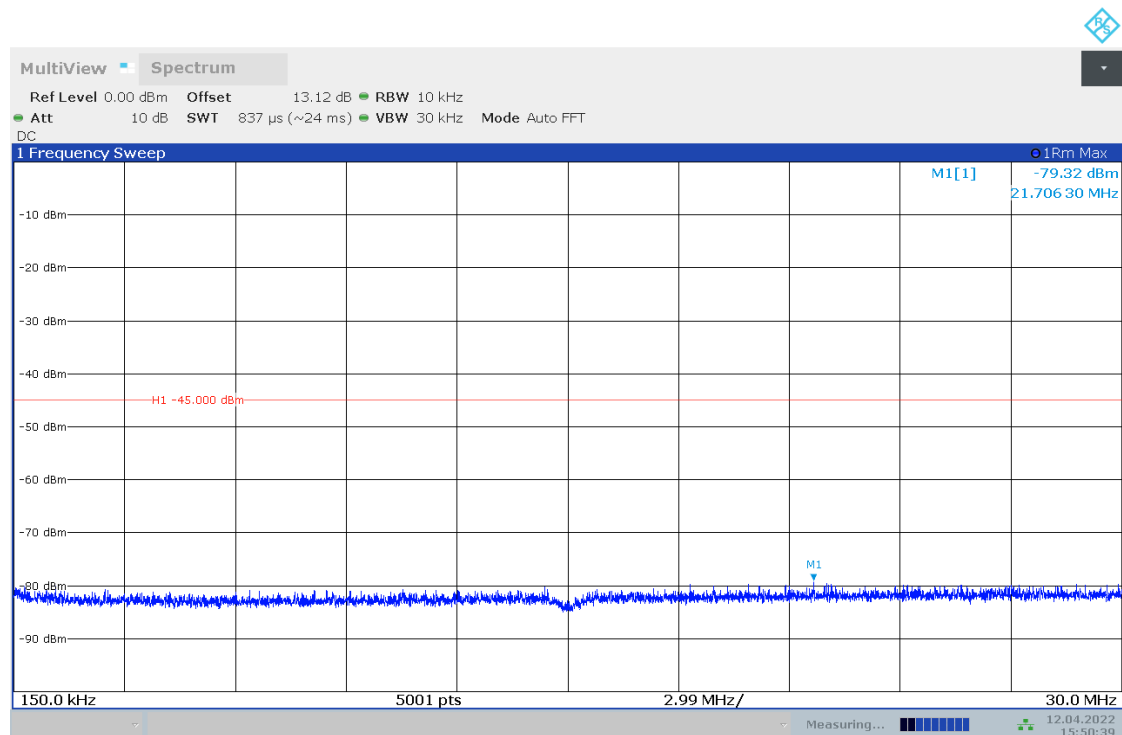


15:49:41 12.04.2022

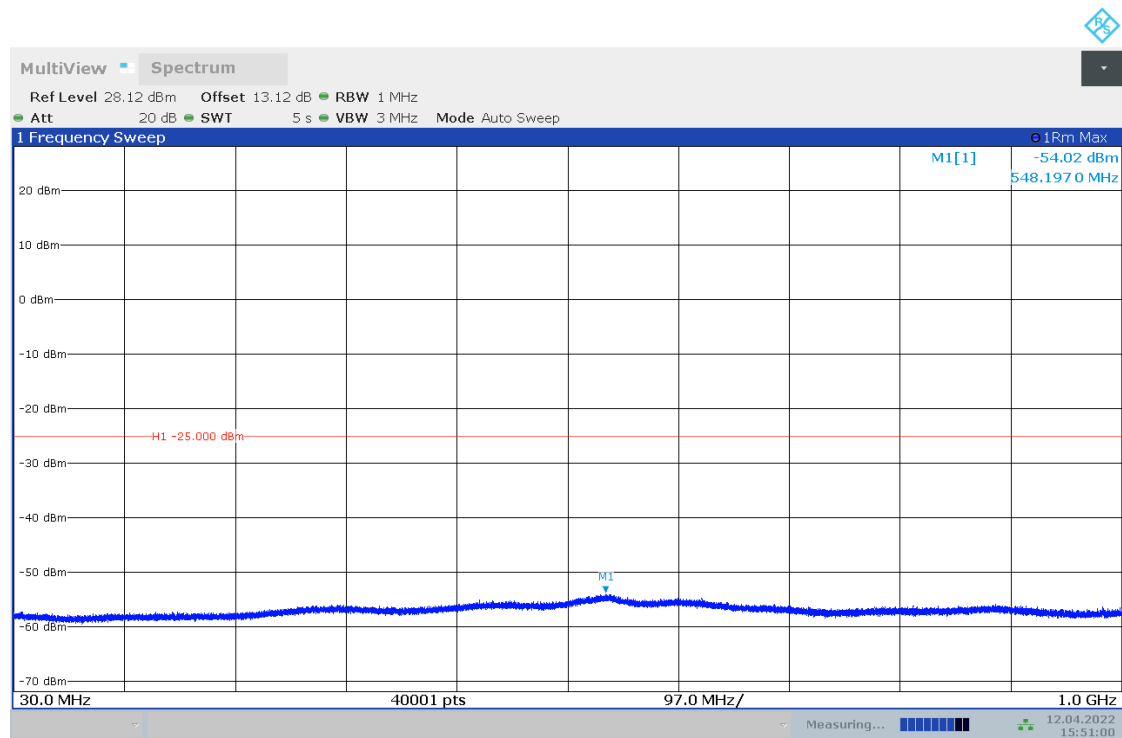
4.2.17 TM2_HCH_15MHz+15MHz_PCCRB75#0_SCCRB75#0



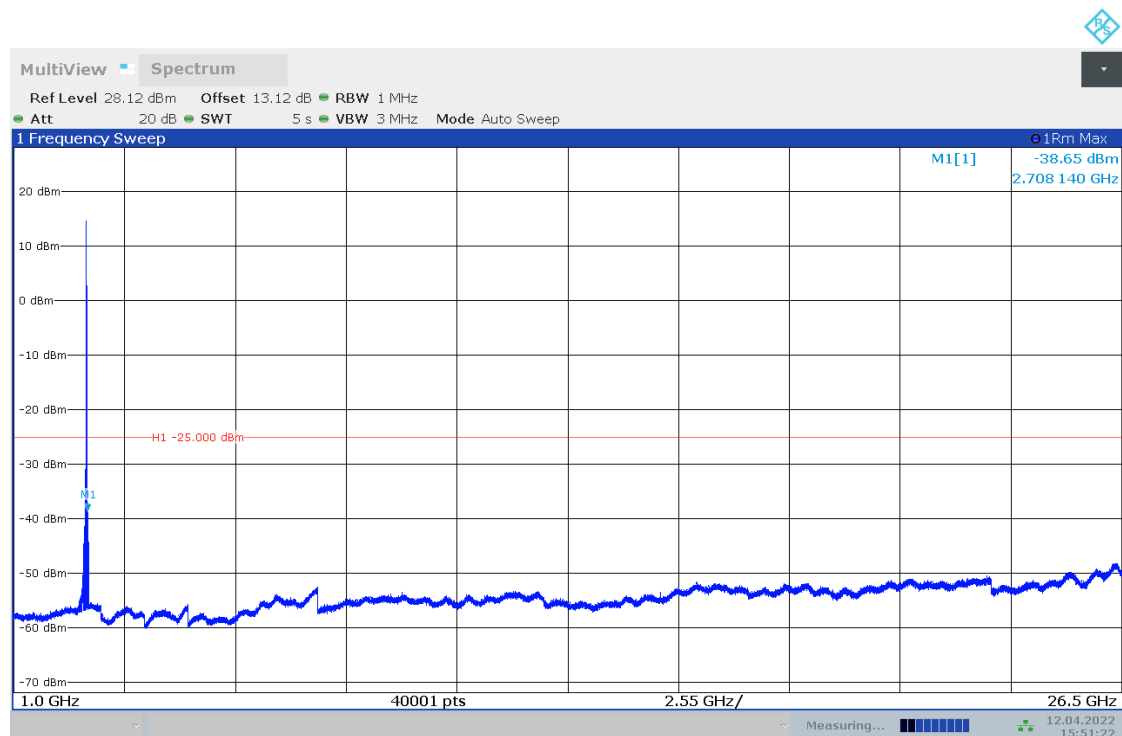
15:50:18 12.04.2022



15:50:39 12.04.2022

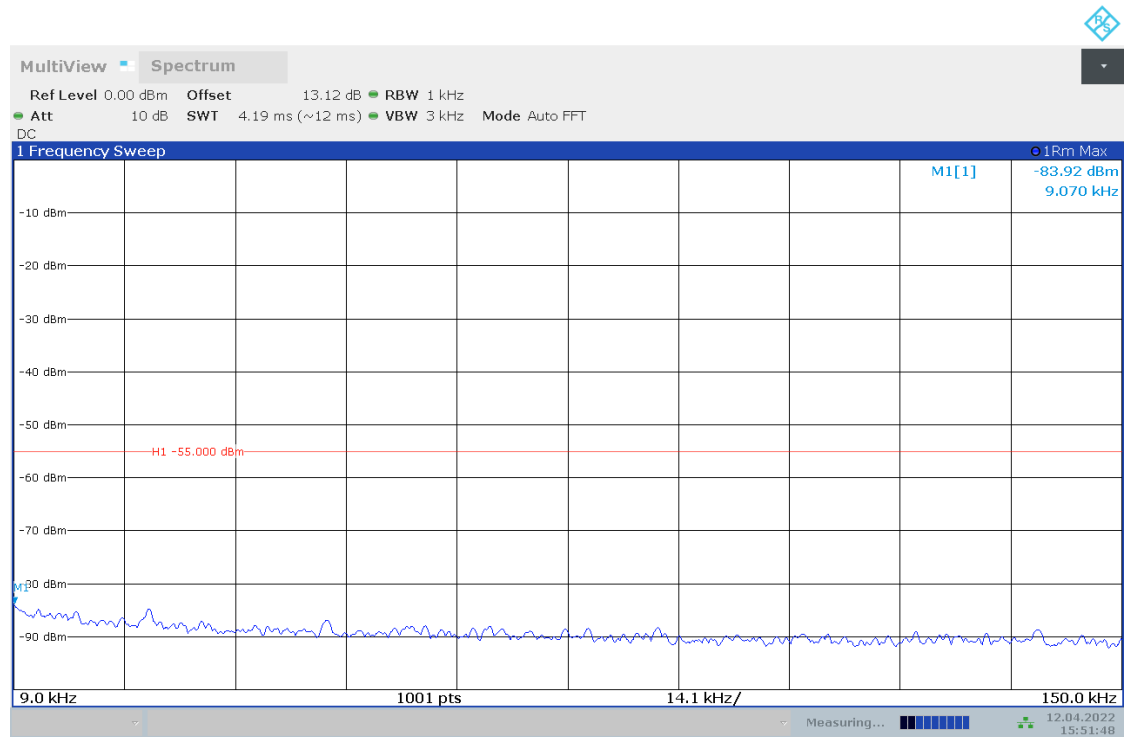


15:51:00 12.04.2022

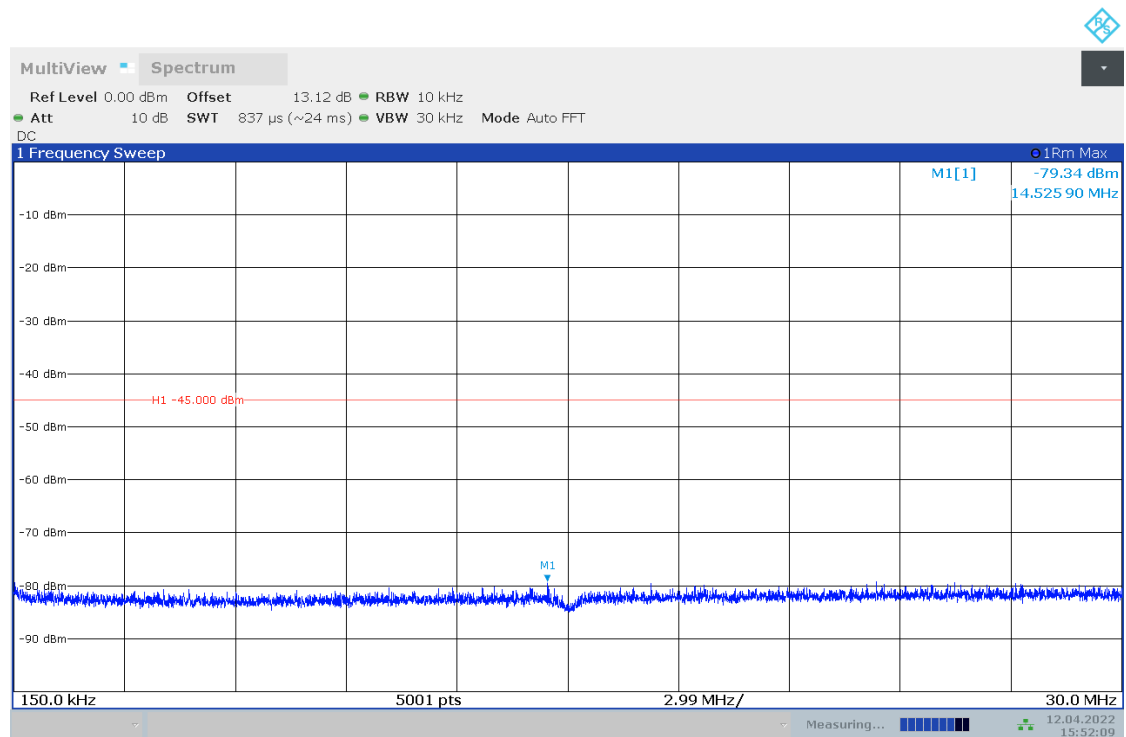


15:51:22 12.04.2022

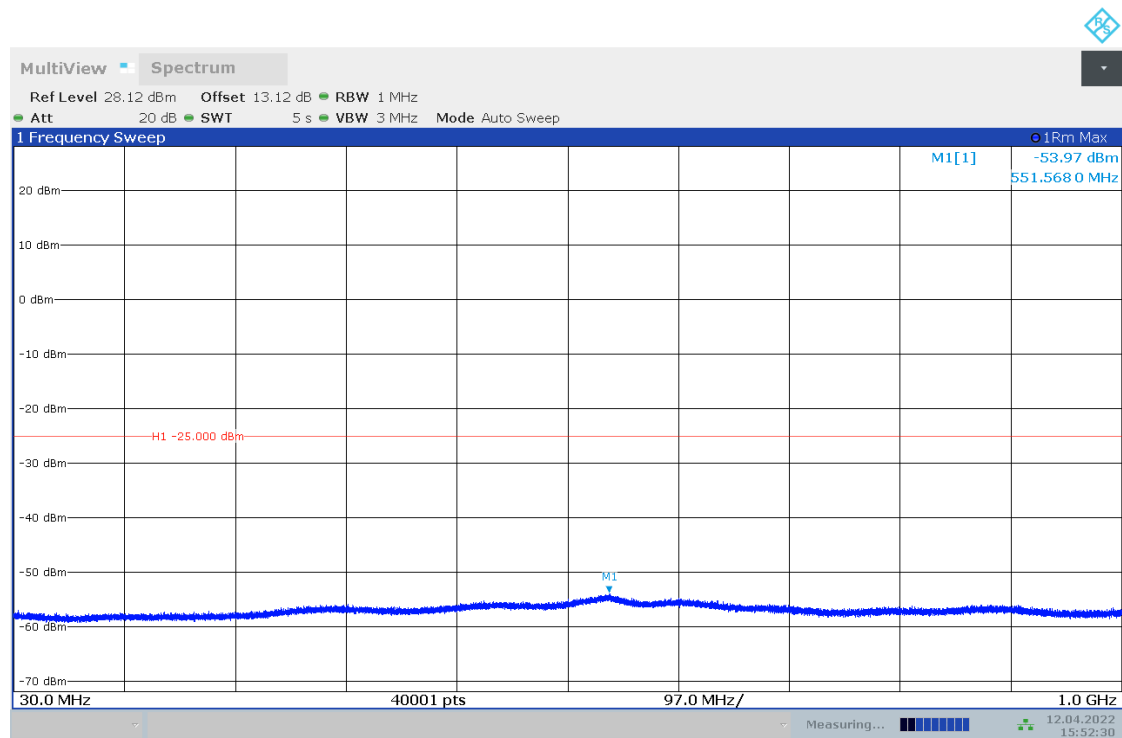
4.2.18 TM2_HCH_15MHz+15MHz_PCCRB1#0_SCCRB1#74



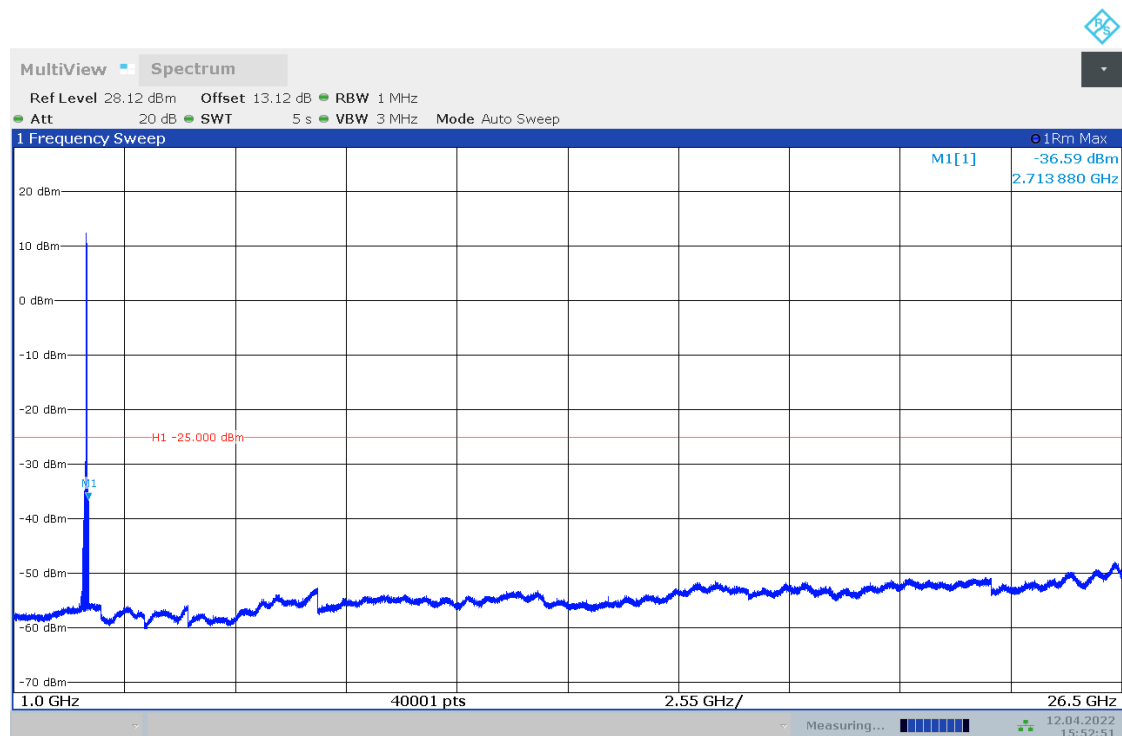
15:51:48 12.04.2022



15:52:09 12.04.2022

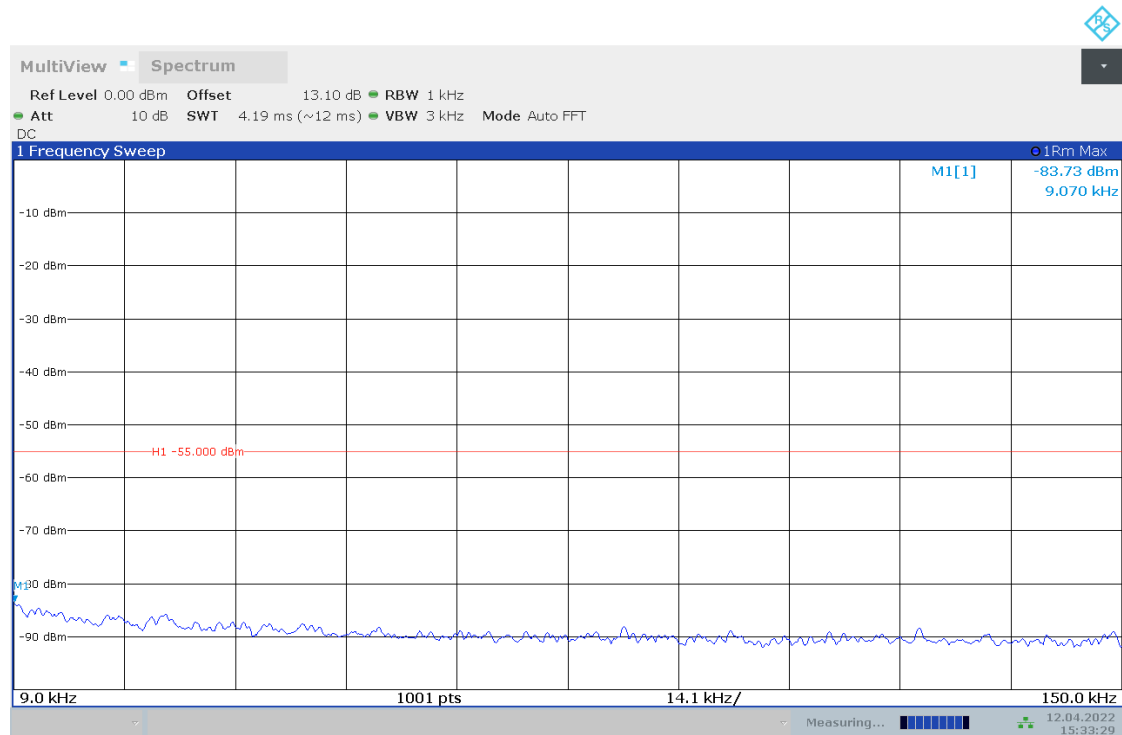


15:52:30 12.04.2022

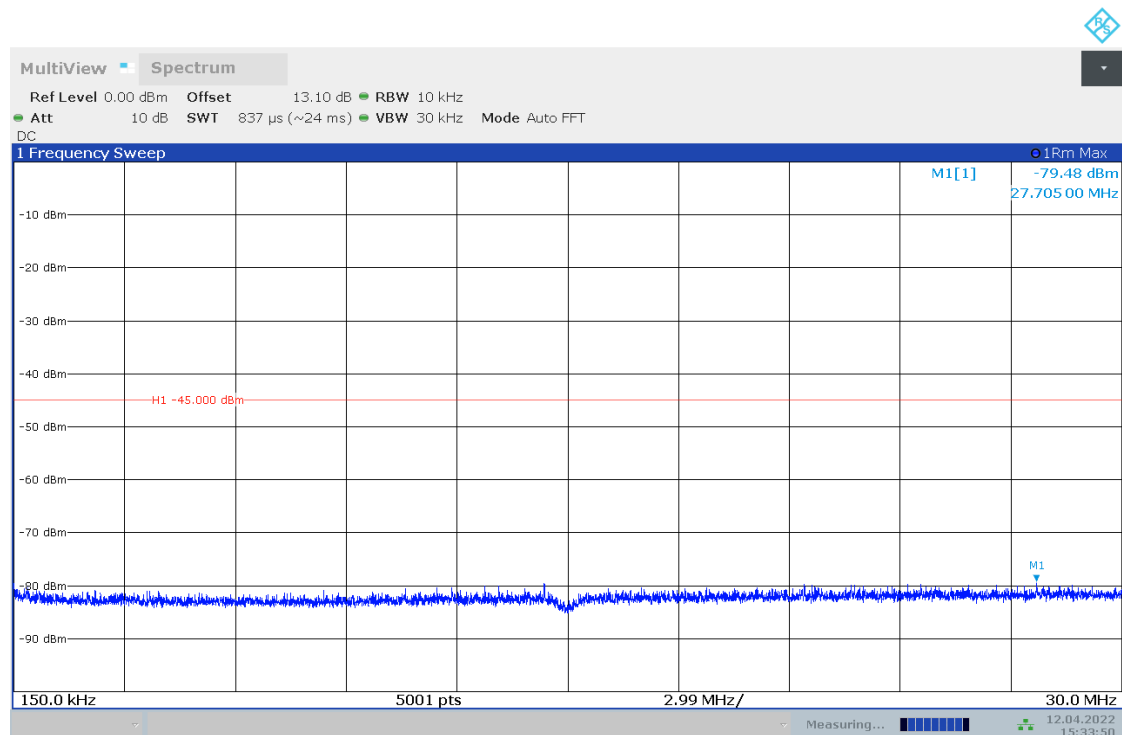


15:52:52 12.04.2022

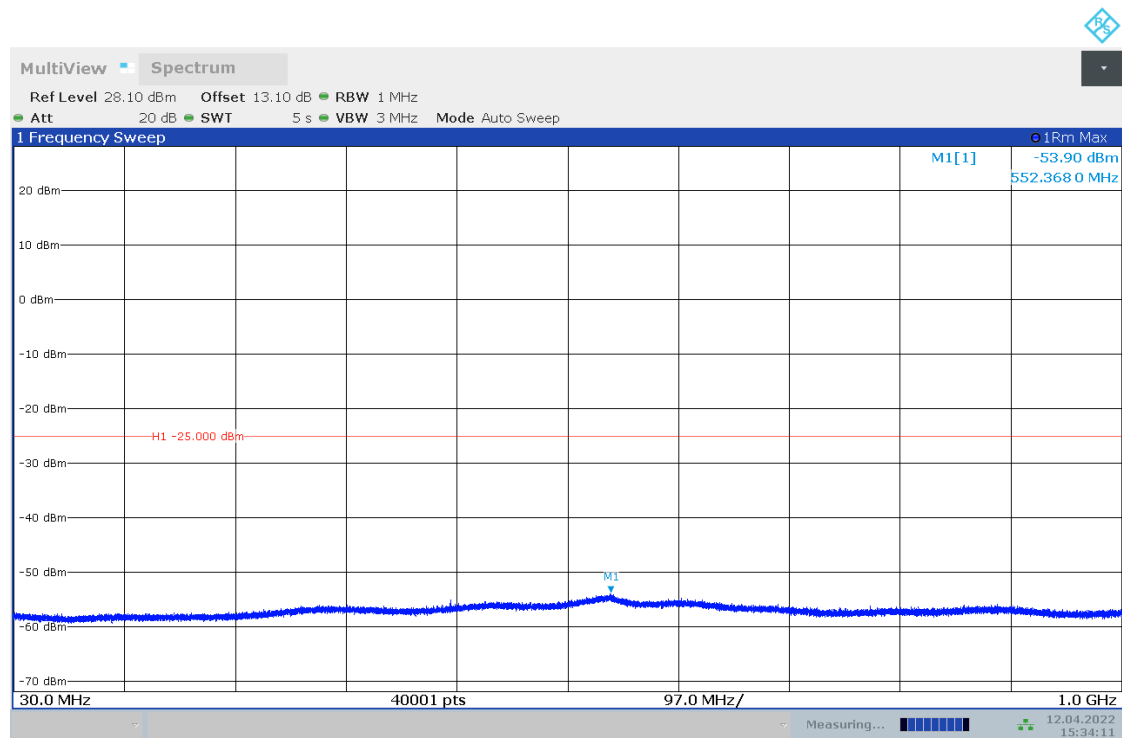
4.2.19 TM2_LCH_20MHz+20MHz_PCCRB100#0_SCCRB100#0



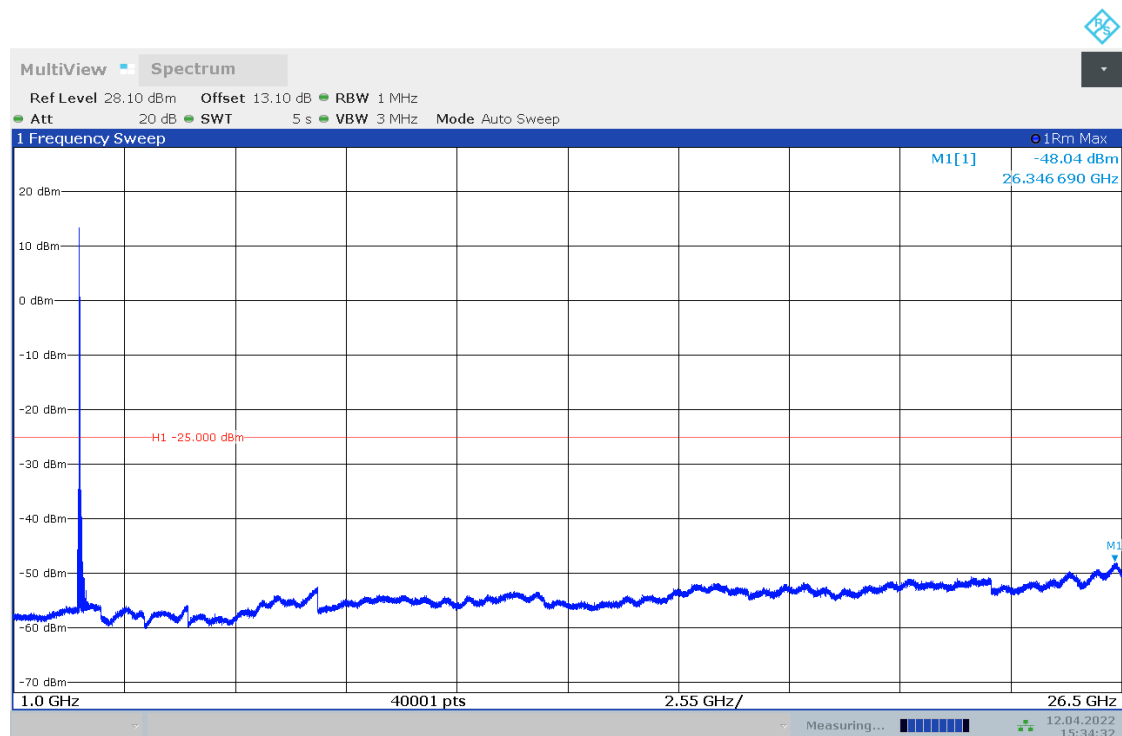
15:33:29 12.04.2022



15:33:50 12.04.2022



15:34:12 12.04.2022

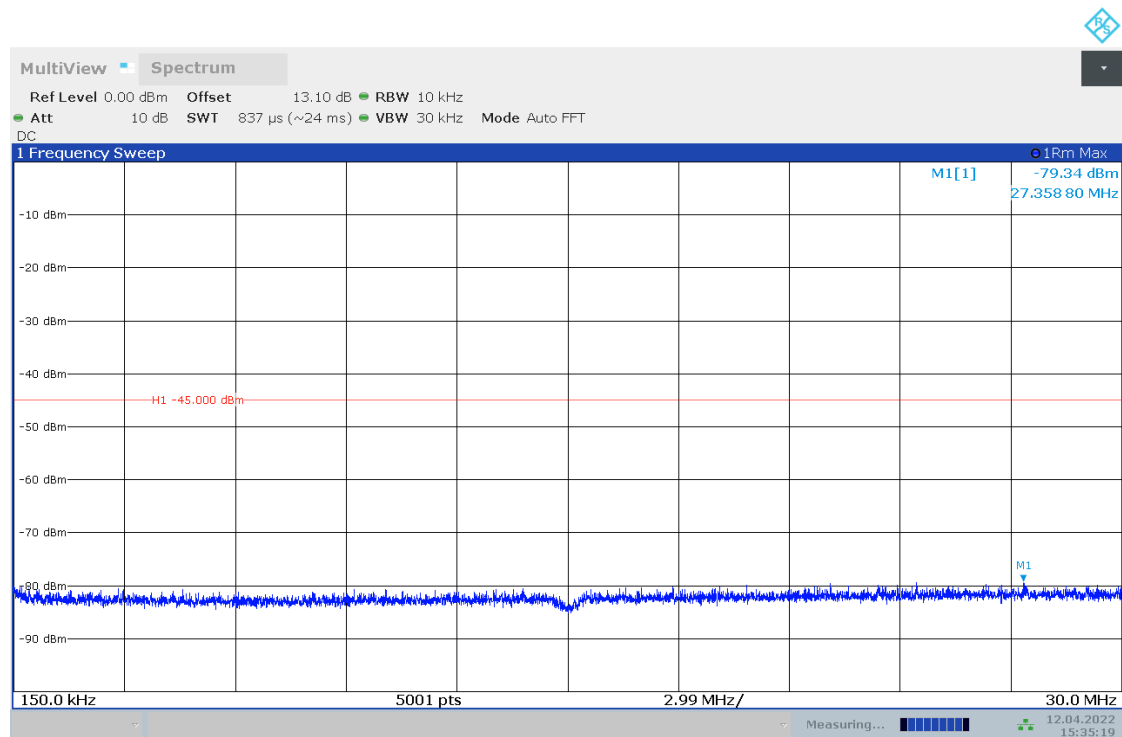


15:34:33 12.04.2022

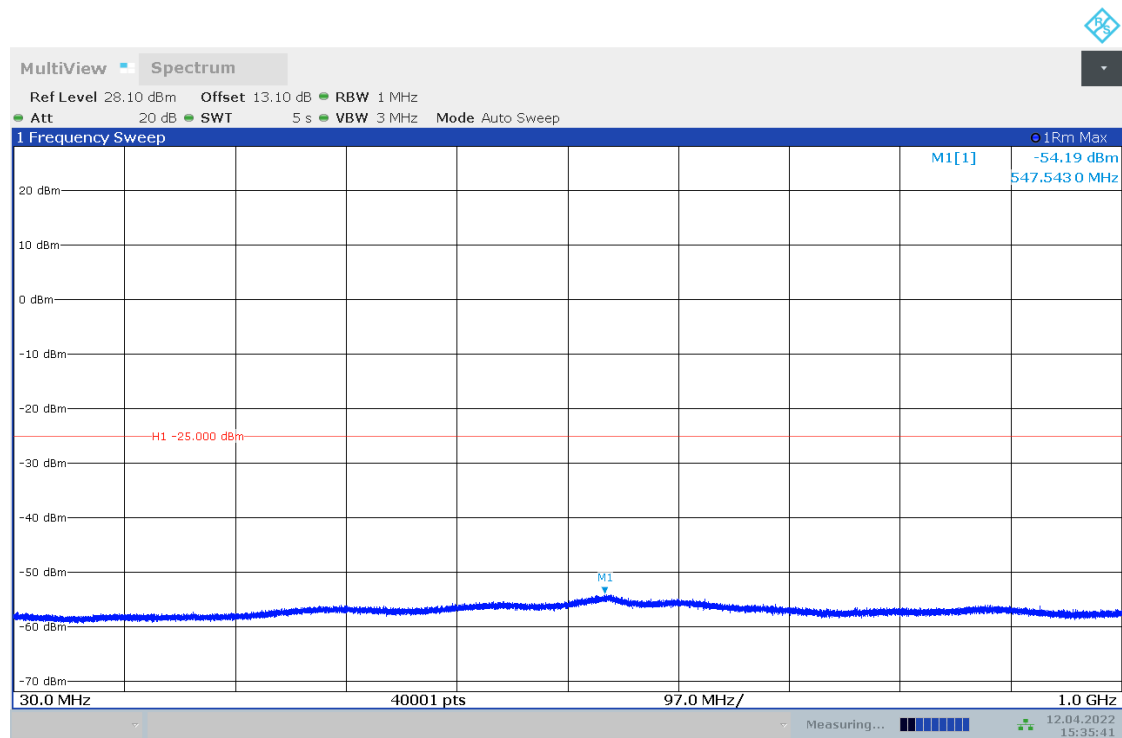
4.2.20 TM2_LCH_20MHz+20MHz_PCCRB1#0_SCCRB1#99



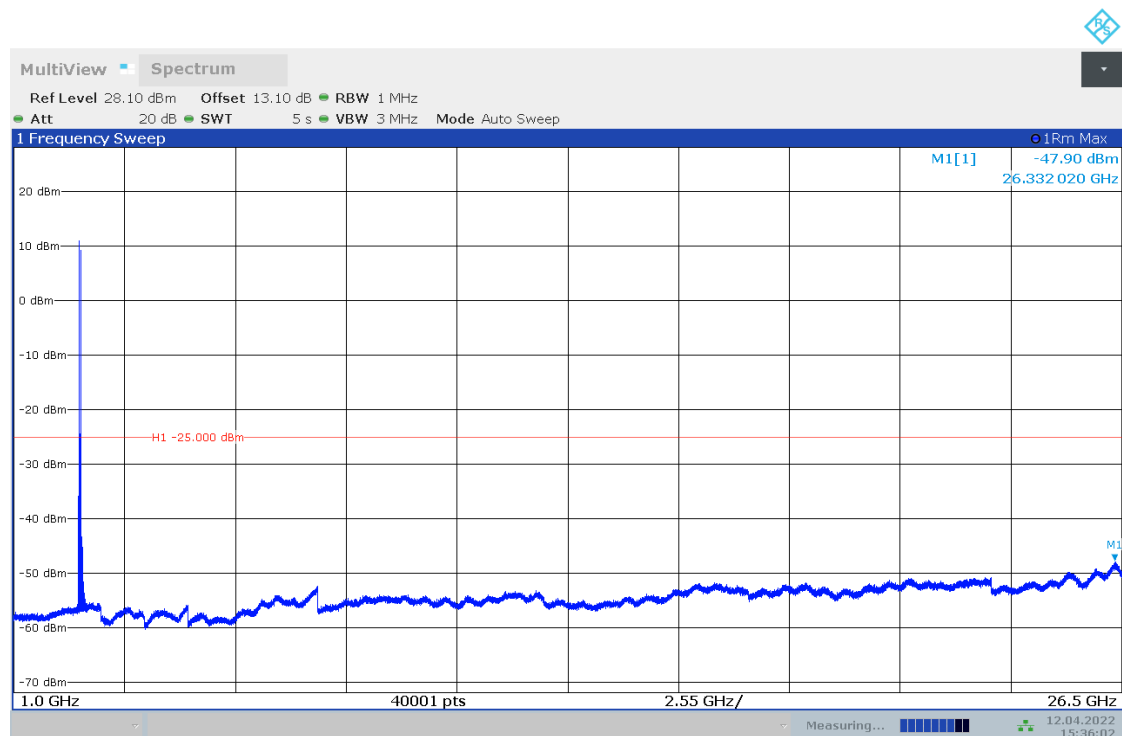
15:34:59 12.04.2022



15:35:20 12.04.2022

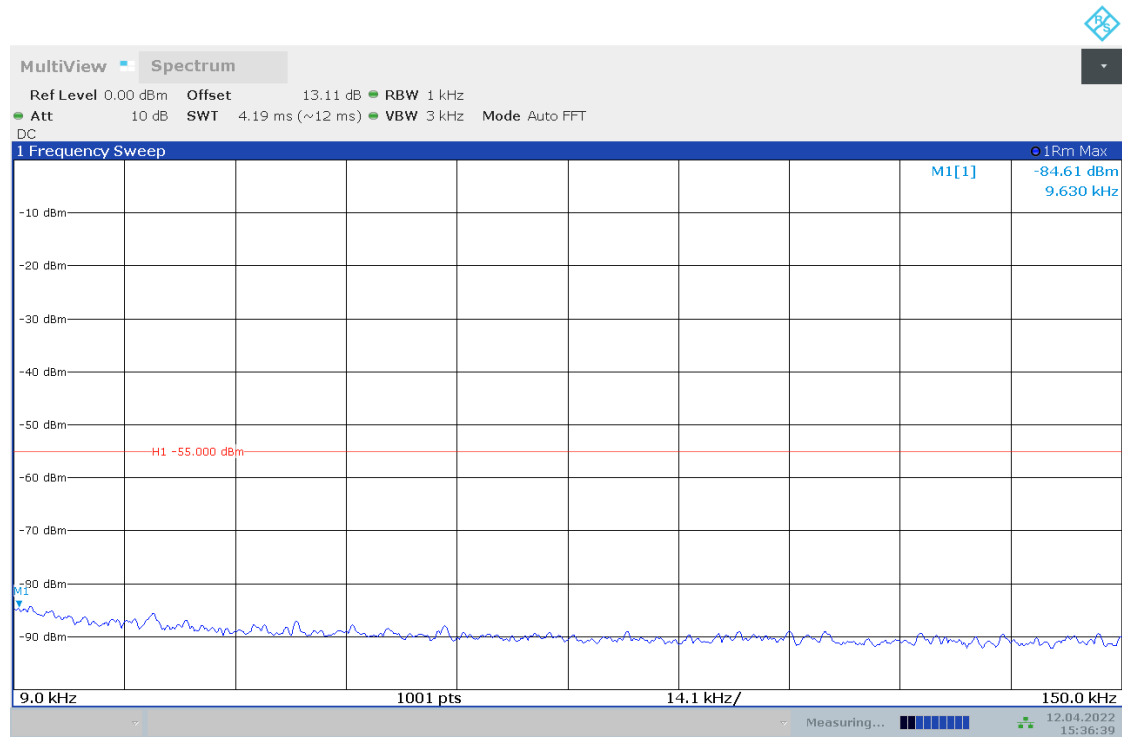


15:35:41 12.04.2022

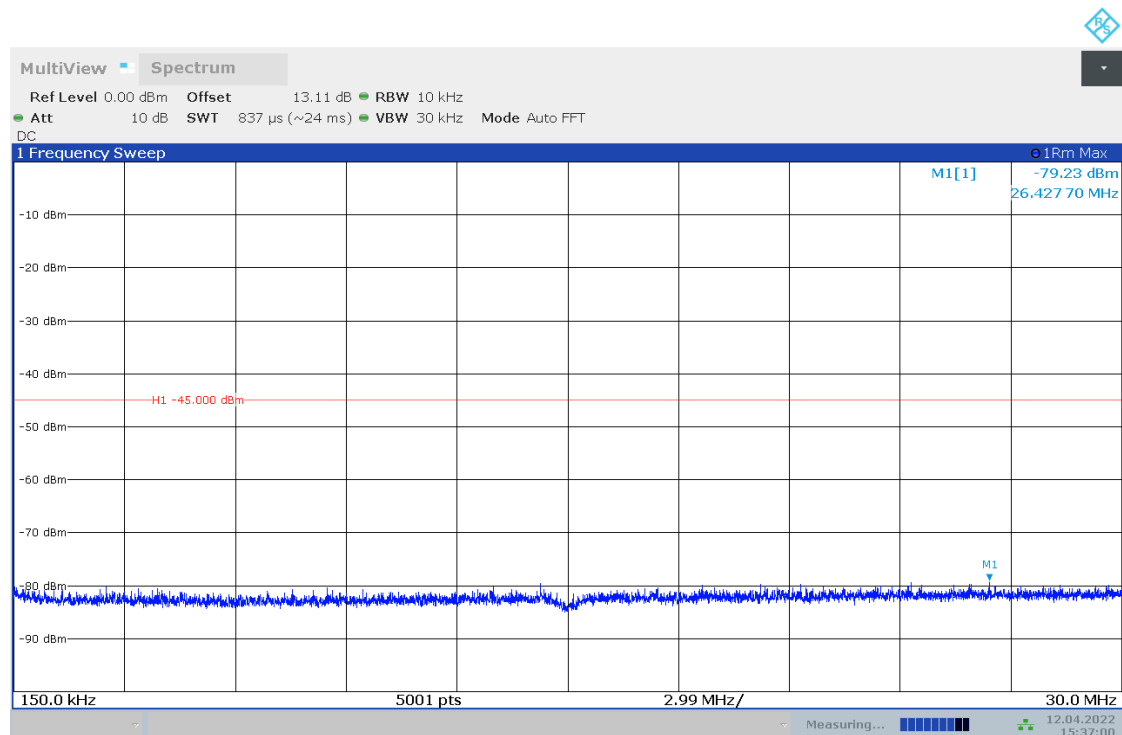


15:36:03 12.04.2022

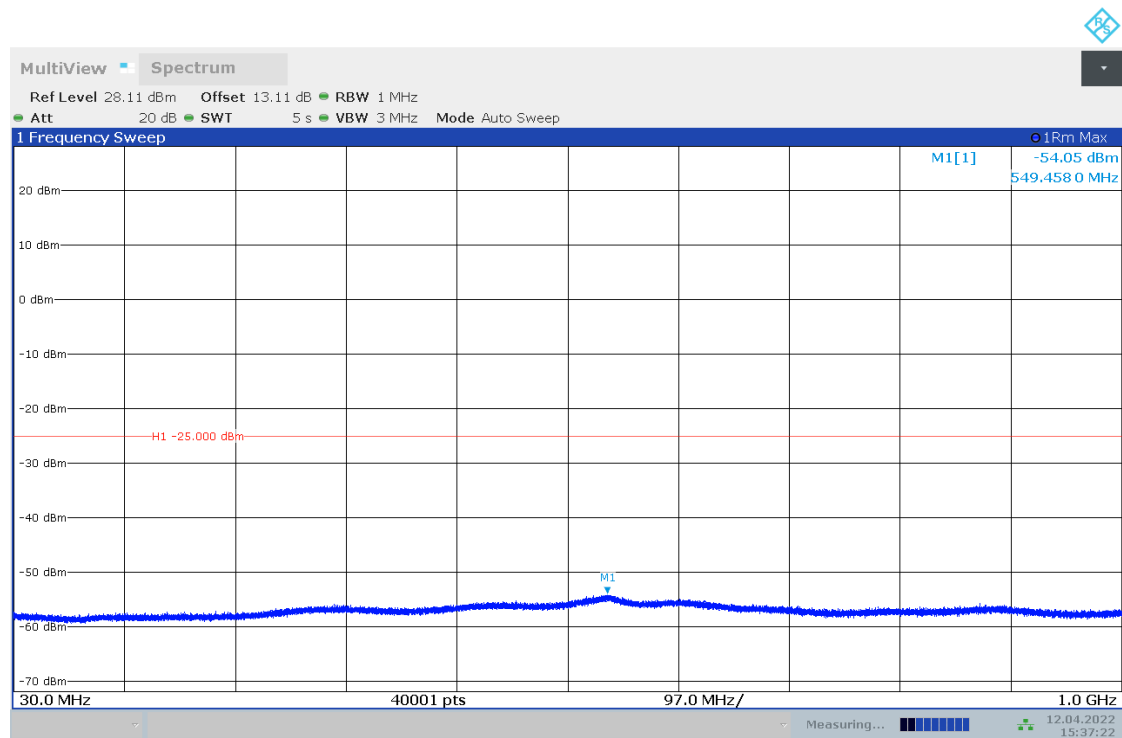
4.2.21 TM2_MCH_20MHz+20MHz_PCCRB100#0_SCCRB100#0



15:36:40 12.04.2022



15:37:01 12.04.2022

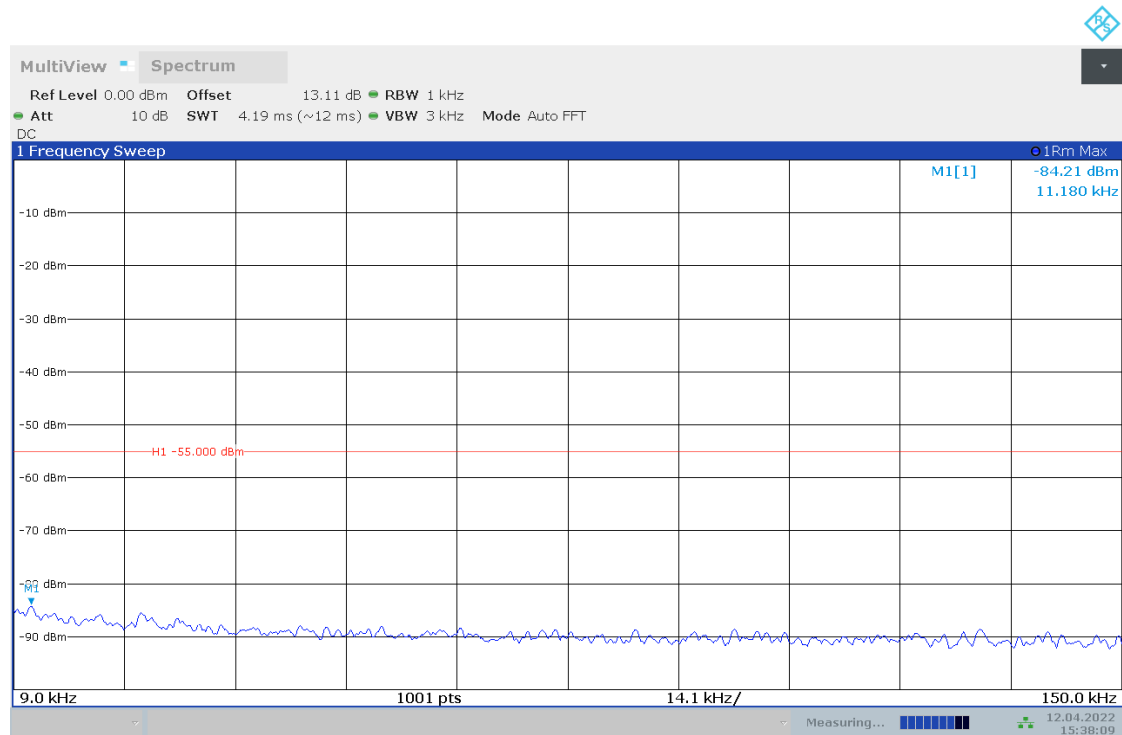


15:37:22 12.04.2022

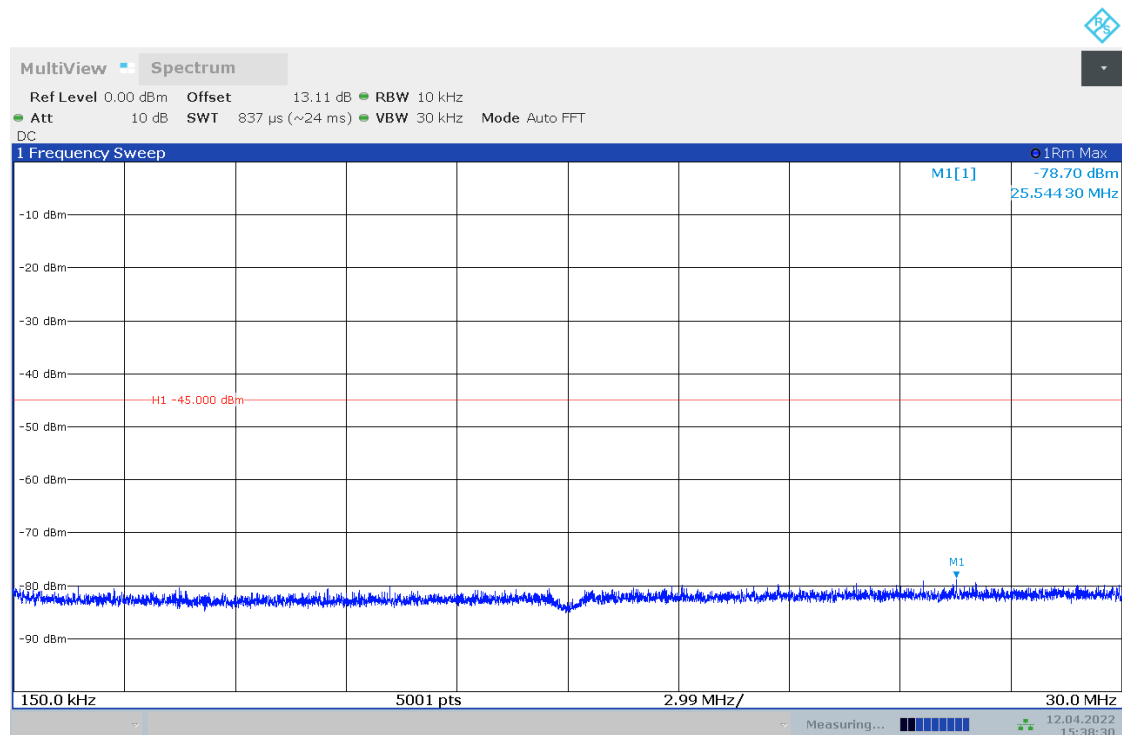


15:37:43 12.04.2022

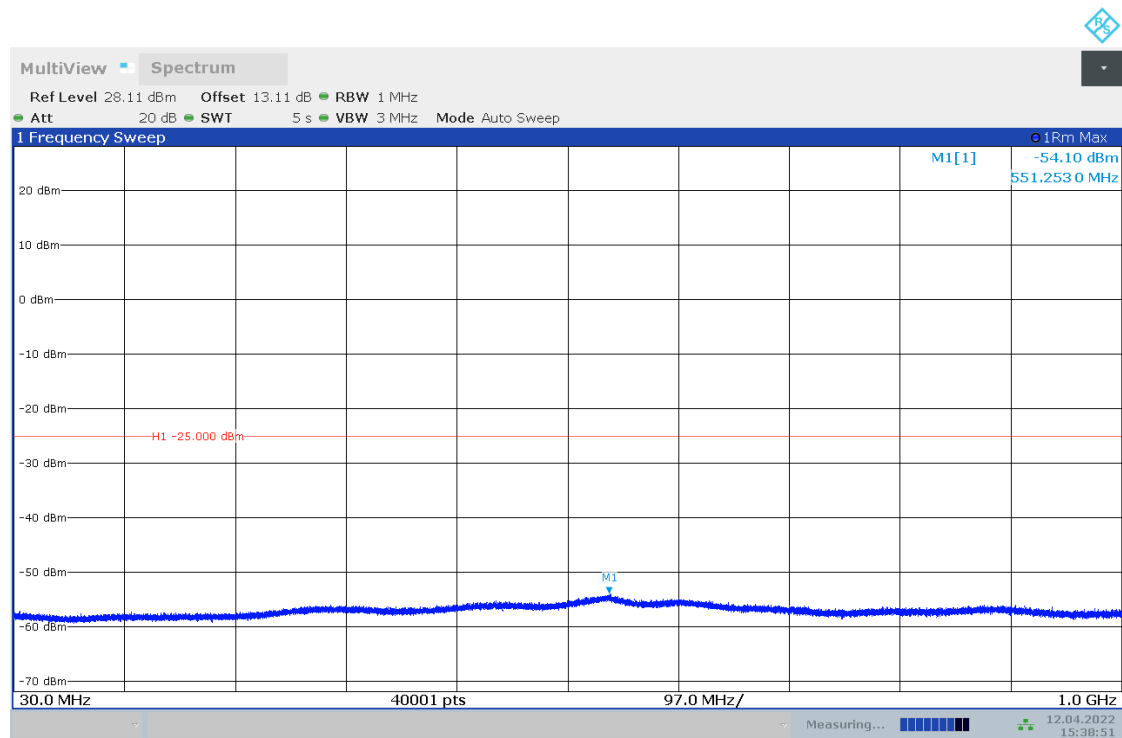
4.2.22 TM2_MCH_20MHz+20MHz_PCCRB1#0_SCCRB1#99



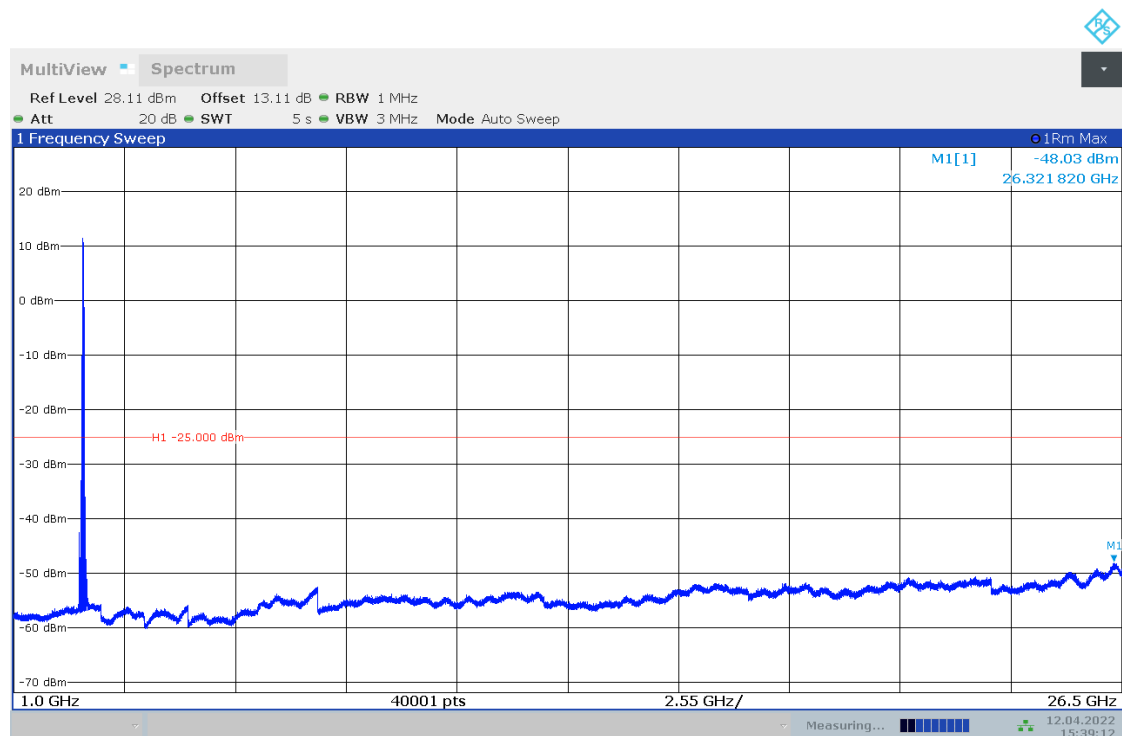
15:38:09 12.04.2022



15:38:30 12.04.2022



15:38:52 12.04.2022

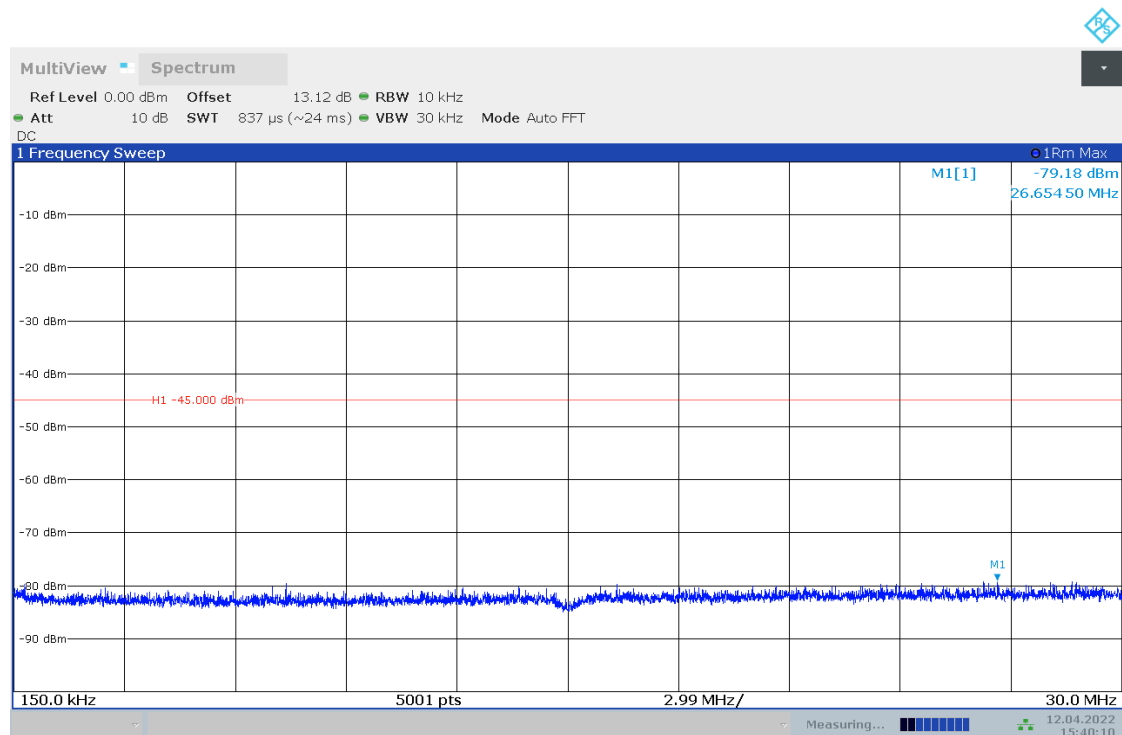


15:39:13 12.04.2022

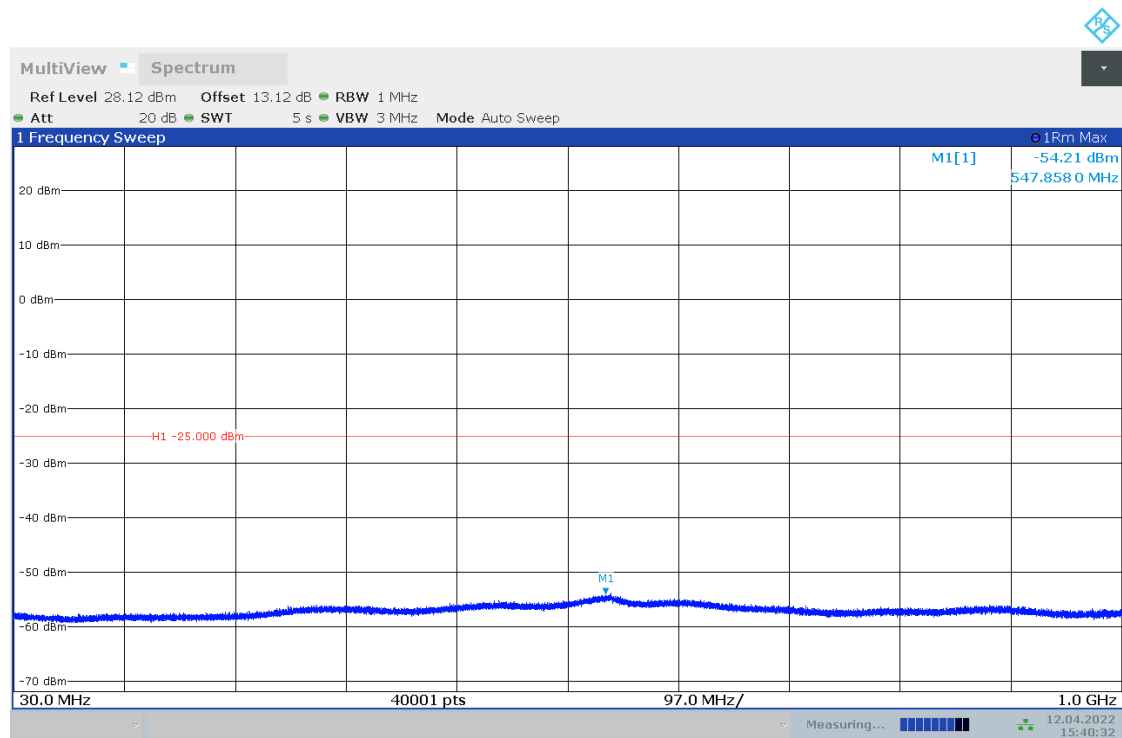
4.2.23 TM2_HCH_20MHz+20MHz_PCCRB100#0_SCCRB100#0



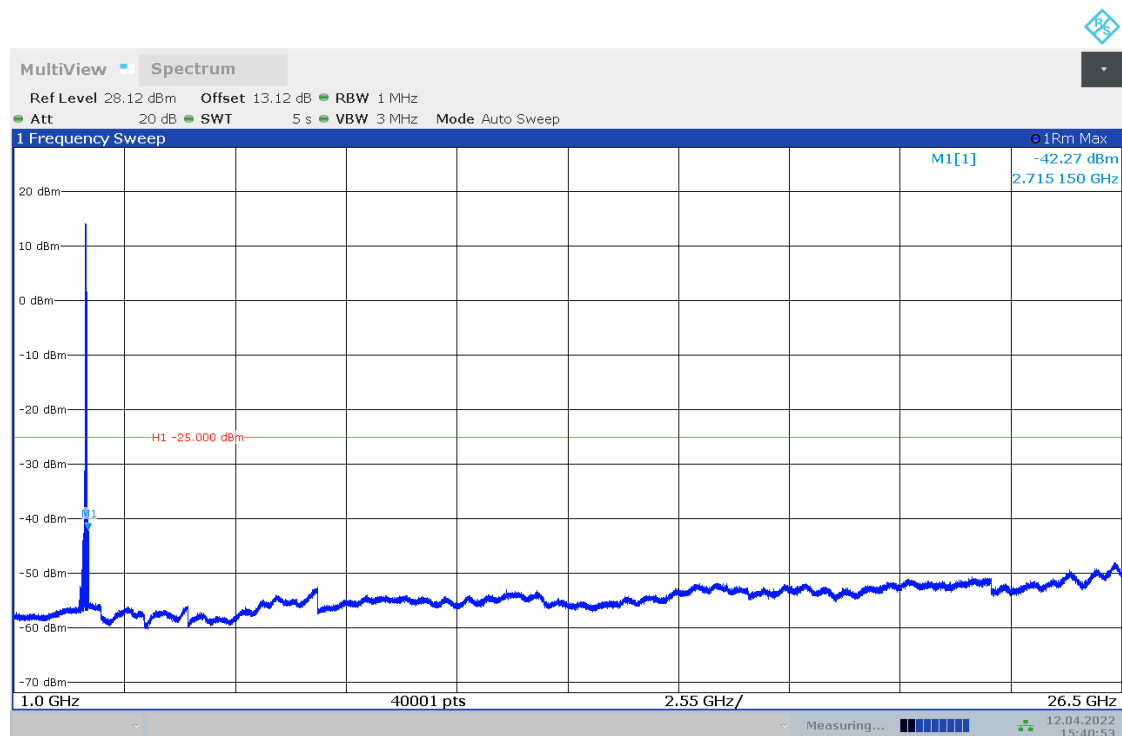
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15:40:11 12.04.2022

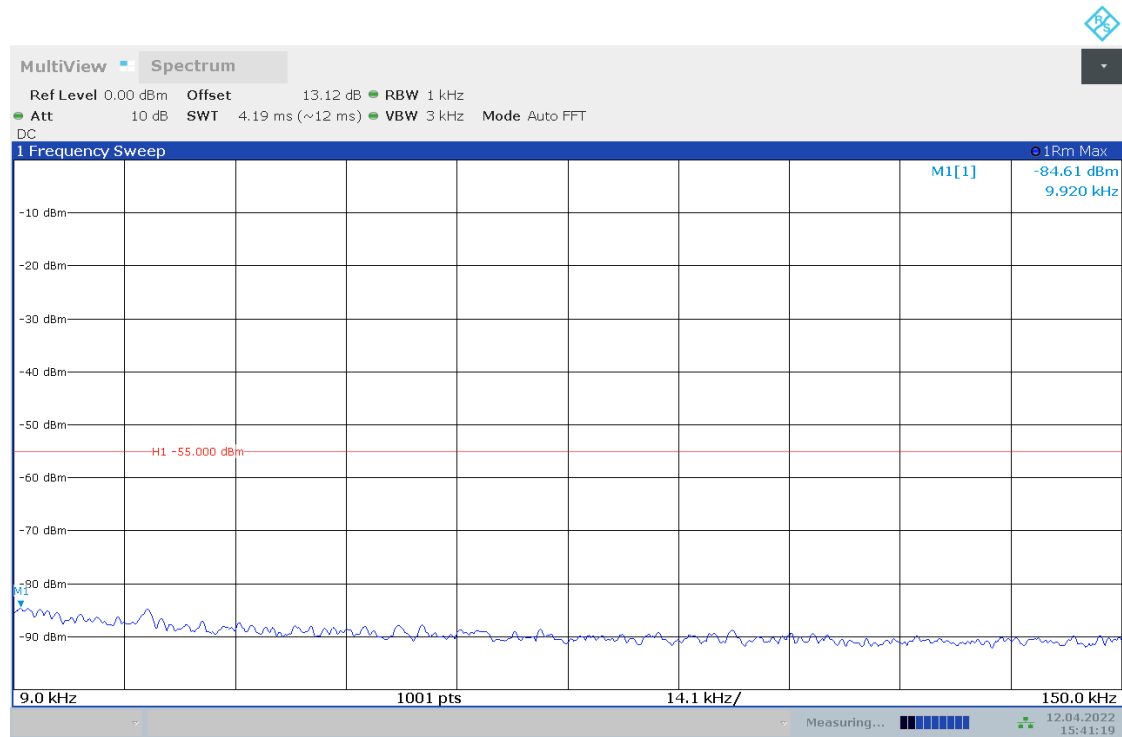


15:40:32 12.04.2022

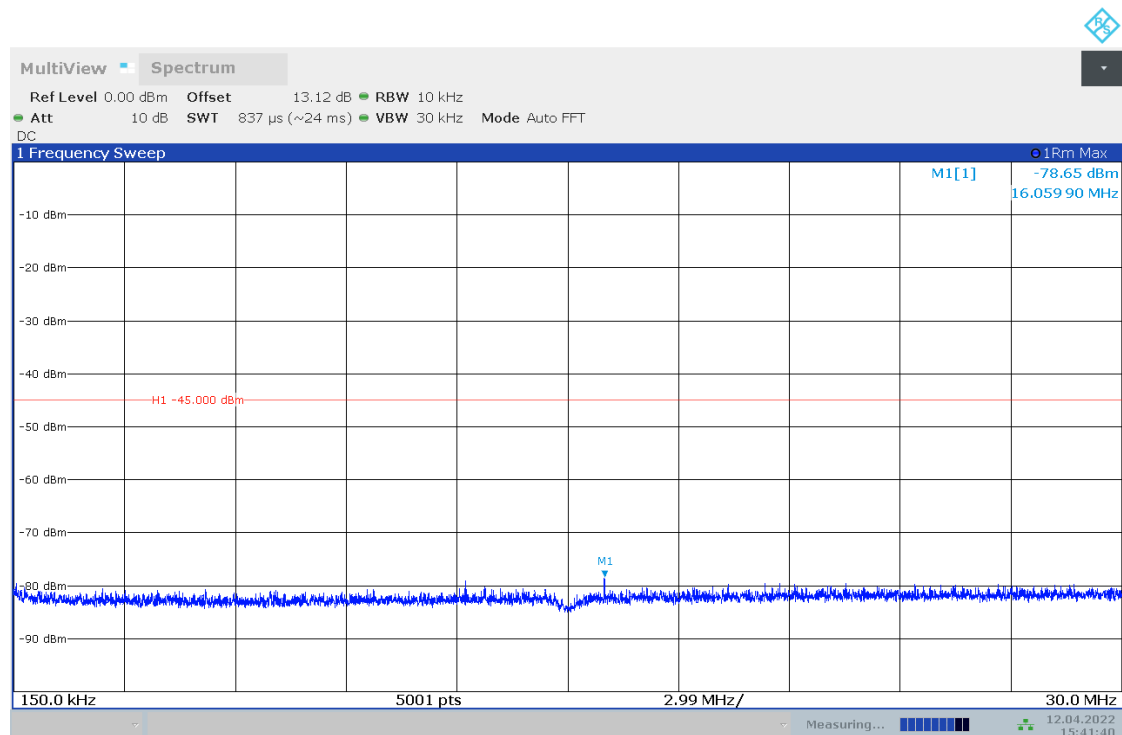


15:40:54 12.04.2022

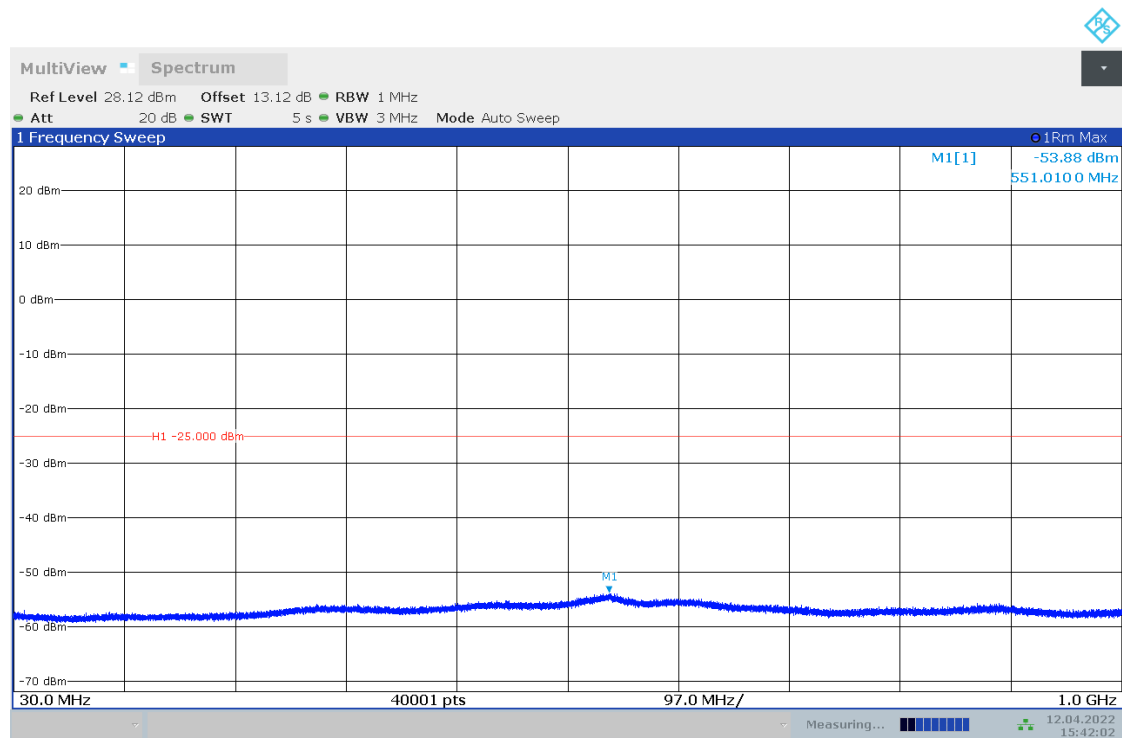
4.2.24 TM2_HCH_20MHz+20MHz_PCCRB1#0_SCCRB1#99



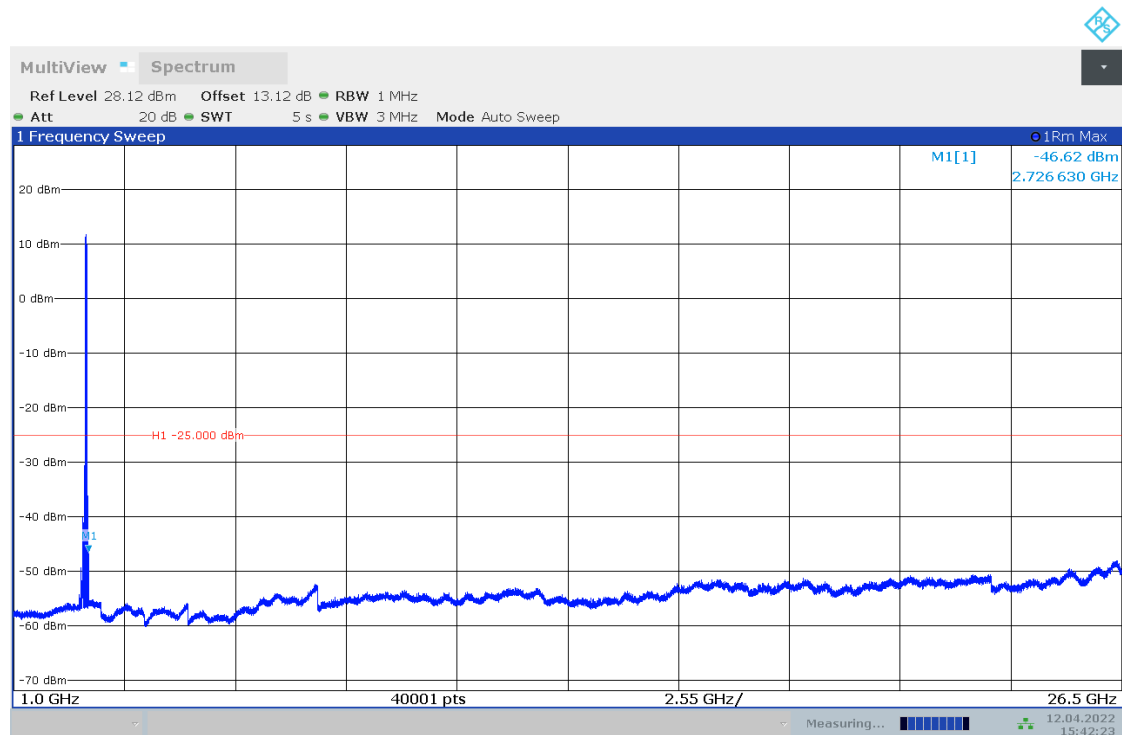
15:41:20 12.04.2022



15:41:41 12.04.2022



15:42:02 12.04.2022



15:42:24 12.04.2022

End of Test Report