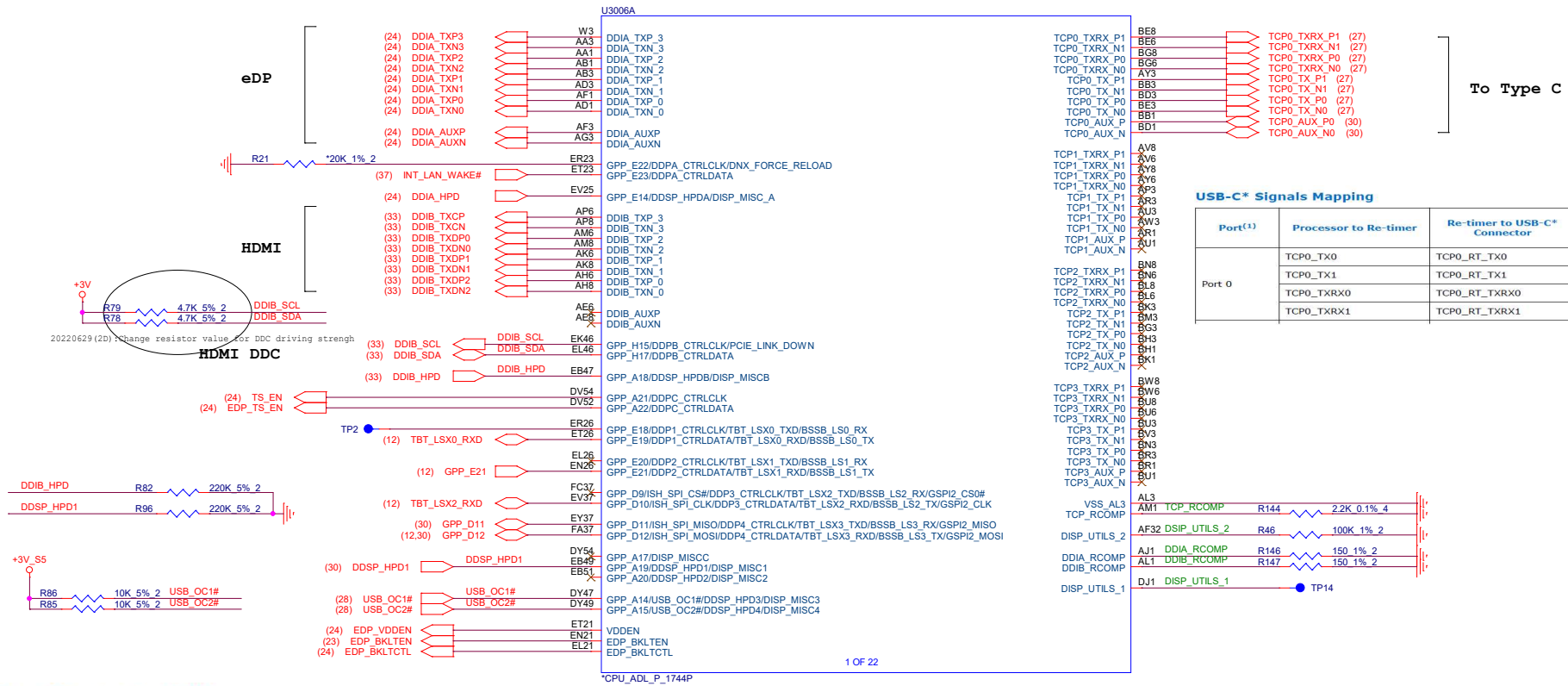
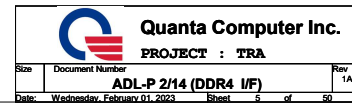


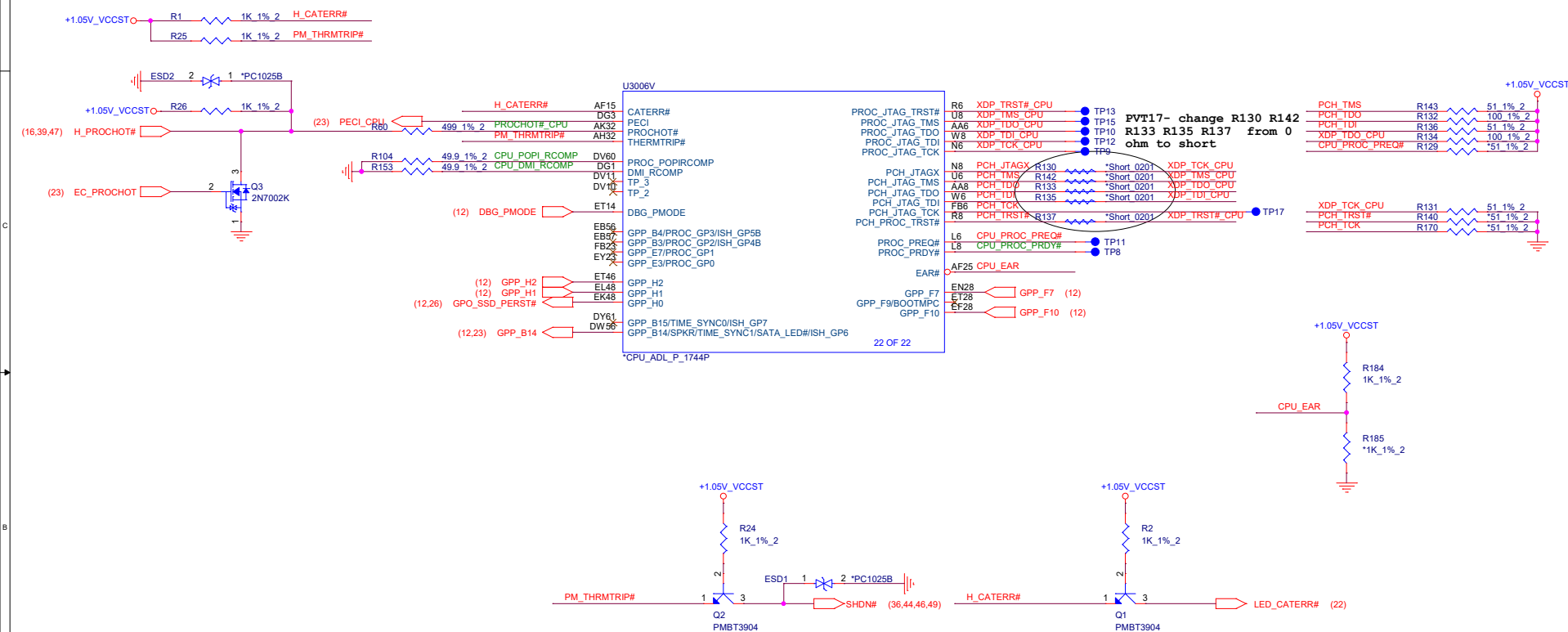


DDI Port Signal Mapping for HDMI*

Description	Signal Mapping		
	DDI	HDMI (Default non-reversed)	HDMI (Reversed)
Main Link (Tx)	DDI_TX0	HDMI Data_2	HDMI CLK
	DDI_TX1	HDMI Data_1	HDMI Data_0
	DDI_TX2	HDMI Data_0	HDMI Data_1
	DDI_TX3	HDMI CLK	HDMI Data_2

LPDDR4 (NIL)





20220318a



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PROJECT : TRA

Size	Document Number	Rev
	ADL-P 3/14 (CPU MISC/JTA)	1A

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This part should not contain any substances which are specified in EM-S303.





System Memory Size (Byte)	Part capacity (bit)	Vendor Part Number	Quanta P/N	D04	D03	D02	D01	D00	Memory Freq(MHz)
Samsung 8GB	Samsung 16Gb	K4U6E3S4AB-MGCL	AKD5RZST571	0	0	1	1	1	4266MHz
Samsung 16GB	Samsung 32Gb/DDP	K4UBE3D4AB-MGCL	AKD5PZST566	0	1	0	0	0	4266MHz
Samsung 32GB	Samsung 64Gb/QDP	K4UCE3Q4AB-MGCL	AKD5QZST544	0	1	0	0	1	4266MHz
Micron 8GB	Micron 16Gb/SDP	MT53E512M32D1NP-046 WT:B	AKD5RZSTL24	0	1	0	1	0	4266MHz
Micron 16GB	Micron 32Gb/DDP	MT53E1G32D2NP-046 WT:B	AKD5SWDTL36	0	1	0	1	1	4266MHz
Micron 32GB	Micron 64Gb/QDP	MT53E2G32D4NQ-046 WT:C	AKD5QWUTL11	0	1	1	0	0	4266MHz
Hynix 8GB	Hynix 16Gb	H54G46CYRBX267	AKD5SGSTW09	0	1	1	0	1	4266MHz
Hynix 16GB	Hynix 32Gb/DDP	H54G56CYRBX247	AKD5SGSTW11	0	1	1	1	0	4266MHz
Hynix 32GB	Hynix 64Gb	H54G68CYRBX248	AKD5TGSTW02	0	1	1	1	1	4266MHz

System Memory Size (Tb/s)	Part capacity (bit)	Vendor Part Number	Quanta P/N	LD4	LD3	LD2	LD1	LD0	Memory Fre (MHz)
Samsung 8GB	SDP 16Gb x16 (T7:NC)	K4AAG165WB-BCWE	AKD5LGUT517	0	0	0	0	0	3200MHz
Samsung 16GB	DDP 32Gb x16 (T7:VSS)	K4ABG165WB-MCWE	AKD5SGST518	0	0	0	0	1	3200MHz
Micron 8GB	SDP 16Gb x16 (T7:NC)	MT40A1G16TB-062E:F	AKD5RSGSTL25	0	0	0	1	0	3200MHz
Micron 16GB	DDP 32Gb x16 (T7:NC)	MT40A2G16TB-062E:B	AKD5SGSTL04	0	0	0	1	1	3200MHz
Hynix 8GB	SDP 16Gb x16 (T7:NC)	H5ANAG6NCJR-XNC	AKD5RGUTW38	0	0	1	0	0	3200MHz
Hynix 16GB	DDP 32Gb x16 (T7:VSS)	H5ANBG6NCMR-XNC	AKD5SGSTW12	0	0	1	0	1	3200MHz
Micron 16GB	DDP 32Gb x16 (T7:NC)	MT40A2G16TB-062E:F	AKD5SGSTL07	0	0	1	1	0	3200MHz

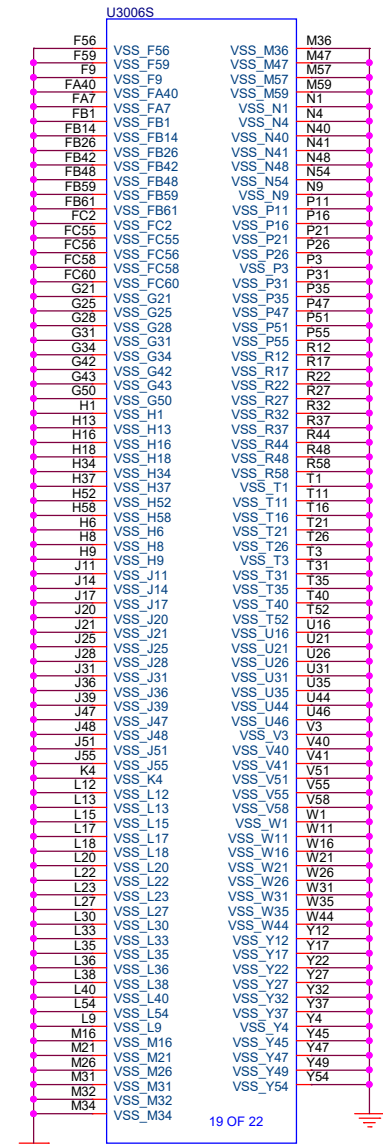
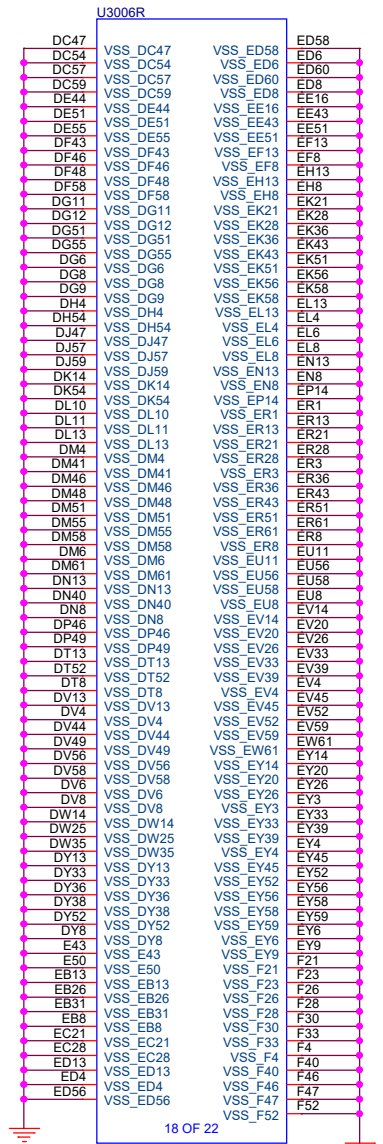
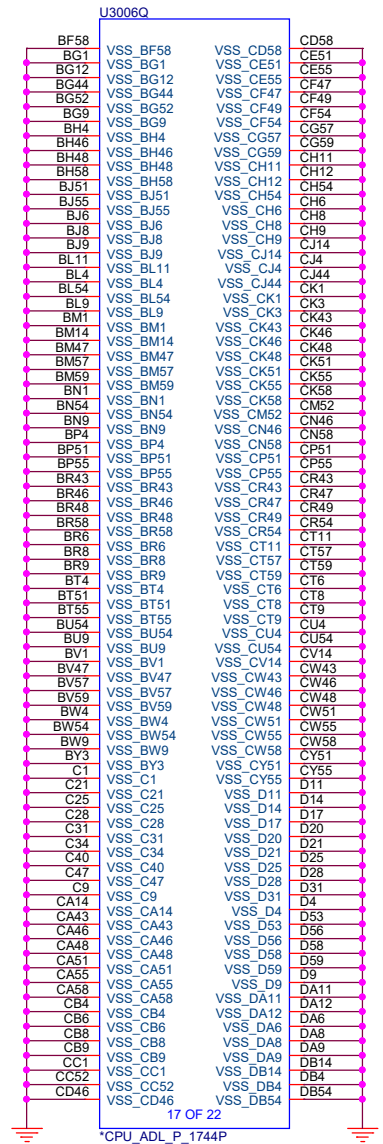
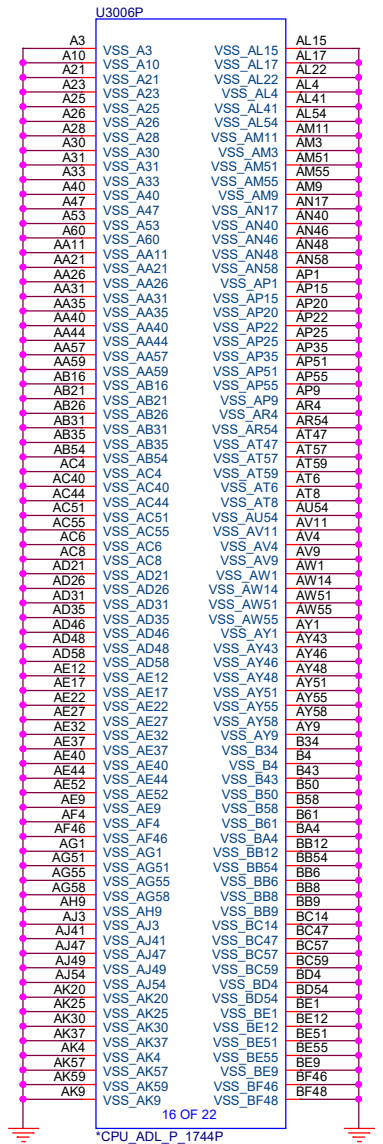
This part should not contain any substances which are specified in EM-S303.

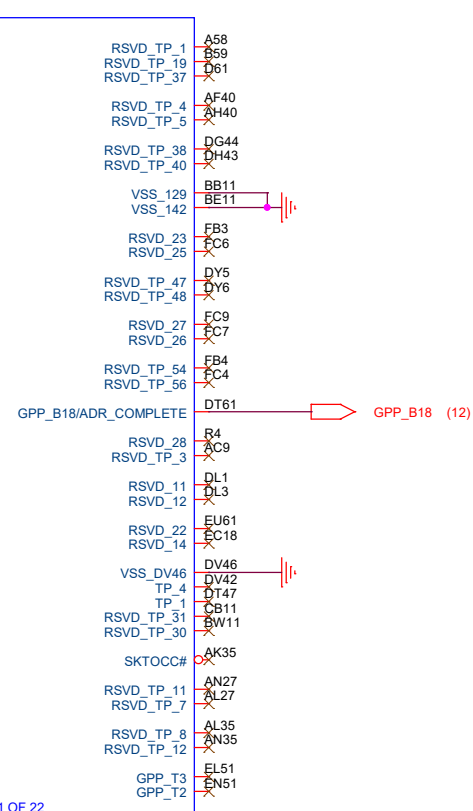
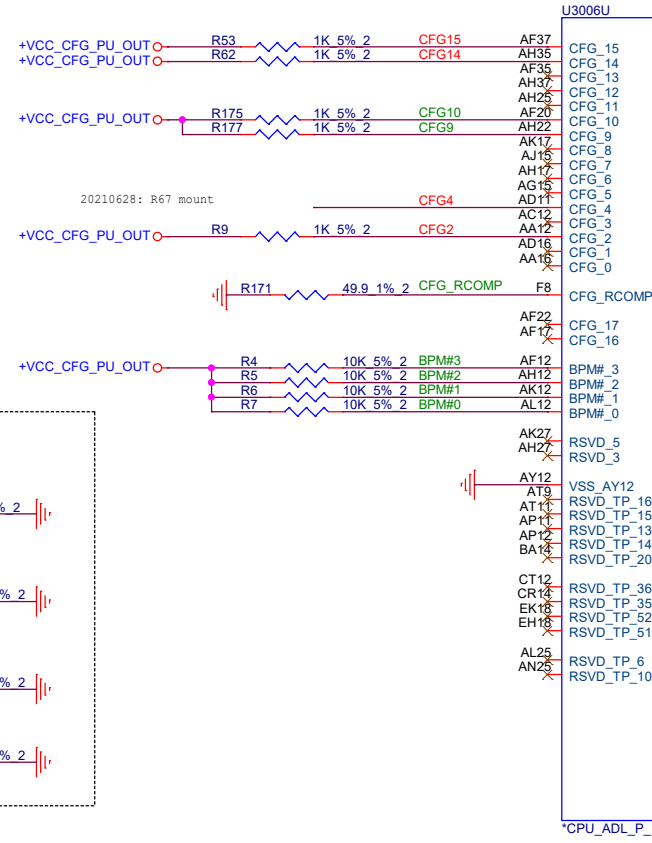
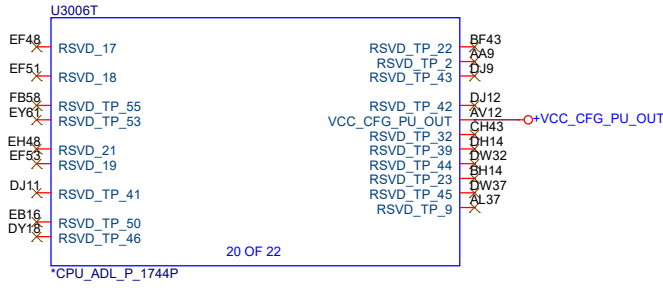


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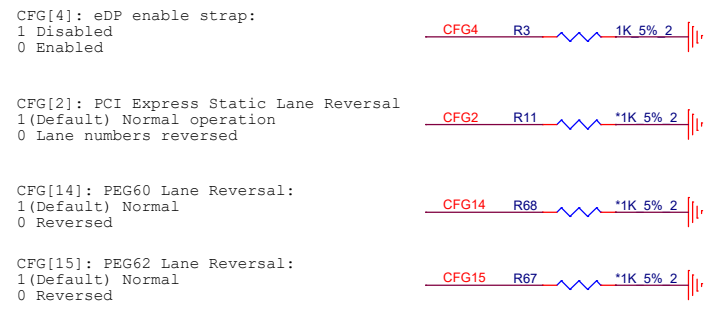
Size	Document Number	Rev
	ADL-P 6/14 (GND)	1A
Date:	Wednesday, February 01, 2023	Sheet 9 of 50

This part should not contain any substances which are specified in EM-S303.





CFG

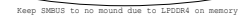
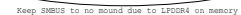


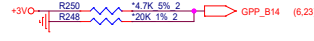
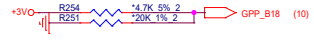
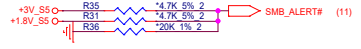
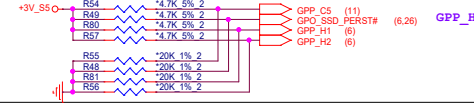
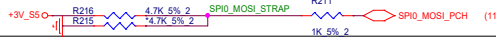
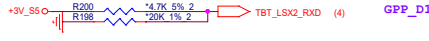
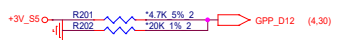
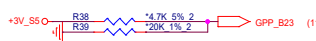
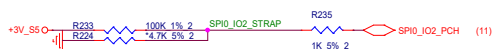
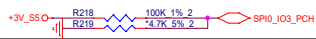
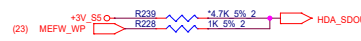
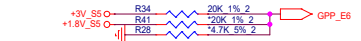
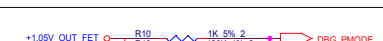
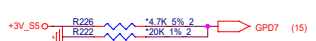
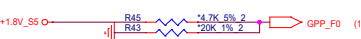
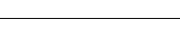
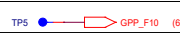
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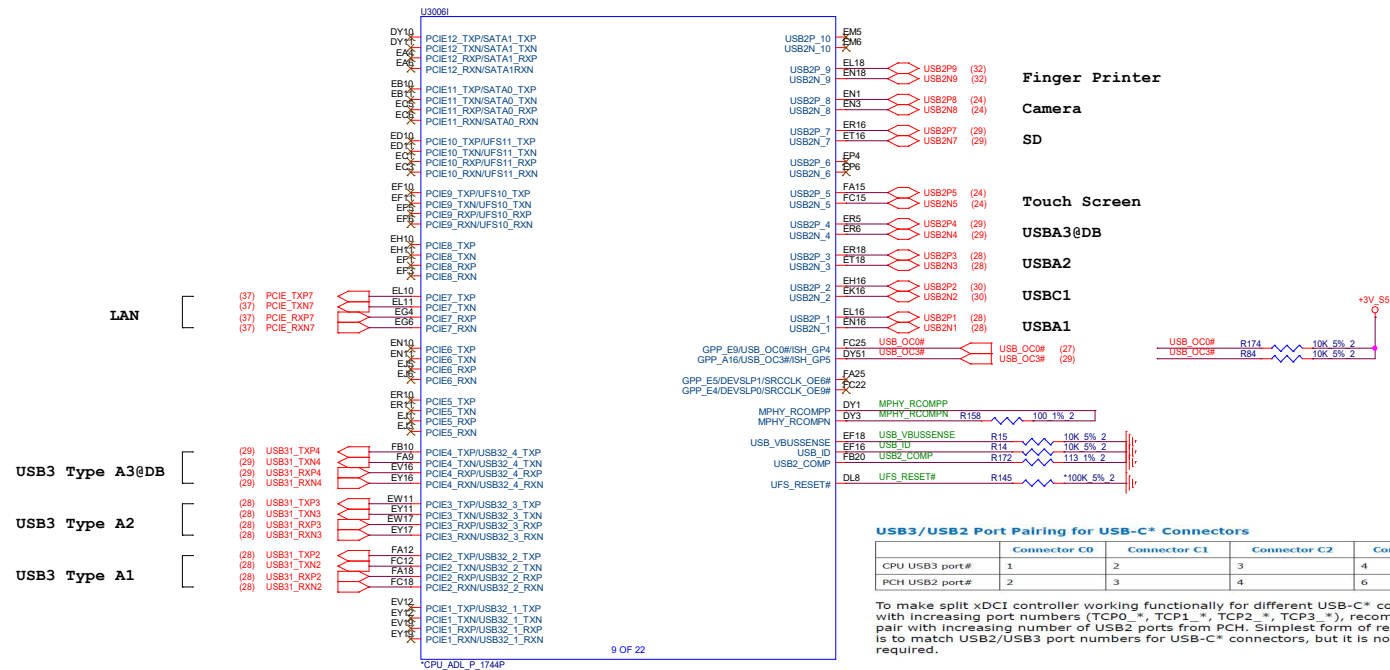
Size	Document Number	Rev
	ADL-P 7/14 (RSVD/XDP)	1A
Date:	Wednesday, February 01, 2023	Sheet 10 of 50

This part should not contain any substances which are specified in EM-S303.



GPIO	Usage	Sampled	FU/PD	Strap
GPP_B14	TOP SWAP 1: Enable 0: Disable(Default)	PCH_PWROK	20K PD(INT)	
GPP_B18	NO Reboot 1: Enable 0: Disable(Default)	PCH_PWROK	20K PD(INT)	
GPP_C2	TLS CONFIDENTIALITY 1: Enable 0: Disable(Default)	RSMRST#	20K PD(INT)	
GPP_C5(0) GPP_H0(1) GPP_H1(2) GPP_H2(3)	Boot Configuration 0000:BIOS/CSME on SPI, eSPI enable 0010:BIOS/CSME on SPI, eSPI disable 0100:BIOS/CSME on eSPI 1100:BIOS on eSPI, CSME on SPI	RSMRST#	20K PD(INT) 20K PD(INT) 20K PD(INT) 20K PD(INT)	
SPI0_MOSI	Reserved	RSMRST#	4.7K PU@DS	
GPP_D10	DDP3 I2C Power Configuration 1: DDP3 I2C/TBT_LSX2=3.3V 0: DDP3 I2C/TBT_LSX2=1.8V	RSMRST#	20K PD(INT)	
GPP_D12	DDP4 I2C Power Configuration 1: DDP4 I2C/TBT_LSX3=3.3V 0: DDP4 I2C/TBT_LSX3=1.8V	RSMRST#	20K PD(INT)	
GPP_B23	CPUNSSC Clock Frequency 1: 19.2MHz 0: 38.4MHz(Default)	RSMRST#	20K PD(INT)	
SPI0_IO2	Reserved	RSMRST#	100K PU@3.3V(EXT) 75K PU@1.8V(EXT)	
SPI0_IO3	Reserved	RSMRST#	100K PU@3.3V(EXT) 75K PU@1.8V(EXT)	
GPP_R2	Flash Security Override 1: Disable 0: Enable(Default)	PCH_PWROK	20K PD(INT)	
GPP_E6	Reserved	RSMRST#	100K PU@3.3V(EXT) 75K PU@1.8V(EXT)	
GPP_E19	DDP1 I2C Power Configuration 1: DDP1 I2C/TBT_LSX0=3.3V 0: DDP1 I2C/TBT_LSX0=1.8V	RSMRST#	20K PD(INT)	
GPP_E21	DDP2 I2C Power Configuration 1: DDP2 I2C/TBT_LSX1=3.3V 0: DDP2 I2C/TBT_LSX1=1.8V	RSMRST#	20K PD(INT)	
DBG_PMODE	Reserved	RSMRST#	20K PU(INT)	
GPD7	Reserved	DSW_PWROK	20K PD(INT)	
GPP_F0	X'tal Frequency Selection 1: 24MHz 0: 38.4MHz(Default)	RSMRST#	20K PD(INT)	
GPP_F2	M2 CNVi Mode Select 1: Integrated CNVi Disable 0: Integrated CNVi Enable	RSMRST#	Note: Intel WIFI module iPU down 1K ohm Platform is Pull high 100K ohm Weak PU(EXT)	
GPP_F7	Reserved	RSMRST#	20K PD(INT)	
GPP_F10	Reserved	RSMRST#	20K PD(INT)	
SPIVCCIOSEL	SPI Operation Voltage 1: SPI Voltage =1.8V 0: SPI Voltage =3.3V	DSW_PWROK	4.7K PU@DSW_PWROK(EXT) 4.7K PD@GND(EXT)	

20220216d

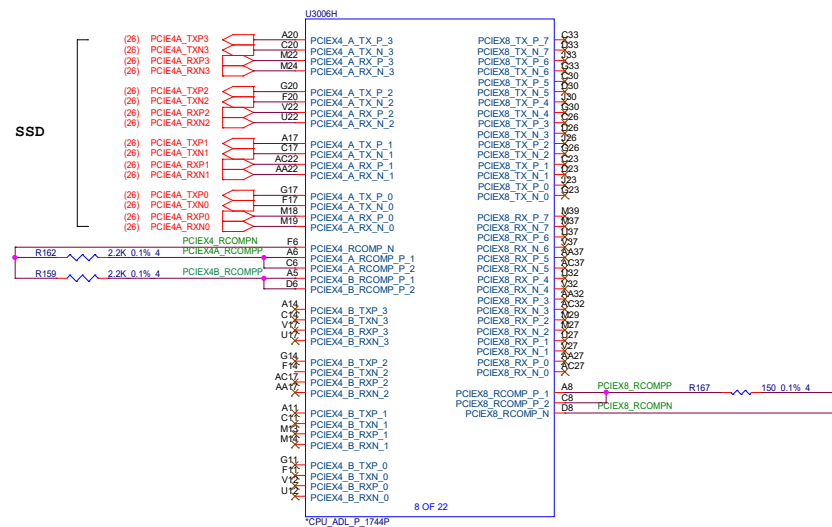


USB3.0 Port Mapping Table

USB3.0	Function
PORT-1	Un-used
PORT-2	USB3.1 GEN1 Type A1
PORT-3	USB3.1 GEN1 Type A2
PORT-4	USB3.1 GEN1 Type A3@DB

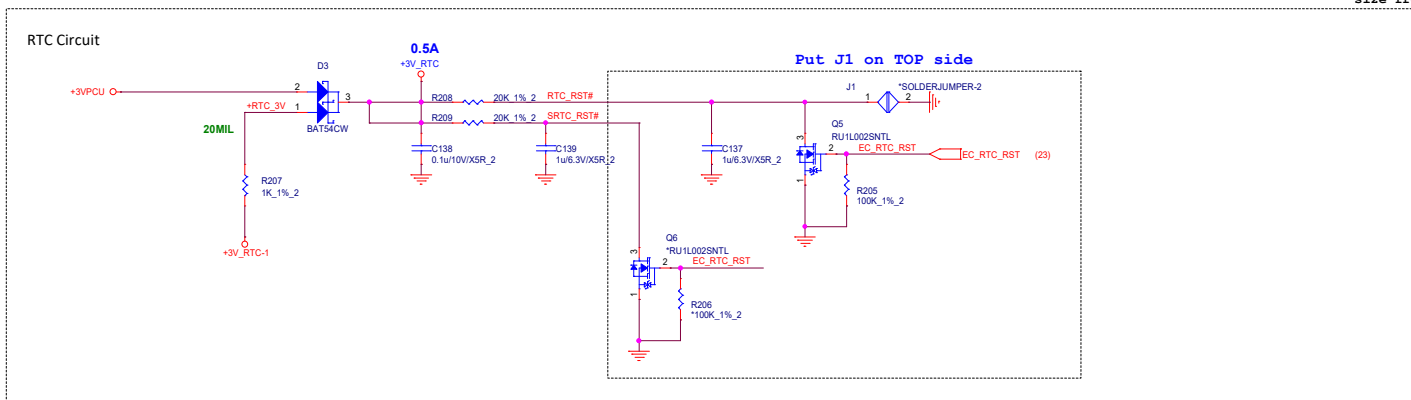
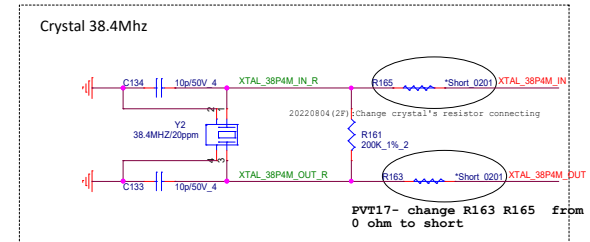
PCI-E Port Mapping Table

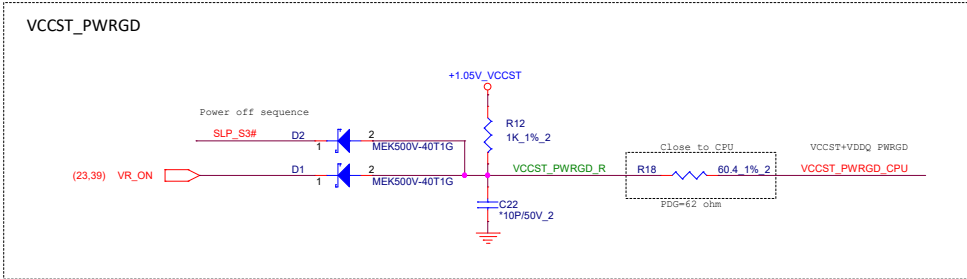
PCI-E Port	Function	CLK RQ Port	Function
Port1	Un-used	Port0	Un-used
Port2	USB	Port1	Un-used
Port3	USB	Port2	Un-used
Port4	USB	Port3	SSD
Port5	Un-used	Port4	Un-used
Port6	Un-used	Port5	Un-used
Port7	G_LAN	Port5	Un-used
Port8	Un-used		
Port9	Un-used		
Port10	Un-used		
Port11	Un-used		
Port12	Un-used		



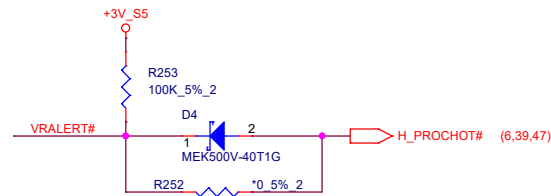
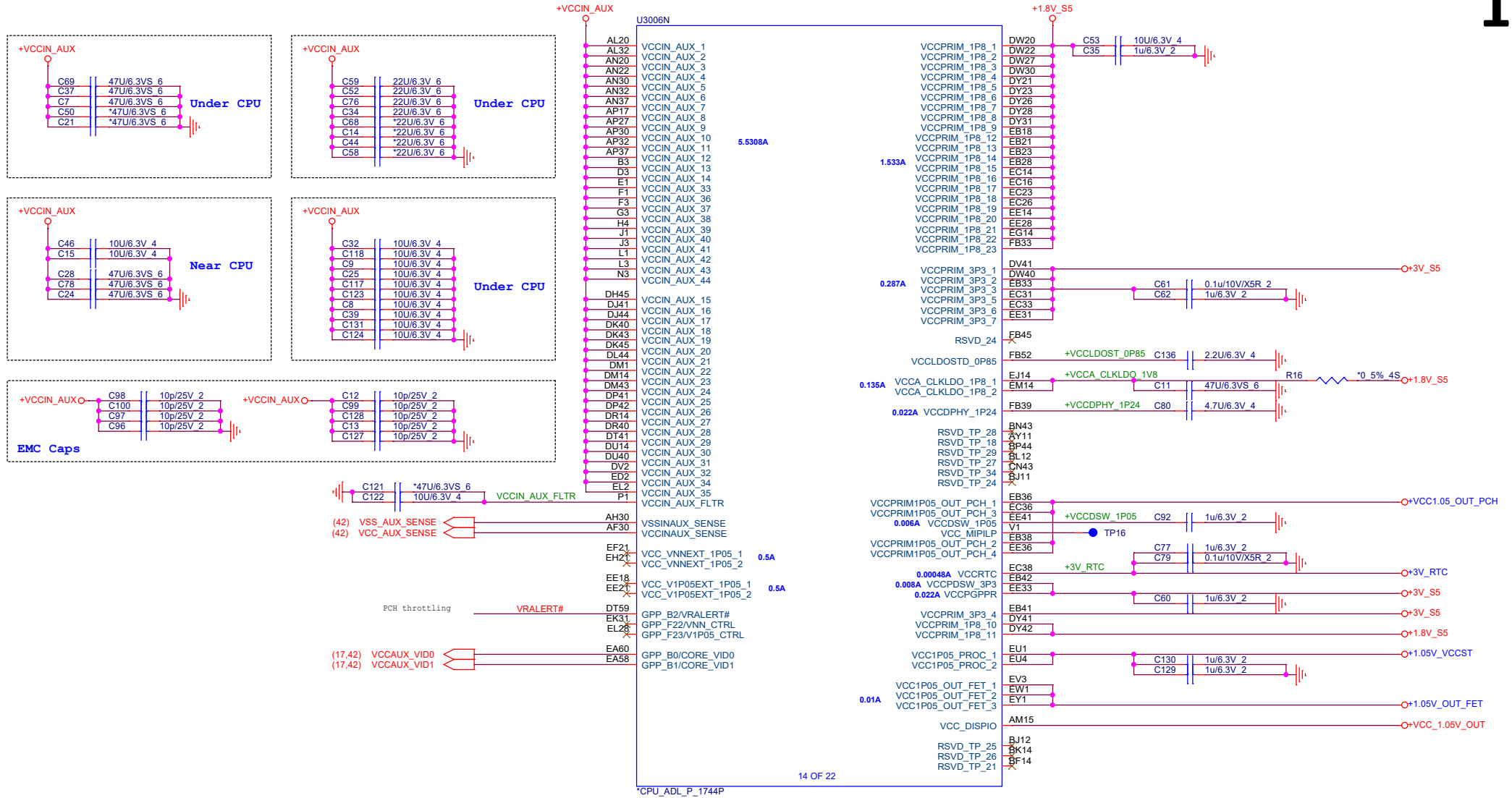
USB2.0 Port Mapping Table

USB2.0	Function
PORT-1	USB1
PORT-2	USBC
PORT-3	USB2
PORT-4	USB3@DB
PORT-5	Touch Screen
PORT-6	NC
PORT-7	SD
PORT-8	Camera
PORT-9	Finger Print
PORT-10	NC





$$5.182A + \text{Adder}(0.0724x2 + 0.102x2) = 5.5308A$$

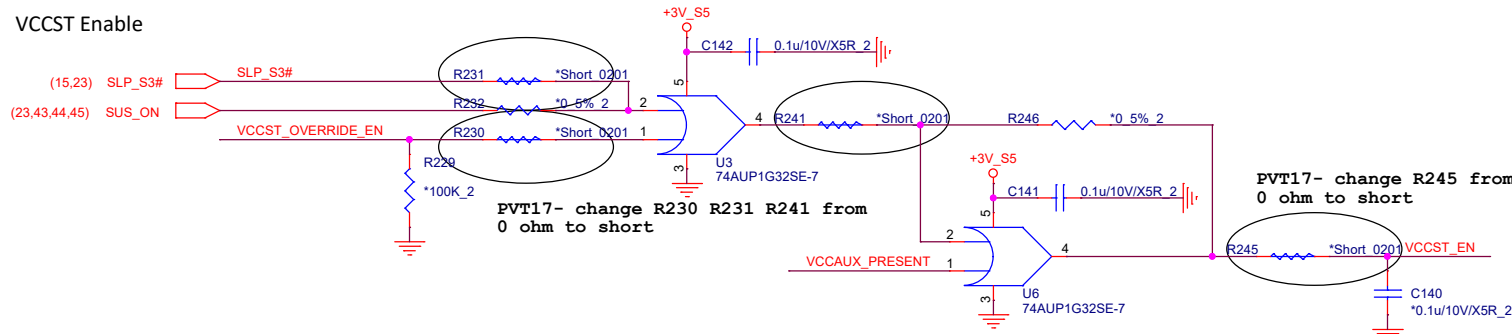


This part should not contain any substances which are specified in EM-5303.

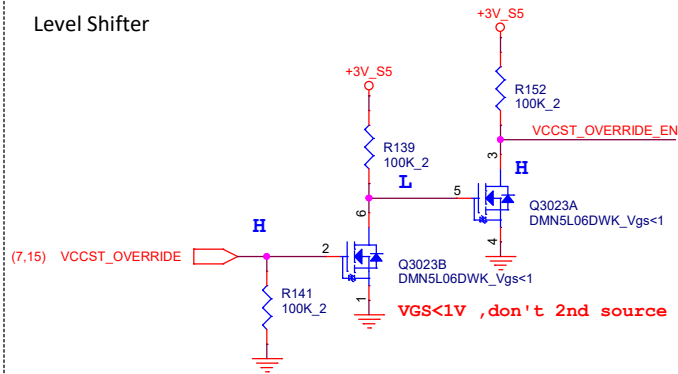
20220216d

		Quanta Computer Inc. PROJECT : TRA	
		ADL-P 13/14(PCH POWER)	
Size	Document Number	Rev 1A	
Date:	Wednesday, February 01, 2023	Sheet	16 of 50

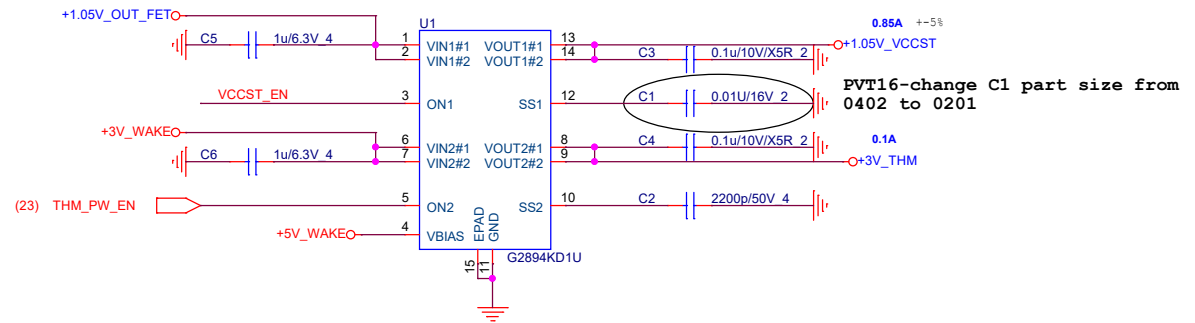
VCCST Enable



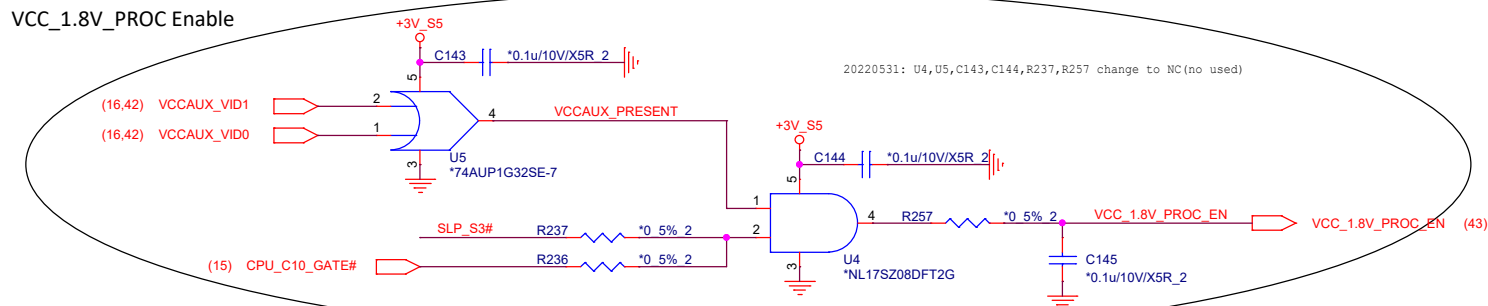
Level Shifter



Source from PCH +=VCC1P05_OUT_FET



VCC_1.8V_PROC Enable



20220531a



Quanta Computer Inc.

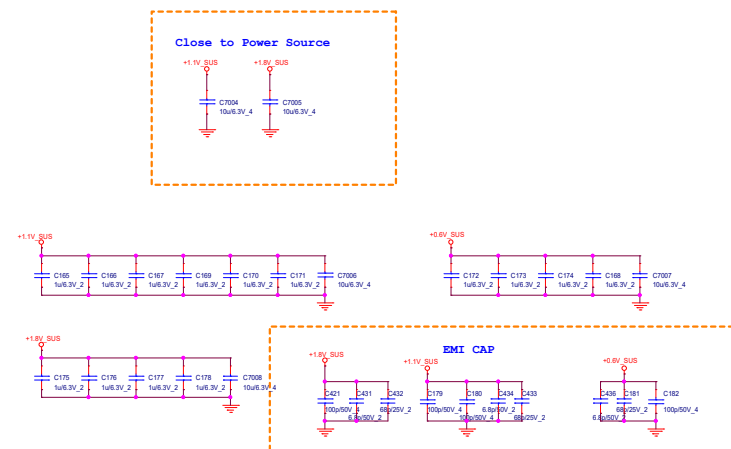
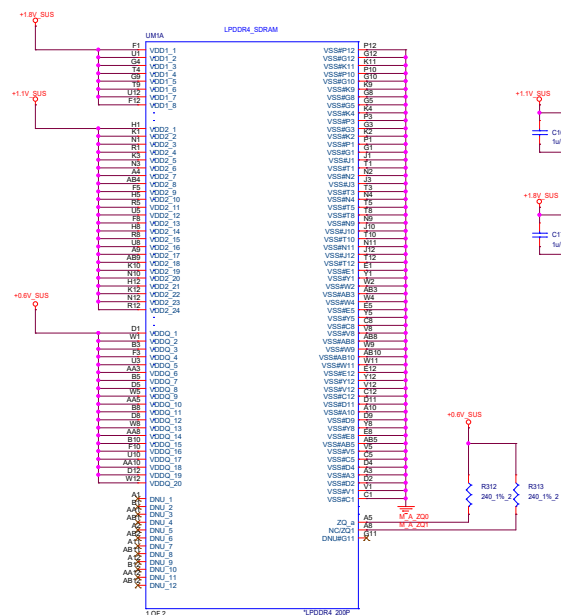
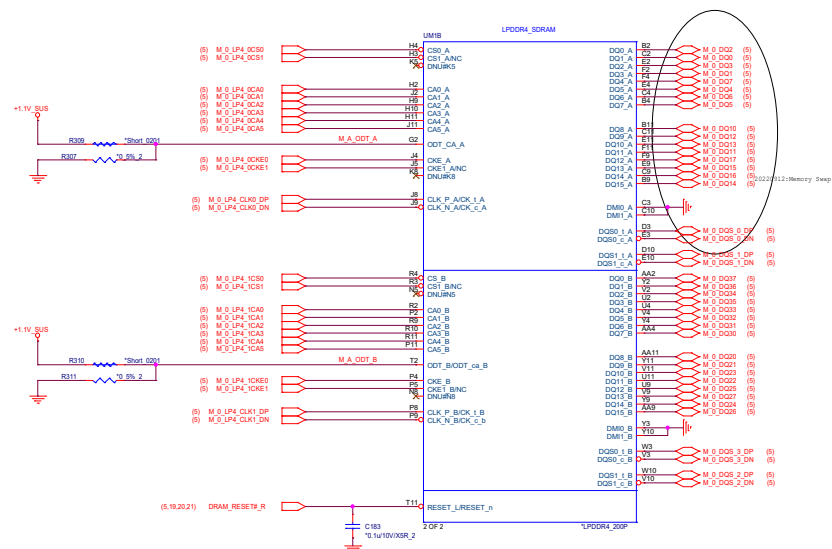
PROJECT : TRA

Size	Document Number	Rev
	ADL-P VCCST/VCC1.8_PROC	2A

Date: Wednesday, February 01, 2023 Sheet 17 of 50

This part should not contain any substances which are specified in EM-S303.

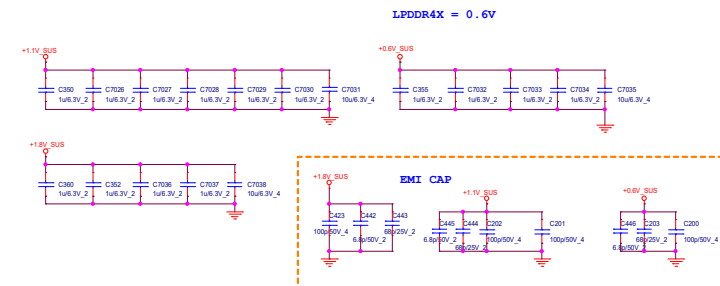
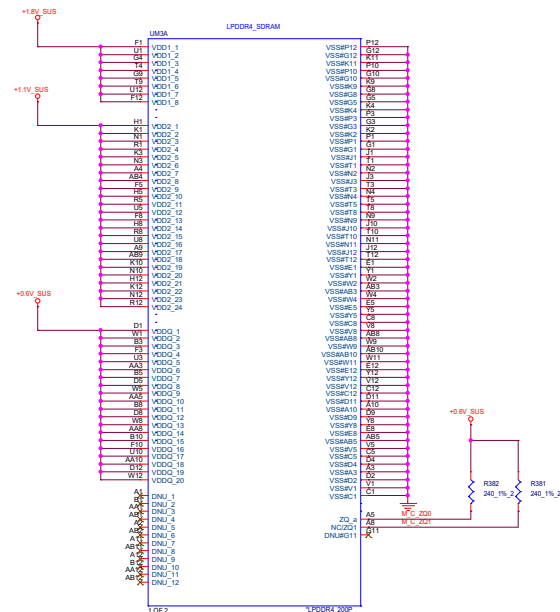
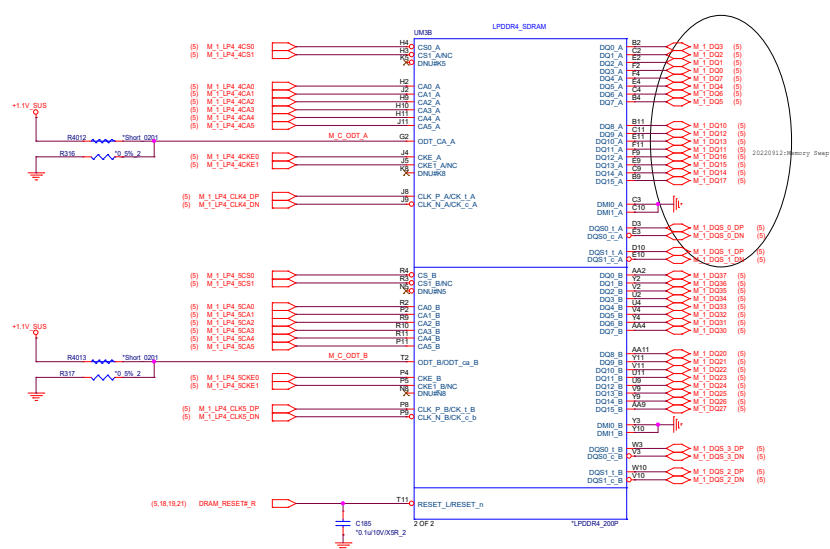
Non-NB Footprint:bga200-hynix-h9hcnbnbkmalhr-0_5s
LPDDR4 CH-A



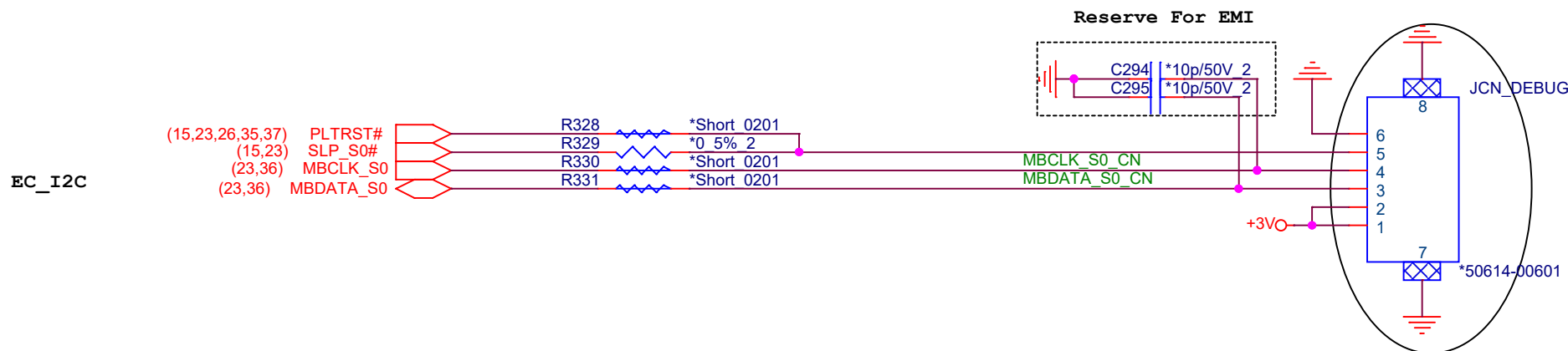
LPDDR4 CH-B



Non-NB Footprint:bga200-hynix-h9hcnnnbkmalhr-0_5s
LPDDR4 CH-C

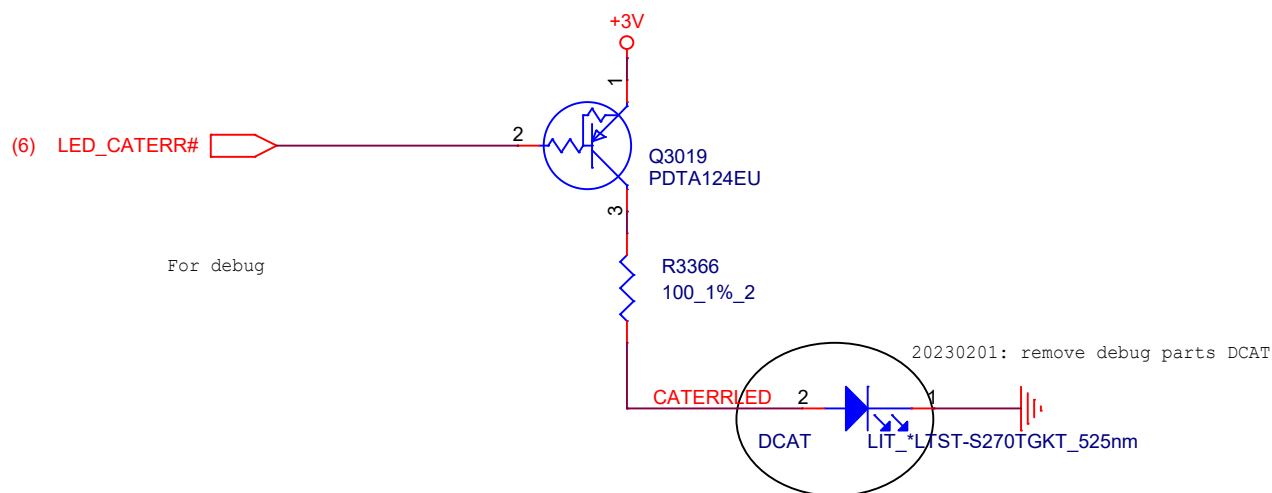


20210709b



CATERR LED

20230201: remove debug parts JCN_DEBUG



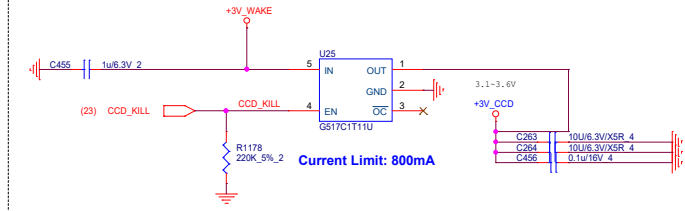
20230201: remove debug parts DCAT

20220303b

		Quanta Computer Inc.	
PROJECT : TRA		Debug	
Size	Document Number	Rev 1A	
Date:	Wednesday, February 01, 2023	Sheet	22 of 50

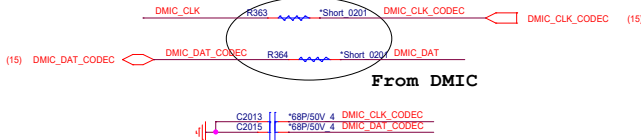
Camera Power

MAX Current: 200mA (R1VCB1003)
150mA (R1VCB1004)



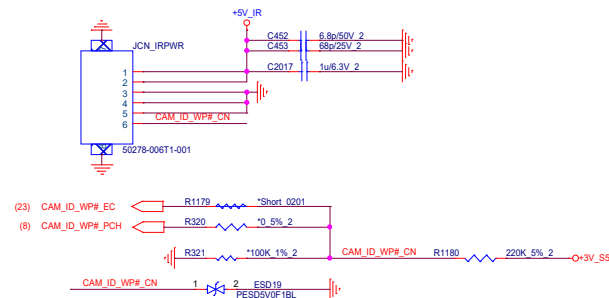
PVT17- change R363 R364 from 0 ohm to short

From PCH



From DMIC

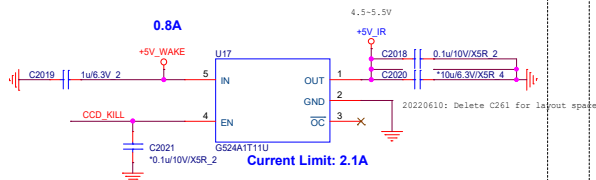
For Hello Camera



Host GPIO Direction	Host GPIO Value	Connected Camera	Result
IN	LOW	Hello cam	Hello cam detected
IN	HIGH	HD cam	HD cam detected
OUT	LOW	Hello cam	Write protected
OUT	HIGH	Hello cam	Write enabled

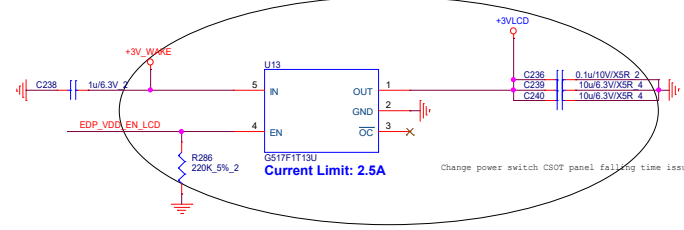
IR Camera (5V) Power

MAX Current: 740mA (R1VCB1004)



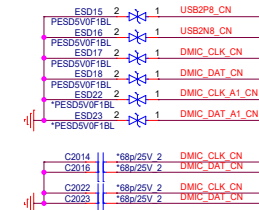
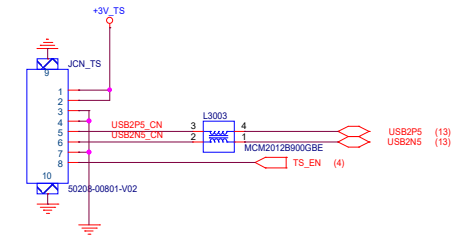
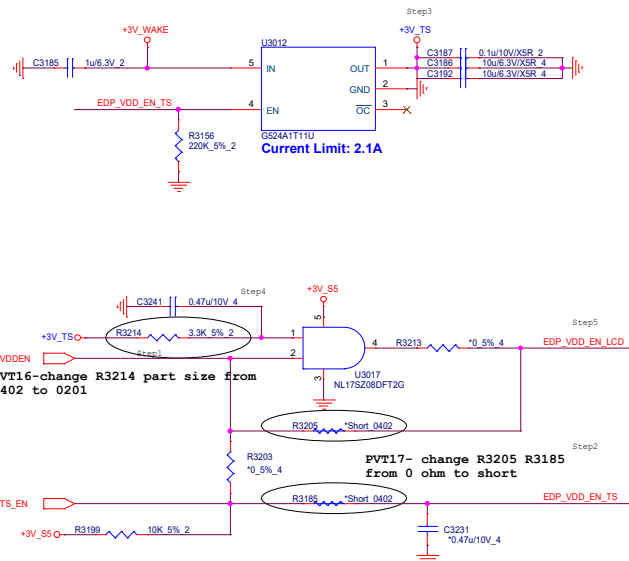
Panel Power

MAX Current: 483mA (for UHD)

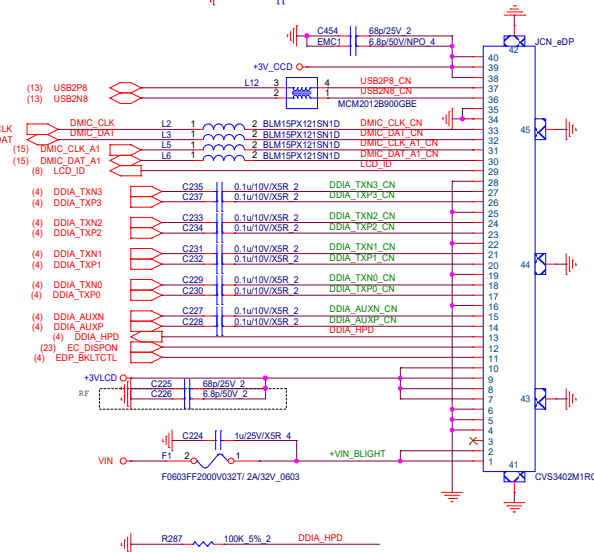


Size	I/F	Vendor	Vendor PN	Quanta PN
14"	eDP	Sharp	LQ140M1JW61	AA140M1J002
14"	eDP	ADT	B140HAN04.0	AA140HAN080
16"	eDP	BOE	NV160UM-N46	AA160UM008
16"	eDP	ADT	B160UAN03.3	AA160UAN004
16"	eDP	CSOT	MNG007DA1-3	AA007DA1007

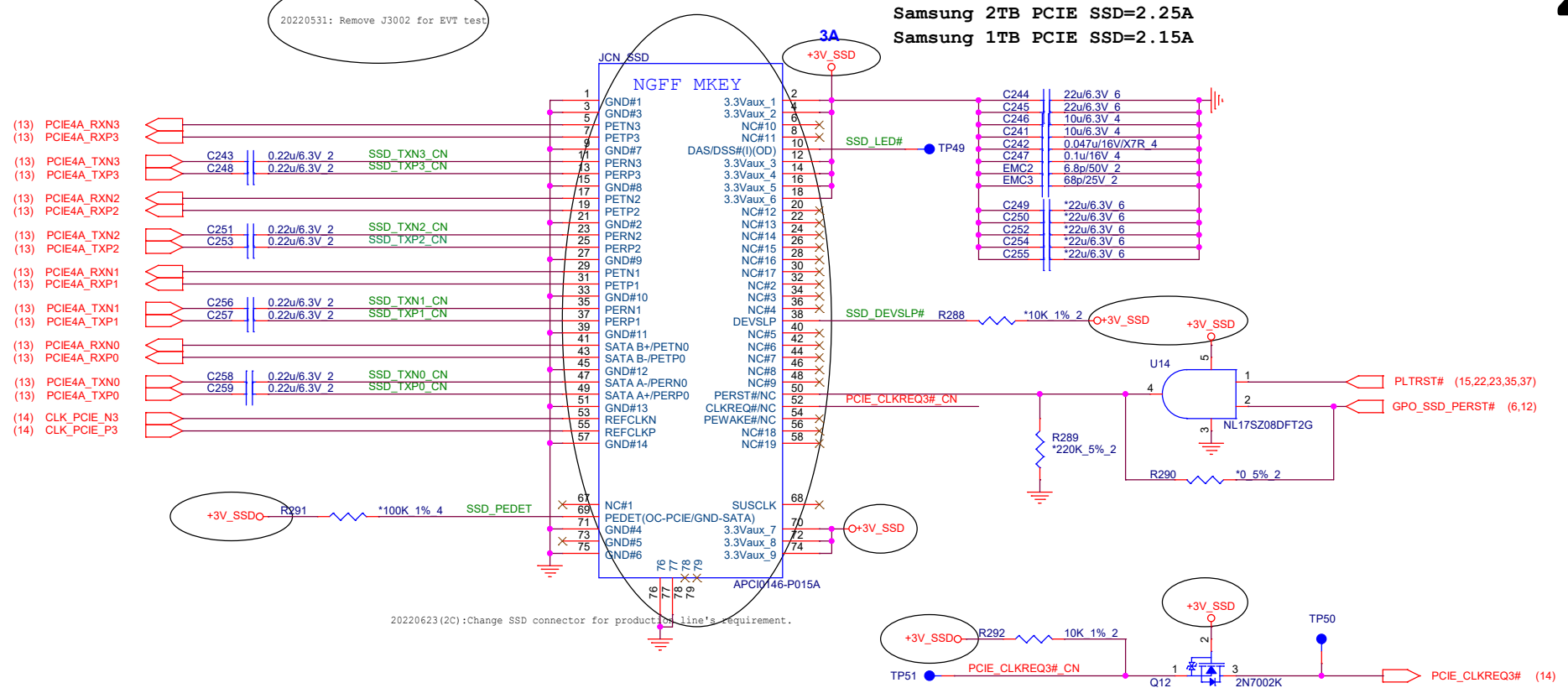
	13"	14"	16"
KB_ID	1	1	0
LCD_ID	0	1	0



eDP



20220623a



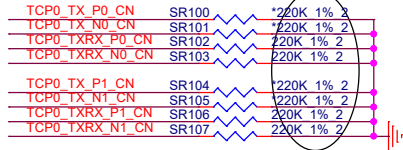
Size	I/F	Module ME	Vendor	Vendor PN
128GB	PCIE3		Samsung	MZVLQ128HCHQ-00B07
256GB	PCIE3		Samsung	MZVLQ256HBJD-00B07
512GB	PCIE3		Samsung	MZVLQ512HBLU-00B07
256GB	PCIE4		Samsung	MZVL2256HCHQ-00B07
512GB	PCIE4		Samsung	MZVL2512HCJQ-00B07

This part should not contain any substances which are specified in EM-S303.

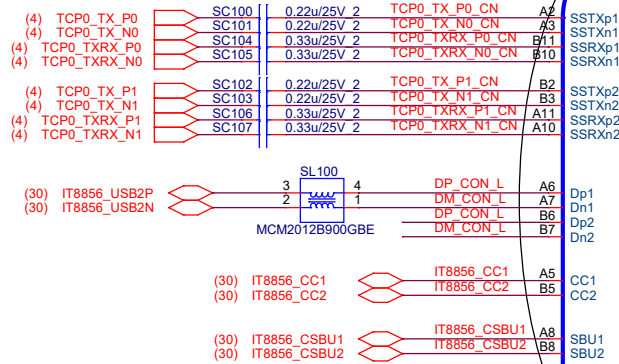
 Quanta Computer Inc. PROJECT : TRA		Rev
		2C
Size	Document Number	
SSD		
Date:	Wednesday, February 01, 2023	Sheet 26 of 50

USB Type-C

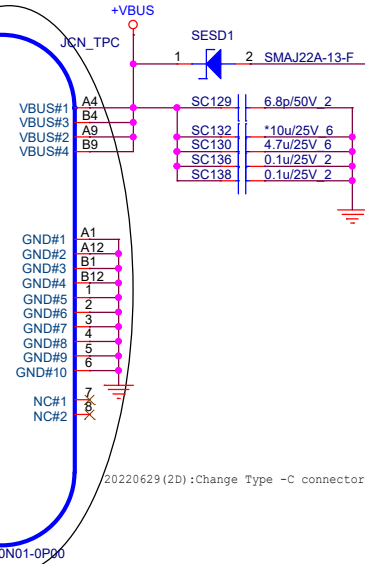
20220531: remove SR100, SR101,SR104,SR105 for SI



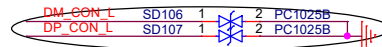
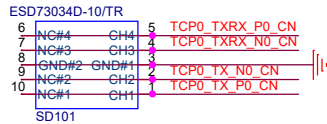
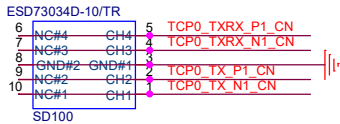
USB3.2 10Gbps
DP1.4a 8.1Gbps



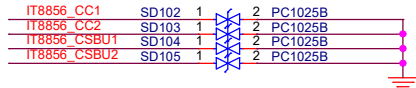
VBUS : 5V



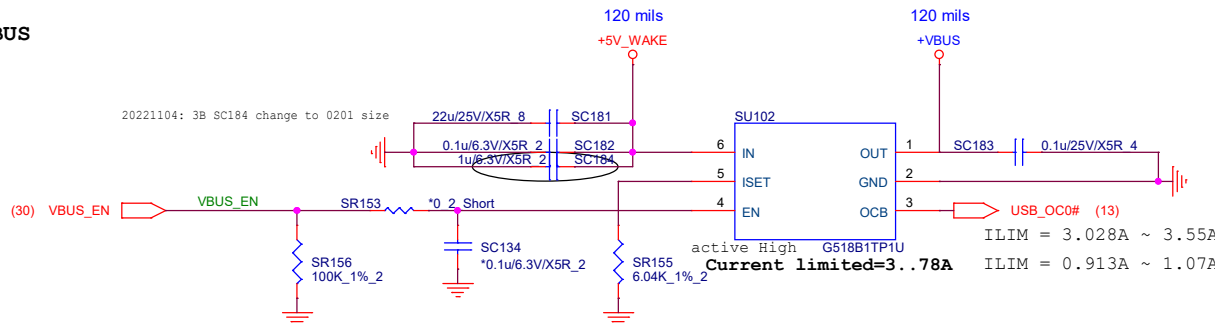
20220629(2D):Change Type -C connector footprint for SMT quality.



20220512: SD19 change to SD106/SD107 for Voltage issue



VBUS



This part should not contain any substances which are specified in EM-S303.

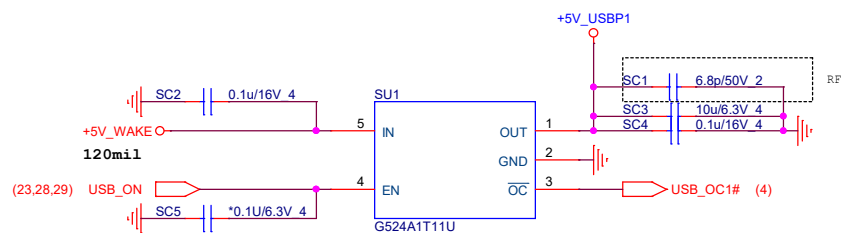
26



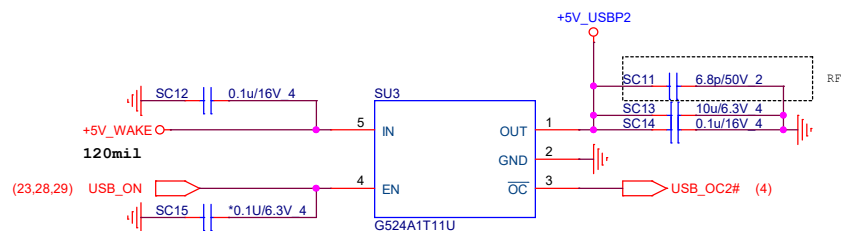
Quanta Computer Inc.
PROJECT : TRA

Size	Document Number	Rev
	Type C MUX PS8802/CON	3B
Date:	Wednesday, February 01, 2023	Sheet 27 of 50

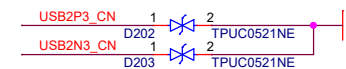
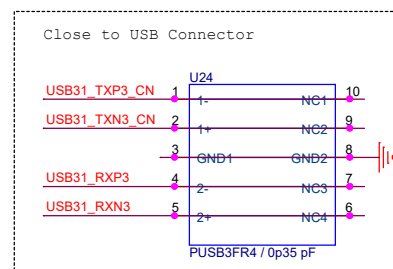
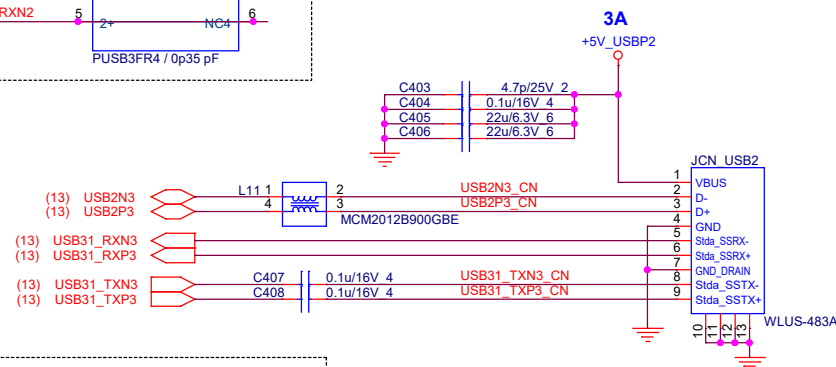
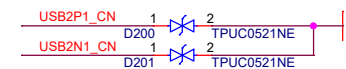
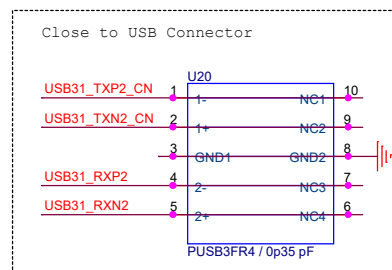
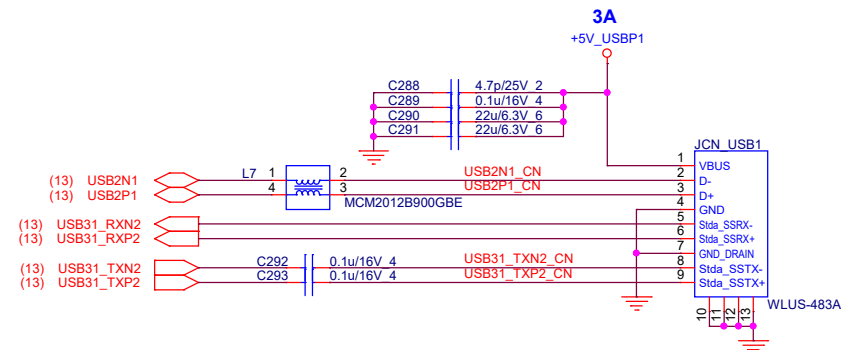
20221104a



For VAIO Power Switch(Current Limit=2.1A)



For VAIO Power Switch(Current Limit=2.1A)



20220309a

		Quanta Computer Inc.	
		PROJECT : TRA	
Size	Document Number	Rev 1A	
TYPE A USB3 Ports			
Date:	Wednesday, February 01, 2023	Sheet	28 of 50

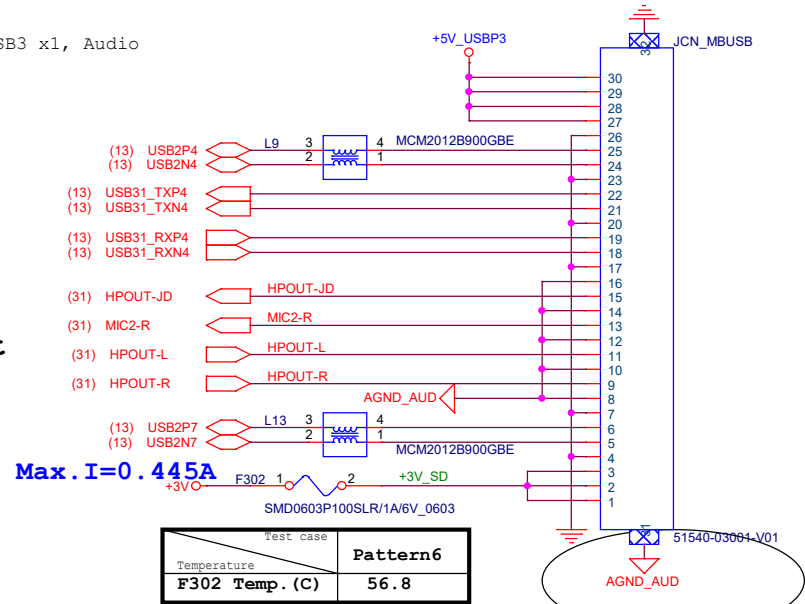
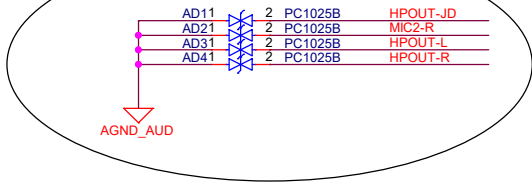
This part should not contain any substances which are specified in EM-S303.

Remove Hall sensor samll board

Micro SD, USB3 x1, Audio

Add TVS diode protect Audio codec.

CTIA TRRS Headset

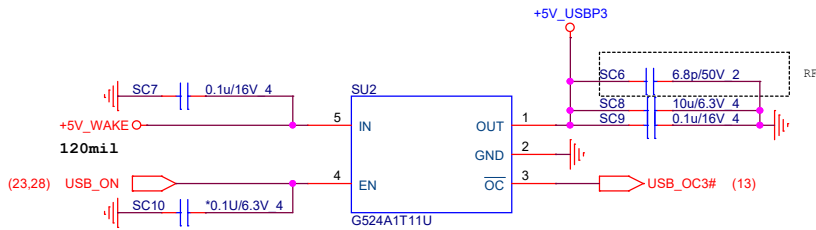


Max. I=0.445A

Test case	Pattern6
Temperature	
F302 Temp. (C)	56.8

Temperature	50C	60C
Current		
Fuse I- hold current(A)	0.74	0.65

change pin 31 of JCN_MBUSB for Audio AGND_AUD sharp.



For VAO Power Switch(Current Limit=2.1A)

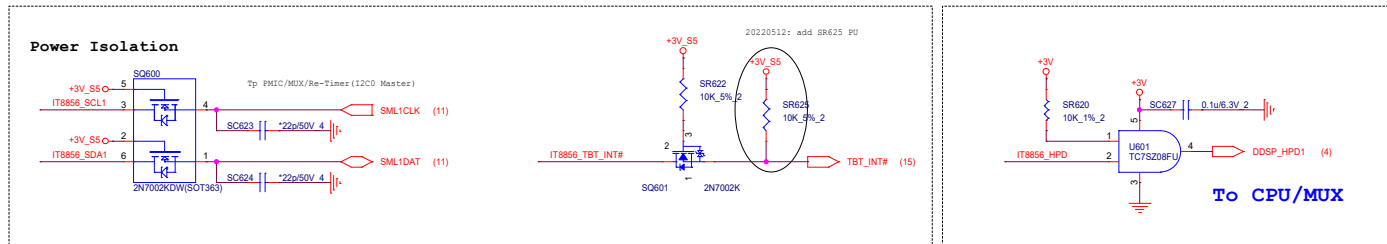
This part should not contain any substances which are specified in EM-S303.

20220304a

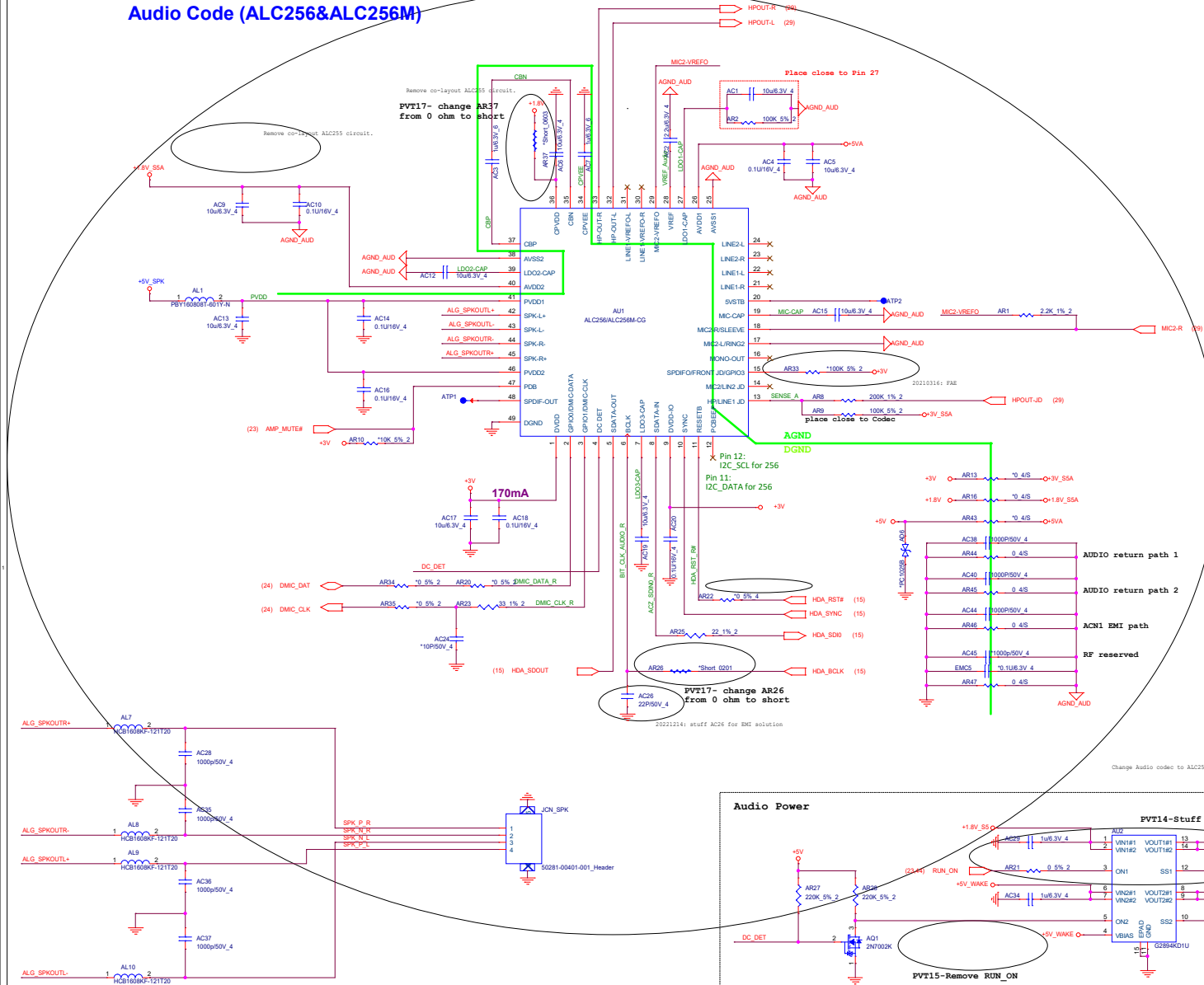


Quanta Computer Inc.
PROJECT : TRA

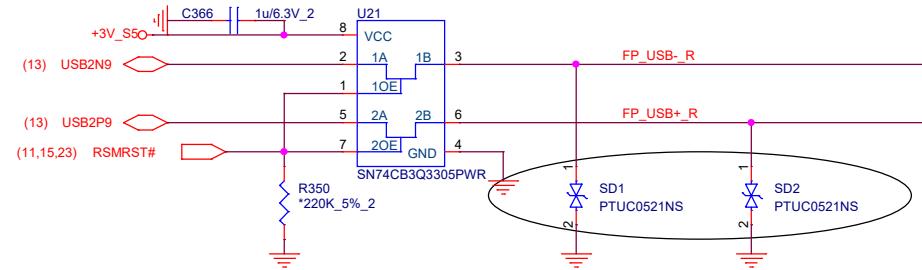
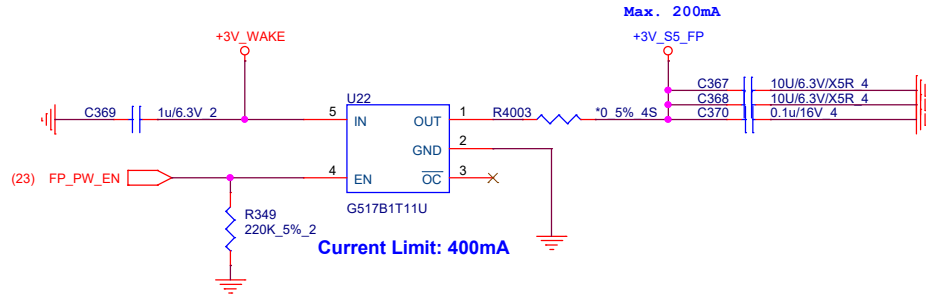
Size	Document Number	Rev
	MB to USB/SENSOR	3A
Date:	Wednesday, February 01, 2023	Sheet 29 of 50



Audio Code (ALC256&ALC256M)

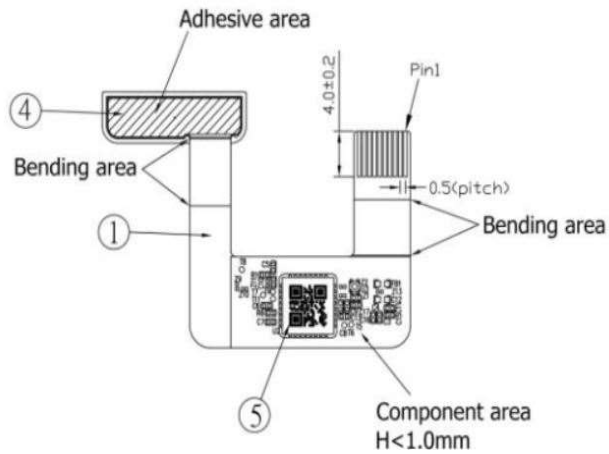


This part should not contain any substances which are specified in EN-6303.



20221129: add SD1 SD2 2nd source PTUC0521NS (BC0521NS200) / PESD5V0F1BSF (BC5V0F1B202)

Bottom view



2.2 USER INTERFACE

Pin Assignments

- Pin 1 = E_GND
- Pin 2 = USB_D+
- Pin 3 = USB_D-
- Pin 4 = GND
- Pin 5 = EC_STATE
- Pin 6 = EC_CTRL
- Pin 7 = (NC)
- Pin 8 = RST_N
- Pin 9 = FP_PRS#
- Pin 10 = VBUS(3V3)

Pull-up & Pull-down Resistors

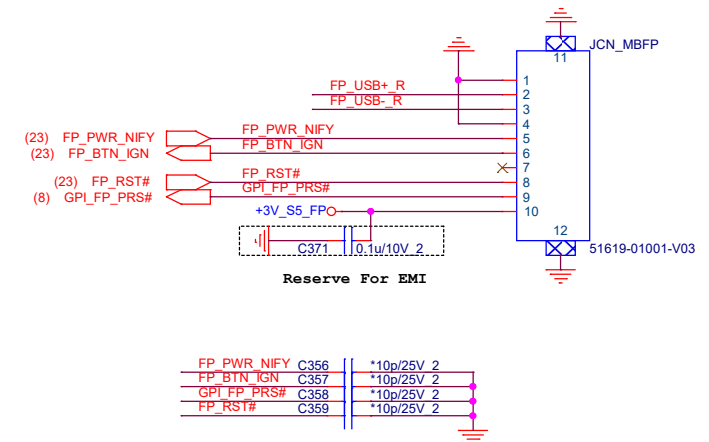
FP_PRS# (FP presence pin)	
High	Non Fingerprint SKU
Low	Fingerprint SKU

Symbol	Parameter	Minimum	Typical	Maximum	Unit
RPU	Pull- up resistor on RST_N input	34	-	100	kΩ
RPD	Pull-down resistor on TEST_CLK input	30	-	107	kΩ
RPDOTP	Pull-down resistor on VDDOTP	128	256	512	kΩ

MB Pull UP 100K , Fingerprint Module pull down 10K

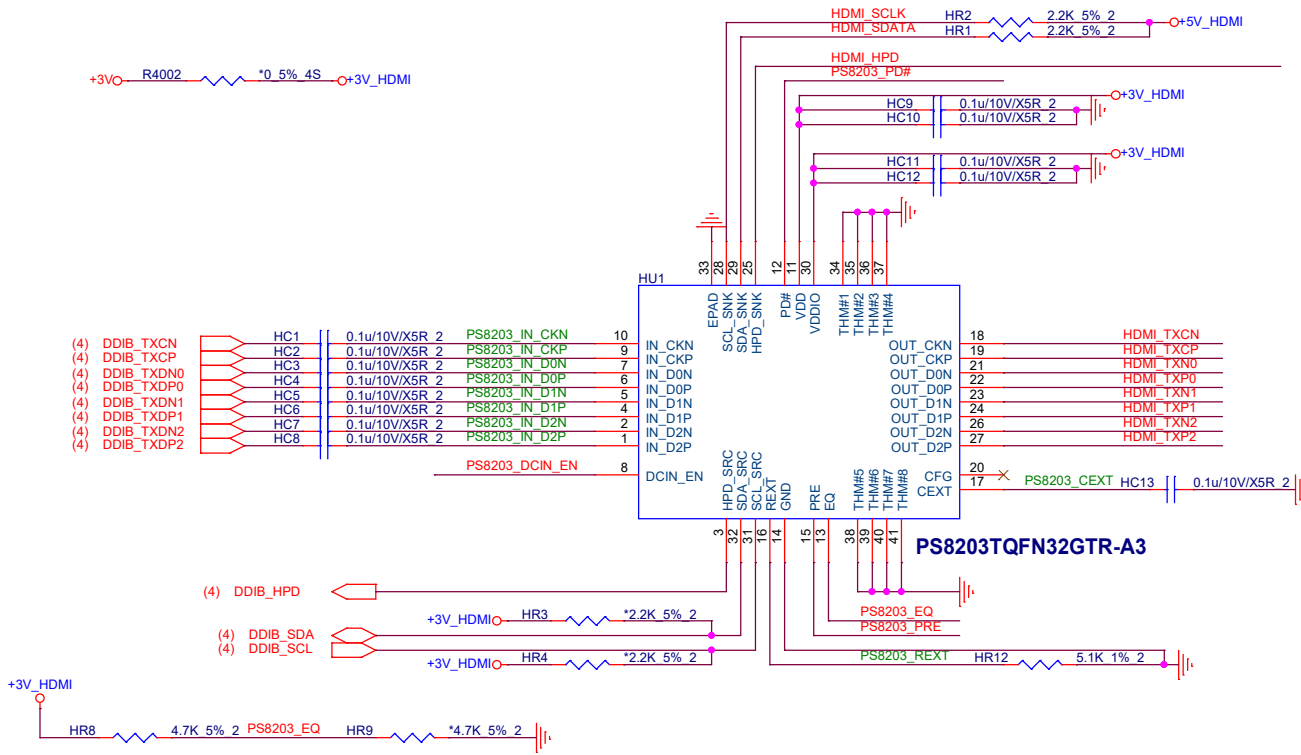
This part should not contain any substances which are specified in EM-S303.

Fingerprint Module



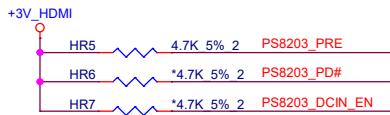
HDMI 1.4b

32



EQ = Receiver equalization setting; Internal pull down at ~150Kohm, 3.3V I/O

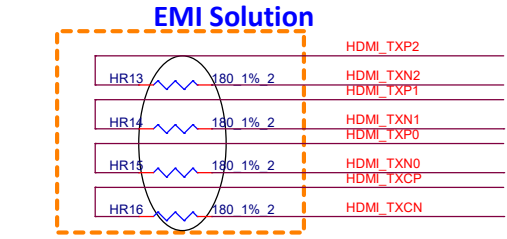
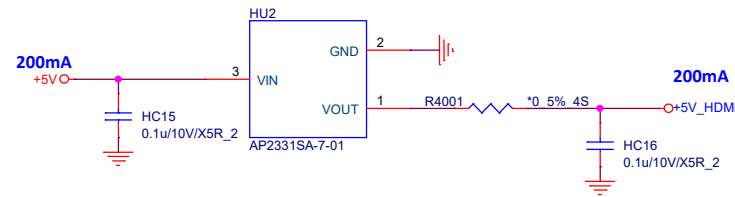
- L : Programmable EQ for channel loss up to 12.4dB @ 3.0Gbps (default)
- H : Programmable EQ for channel loss up to 4.3dB @ 3.0Gbps
- M : Programmable EQ for channel loss up to 8.6dB @ 3.0Gbps



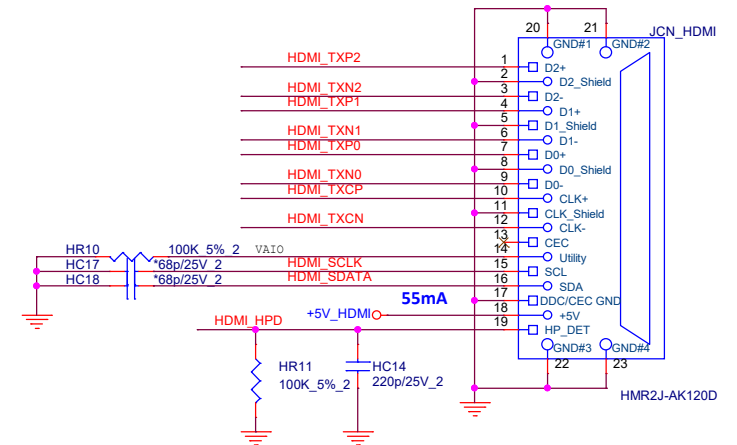
PRE = Output pre-emphasis setting for data; Internal pull down at ~150Kohm, 3.3V I/O
L : No pre-emphasis (default)
H : 2.5dB pre-emphasis

PD# = Chip power down. Active LOW; Internal pull up at ~150Kohm, 3.3V I/O
L : Normal operation (default)
L : Chip power down

DCIN_EN = DC coupling enable; Internal pull down at ~150Kohm, 3.3V I/O
L : AC coupling (default)
H : DC coupling input



20220602: HR13-HR16 change from 150 ohm to 180 ohm for SI

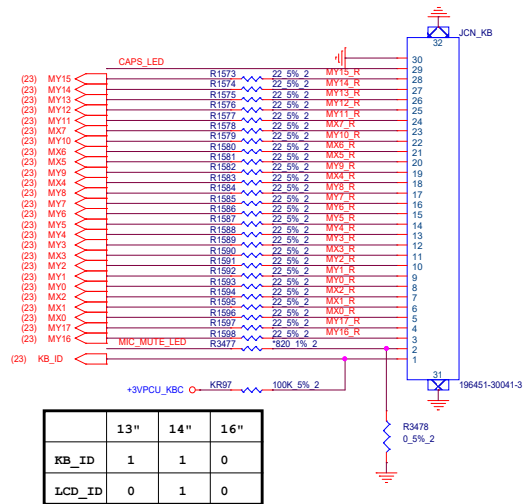


20220602a

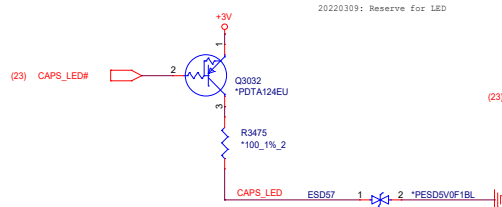
 Quanta Computer Inc. PROJECT : TRA		
Size	Document Number	Rev
	HDMI 1.4b PS8203A	2A
Date:	Wednesday, February 01, 2023	Sheet 33 of 50

This part should not contain any substances which are specified in EM-S303.

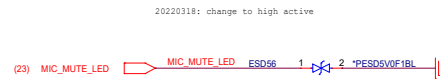
Keyboard Connector



CAPS LED (Reserve)

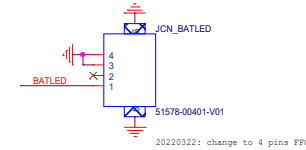


MIC LED (Reserve)

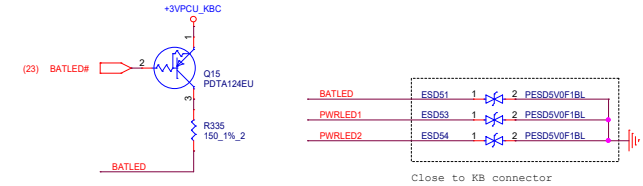


MY17_R	C3459	10p/25V_2
MY16_R	C3458	10p/25V_2
MY15_R	C3457	10p/25V_2
MY14_R	C3456	10p/25V_2
MY13_R	C3455	10p/25V_2
MY12_R	C3454	10p/25V_2
MY11_R	C3453	10p/25V_2
MY10_R	C3452	10p/25V_2
MY9_R	C3451	10p/25V_2
MY8_R	C3450	10p/25V_2
MY7_R	C3449	10p/25V_2
MY6_R	C3448	10p/25V_2
MY5_R	C3447	10p/25V_2
MY4_R	C3446	10p/25V_2
MY3_R	C3445	10p/25V_2
MY2_R	C3444	10p/25V_2
MY1_R	C3443	10p/25V_2
MY0_R	C3442	10p/25V_2
MX7_R	C3441	10p/25V_2
MX6_R	C3440	10p/25V_2
MX5_R	C3439	10p/25V_2
MX4_R	C3438	10p/25V_2
MX3_R	C3437	10p/25V_2
MX2_R	C3436	10p/25V_2
MX1_R	C3435	10p/25V_2
MX0_R	C3434	10p/25V_2
MY16_R	C3460	*TVS ESD3V3S1B
MY17_R	C3461	*TVS ESD3V3S1B
MY12_R	C307	*TVS ESD3V3S1B
MY13_R	C308	*TVS ESD3V3S1B
MY14_R	C310	*TVS ESD3V3S1B
MY15_R	C312	*TVS ESD3V3S1B
MX6	C314	*TVS ESD3V3S1B
MY10	C316	*TVS ESD3V3S1B
MX7	C318	*TVS ESD3V3S1B
MY11	C320	*TVS ESD3V3S1B
MY8	C322	*TVS ESD3V3S1B
MX4	C324	*TVS ESD3V3S1B
MY9	C326	*TVS ESD3V3S1B
MX5	C328	*TVS ESD3V3S1B
MY4	C330	*TVS ESD3V3S1B
MY5	C333	*TVS ESD3V3S1B
MY6	C336	*TVS ESD3V3S1B
MY7	C339	*TVS ESD3V3S1B
MY1	C341	*TVS ESD3V3S1B
MY2	C343	*TVS ESD3V3S1B
MX3	C345	*TVS ESD3V3S1B
MY3	C347	*TVS ESD3V3S1B
MX0	C349	*TVS ESD3V3S1B
MX1	C351	*TVS ESD3V3S1B
MX2	C353	*TVS ESD3V3S1B
MY0	C354	*TVS ESD3V3S1B

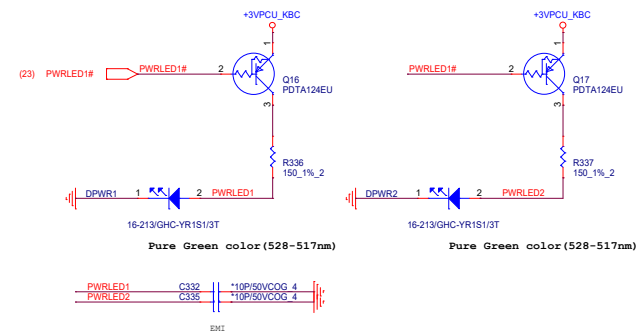
LED



BATTERY LED

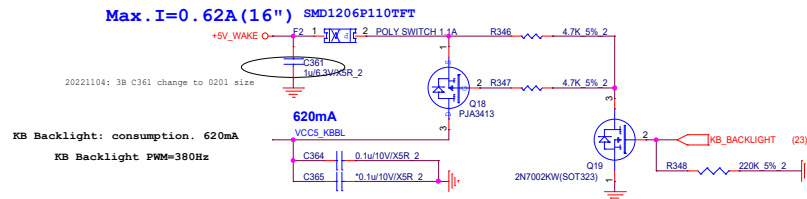


POWER LED



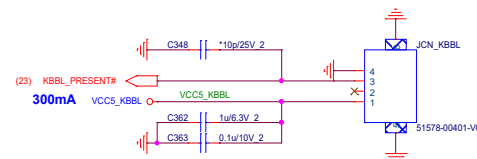
Keyboard Backlight

Test case	Pattern6	
Temperature	43.7	
F2 Temp. (C)	43.7	
Current	40C	50C
Fuse I- hold current(A)	0.92	0.83



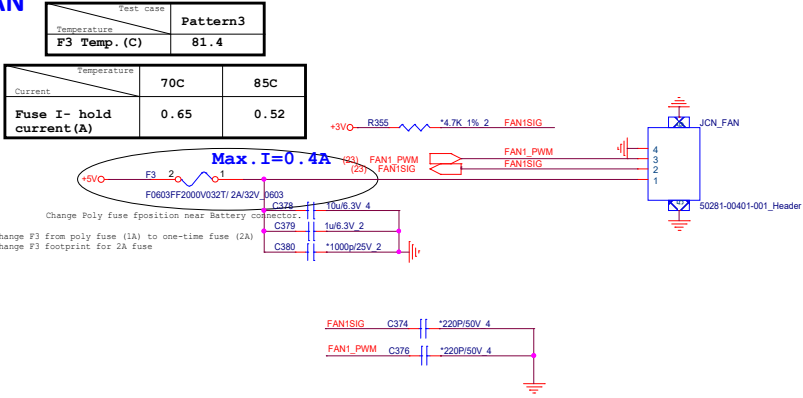
KB Backlight Connector

Pitch 0.5mm

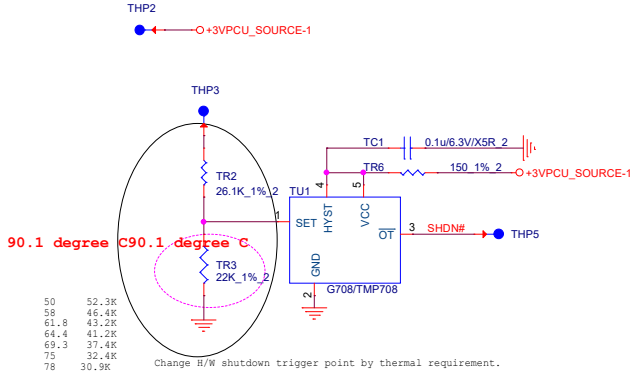


20221104a

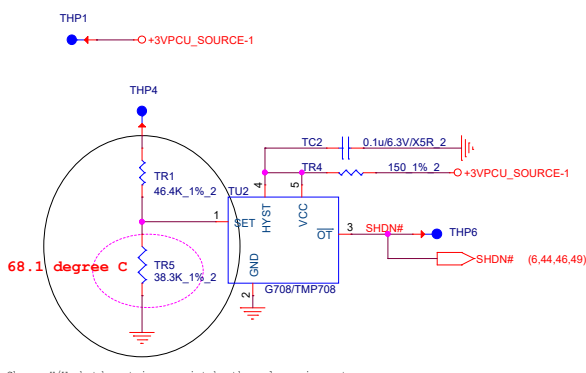
FAN



As close as possible to CPU



Thermally far from heat source



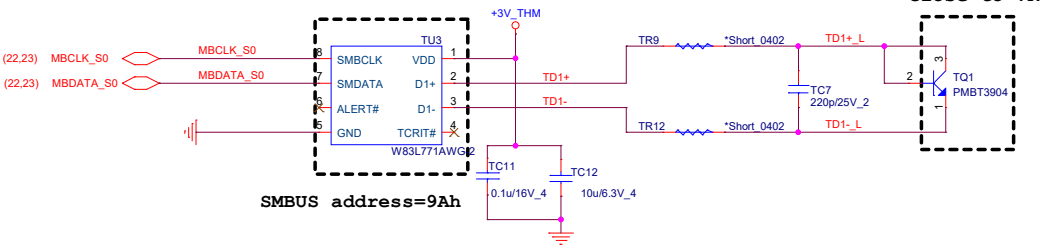
Change H/W shutdown trigger point by thermal requirement.

$RSET(K\Omega) = 0.0012T^2 - 0.9308T + 96.147$

UMA SKU	Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near USB3 CN sensor temp		68.1	TR5=38.3K	TR5=38.3K		
Near CPU sensor temp		90.1	TR3=22K	TR3=22K		

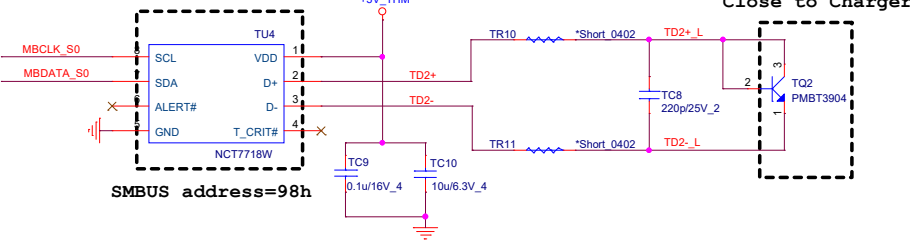
S/W Thermal Protect

Close to main heatpipe

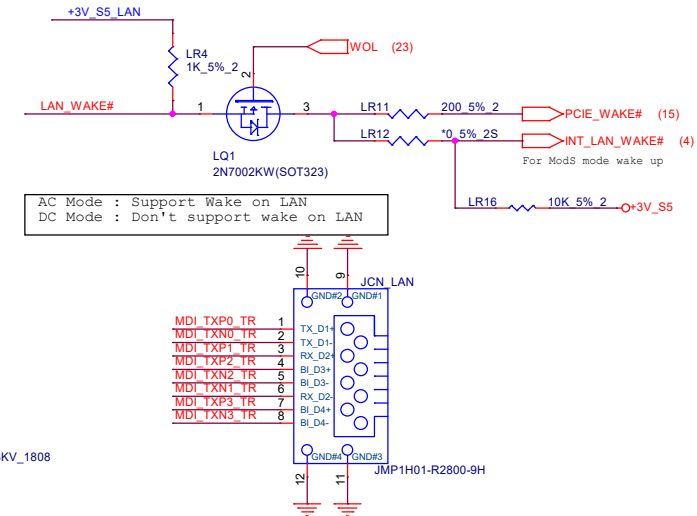
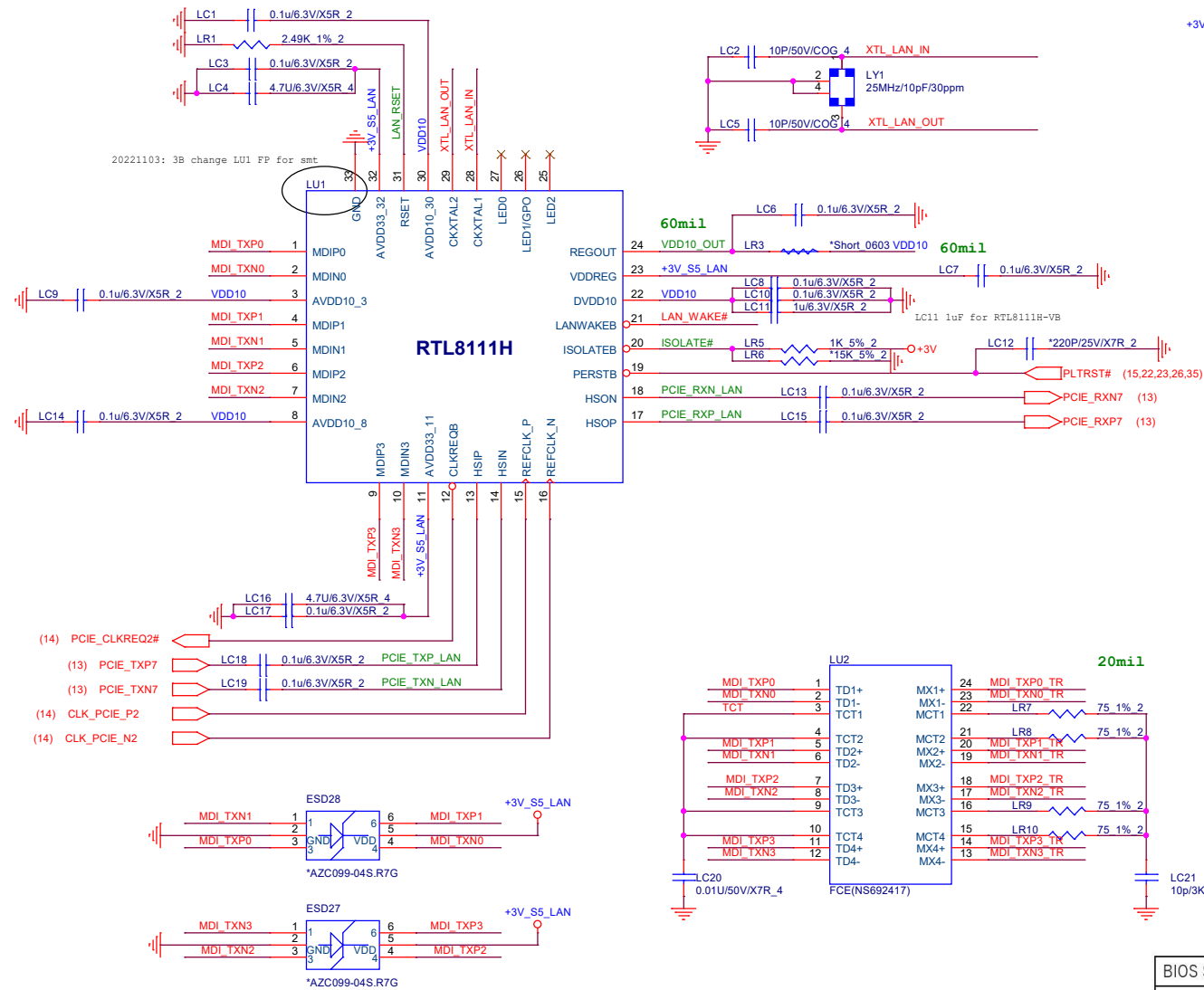


Close to VR

Close to DDR4



Close to Charger



AC Mode : Support Wake on LAN
DC Mode : Don't support wake on LAN

BIOS Setup	WoLAN from Power Off Disabled		WoLAN from Power Off Enabled	
	S5 Power	WOL signal	S5 Power	WOL Signal
S0 (Including ModS)	On	H	On	H
S3	On	H	On	H
S4/S5 (AC)	Off(*)	L	On	H
S4/S5 (DC)	Off(*)	L	Off(*)	L

* If S5 power is required by other condition, it is On.

20221103a

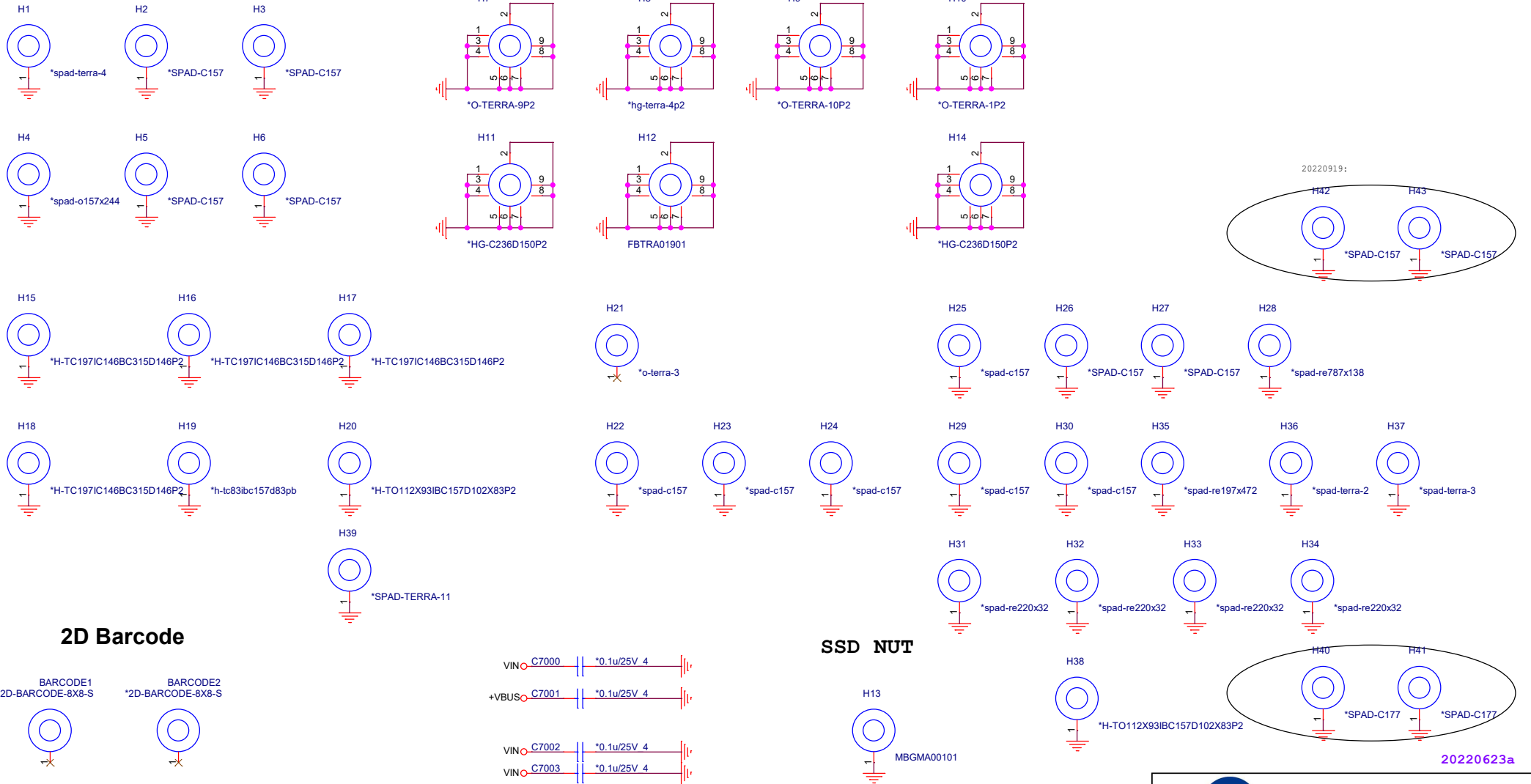


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Size	Document Number	Rev
		2E
LAN RTL-8111H		
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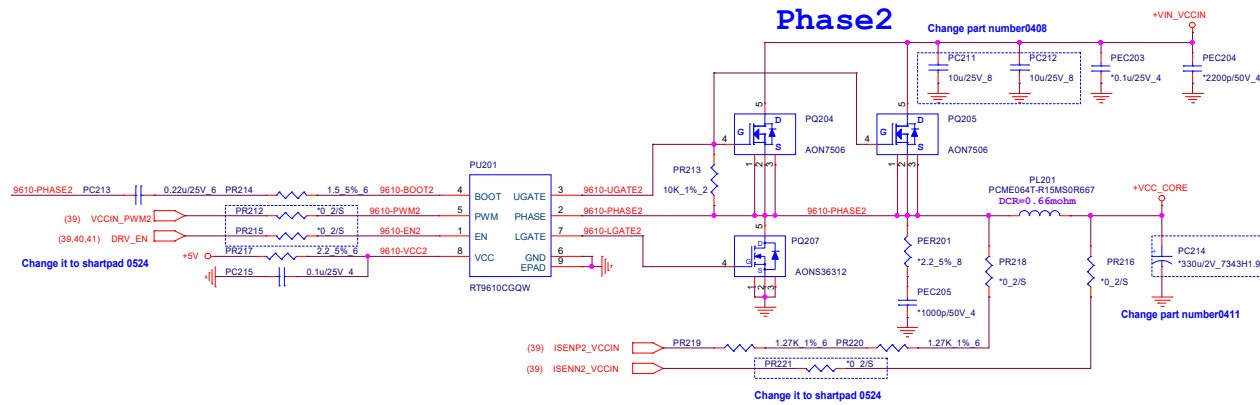
Screw Hole

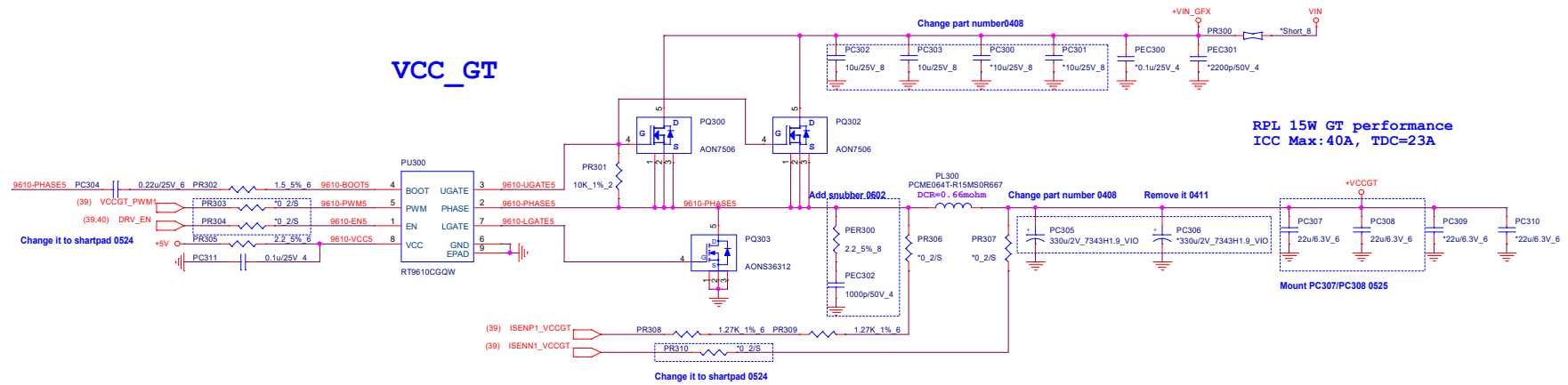
37



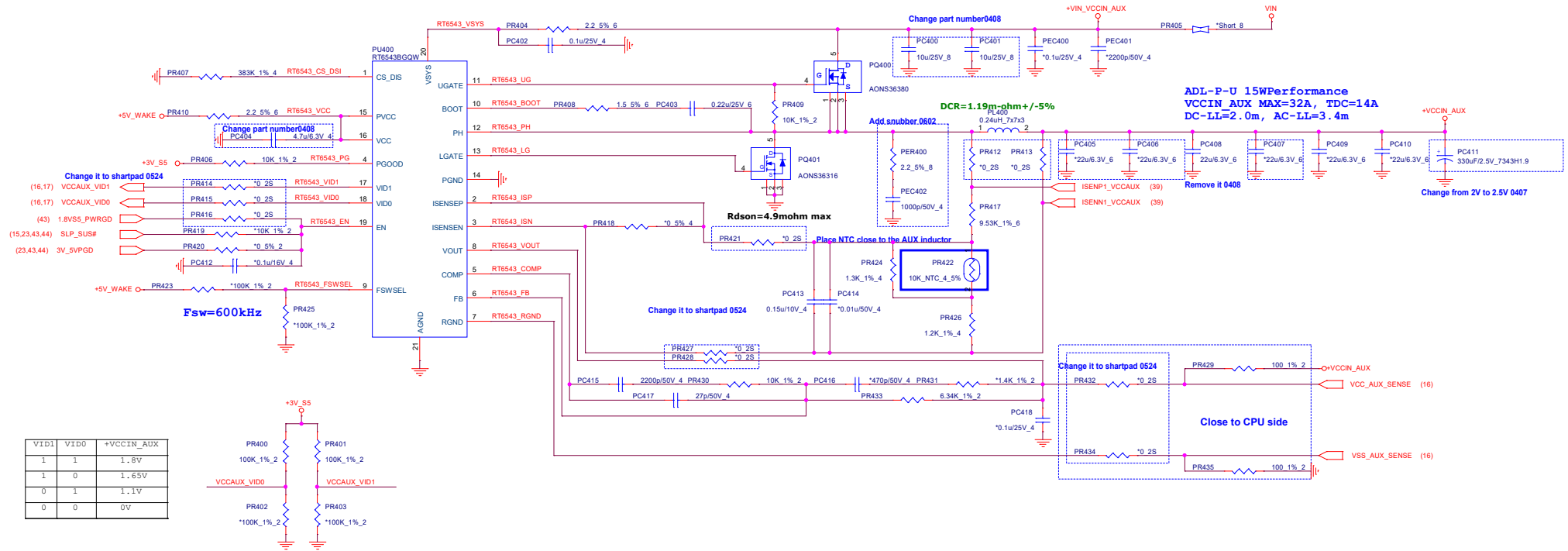
This part should not contain any substances which are specified in EM-S303.

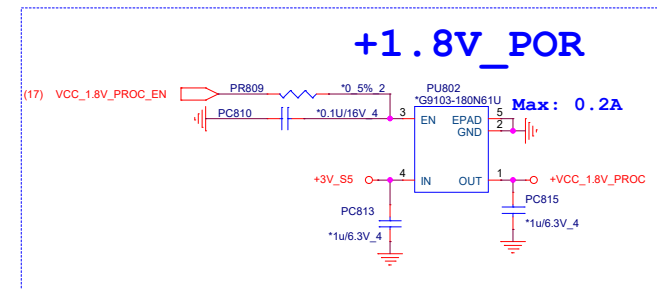
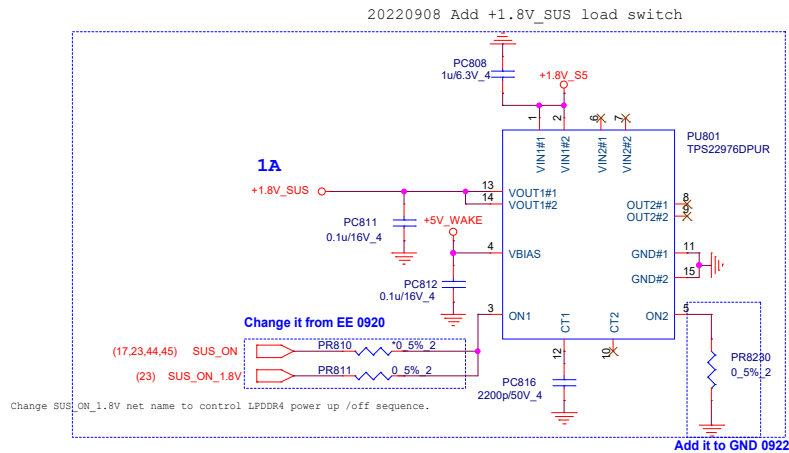
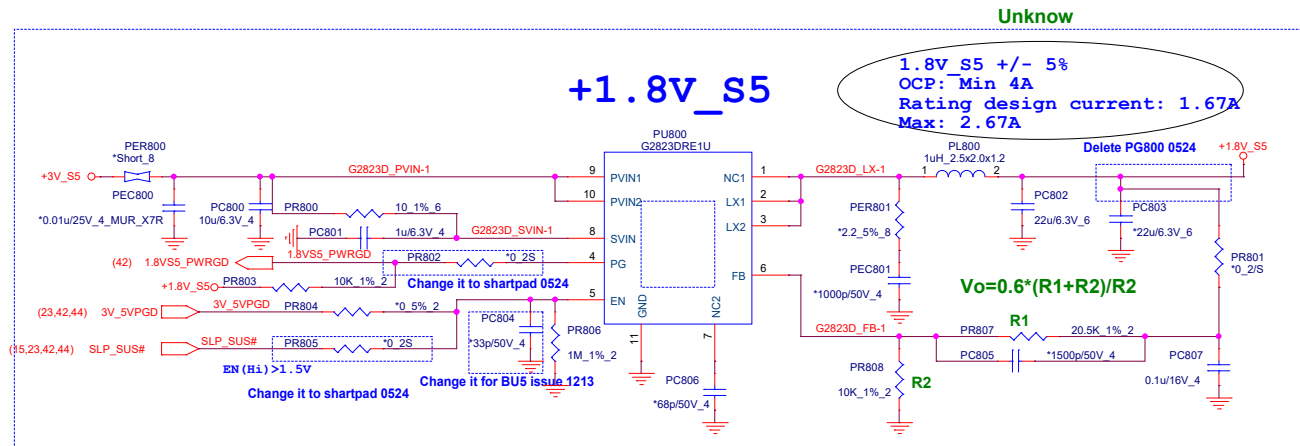
		Quanta Computer Inc.	
		PROJECT : TRA	
Size	Document Number	Rev 2F	
SCREW HOLE			
Date:	Wednesday, February 01, 2023	Sheet	38 of 50





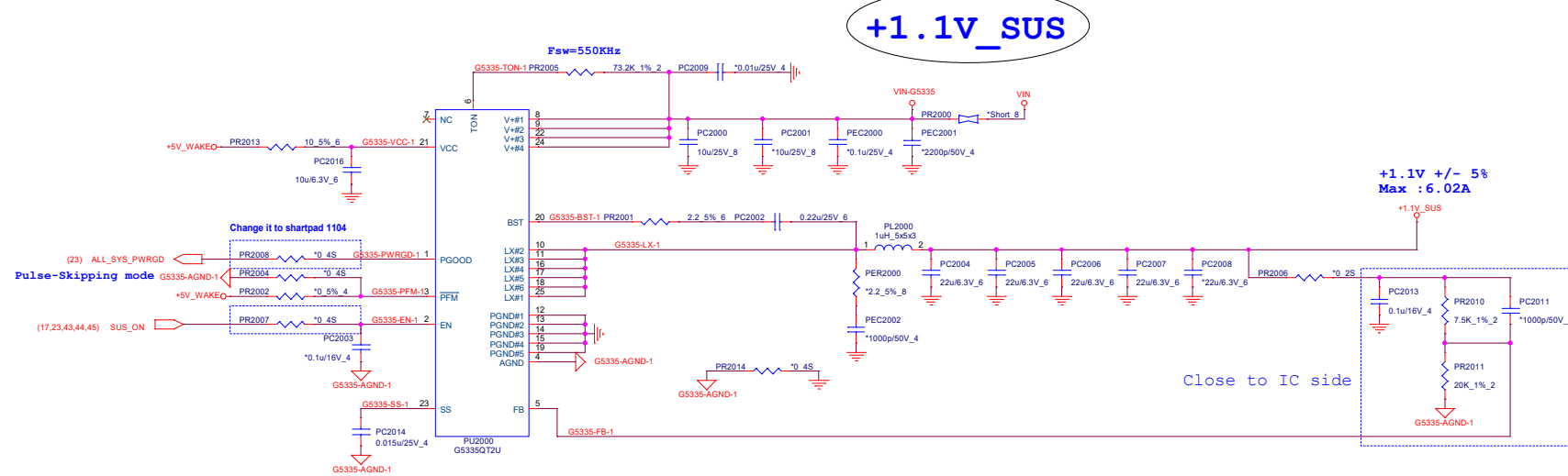
Platform	SKU	Phase	TDC	ICCMAX	DC LL	AC LL	Freq
ADL	15W-Performance	1	14A	32A	2.0mohm	3.4mohm	600khz
RPL	15W-Performance	1	14A	32A	2.0mohm	3.4mohm	600khz



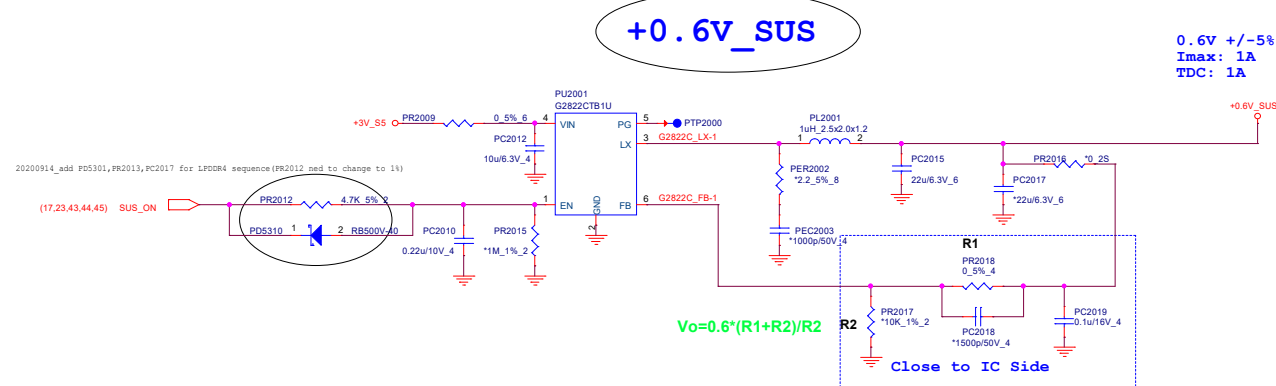


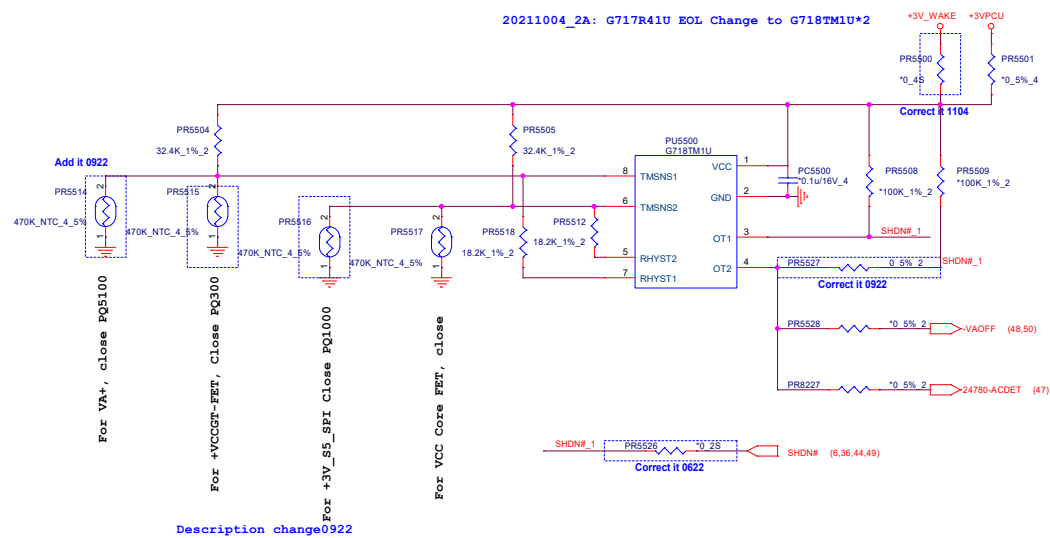


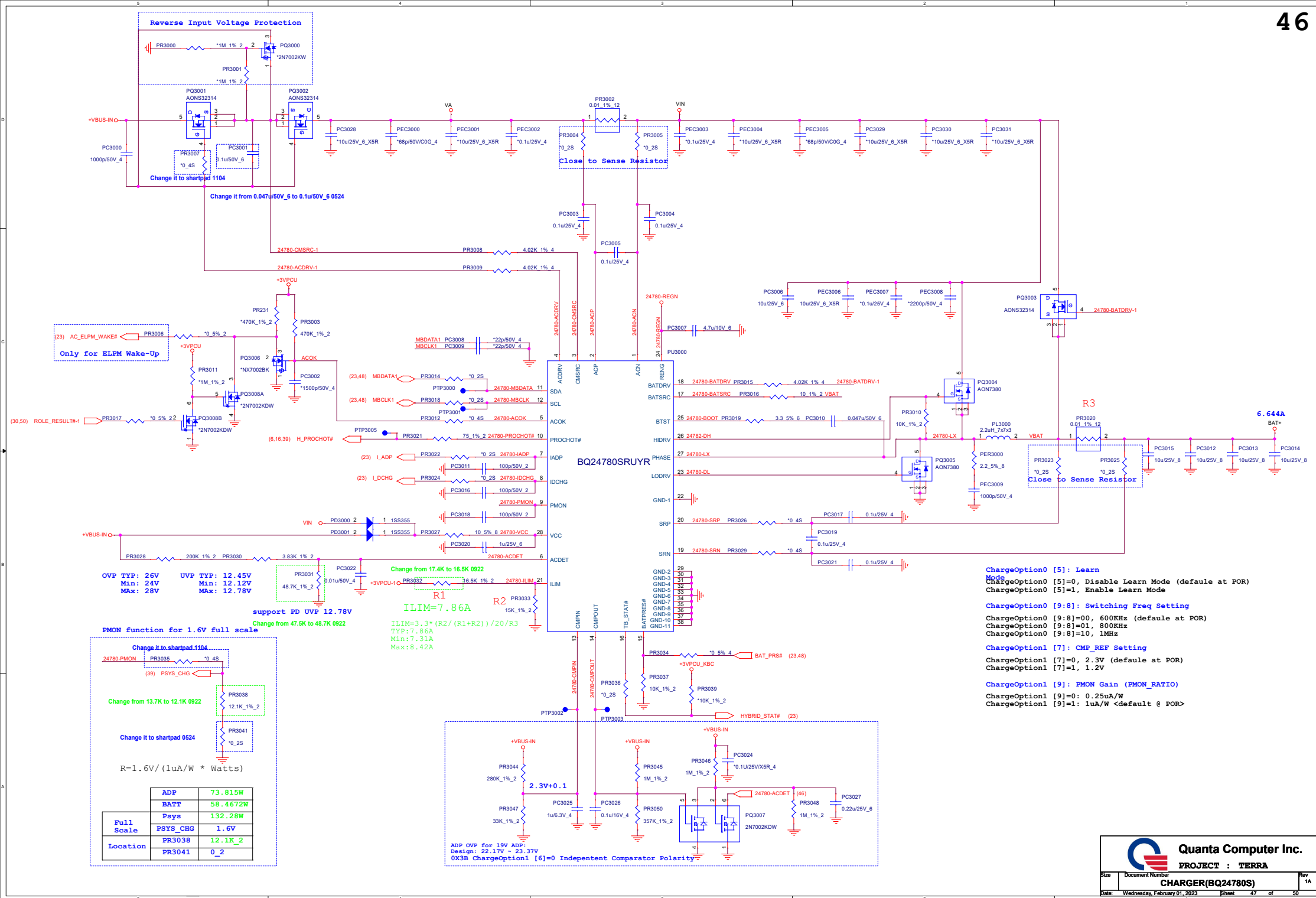
20220908 Change from +1.2V_SUS to +1.1V_SUS.



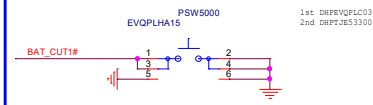
20220908 Change from +2.5V_SUS to +0.6V_SUS.



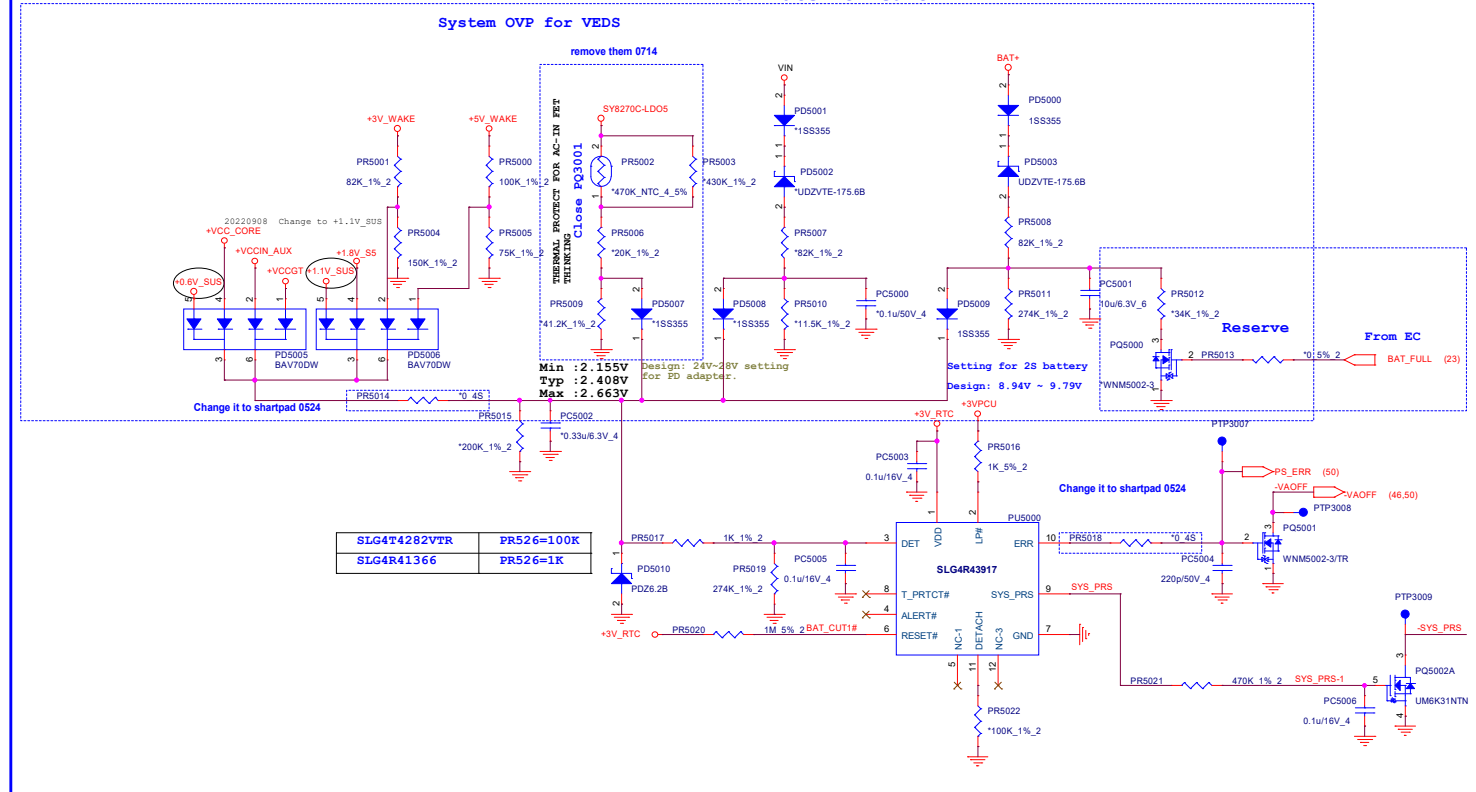




PSW5000	1st DHPEVQPLC03
EVQPLHA15	2nd DHPTJE53300



System OVP for VEDS



BATTERY CONNECTOR

