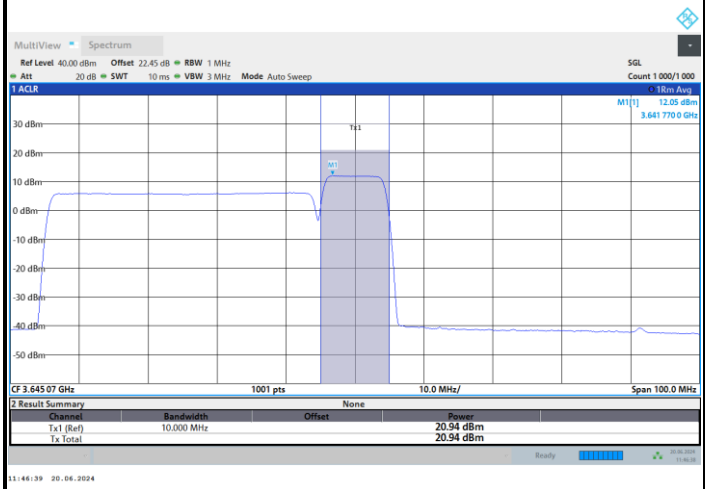
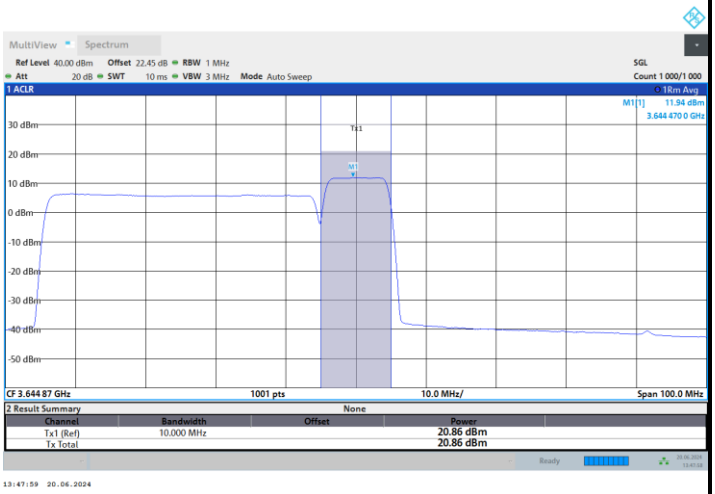
FR1 n48 / 40MHz+10MHz / Middle Channel / Conducted (dBm/10MHz)

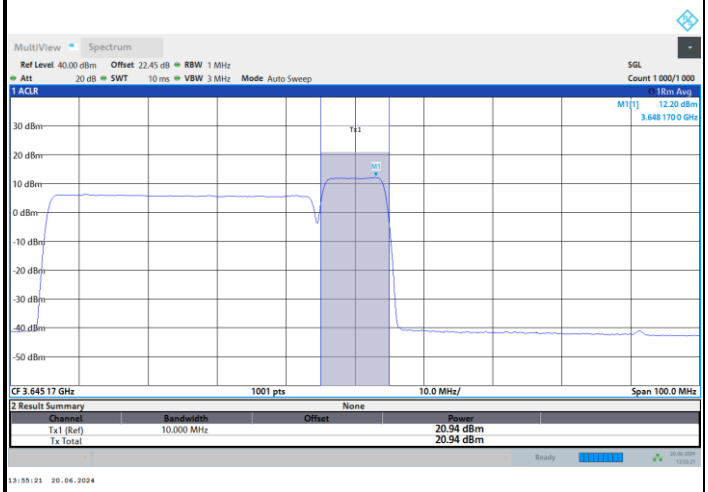
QPSK



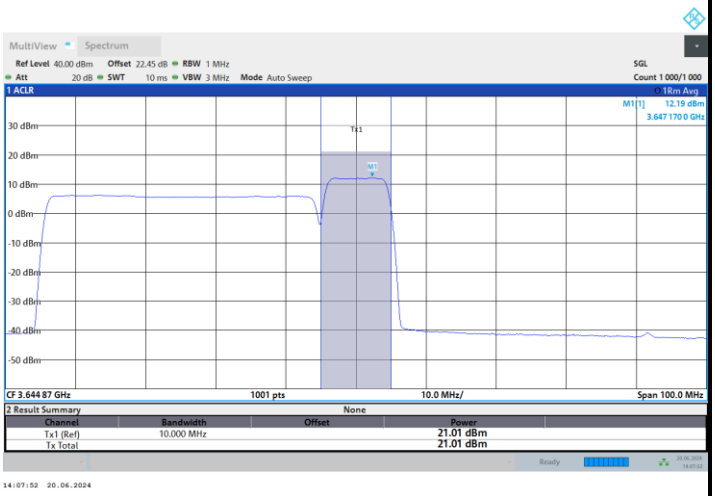
16QAM



64QAM

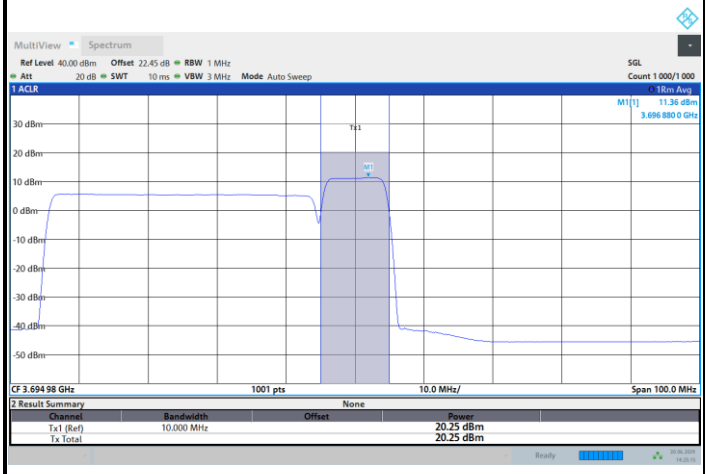


256QAM

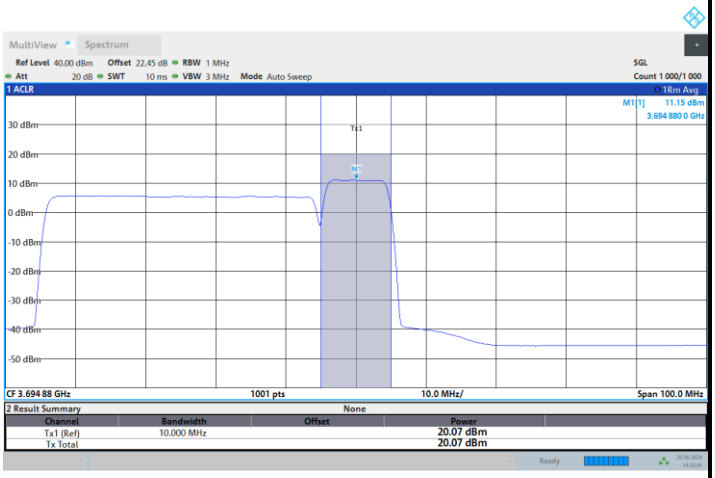


FR1 n48 / 40MHz+10MHz / Highest Channel / Conducted (dBm/10MHz)

QPSK

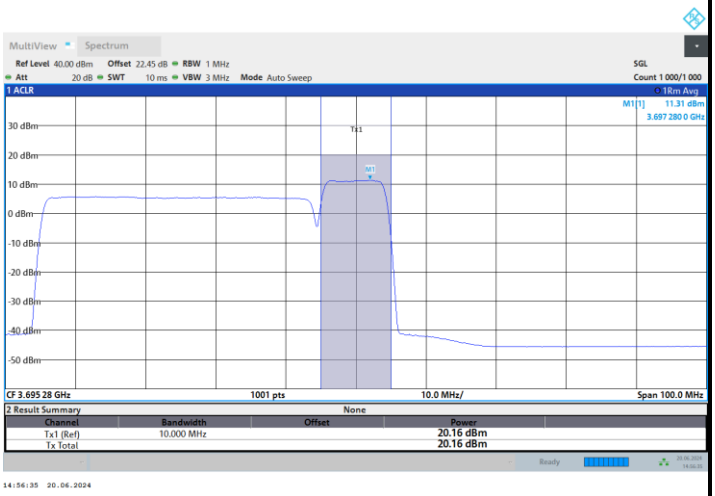


16QAM



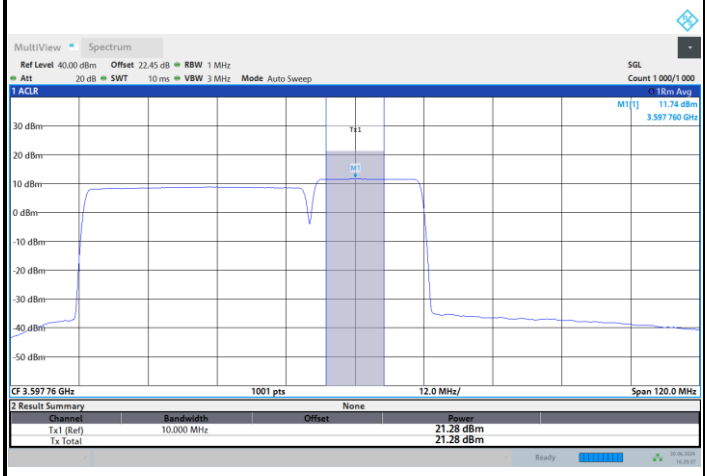
64QAM

256QAM

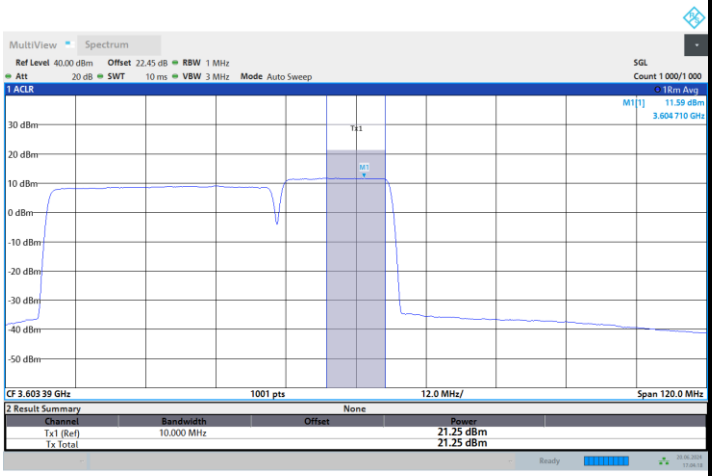


FR1 n48 / 40MHz+20MHz / Lowest Channel / Conducted (dBm/10MHz)

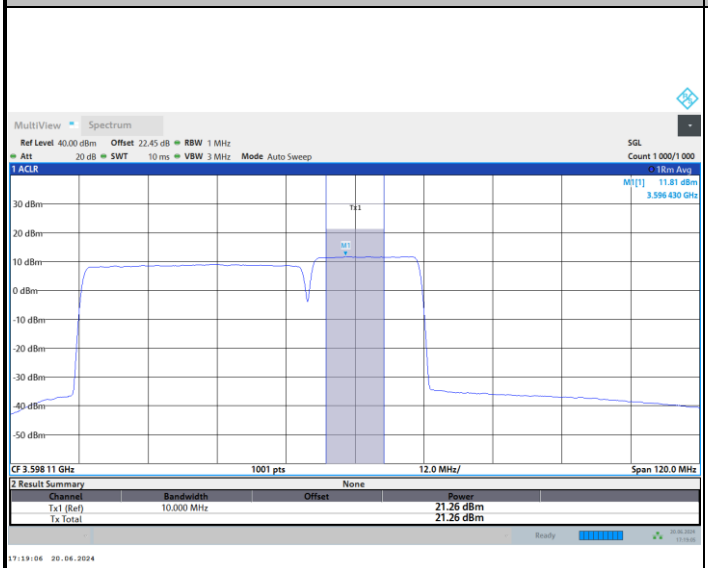
QPSK



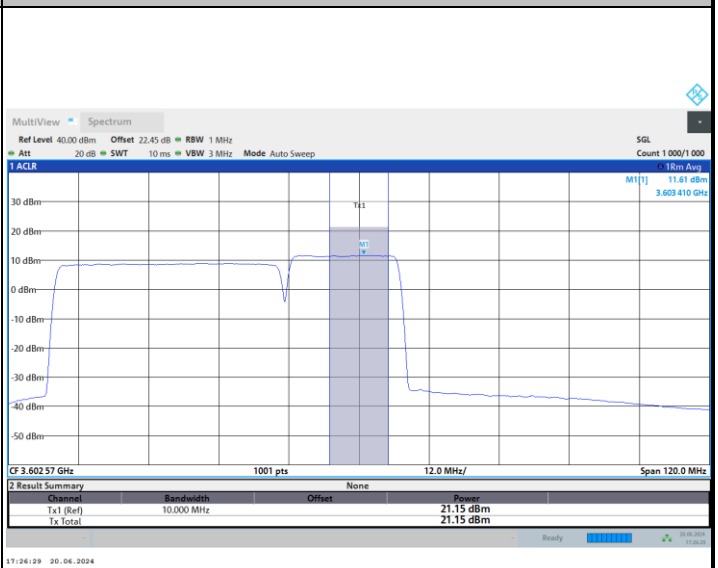
16QAM



64QAM

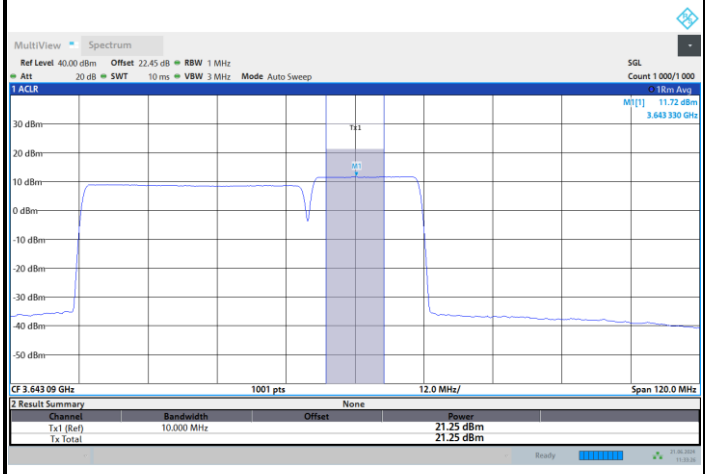


256QAM

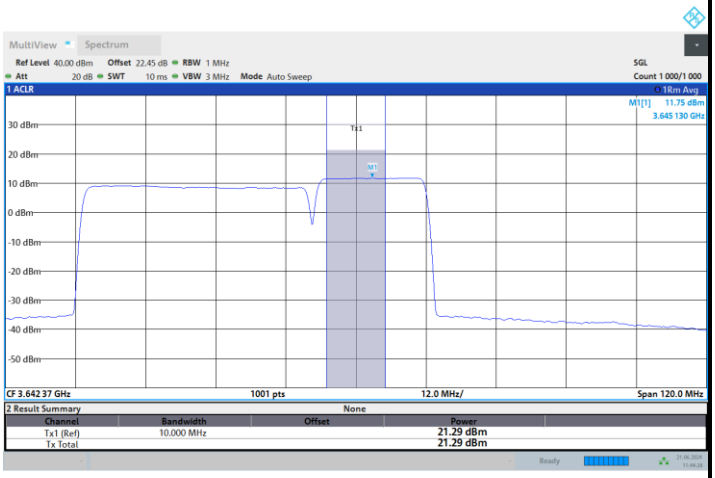


FR1 n48 / 40MHz+20MHz / Middle Channel / Conducted (dBm/10MHz)

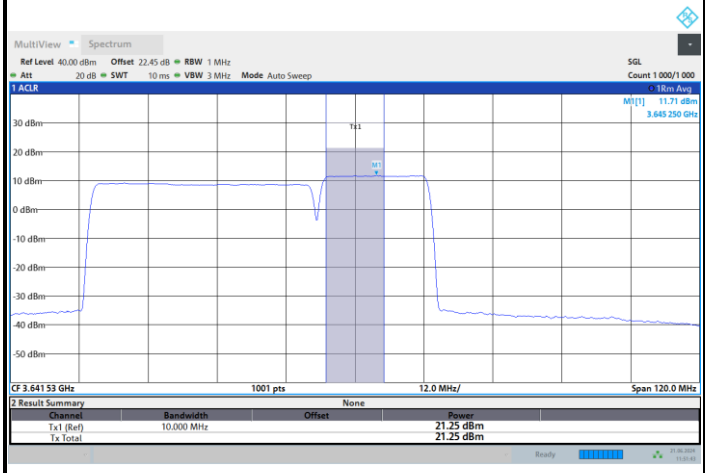
QPSK



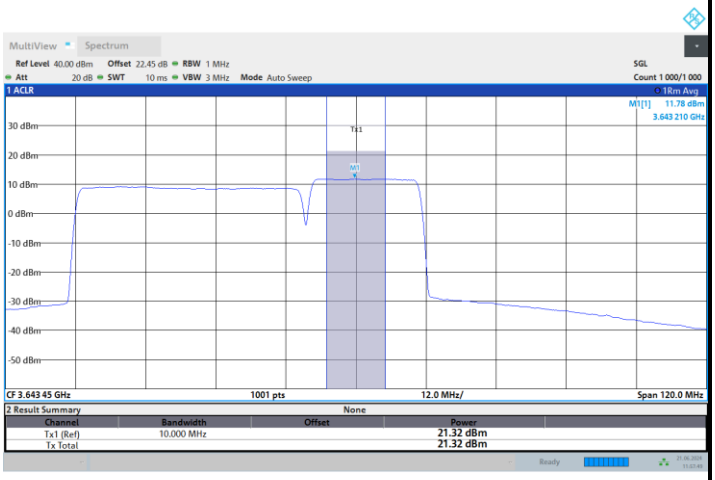
16QAM



64QAM

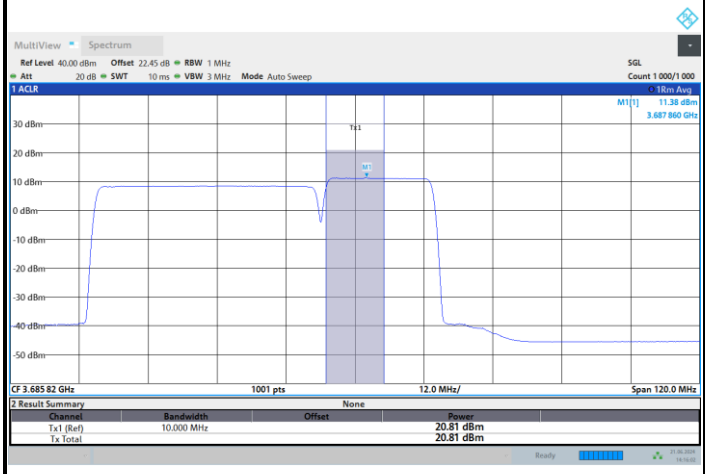


256QAM

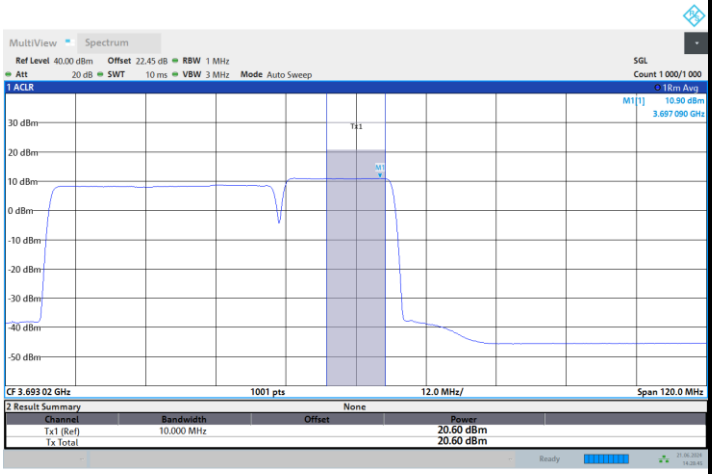


FR1 n48 / 40MHz+20MHz / Highest Channel / Conducted (dBm/10MHz)

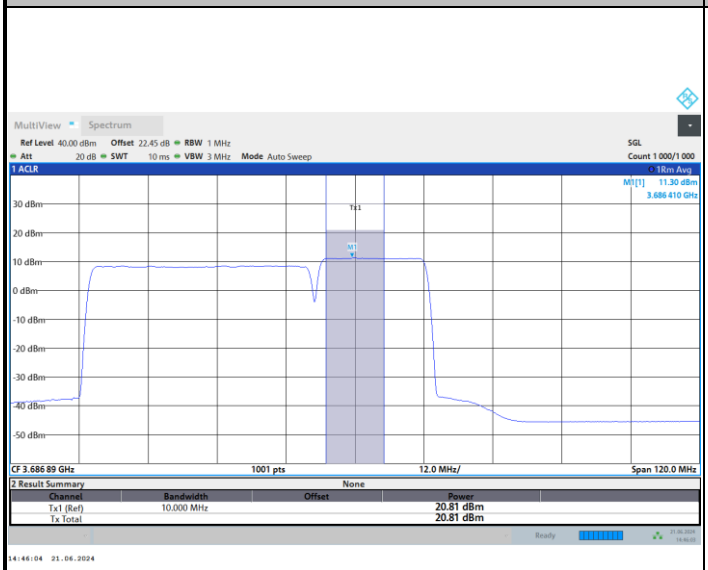
QPSK



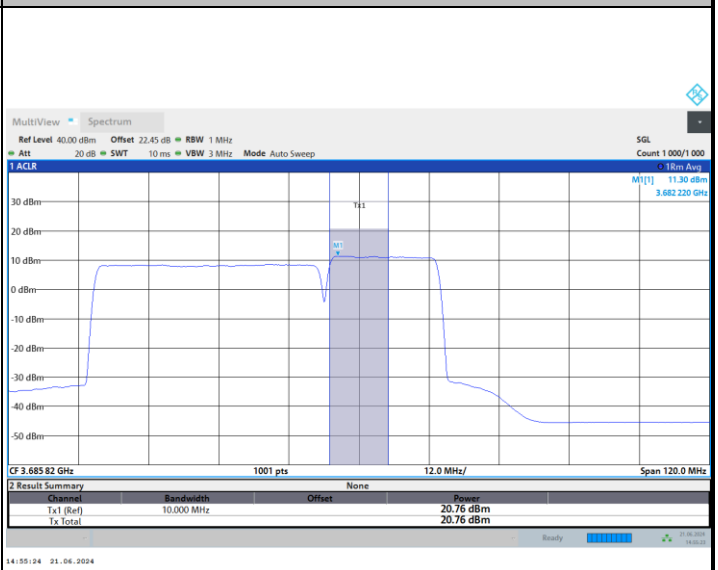
16QAM



64QAM

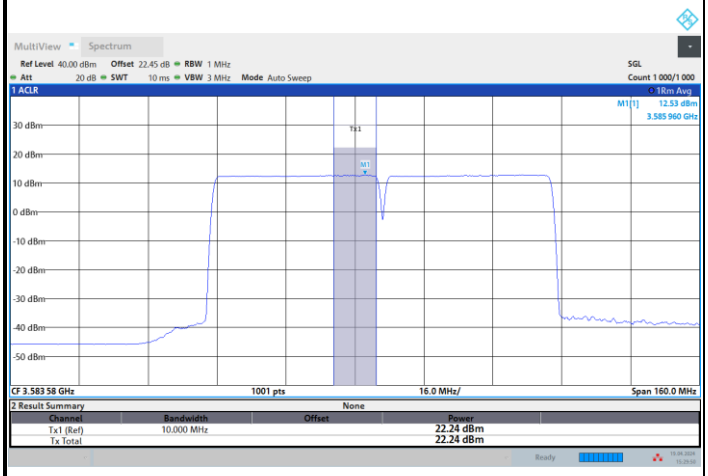


256QAM

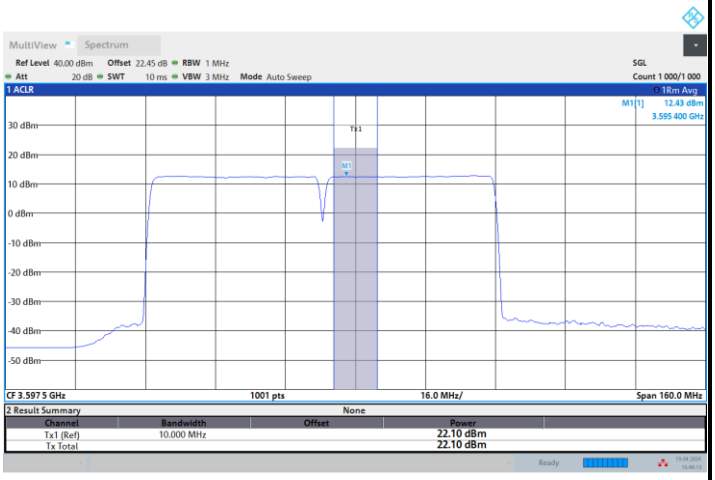


FR1 n48 / 40MHz+40MHz / Lowest Channel / Conducted (dBm/10MHz)

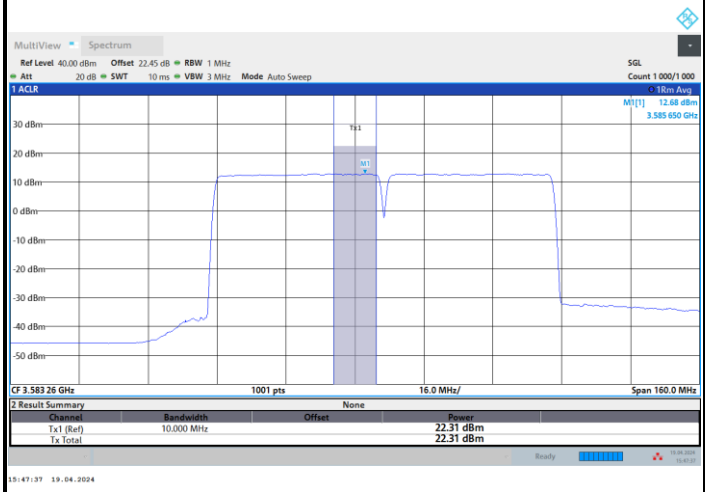
QPSK



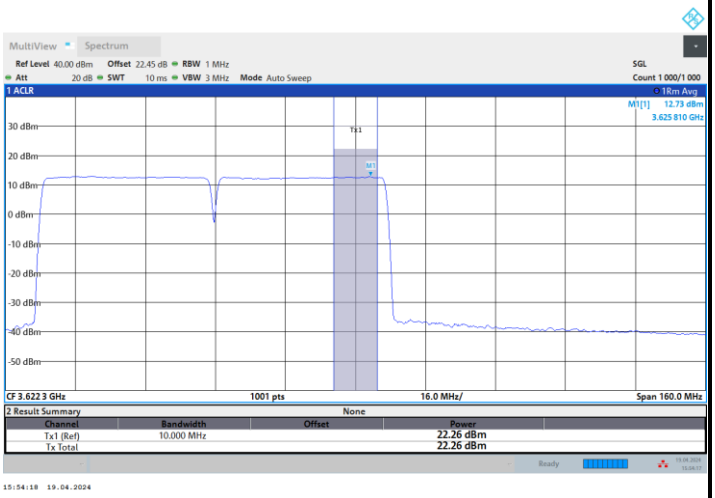
16QAM



64QAM

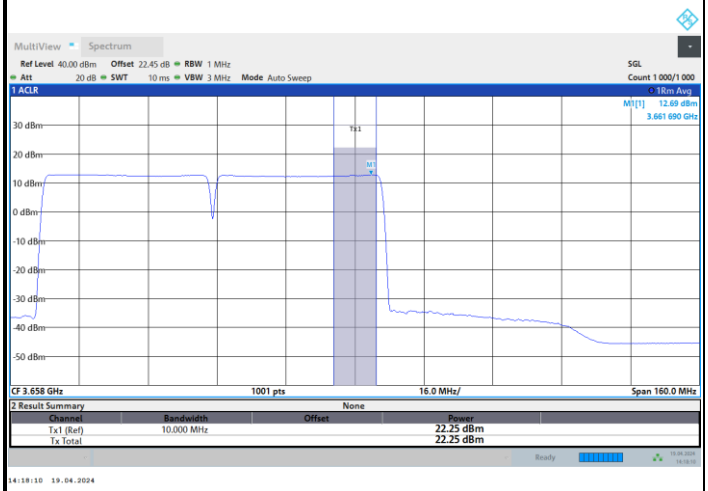


256QAM

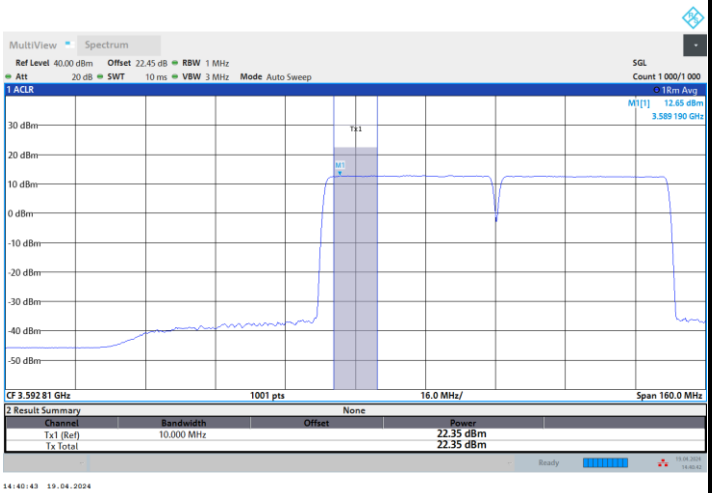


FR1 n48 / 40MHz+40MHz / Middle Channel / Conducted (dBm/10MHz)

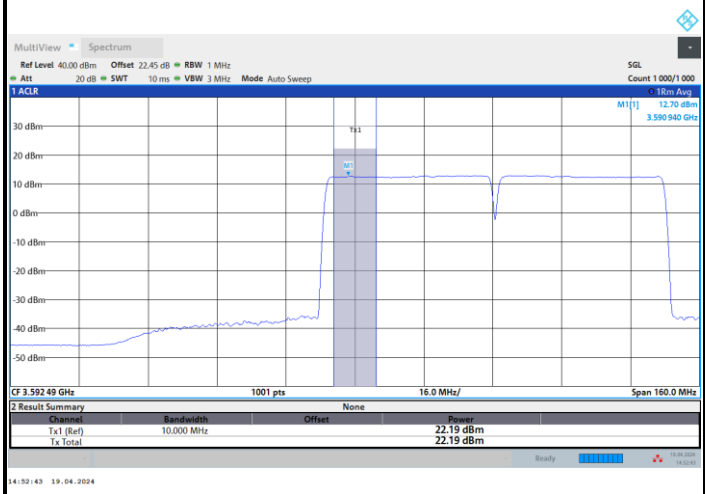
QPSK



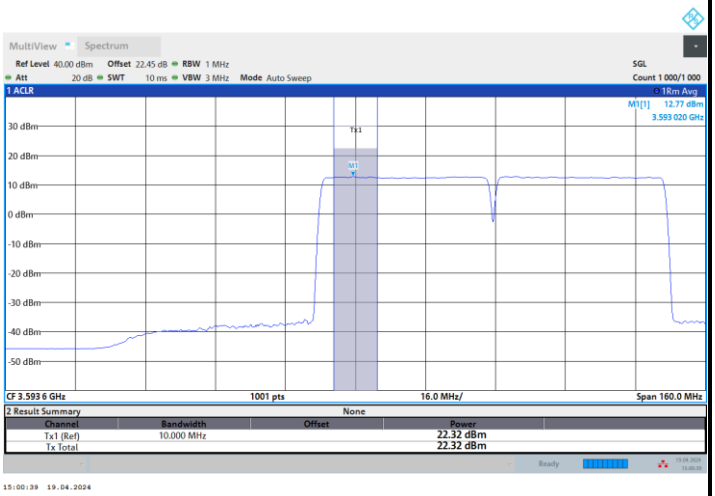
16QAM



64QAM

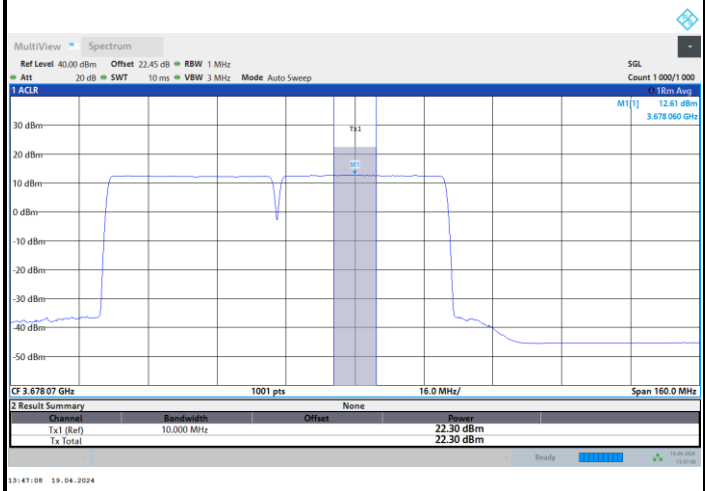


256QAM

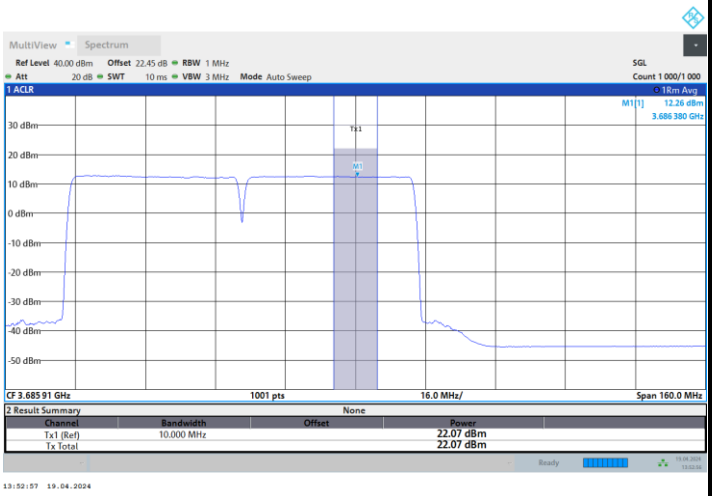


FR1 n48 / 40MHz+40MHz / Highest Channel / Conducted (dBm/10MHz)

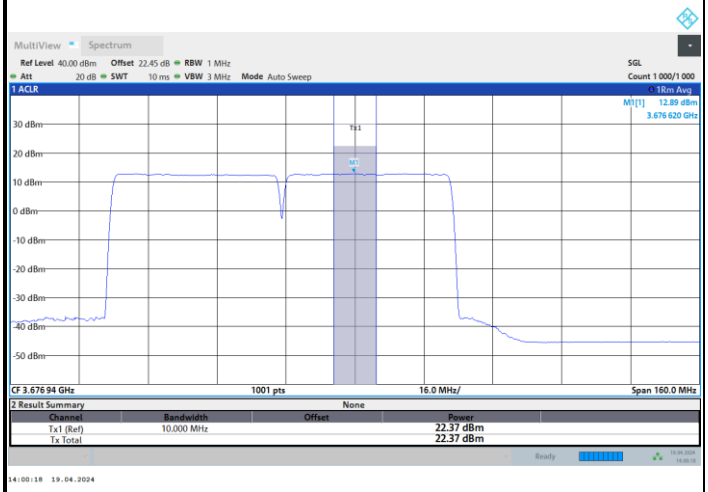
QPSK



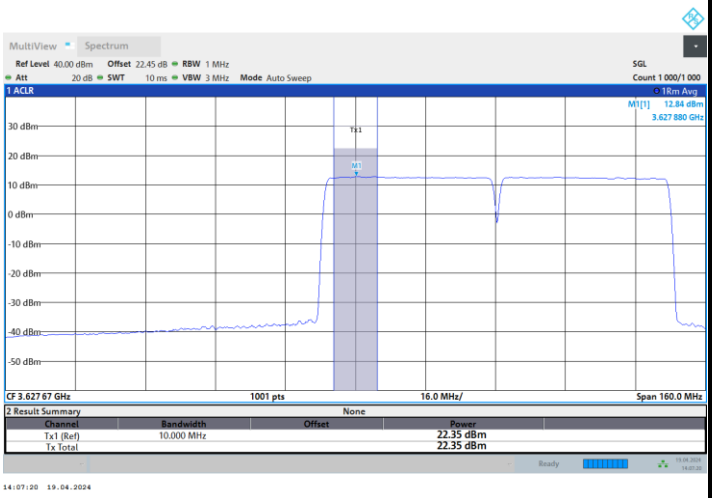
16QAM



64QAM

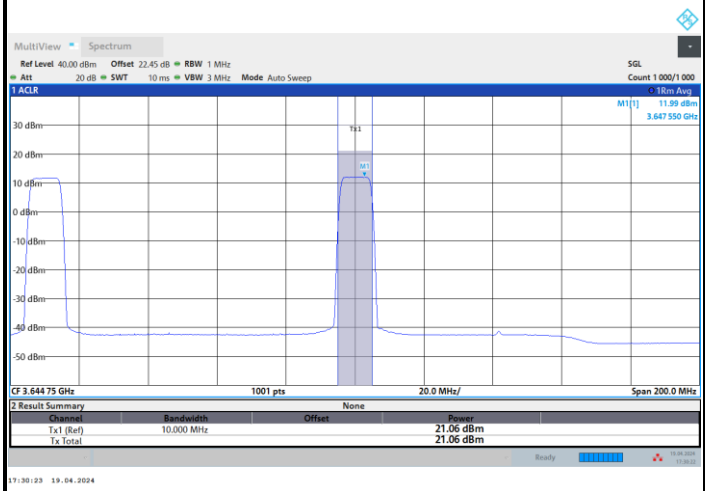


256QAM

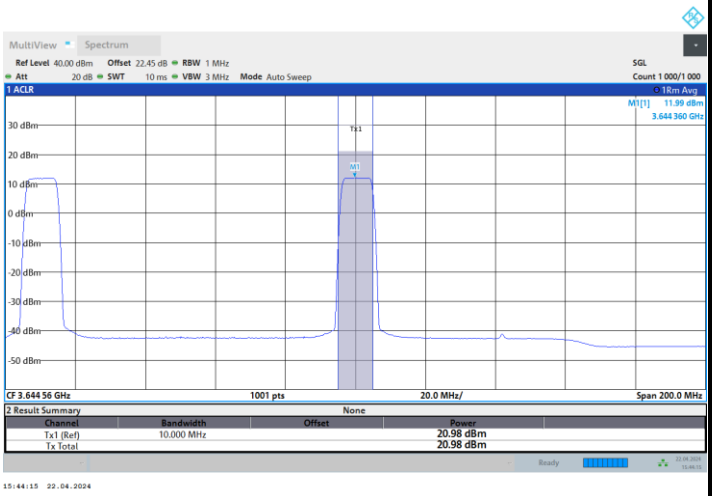


FR1 n48 / 10MHz+10MHz (Non Contiguous) / Lowest Channel / Conducted (dBm/10MHz)

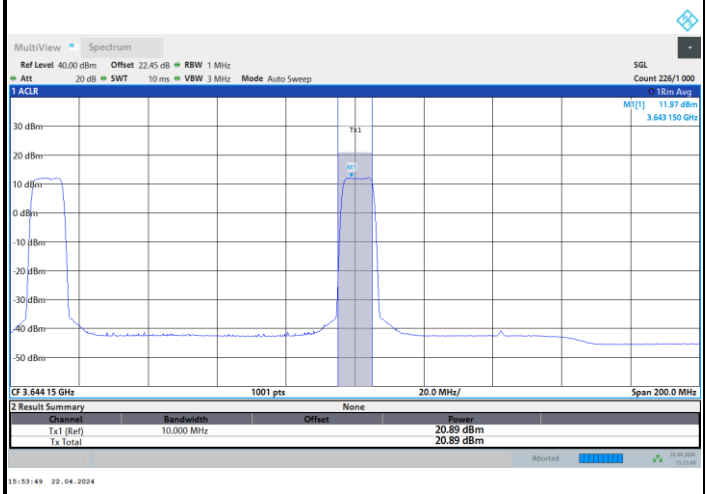
QPSK



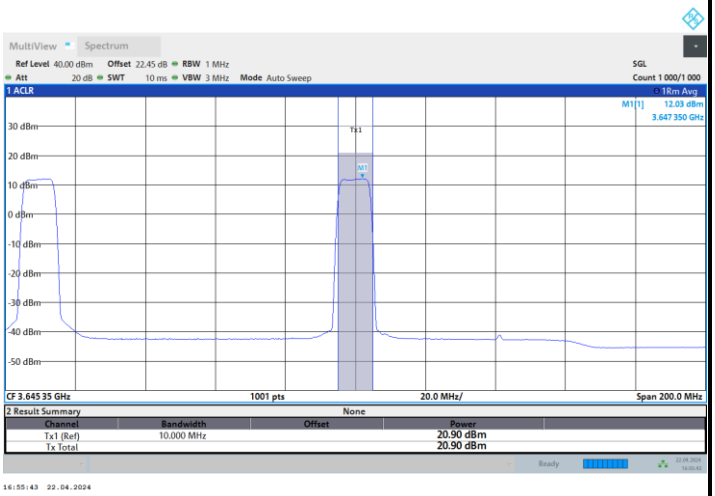
16QAM



64QAM

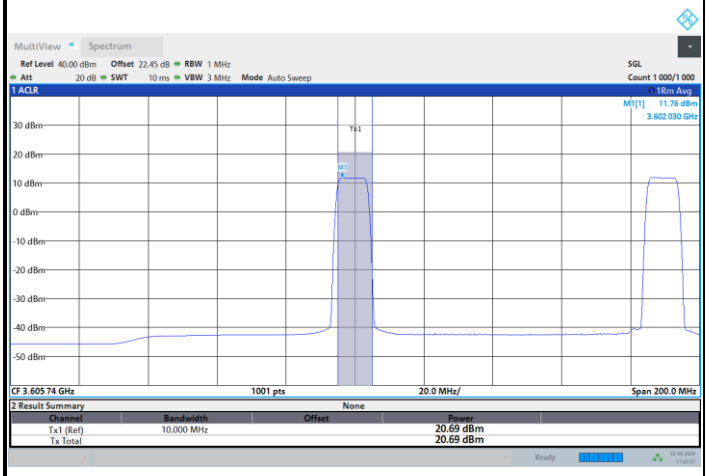


256QAM

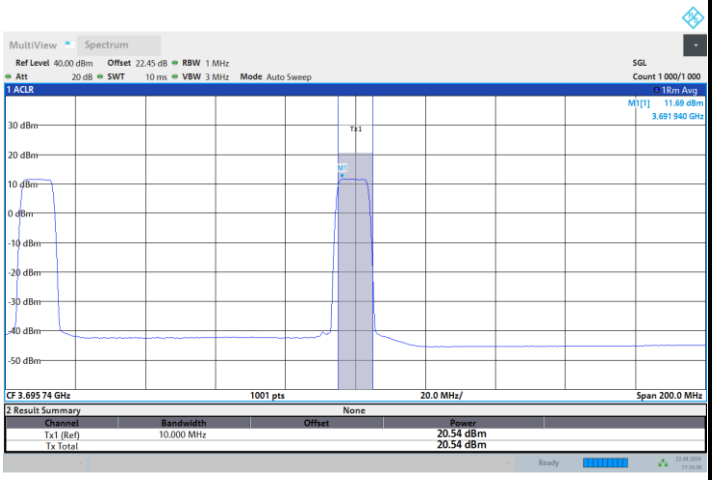


FR1 n48 / 10MHz+10MHz (Non Contiguous) / Highest Channel / Conducted (dBm/10MHz)

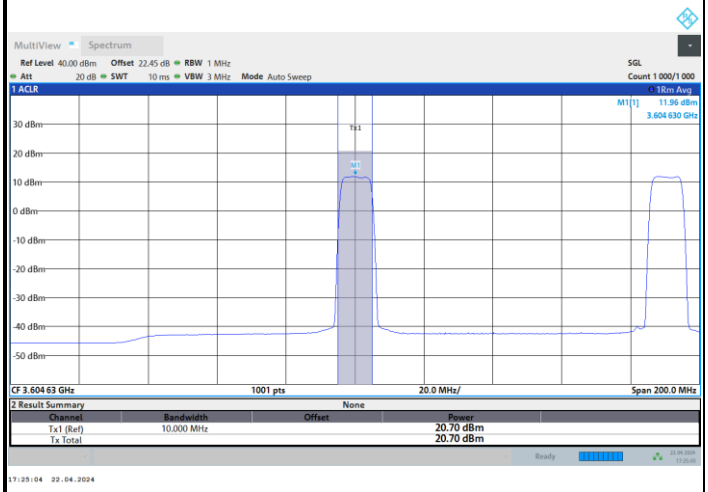
QPSK



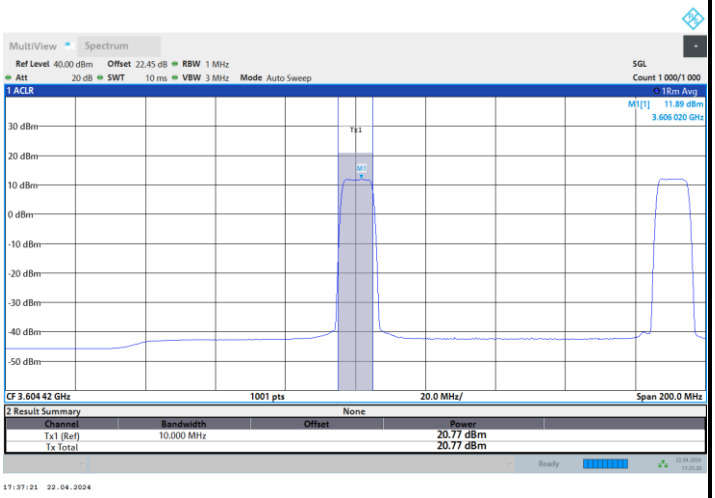
16QAM

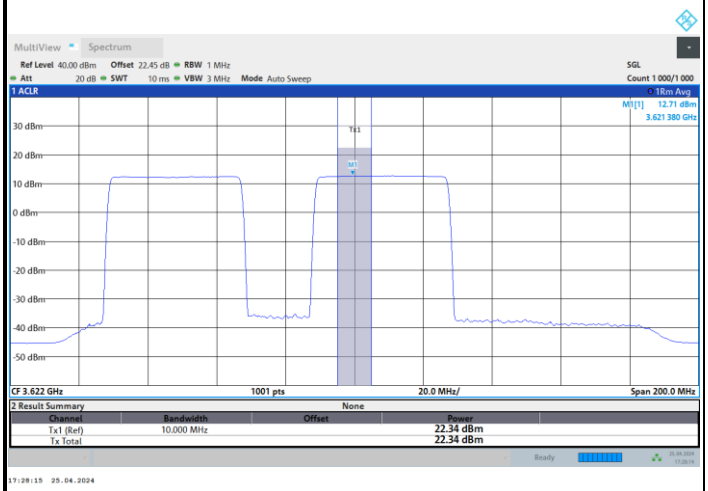
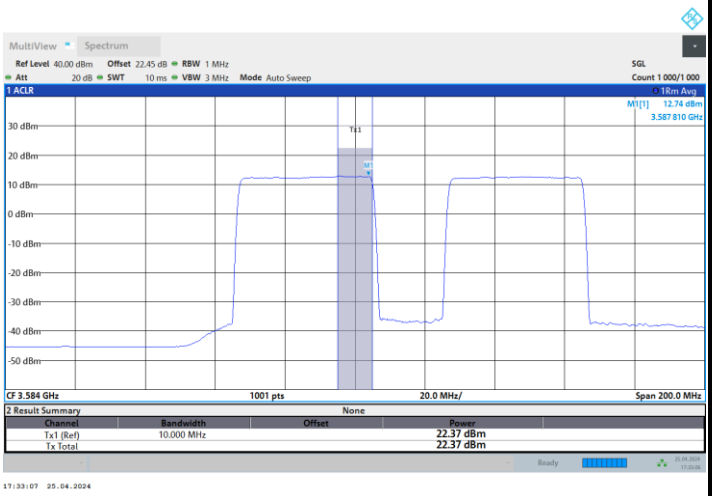
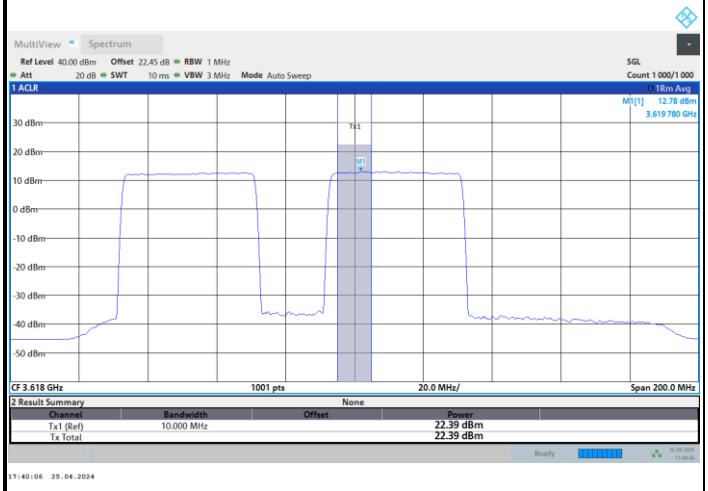
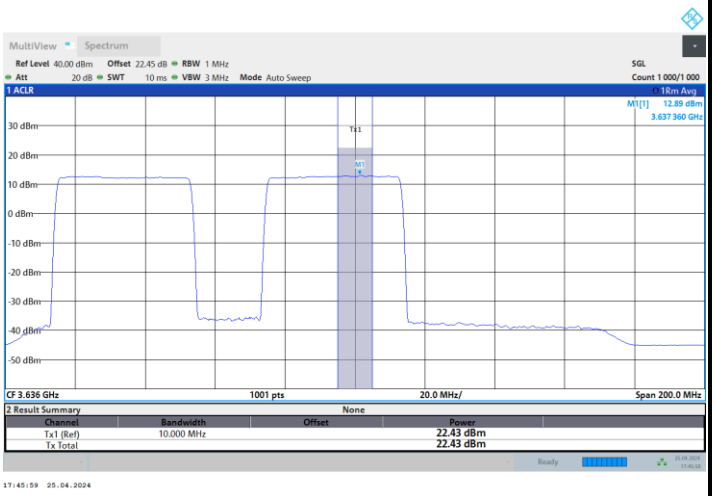


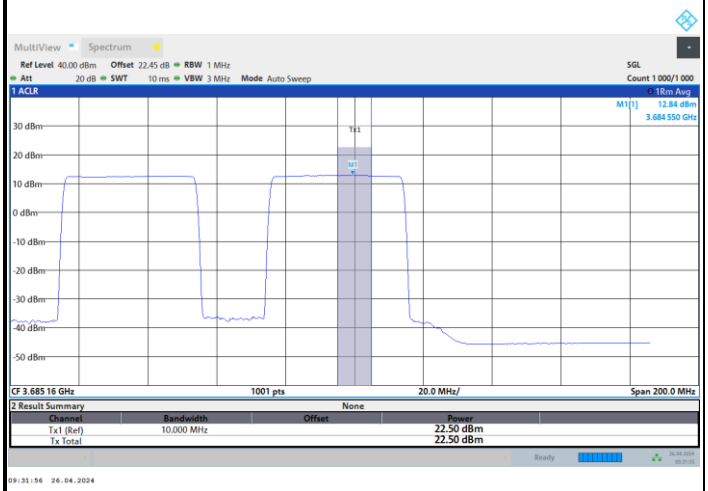
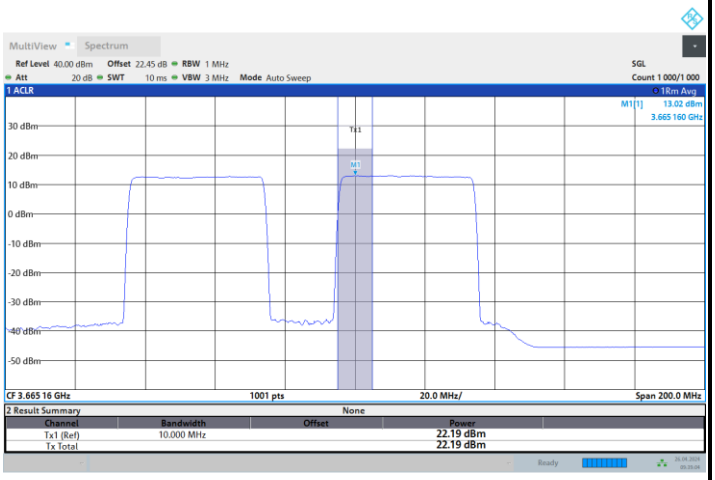
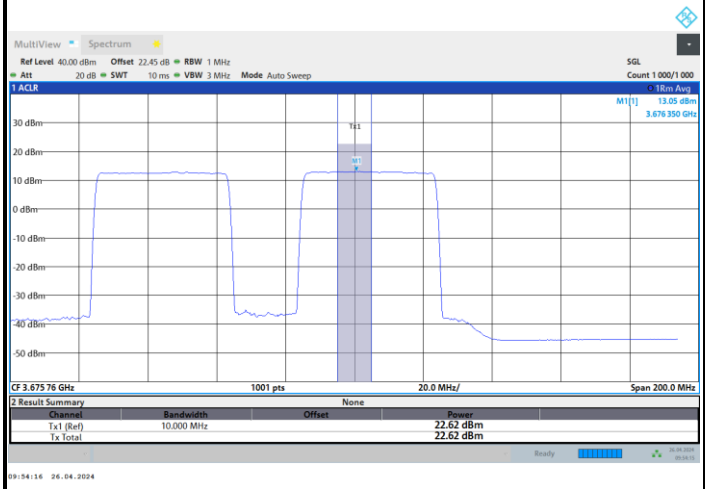
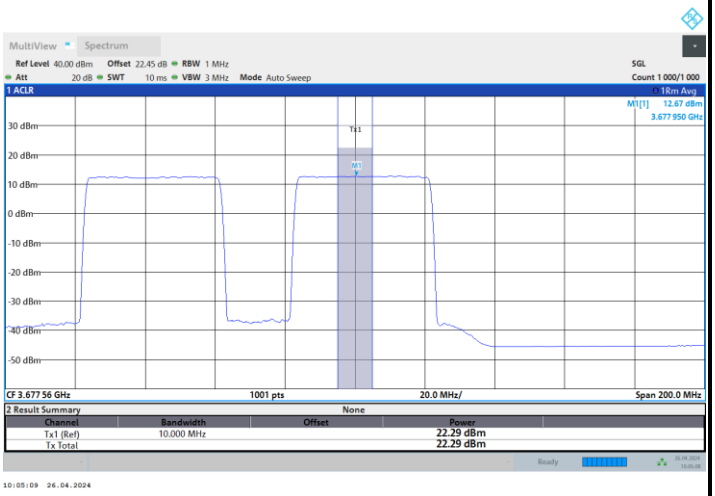
64QAM



256QAM



FR1 n48 / 40MHz+40MHz (Non Contiguous) / Lowest Channel / Conducted (dBm/10MHz)	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 40MHz+40MHz (Non Contiguous) / Highest Channel / Conducted (dBm/10MHz)	
QPSK	16QAM
	
64QAM	256QAM
	

Power Spectral Density

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Lowest Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	12.82	12.37	12.14	11.89	11.69	11.57	12.49	12.45
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	12.80	12.61	12.18	12.21	11.86	11.81	12.61	12.73
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	12.27	12.37	11.66	11.65	11.86	11.70	11.89	11.96
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	12.26	12.44	11.56	11.50	11.89	11.96	11.84	11.73
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Lowest CH	12.90	12.91						
Mod.	64QAM	256QAM						
Lowest CH	12.99	13.05						

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Lowest Channel (Non-Contiguous)							
BW	10MHz+10MHz		40MHz+40MHz					
Mod.	QPSK	16QAM	QPSK	16QAM				
Lowest CH	12.43	11.96	12.62	12.79				
Mod.	64QAM	256QAM	64QAM	256QAM				
Lowest CH	12.31	12.18	12.71	12.67				

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Lowest Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	36.34	35.89	35.66	35.41	35.21	35.09	36.01	35.97
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	36.32	36.13	35.70	35.73	35.38	35.33	36.13	36.25
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Lowest CH	35.79	35.89	35.18	35.17	35.38	35.22	35.41	35.48
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Lowest CH	35.78	35.96	35.08	35.02	35.41	35.48	35.36	35.25
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Lowest CH	36.42	36.43						
Mod.	64QAM	256QAM						
Lowest CH	36.51	36.57						
Limit	37dBm/MHz							
Result	PASS							

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Lowest Channel (Non-Contiguous)							
BW	10MHz+10MHz		40MHz+40MHz					
Mod.	QPSK	16QAM	QPSK	16QAM				
Lowest CH	35.95	35.48	36.14	36.31				
Mod.	64QAM	256QAM	64QAM	256QAM				
Lowest CH	35.83	35.7	36.23	36.19				
Limit	37dBm/MHz							
Result	PASS							

Note

1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 17.5dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Middle Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	12.18	12.40	11.68	11.81	12.20	11.98	12.15	12.15
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	12.17	12.45	12.00	11.87	12.44	12.31	12.28	11.96
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	12.12	12.14	11.50	11.61	11.76	11.84	12.21	11.95
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	12.14	12.07	11.45	11.54	11.85	12.04	12.20	12.19
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Middle CH	12.95	12.94						
Mod.	64QAM	256QAM						
Middle CH	13.02	13.06						

Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Middle Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	35.70	35.92	35.20	35.33	35.72	35.5	35.67	35.67
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	35.69	35.97	35.52	35.39	35.96	35.83	35.80	35.48
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Middle CH	35.64	35.66	35.02	35.13	35.28	35.36	35.73	35.47
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Middle CH	35.66	35.59	34.97	35.06	35.37	35.56	35.72	35.71
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Middle CH	36.47	36.46						
Mod.	64QAM	256QAM						
Middle CH	36.54	36.58						
Limit	37dBm/MHz							
Result	PASS							

Note

1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 17.5dBi MIMO antenna gain.

Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Highest Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	12.10	12.02	11.94	12.10	11.86	11.84	11.43	11.44
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	12.06	12.11	12.20	12.13	12.14	11.97	11.33	11.54
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	12.14	11.99	11.63	11.60	11.41	11.53	11.19	11.15
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	12.26	12.04	11.62	11.73	11.37	11.47	11.26	11.19
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Highest CH	12.93	12.86						
Mod.	64QAM	256QAM						
Highest CH	13.01	12.84						

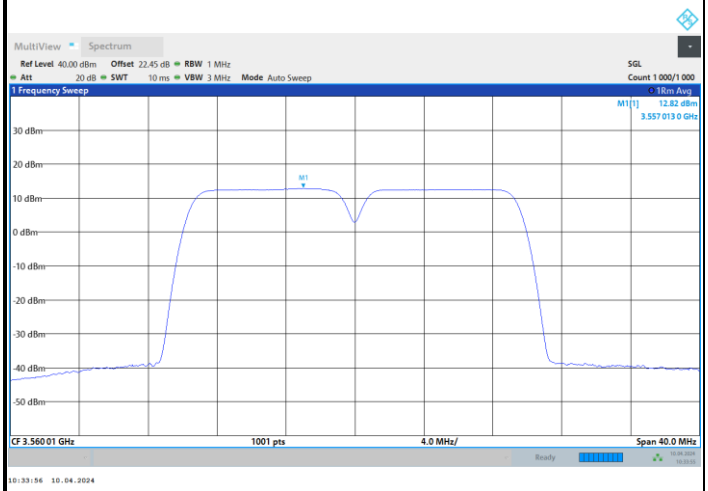
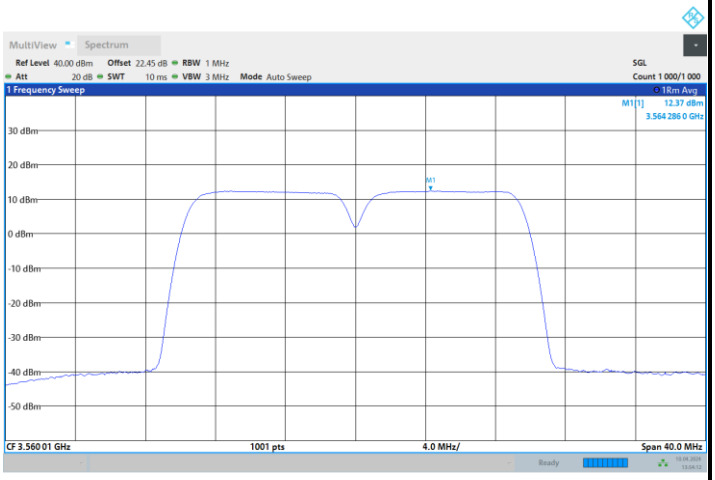
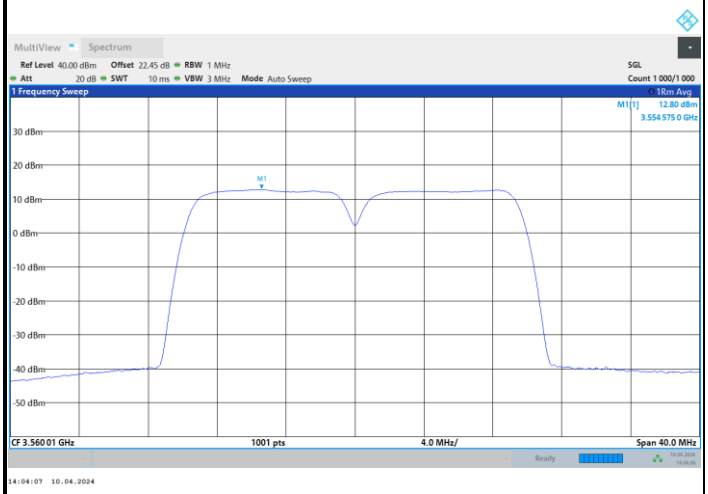
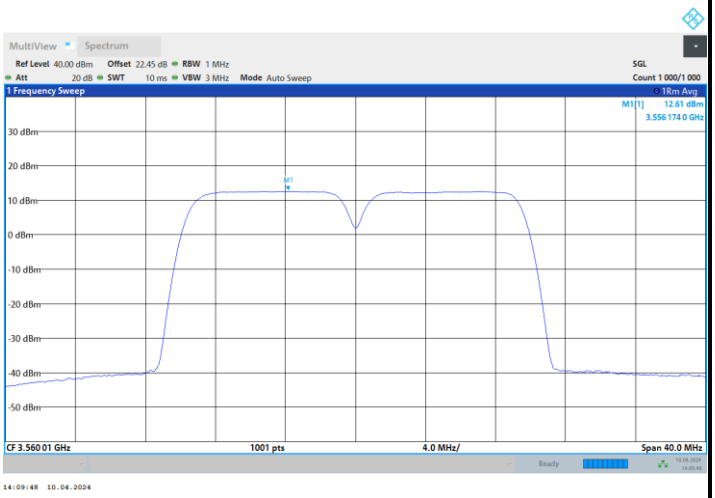
Mode	FR1 n48 : Conducted PSD (dBm/MHz) <SISO> Highest Channel (Non-Contiguous)							
BW	10MHz+10MHz		40MHz+40MHz					
Mod.	QPSK	16QAM	QPSK	16QAM				
Highest CH	12.07	11.90	12.86	13.17				
Mod.	64QAM	256QAM	64QAM	256QAM				
Highest CH	11.97	12.04	13.11	12.84				

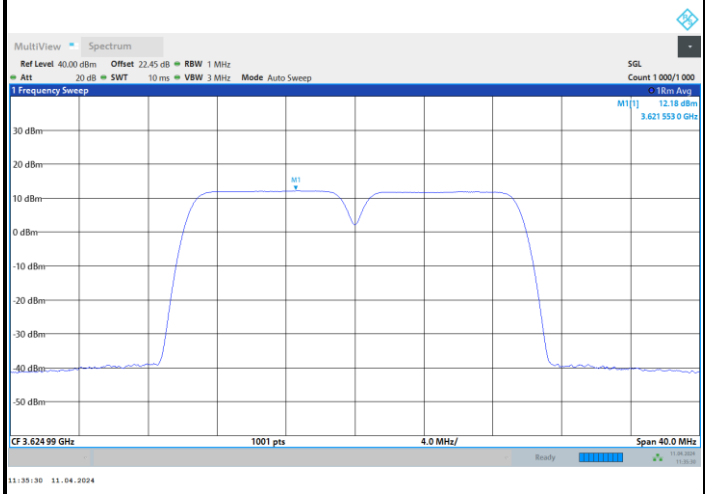
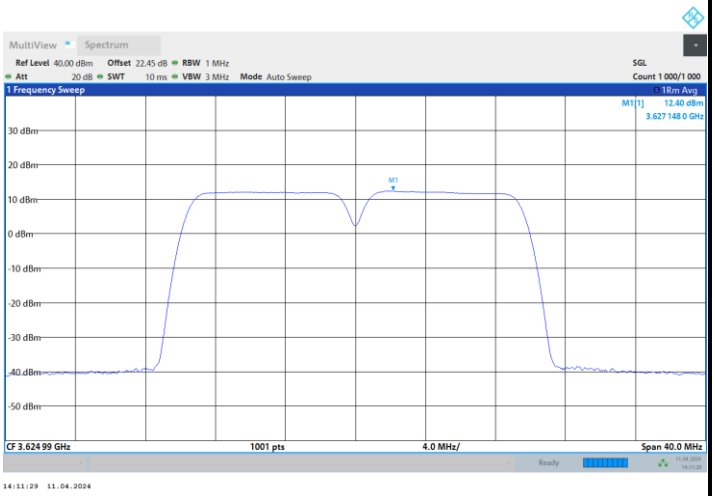
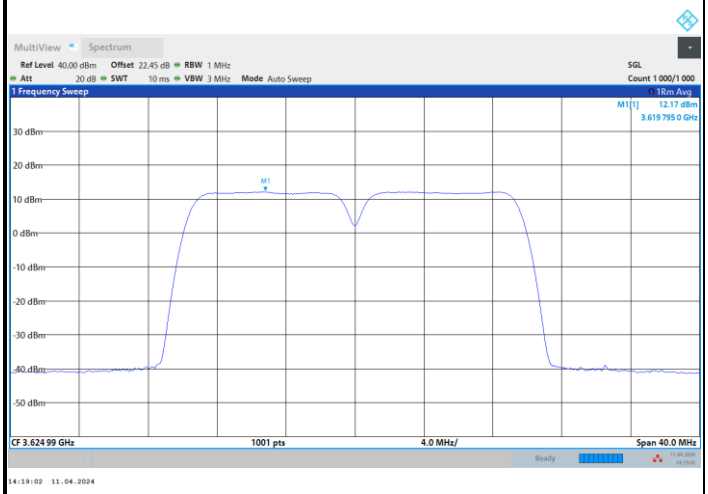
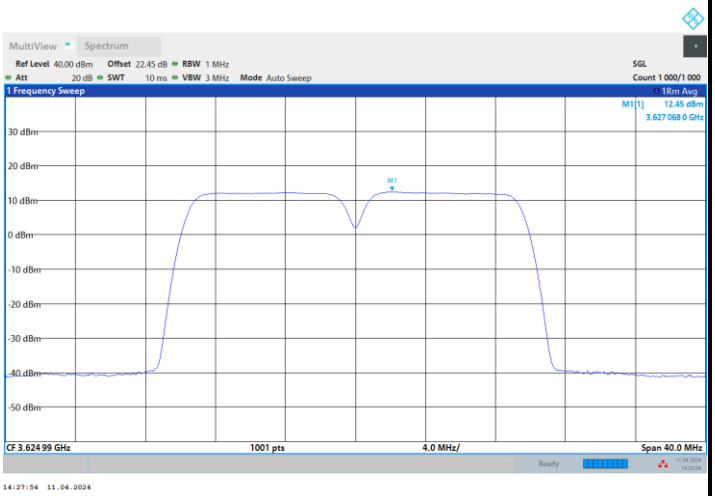
Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Highest Channel (Contiguous)							
BW	10MHz+10MHz		10MHz+20MHz		10MHz+40MHz		20MHz+10MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	35.62	35.54	35.46	35.62	35.38	35.36	34.95	34.96
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	35.58	35.63	35.72	35.65	35.66	35.49	34.85	35.06
BW	20MHz+20MHz		20MHz+40MHz		40MHz+10MHz		40MHz+20MHz	
Mod.	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM	QPSK	16QAM
Highest CH	35.66	35.51	35.15	35.12	34.93	35.05	34.71	34.67
Mod.	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM	64QAM	256QAM
Highest CH	35.78	35.56	35.14	35.25	34.89	34.99	34.78	34.71
BW	40MHz+40MHz							
Mod.	QPSK	16QAM						
Highest CH	36.45	36.38						
Mod.	64QAM	256QAM						
Highest CH	36.53	36.36						
Limit	37dBm/MHz							
Result	PASS							

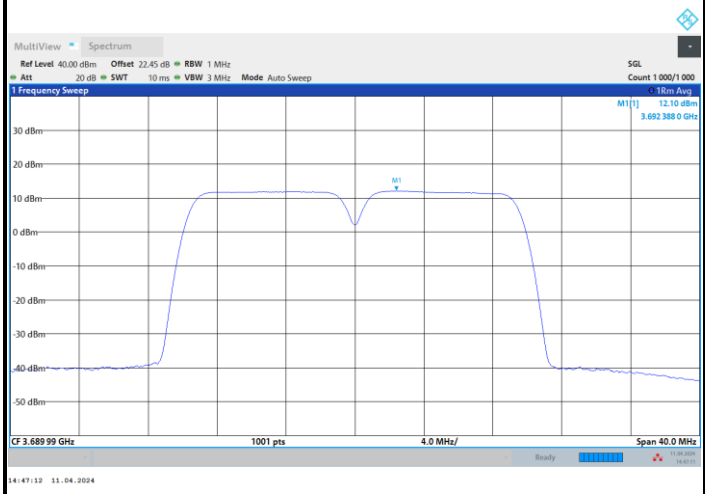
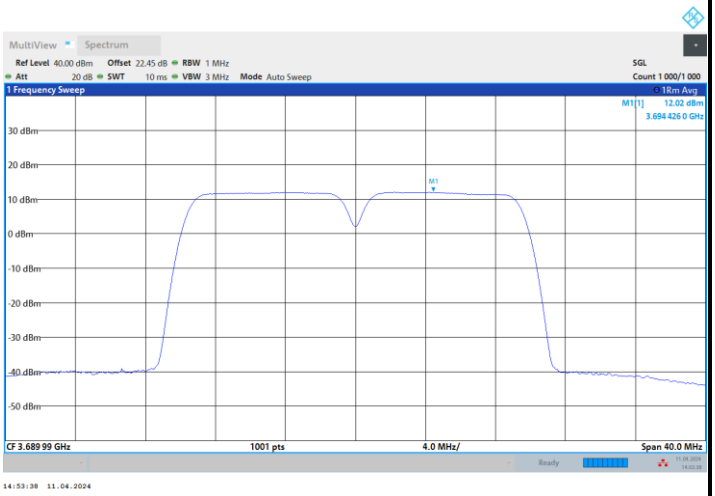
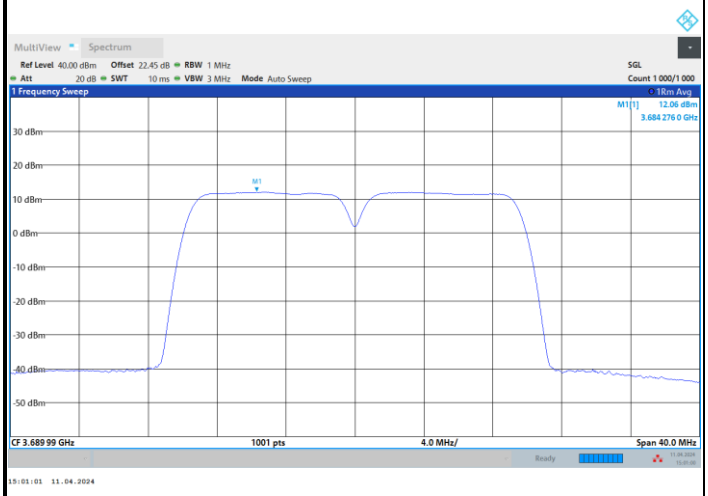
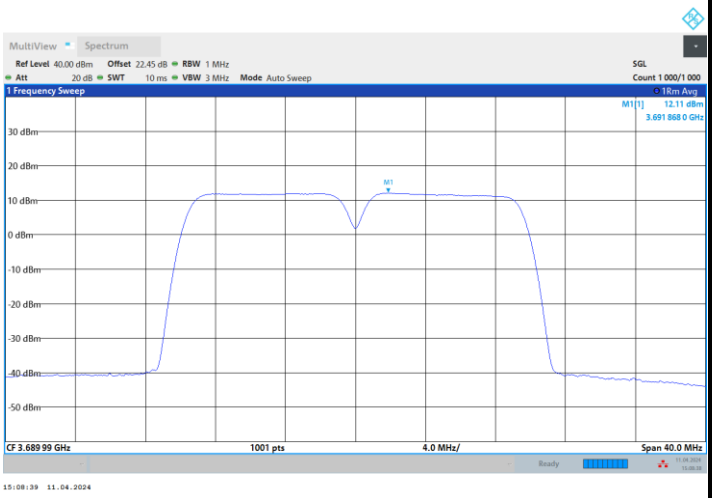
Mode	FR1 n48 : EIRP PSD (dBm/MHz) <MIMO 4TX> Highest Channel (Non-Contiguous)							
BW	10MHz+10MHz		40MHz+40MHz					
Mod.	QPSK	16QAM	QPSK	16QAM				
Highest CH	35.59	35.42	36.38	36.69				
Mod.	64QAM	256QAM	64QAM	256QAM				
Highest CH	35.49	35.56	36.63	36.36				
Limit	37dBm/MHz							
Result	PASS							

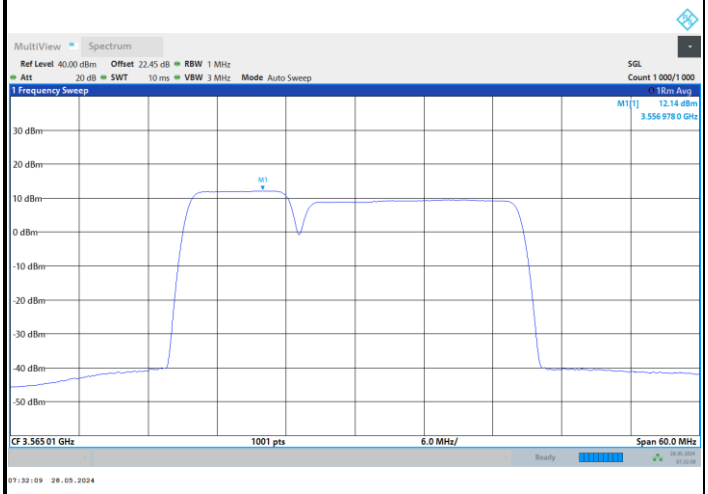
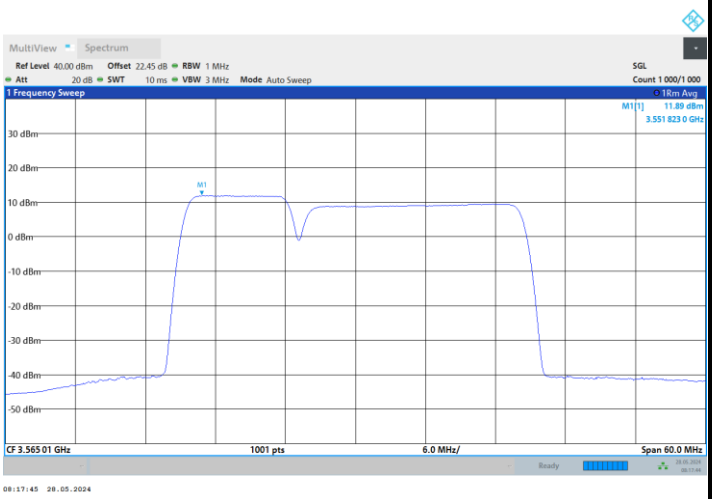
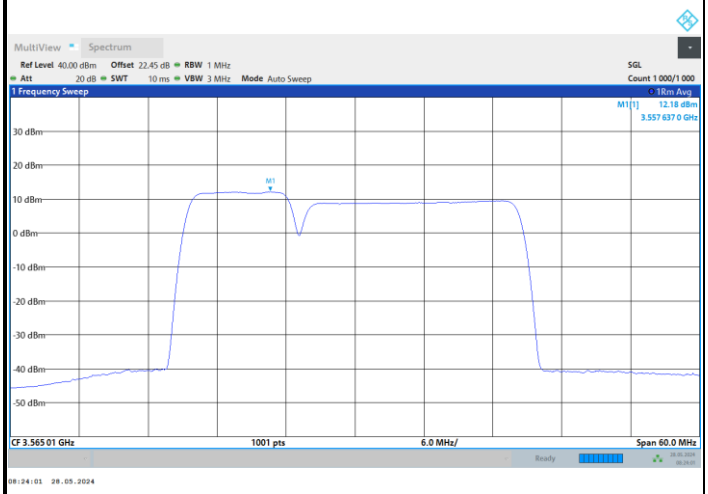
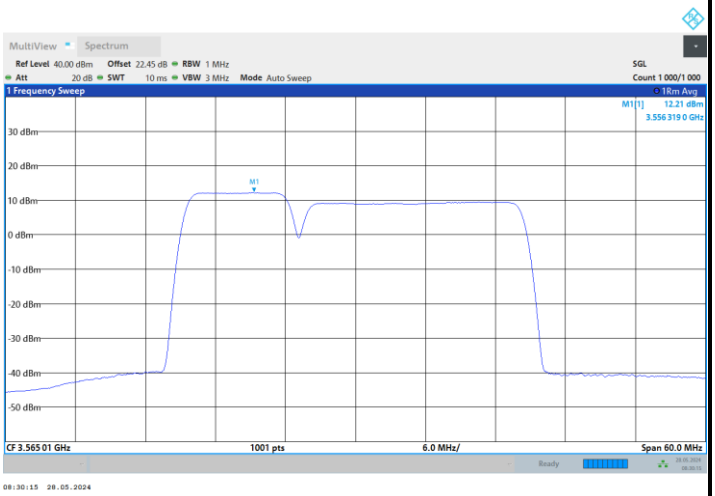
Note

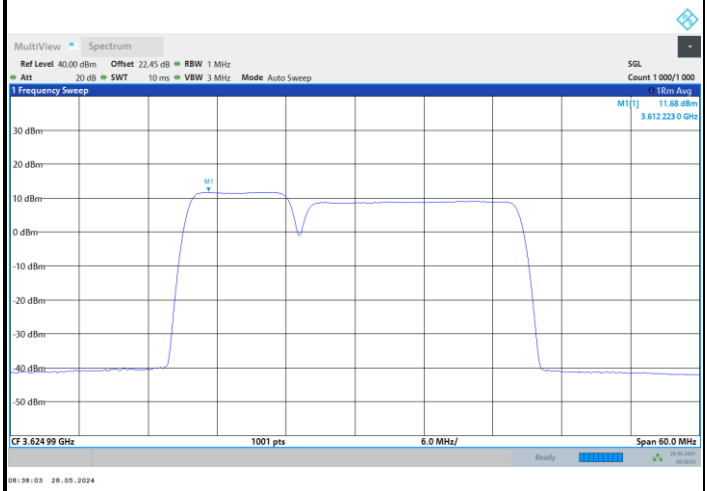
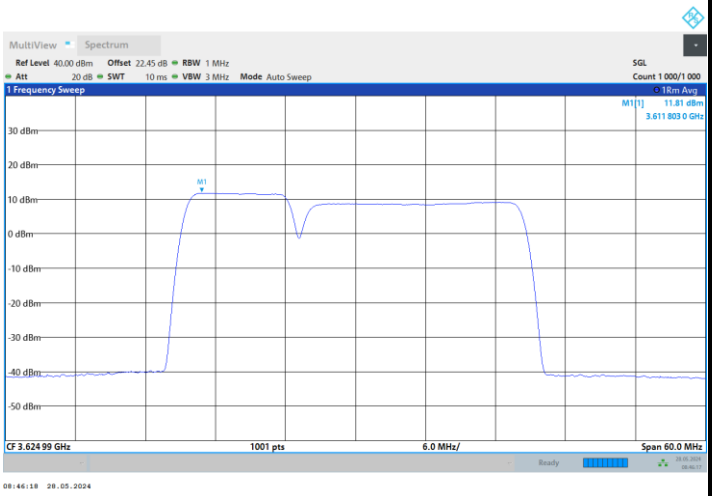
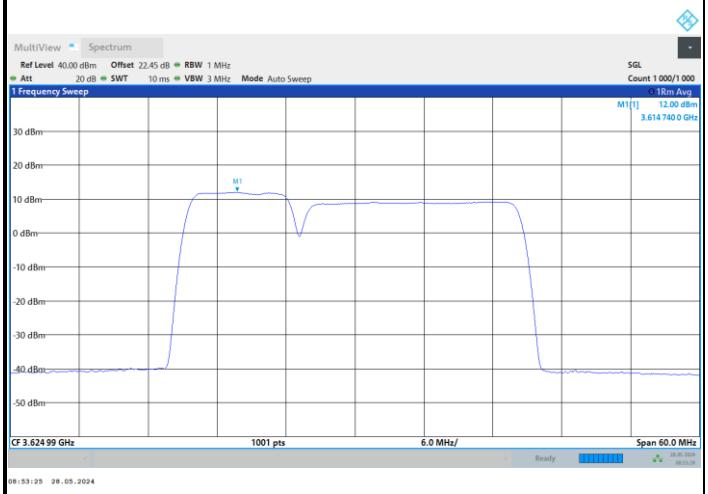
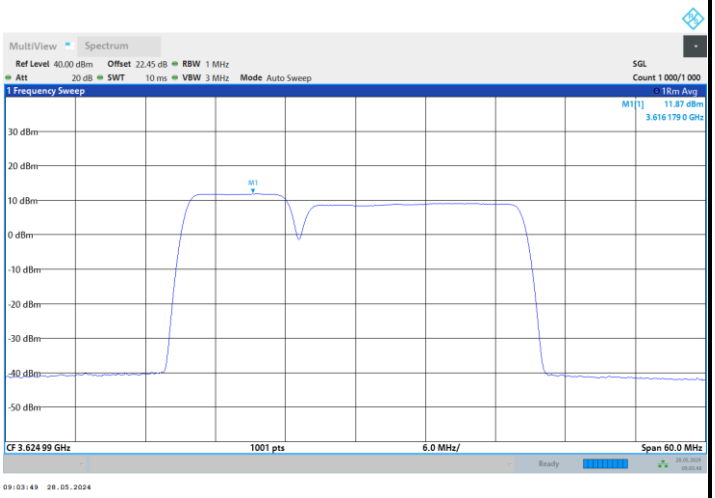
1. The measured conducted PSD result has included duty cycle offset factor.
2. The EIRP PSD = conducted PSD result + 6.02dB (4TX) + 17.5dBi MIMO antenna gain.

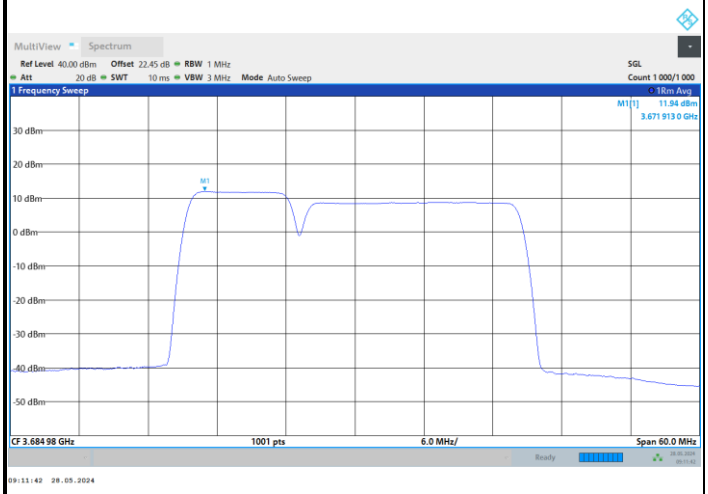
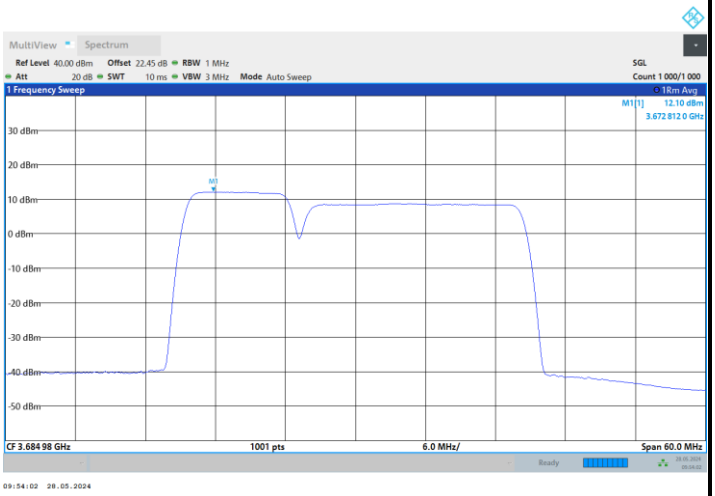
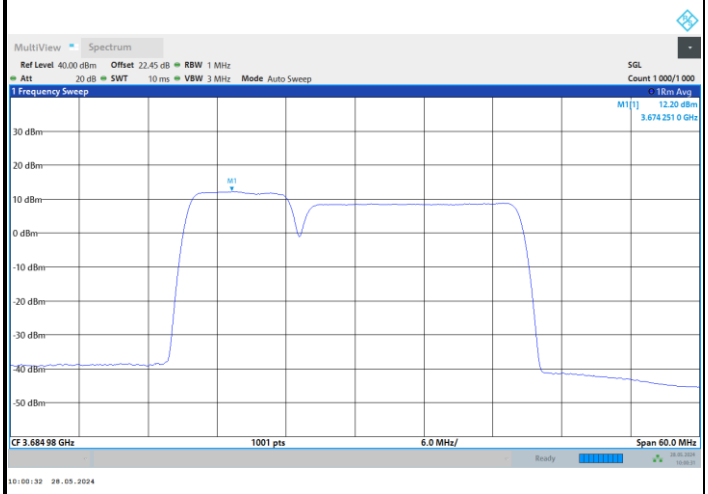
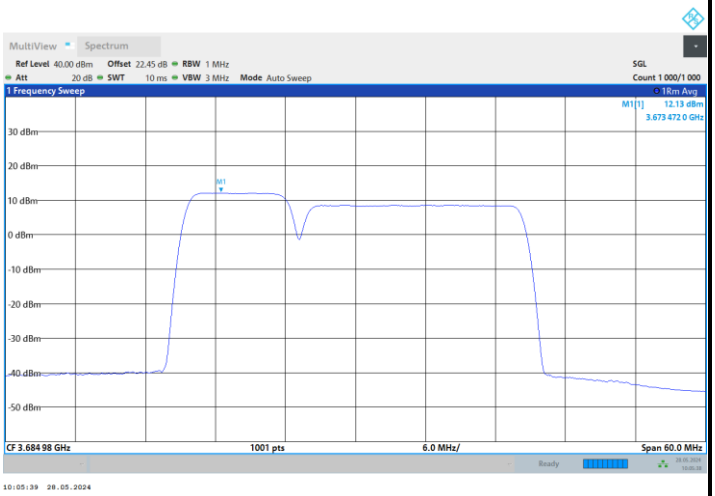
FR1 n48 / 10MHz+10MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

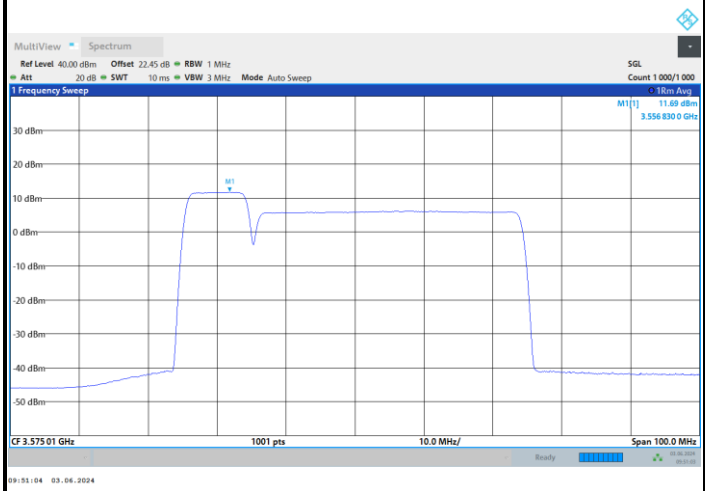
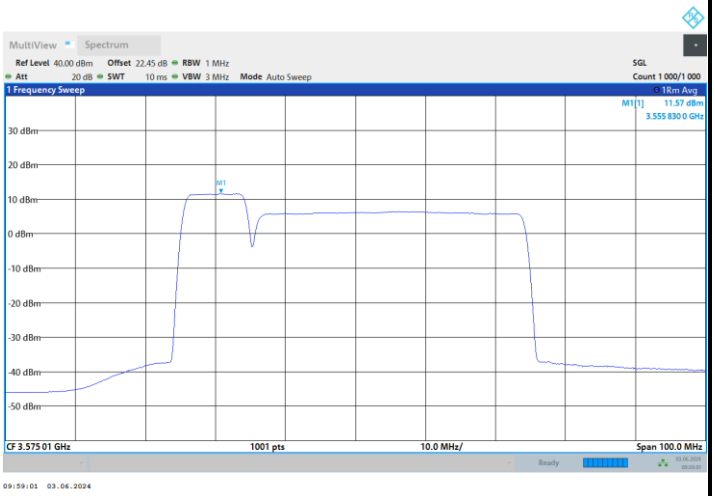
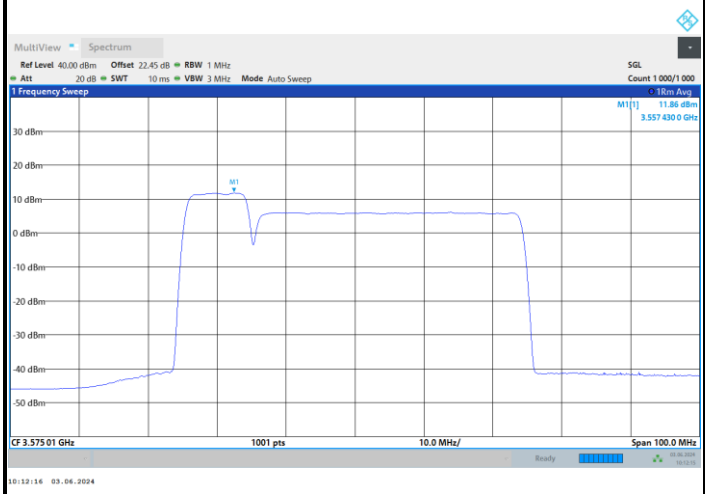
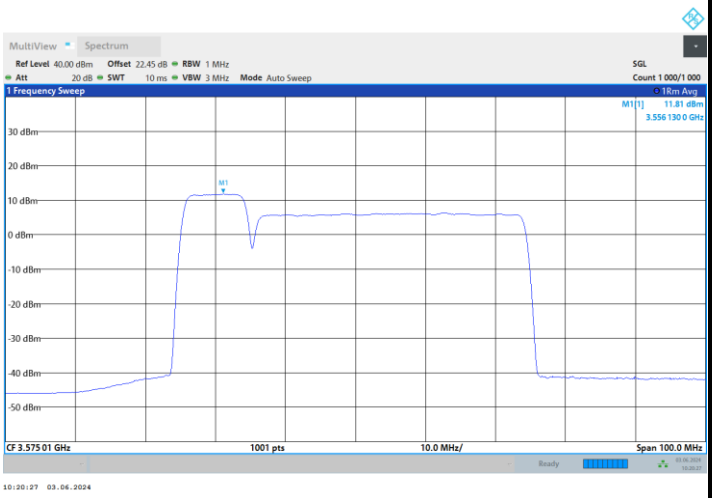
FR1 n48 / 10MHz+10MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

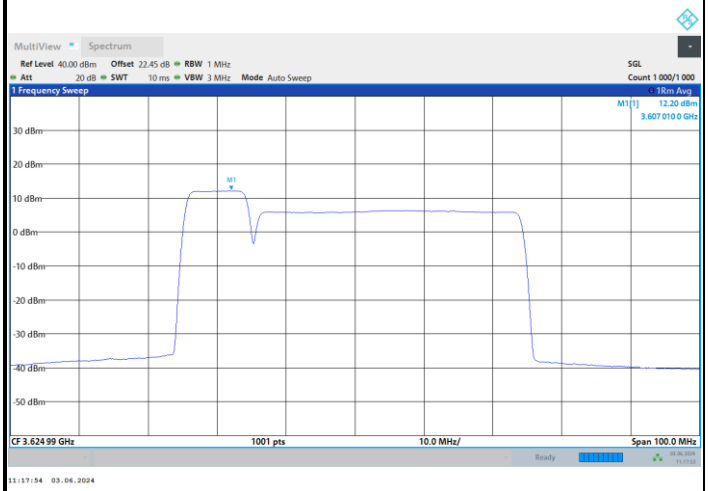
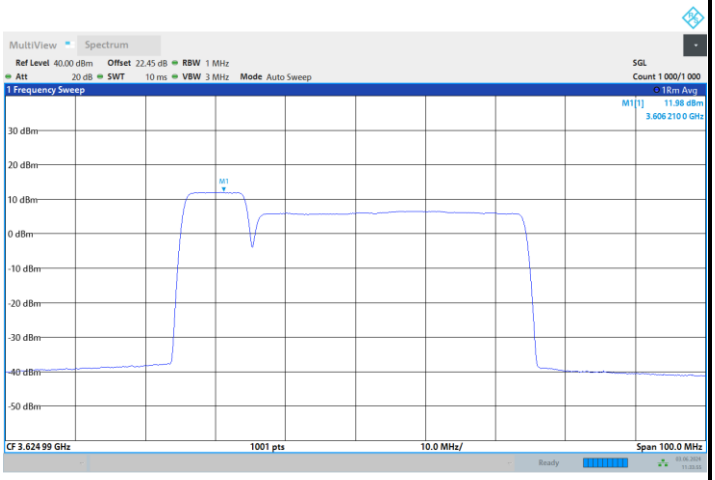
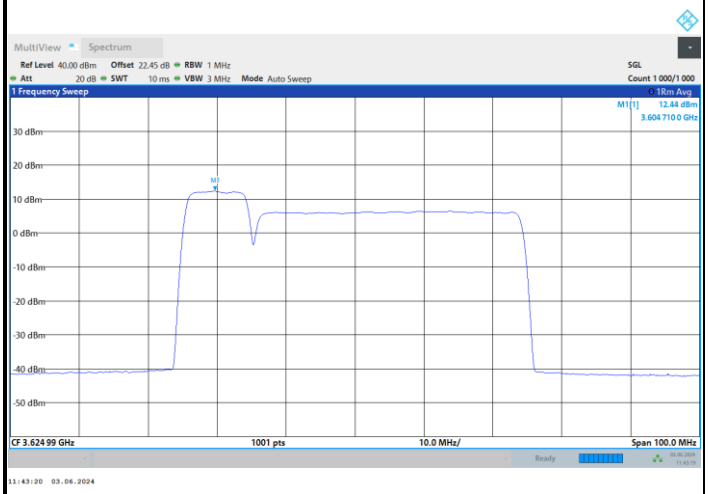
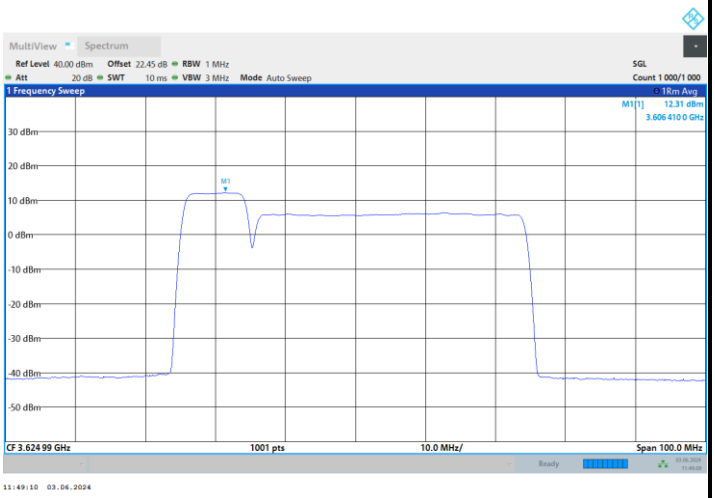
FR1 n48 / 10MHz+10MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

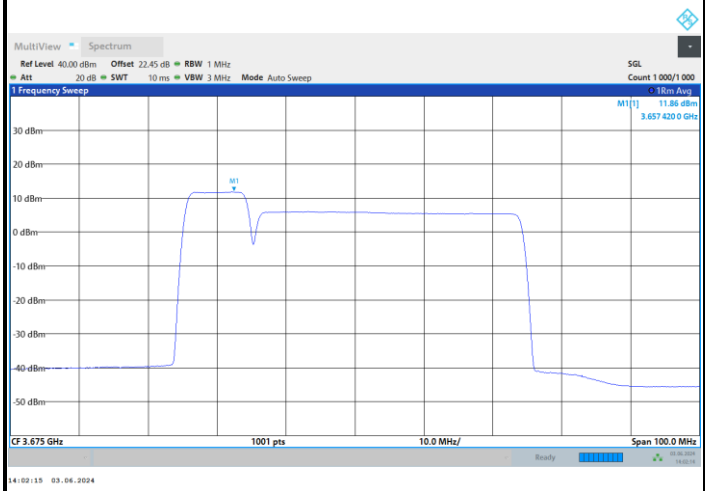
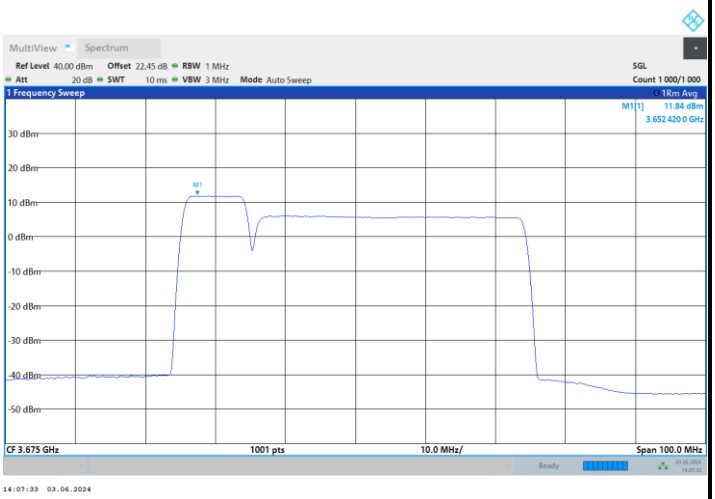
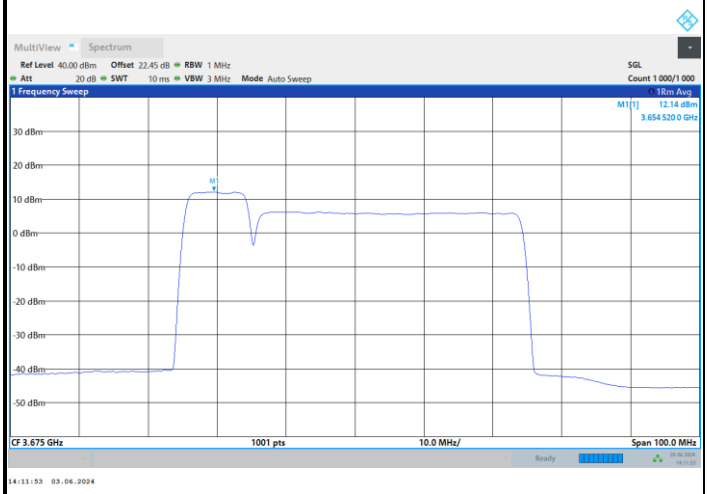
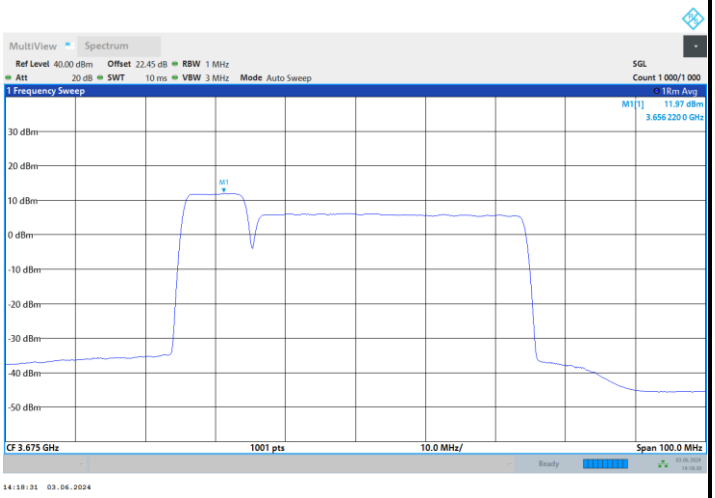
FR1 n48 / 10MHz+20MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

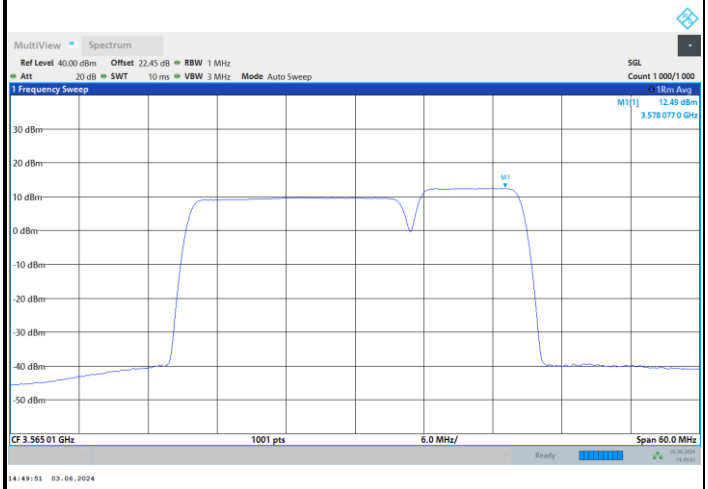
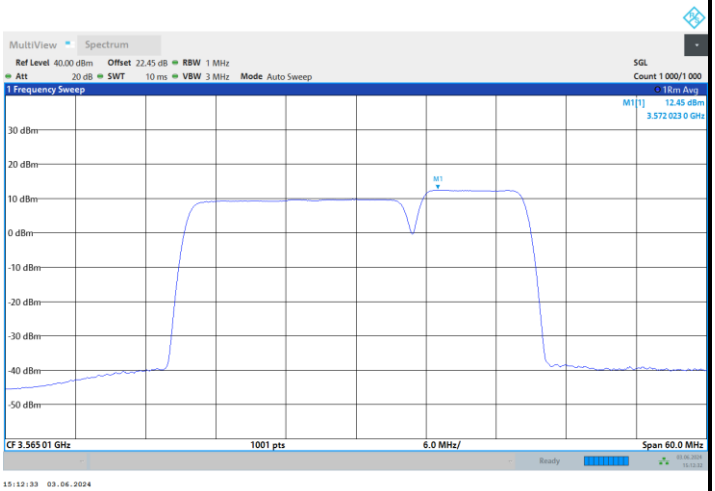
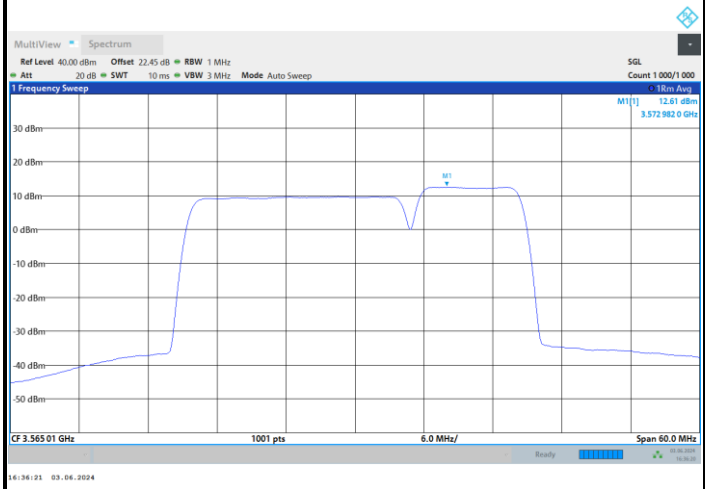
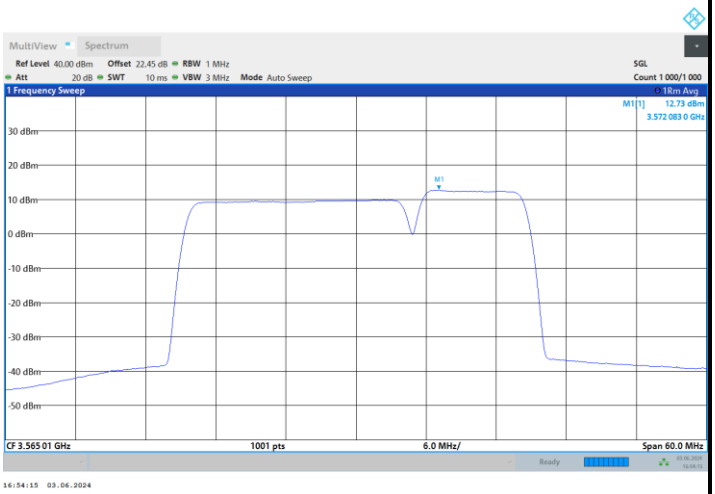
FR1 n48 / 10MHz+20MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

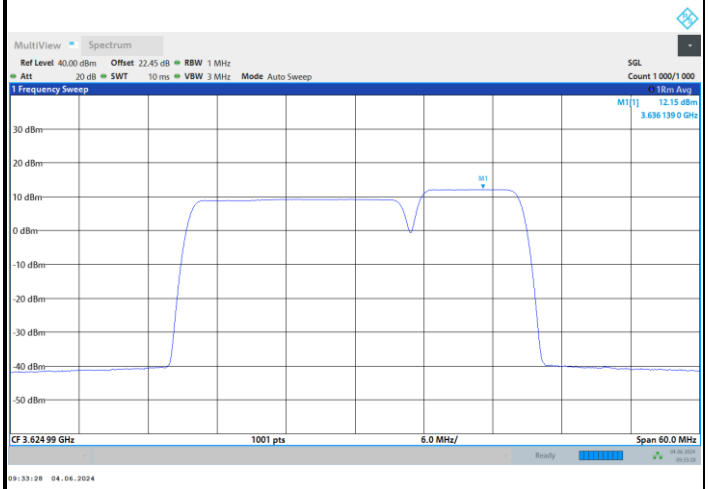
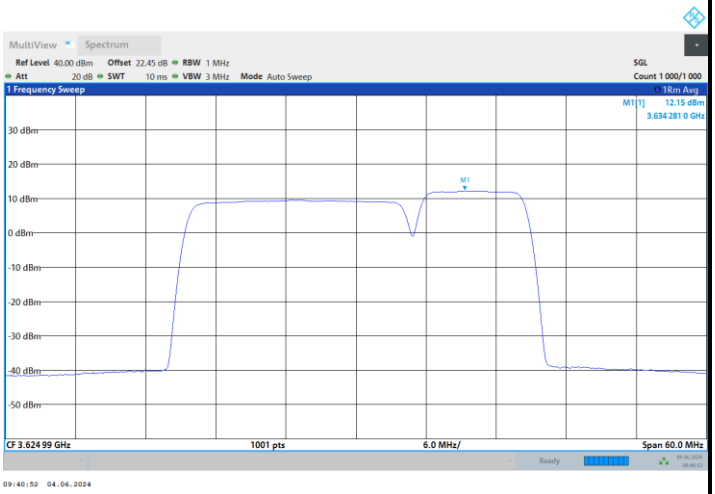
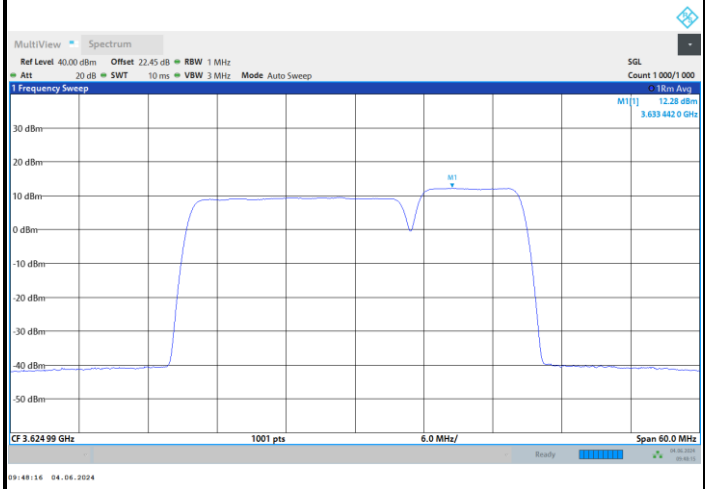
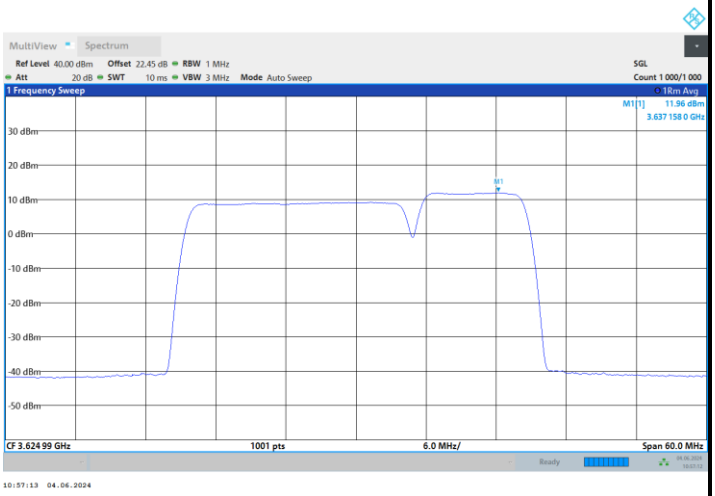
FR1 n48 / 10MHz+20MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

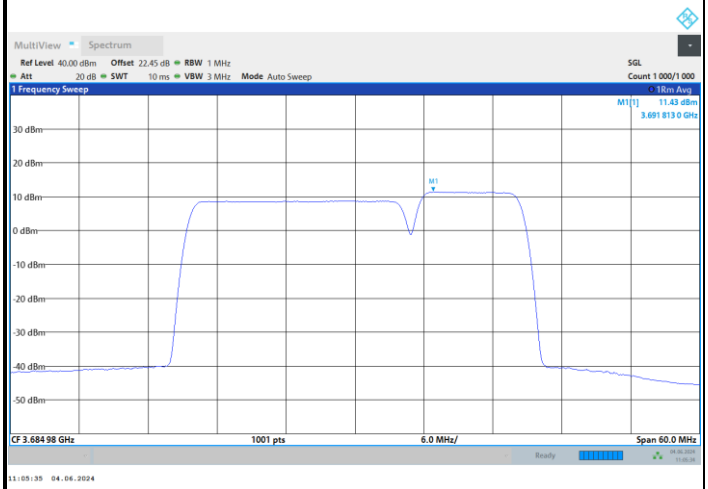
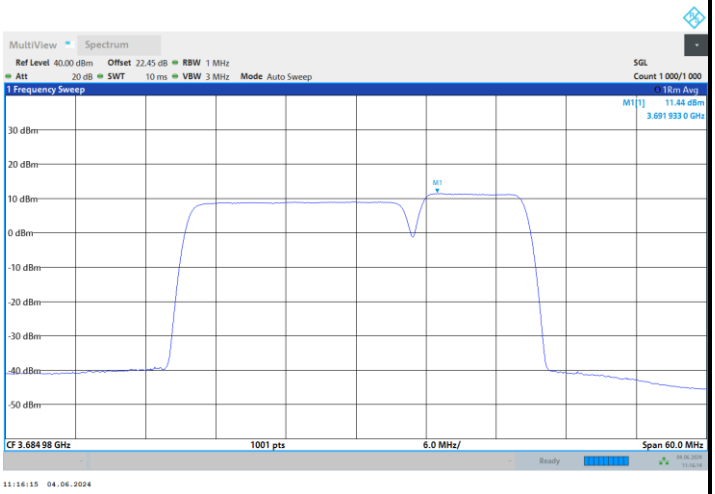
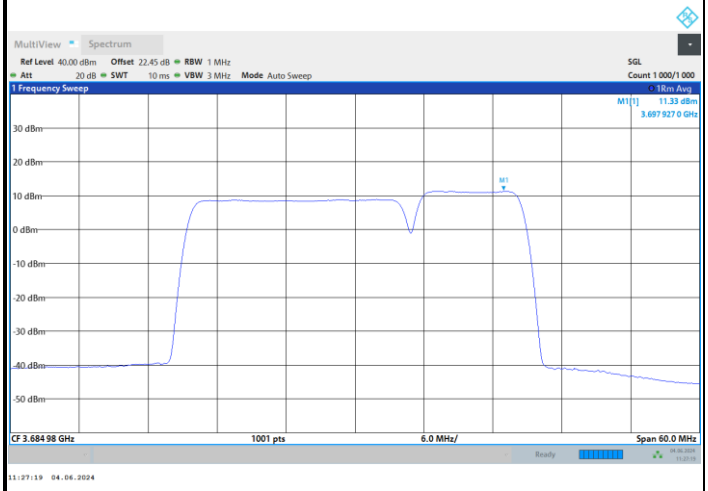
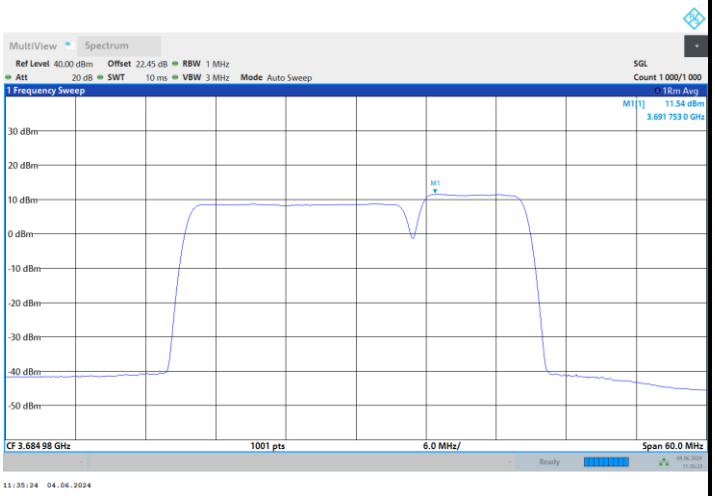
FR1 n48 / 10MHz+40MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

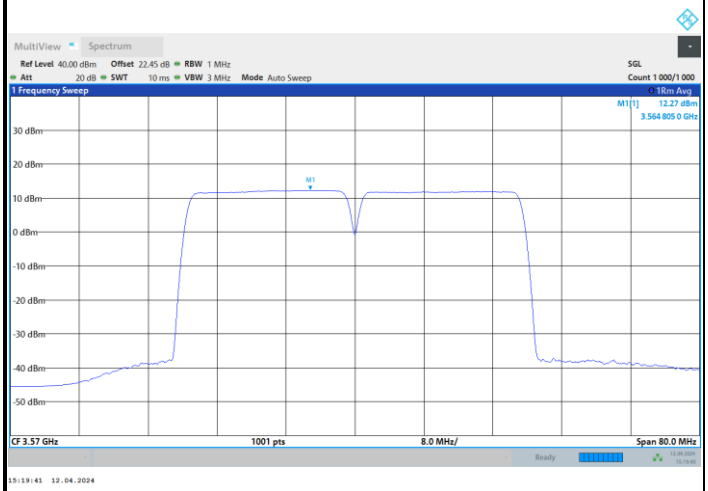
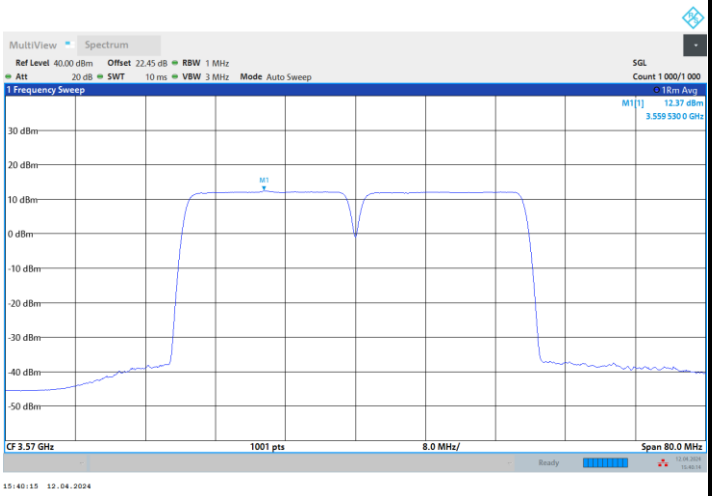
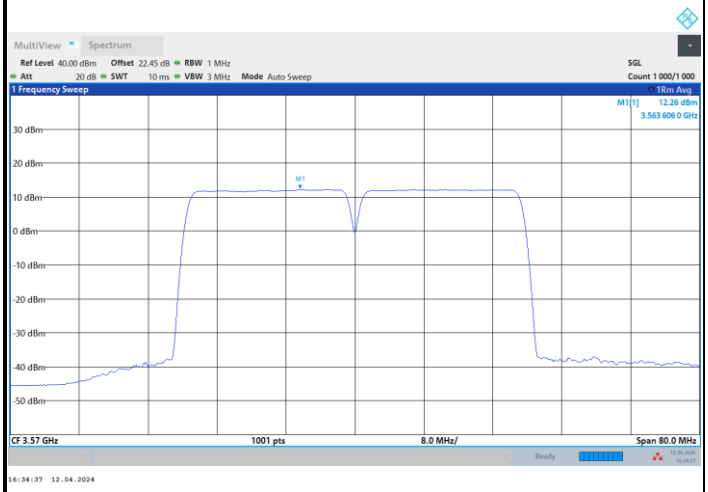
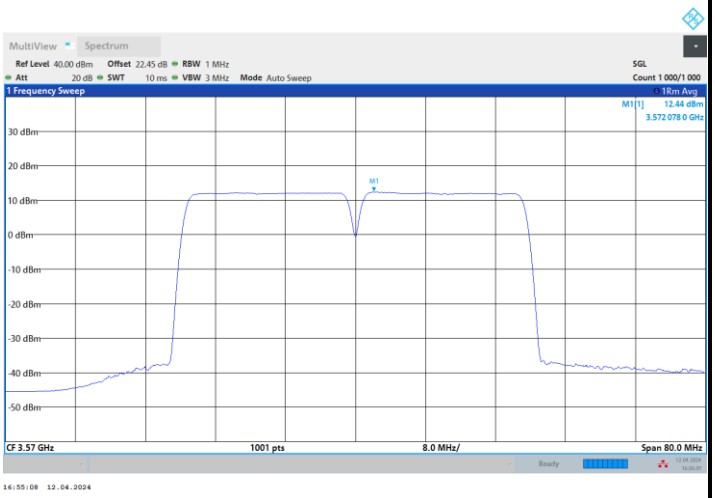
FR1 n48 / 10MHz+40MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

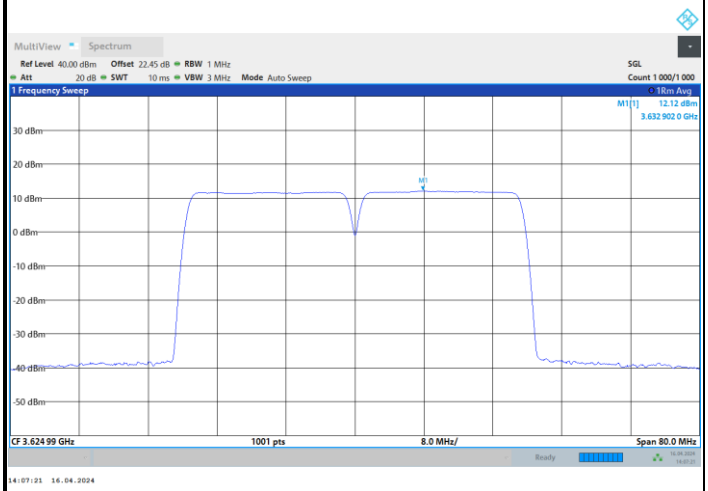
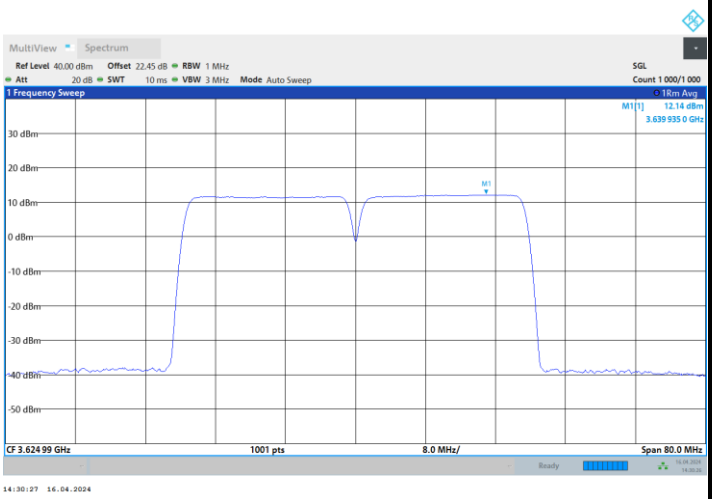
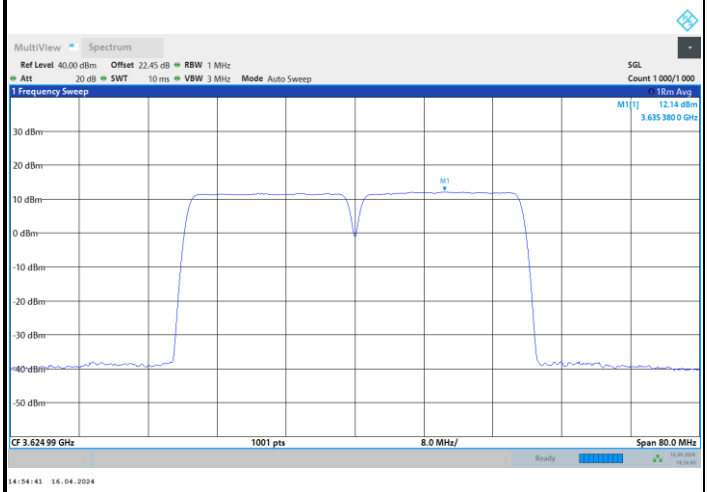
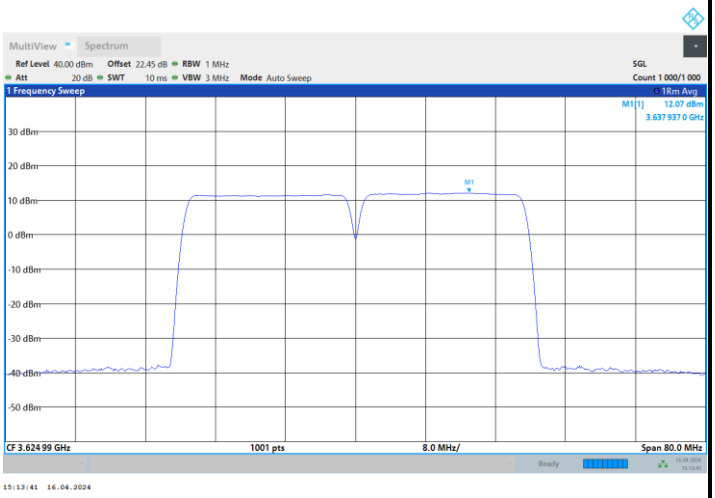
FR1 n48 /10MHz+40MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

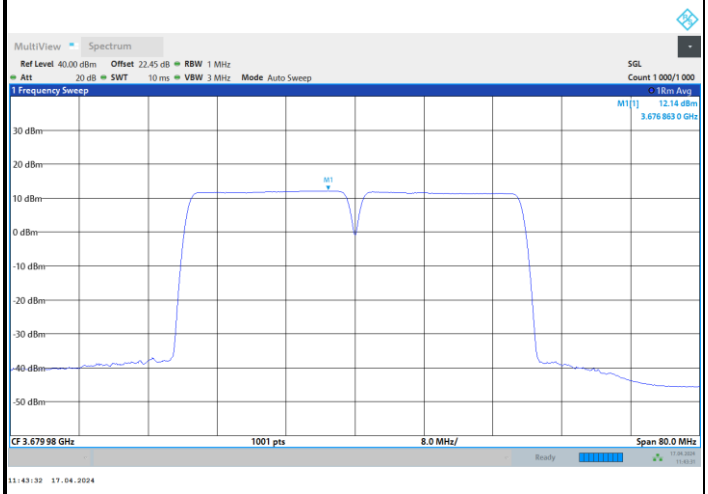
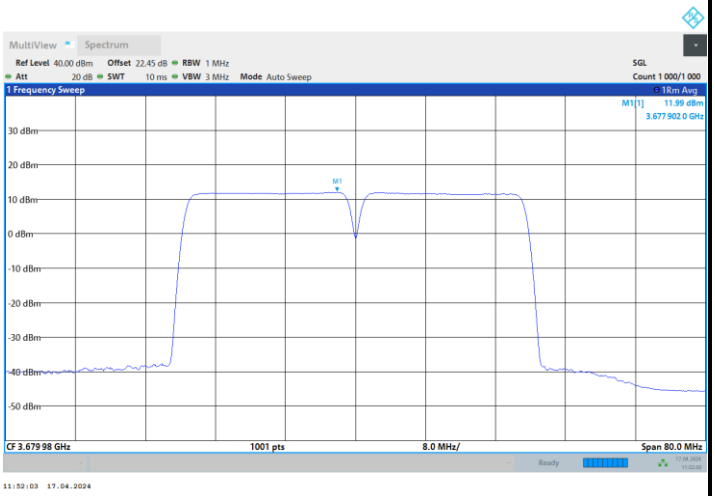
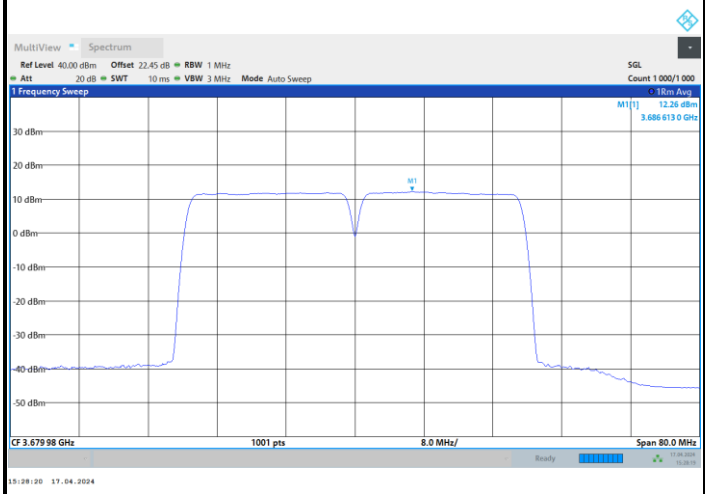
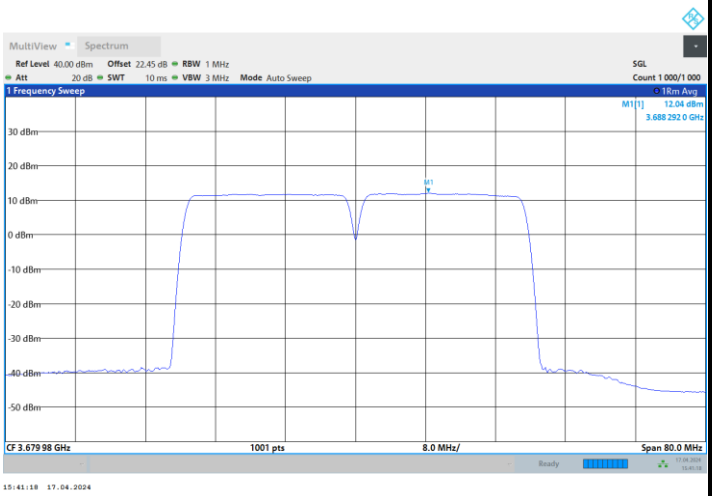
FR1 n48 / 20MHz+10MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

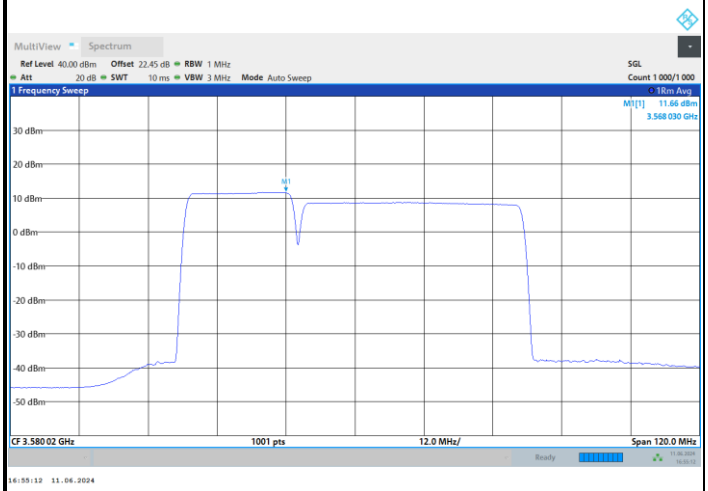
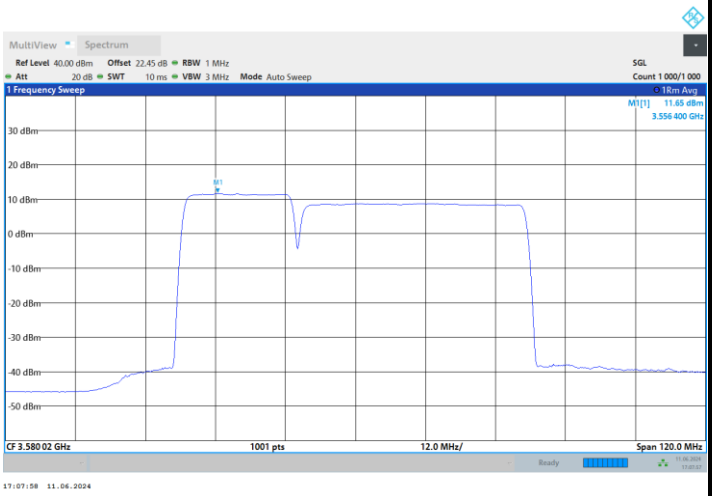
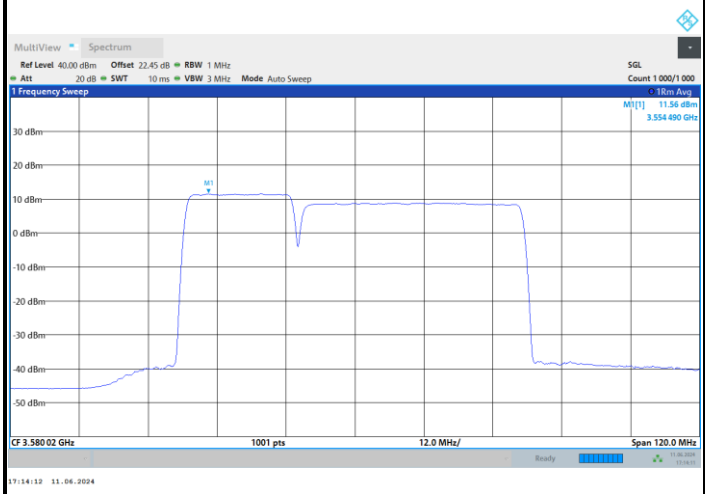
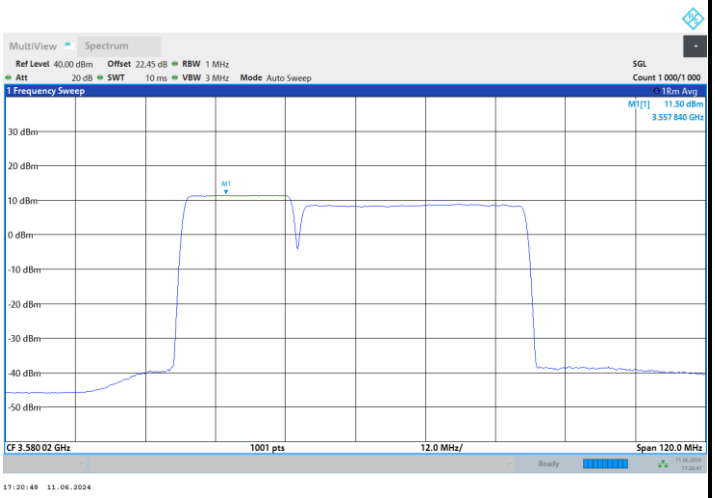
FR1 n48 / 20MHz+10MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

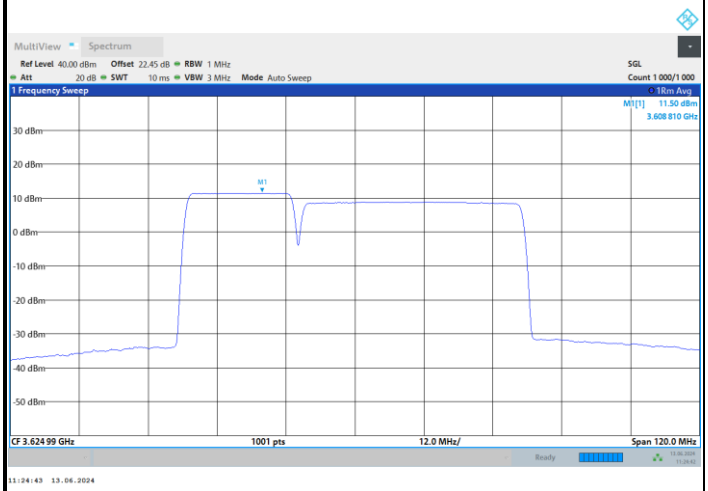
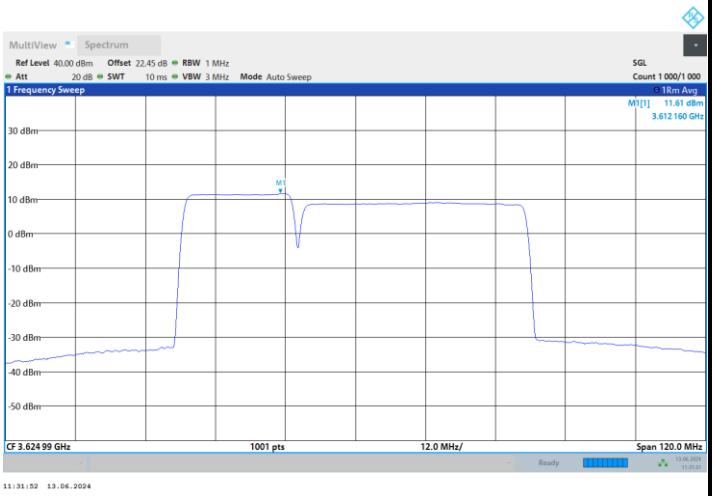
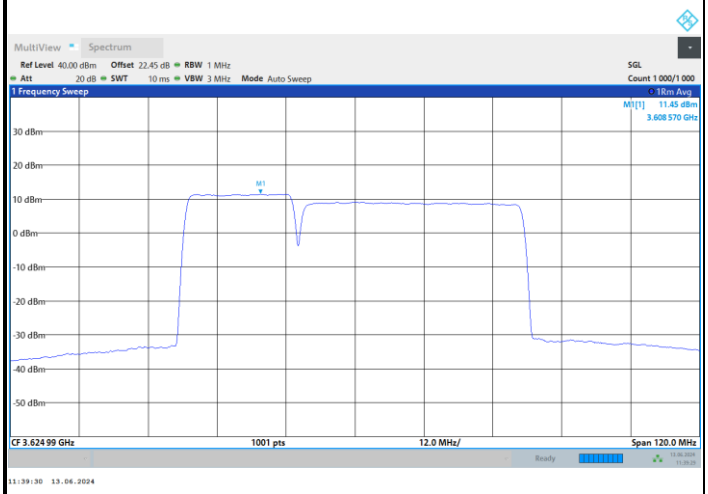
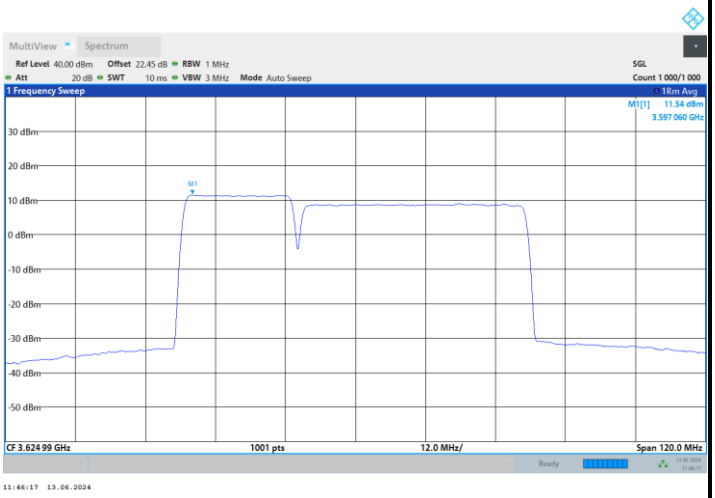
FR1 n48 / 20MHz+10MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

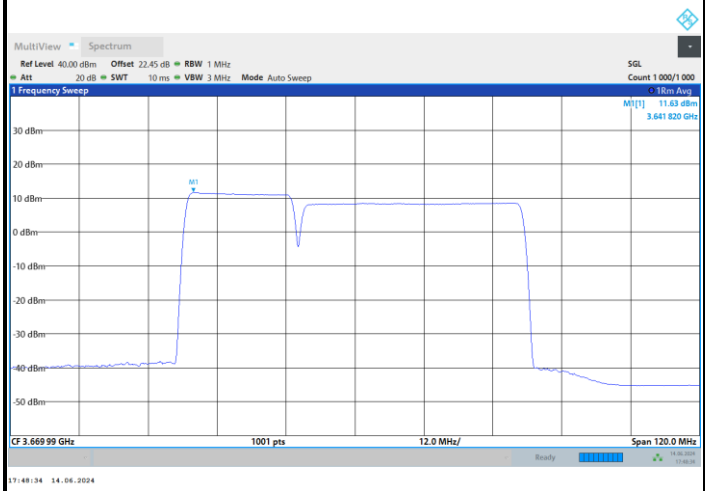
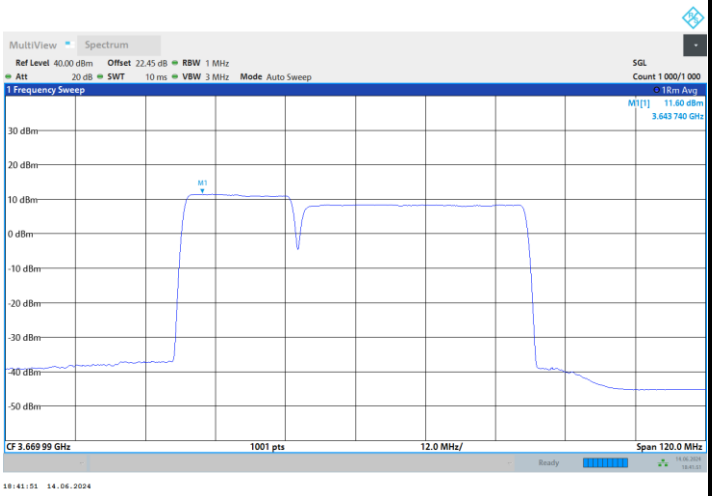
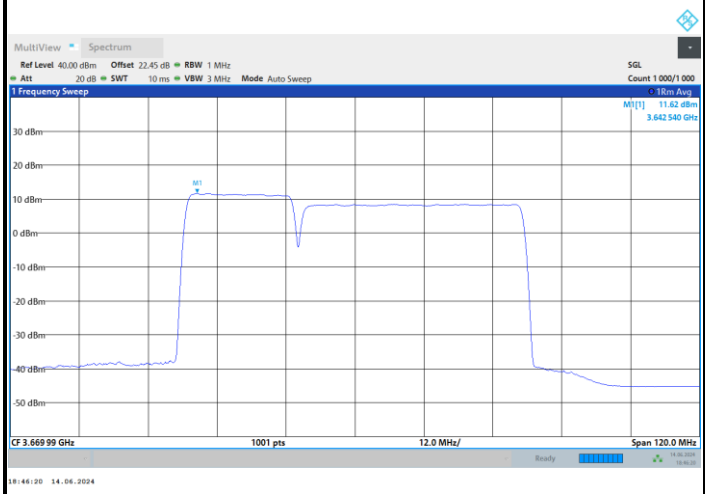
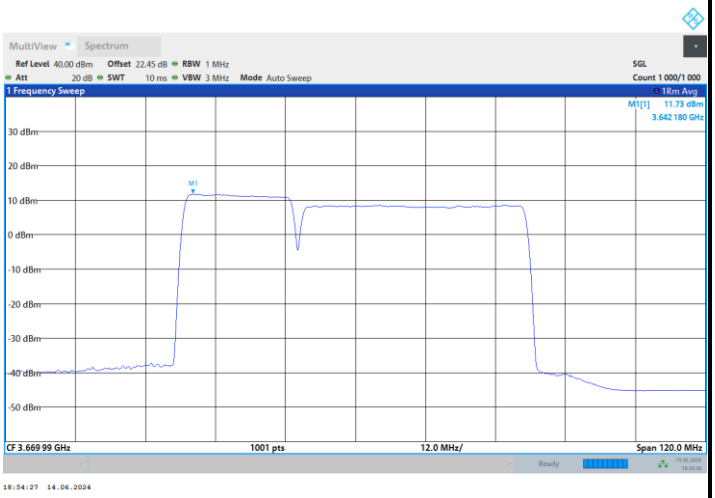
FR1 n48 / 20MHz+20MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

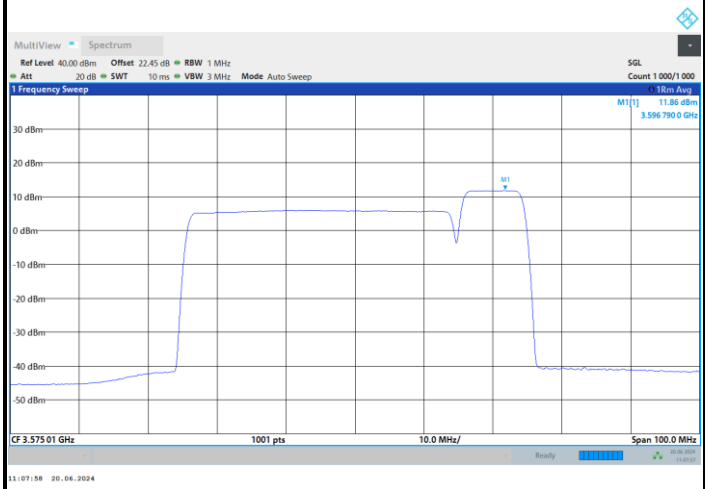
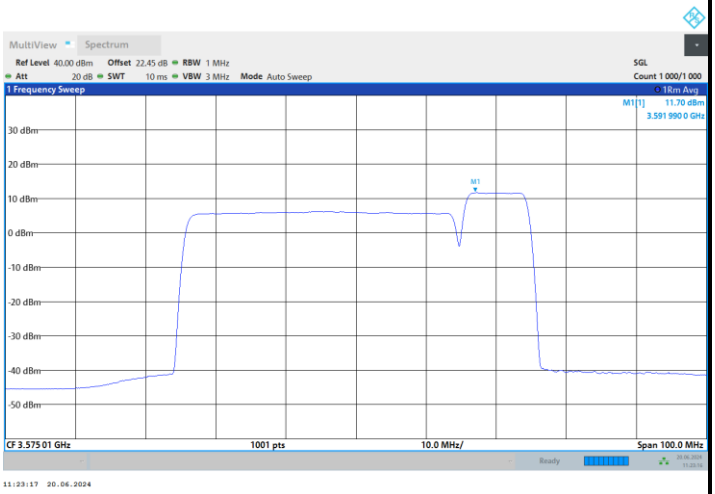
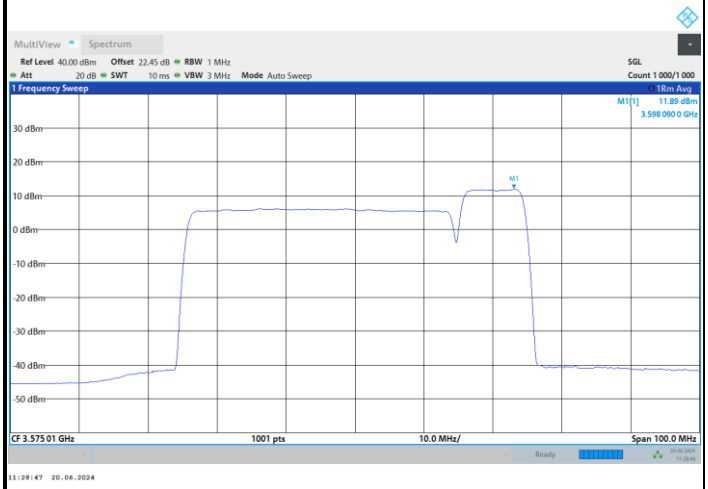
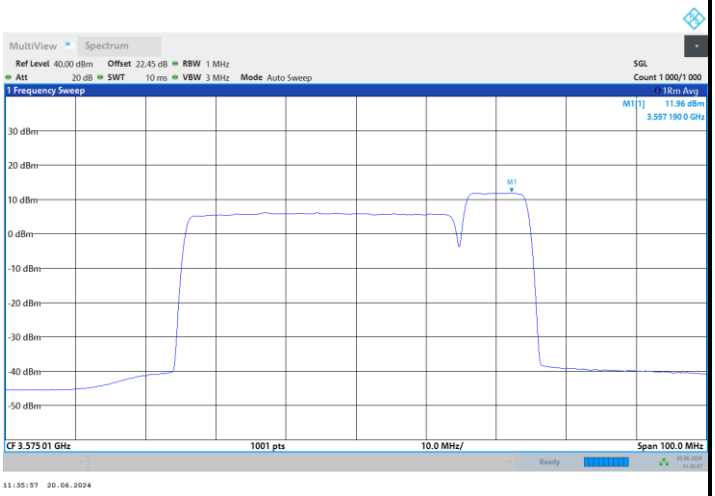
FR1 n48 / 20MHz+20MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

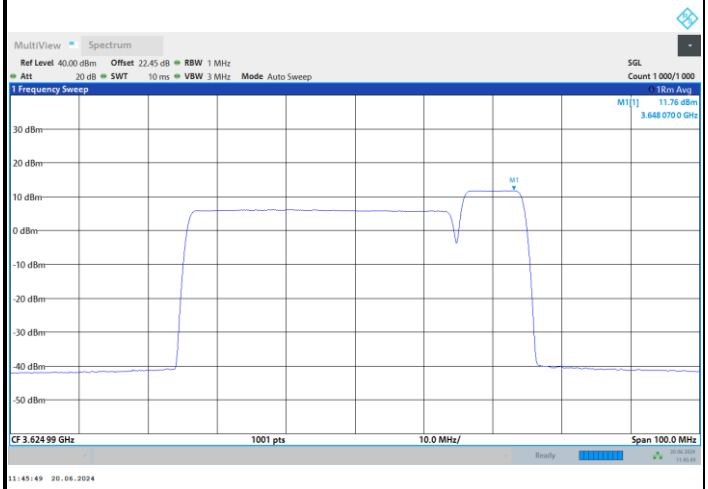
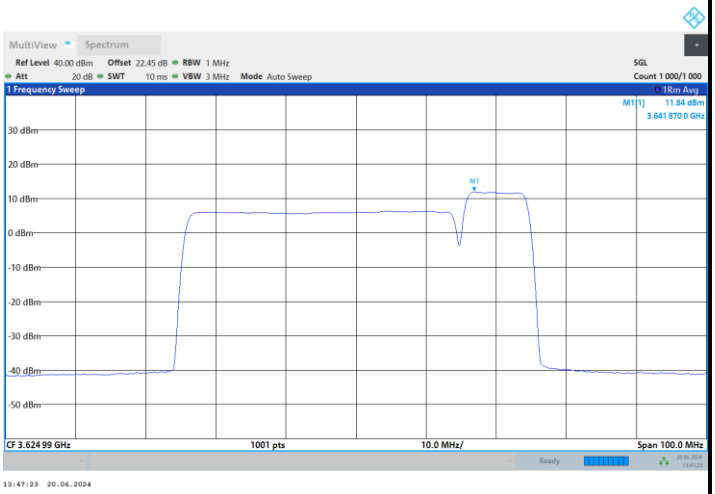
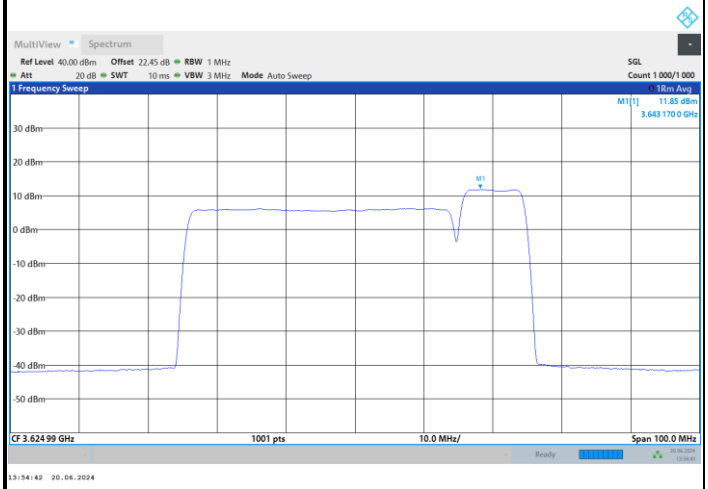
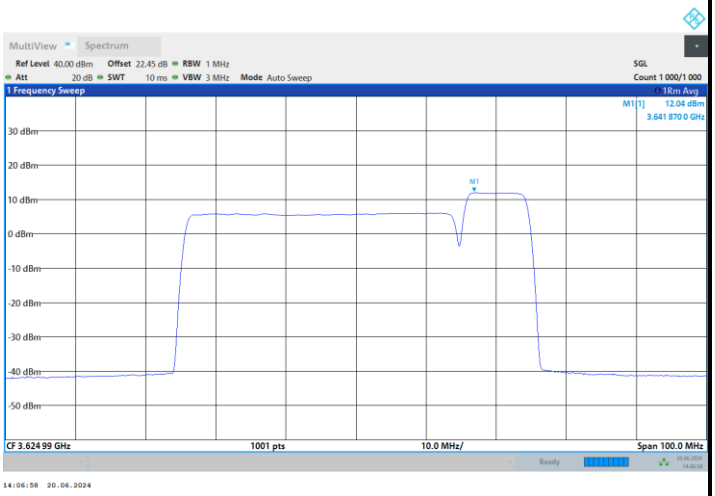
FR1 n48 / 20MHz+20MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

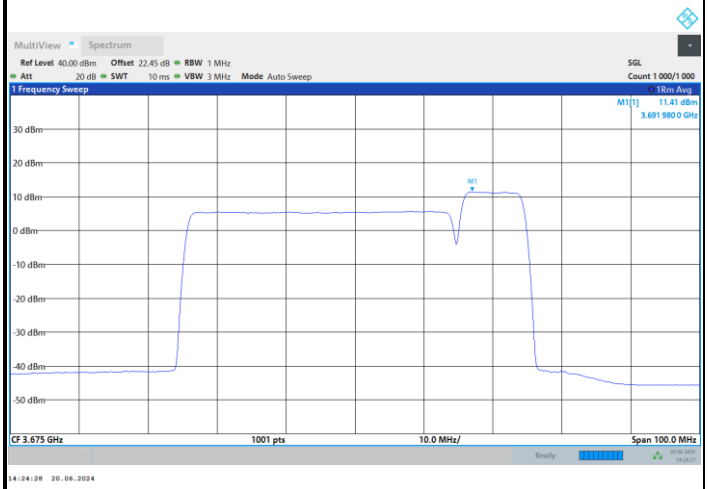
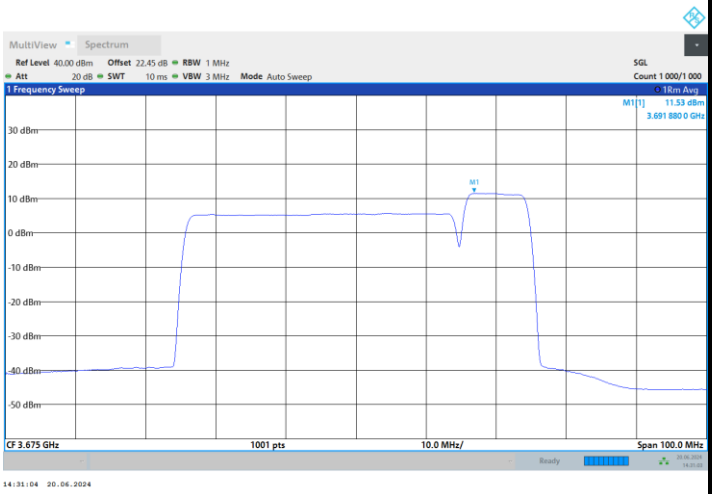
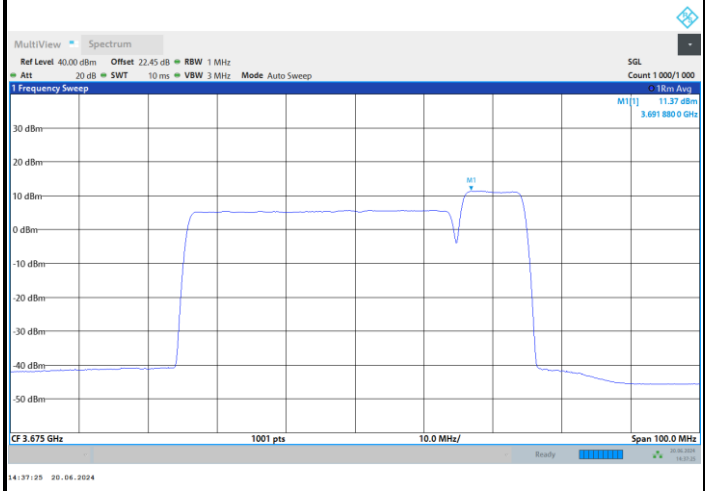
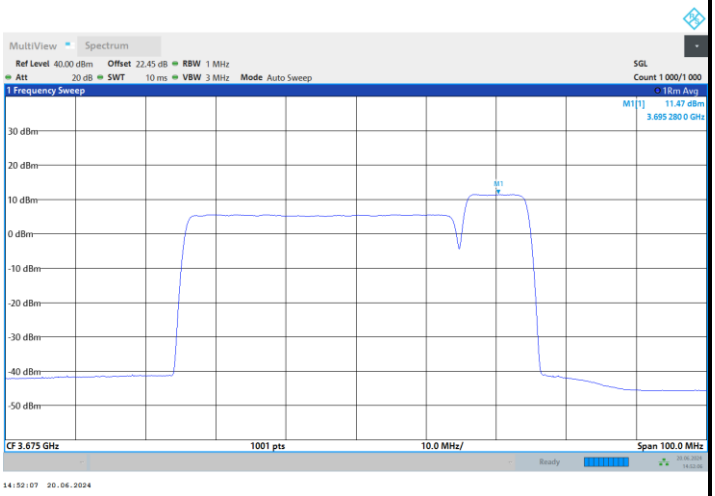
FR1 n48 / 20MHz+40MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 20MHz+40MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 20MHz+40MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 40MHz+10MHz / Lowest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 40MHz+10MHz / Middle Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM
	

FR1 n48 / 40MHz+10MHz / Highest Channel / PSD	
QPSK	16QAM
	
64QAM	256QAM