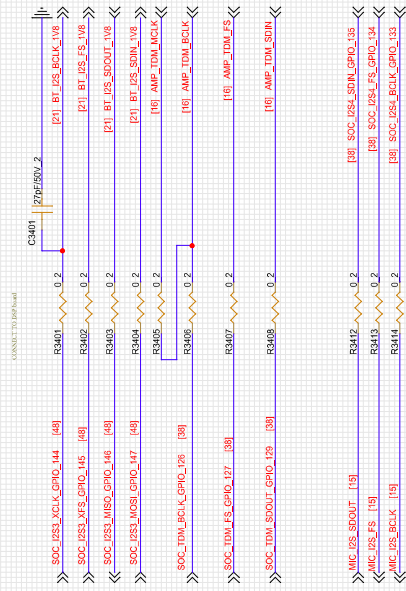
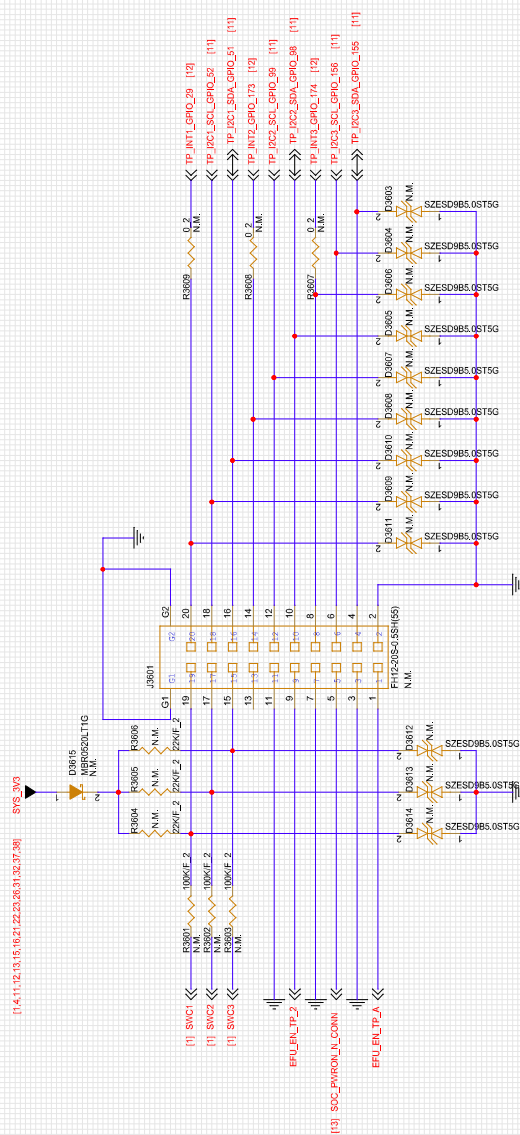
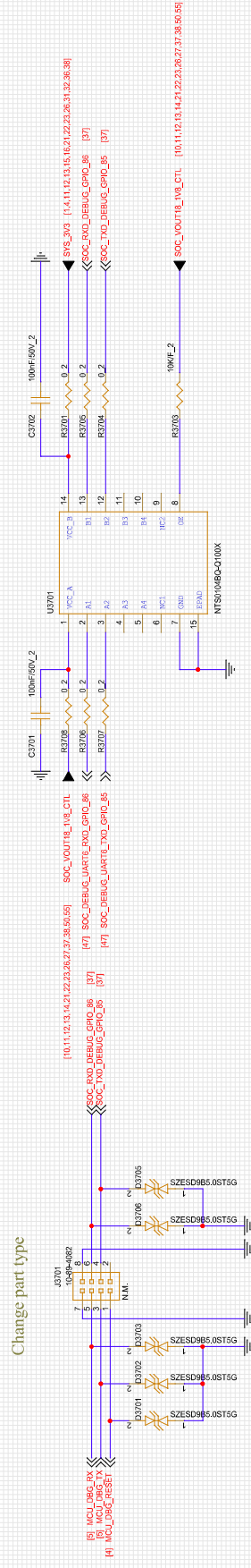
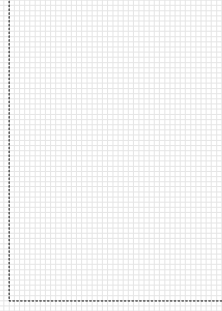


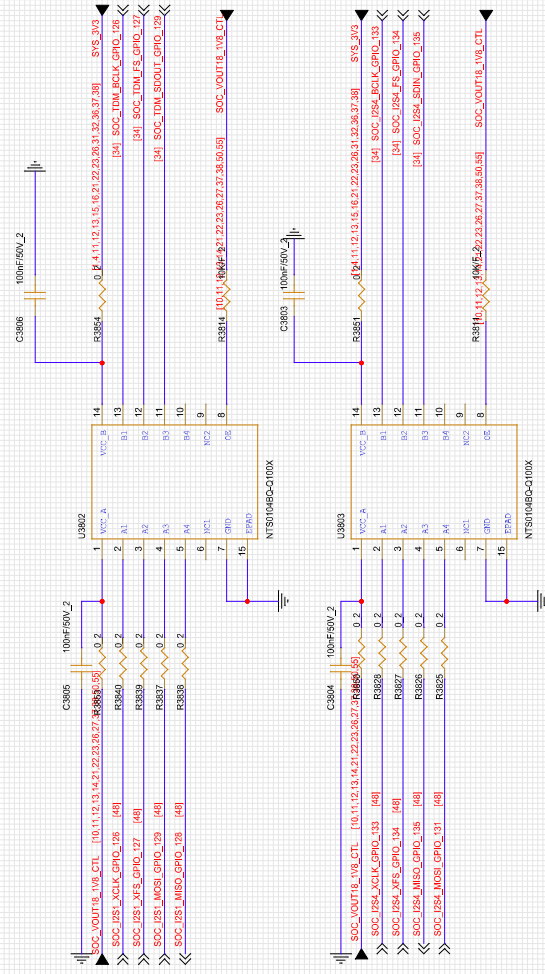


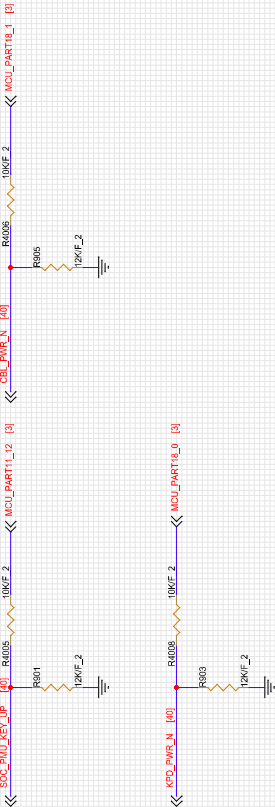




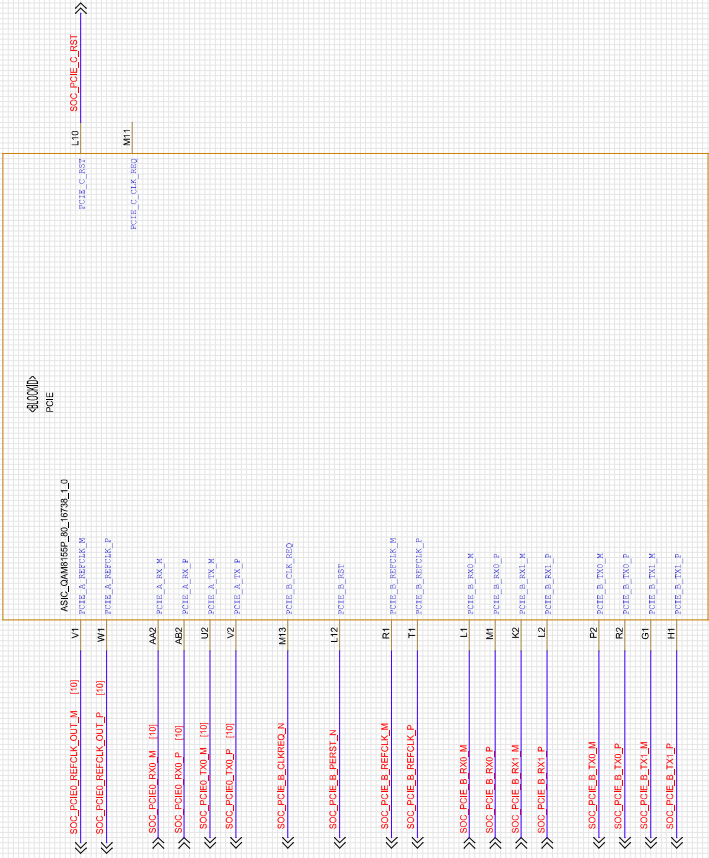
Add camera power control

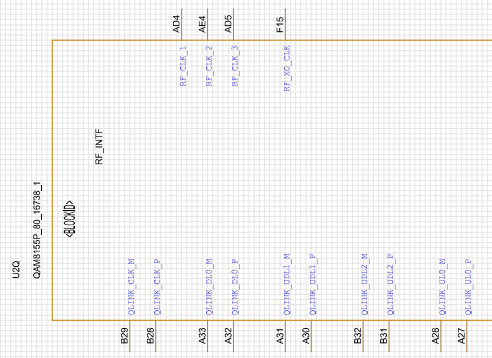




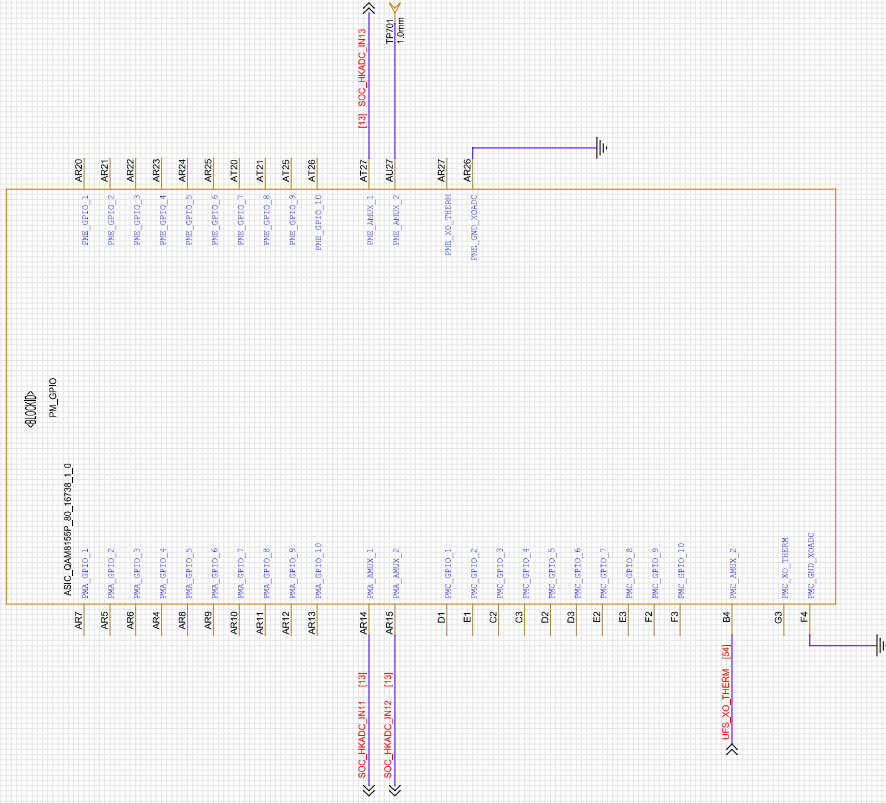


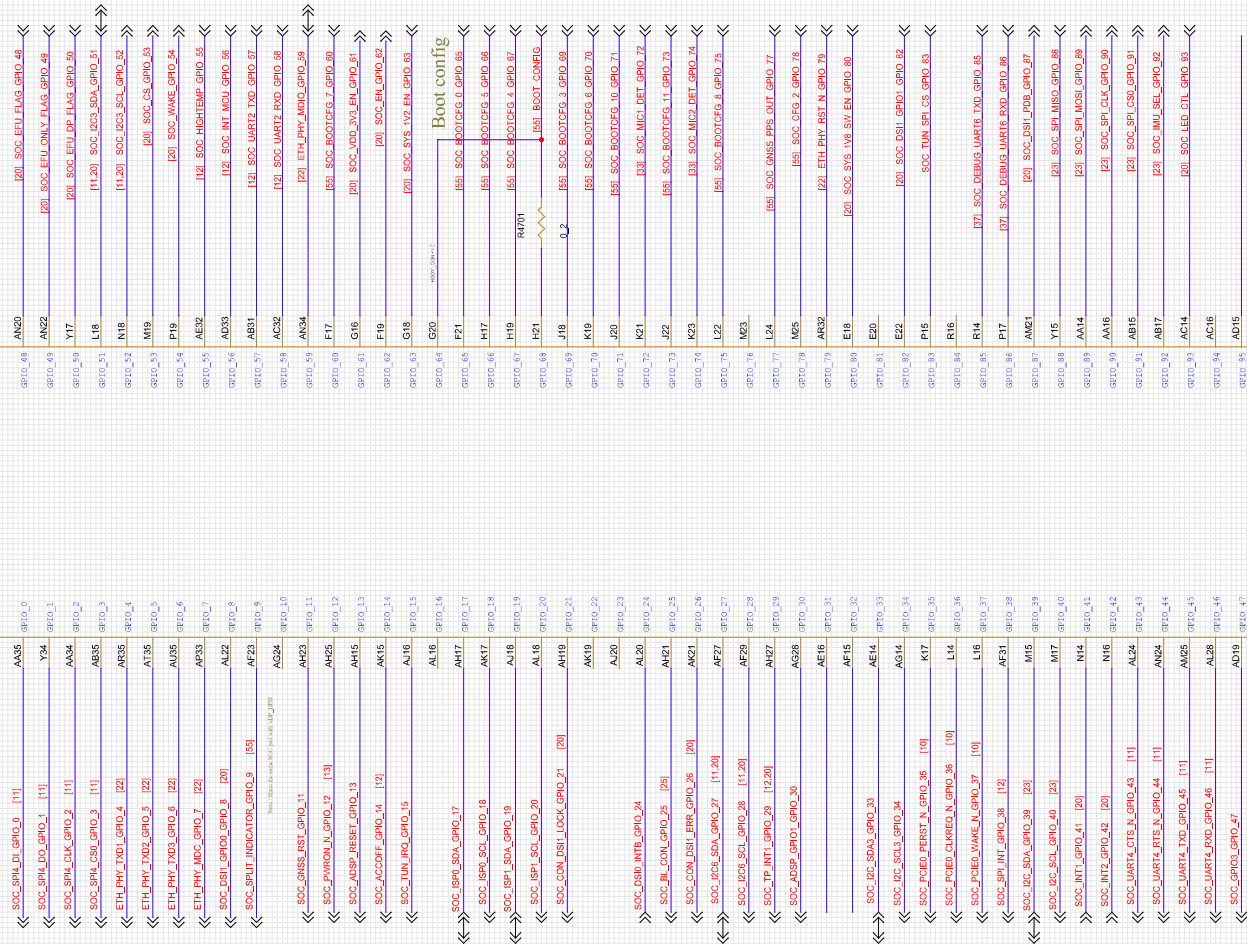
U2L
QAMBI55P_B0_16758_1





U2M
CAM6155P_90_16738_1





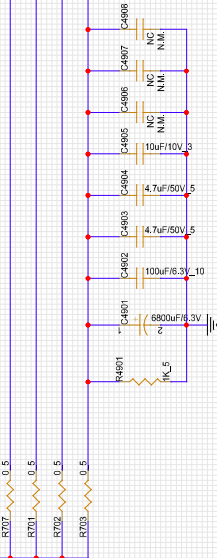
Add Screen-split indicator

GPIO2		GPIO2	
ASIC_QAM8155P_0_16738_L_0		GPIO2	
<< SOC_CSI1_INTB_GPIO_96	AJ22	GPIO_143	K56
<< SOC_3V3_EN_GPIO_97 [20]	AG22	GPIO_144	J98
<< SOC_I2C4_SDA_GPIO_98 [11]	AB34	GPIO_145	F98
<< SOC_I2C4_SCL_GPIO_99 [11]	AC34	GPIO_146	K98
<< SOC_MUX_EN_GPIO_100 [12]	AC35	GPIO_147	L35
<< SOC_SSD_FEBET_GPIO_101	AD35	GPIO_148	G94
	R18	GPIO_149	K94
<< SOC_POE_C_WAKE_GPIO_103	T19	GPIO_150	J94
	U18	GPIO_151	K94
	M61	GPIO_152	L34
<< SOC_DSL_P0B_GPIO_105 [20]		GPIO_153	AE34
	N32	GPIO_154	AF30
<< SOC_TUNING_RST_N_GPIO_107 [10]	N30	GPIO_155	C94
	P31	GPIO_156	D94
	M27	GPIO_157	E34
	N26	GPIO_158	B95
<< SOC_USB_A_EN_GPIO_111 [27]	M29	GPIO_159	C95
<< SOC_USB2_EN_GPIO_112 [27]	N28	GPIO_160	D95
<< SOC_DP_EN_GPIO_113 [28]	AD34	GPIO_161	F31
<< ETH_PHY_0_TX_CLK_GPIO_114 [22]	AT36	GPIO_162	C92
<< ETH_PHY_0_TX_CLK_GPIO_115 [22]	AR33	GPIO_163	K91
<< ETH_PHY_0_TX_DV_GPIO_116 [22]	AT33	GPIO_164	J92
<< ETH_PHY_0_RX0_GPIO_117 [22]	AU33	GPIO_165	F39
<< ETH_PHY_0_RX1_GPIO_118 [22]	AR34	GPIO_166	F33
<< ETH_PHY_0_RX2_GPIO_119 [22]	AT34	GPIO_167	G90
<< ETH_PHY_0_RX3_GPIO_120 [22]	AU34	GPIO_168	J90
<< ETH_PHY_1_TX_EN_GPIO_121 [22]	AP35	GPIO_169	T15
<< ETH_PHY_1_TX0_GPIO_122 [22]	AR36	GPIO_170	U14
<< SOC_JANSSSE_GPIO_123	AM34	GPIO_171	T17
<< ETH_PHY_INT_N_GPIO_124 [22]	AP34	GPIO_172	U16
	F34	GPIO_173	V15
<< SOC_I2S1_XCLK_GPIO_126 [98]	C96	GPIO_174	V17
<< SOC_I2S1_XFS_GPIO_127 [98]	D96		
<< SOC_I2S1_MISO_GPIO_128 [98]	F96		
<< SOC_I2S1_MOSI_GPIO_129 [98]	F96		
	G96		
<< SOC_I2S4_MOSI_GPIO_131 [98]	F35		
	E35		
<< SOC_I2S4_XCLK_GPIO_133 [98]	J95		
<< SOC_I2S4_XFS_GPIO_134 [98]	H95		
<< SOC_I2S4_MISO_GPIO_135 [98]	G95		
<< SOC_BOOT_MODE_GPIO_136 [13]	K97		
	D97		
	E97		
	F97		
	G97		
	H97		
	J97		

U2N
QAMR155P_90_16738_1_0

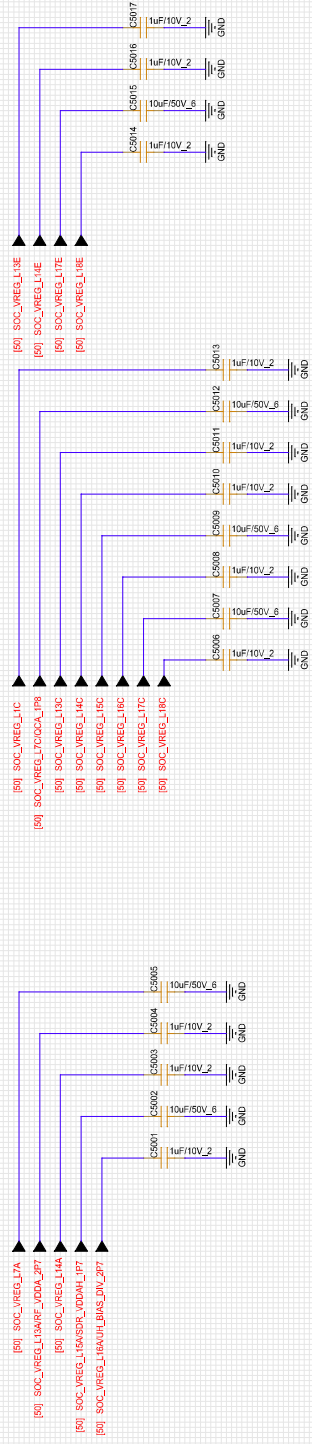
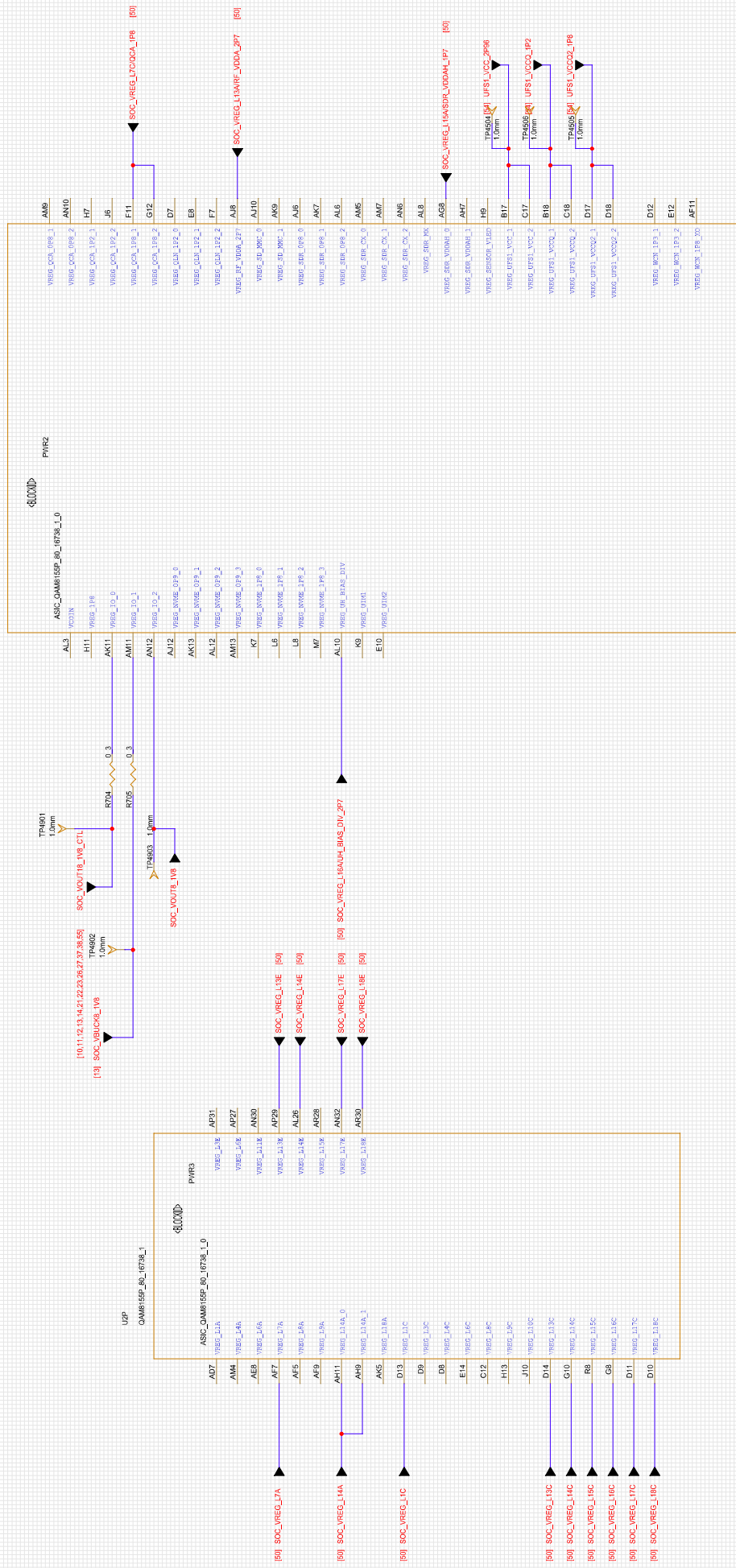
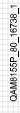
QAMR155P_90_16738_1_0

[0:31] SPI_3V3

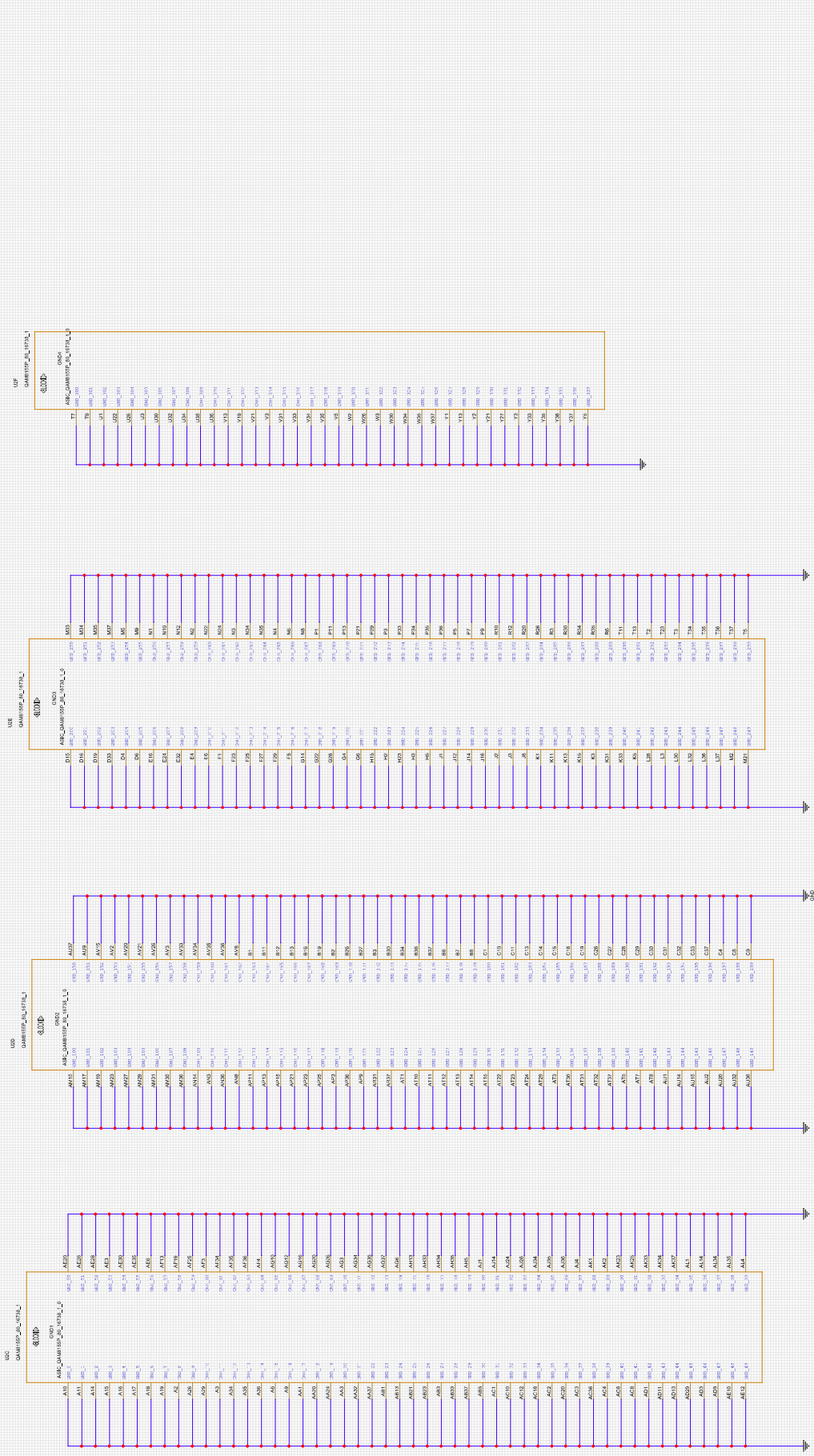


PWR1

U5	V3V3_3P3_0	W28	V3V3_3P3_0
U8	V3V3_3P3_1	A430	V3V3_3P3_1
U10	V3V3_3P3_2	AB29	V3V3_3P3_2
U12	V3V3_3P3_3	AC30	V3V3_3P3_3
V7	V3V3_3P3_4	Y29	V3V3_3P3_4
V9	V3V3_3P3_5	R22	V3V3_3P3_5
V11	V3V3_3P3_6	AC36	V3V3_3P3_6
W6	V3V3_3P3_7	AB25	V3V3_3P3_7
W8	V3V3_3P3_8	Y25	V3V3_3P3_8
W10	V3V3_3P3_9	A235	V3V3_3P3_9
W12	V3V3_3P3_10	T25	V3V3_3P3_10
Y7	V3V3_3P3_11	U24	V3V3_3P3_11
Y9	V3V3_3P3_12	V23	V3V3_3P3_12
Y11	V3V3_3P3_13	V25	V3V3_3P3_13
AA5	V3V3_3P3_14	U28	V3V3_3P3_14
AA8	V3V3_3P3_15	T28	V3V3_3P3_15
AA10	V3V3_3P3_16	V27	V3V3_3P3_16
AA12	V3V3_3P3_17		
AB7	V3V3_3P3_18		
AB9	V3V3_3P3_19	R28	V3V3_3P3_19
AB11	V3V3_3P3_20	F9	V3V3_3P3_20
		AA22	V3V3_3P3_21
		AC26	V3V3_3P3_22
		W20	V3V3_3P3_23
		AC22	V3V3_3P3_24
		A201	V3V3_3P3_25
		T21	V3V3_3P3_26
		AN28	V3V3_3P3_27
		F13	V3V3_3P3_28
		P25	V3V3_3P3_29
		P23	V3V3_3P3_30
		AN26	V3V3_3P3_31
		W22	V3V3_3P3_32



PDN compliance



N1	
ASIC_OM815SP_00_1679L_UJ	
A1	INC_50
A4	INC_51
A20	INC_52
A21	INC_53
A22	INC_54
A23	INC_55
A24	INC_56
A25	INC_57
A37	INC_58
A44	INC_59
A16	INC_60
A28	INC_61
A84	INC_62
A819	INC_63
A827	INC_64
AC24	INC_65
AC28	INC_66
AD17	INC_67
AD21	INC_68
AD23	INC_69
AD27	INC_70
AE16	INC_71
AE22	INC_72
AE24	INC_73
AF17	INC_74
AF21	INC_75
AG18	INC_76
AG30	INC_77
AG32	INC_78
AH29	INC_79
AH31	INC_80
AJ28	INC_81
AJ30	INC_82
AK32	INC_83
AK27	INC_84
AK28	INC_85
AK31	INC_86
AK33	INC_87
AL32	INC_88
AM33	INC_89
AN16	INC_90
AN18	INC_91
AP17	INC_92
AP19	INC_93
AR16	INC_94
AR17	INC_95
AR18	INC_96
AR19	INC_97
AQ29	INC_98
AT16	INC_99

>> SOC_INT_THERM [1:4]

N2	
ASIC_OM815SP_00_1679L_UJ	
G24	INC_100
H29	INC_101
J28	INC_102
K25	INC_103
K29	INC_104
L20	INC_105
L26	INC_106
N20	INC_107
P4	INC_108
P27	INC_109
R4	INC_110
R24	INC_111
R32	INC_112
T4	INC_113
T27	INC_114
T31	INC_115
T33	INC_116
U4	INC_117
U20	INC_118
V4	INC_119
V29	INC_120
W4	INC_121
W14	INC_122
W16	INC_123
W18	INC_124
W24	INC_125
W32	INC_126
Y4	INC_127
Y19	INC_128
Y23	INC_129
Y31	INC_130

