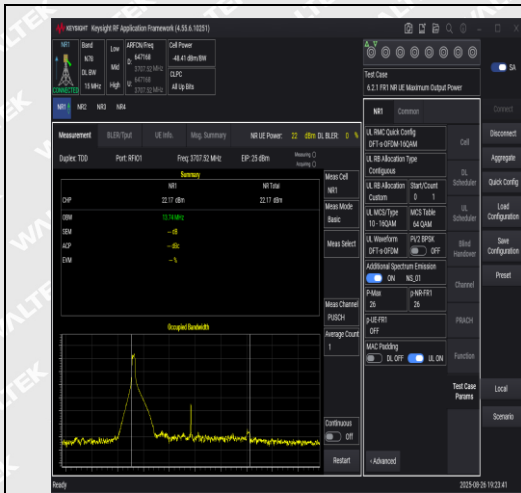
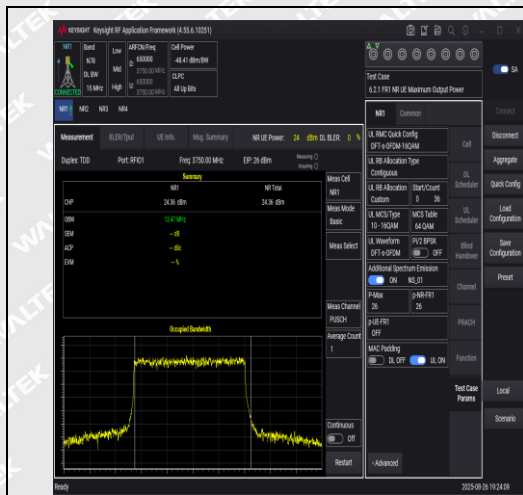
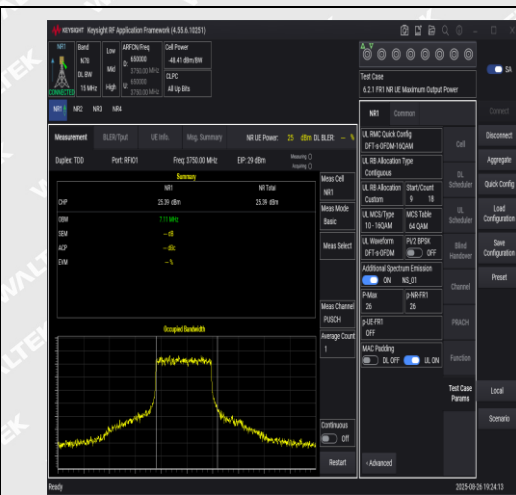


NTNV-N78-3700-3800-PC2-30-15-L-DFT-16QAM-
Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-15-L-DFT-16QAM-
Edge_1RB_Right-PASSNTNV-N78-3700-3800-PC2-30-15-M-DFT-QPSK-
Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-15-M-DFT-QPSK-I
nner_Full-PASSNTNV-N78-3700-3800-PC2-30-15-M-DFT-QPSK-
Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-15-M-DFT-QPSK-
Edge_1RB_Right-PASS



NTNV-N78-3700-3800-PC2-30-15-M-DFT-16QAM
-Outer_Full-PASS



NTNV-N78-3700-3800-PC2-30-15-M-DFT-16QAM
-Inner_Full-PASS



NTNV-N78-3700-3800-PC2-30-15-M-DFT-16QAM
-Edge_1RB_Left-PASS



NTNV-N78-3700-3800-PC2-30-15-M-DFT-16QAM
-Edge_1RB_Right-PASS



NTNV-N78-3700-3800-PC2-30-15-H-DFT-QPSK-
Outer_Full-PASS



NTNV-N78-3700-3800-PC2-30-15-H-DFT-QPSK-I
Inner_Full-PASS

NTNV-N78-3700-3800-PC2-30-15-H-DFT-QPSK-
Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-15-H-DFT-QPSK-
Edge_1RB_Right-PASSNTNV-N78-3700-3800-PC2-30-15-H-DFT-16QAM-
Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-15-H-DFT-16QAM-
Inner_Full-PASSNTNV-N78-3700-3800-PC2-30-15-H-DFT-16QAM-
Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-15-H-DFT-16QAM-
Edge_1RB_Right-PASS