

NTNV-N78-3700-3800-PC2-30-100-L-DFT-QPSK-
Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-100-L-DFT-QPSK-
Inner_Full-PASSNTNV-N78-3700-3800-PC2-30-100-L-DFT-QPSK-
Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-100-L-DFT-QPSK-
Edge_1RB_Right-PASSNTNV-N78-3700-3800-PC2-30-100-L-DFT-16QA
M-Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-100-L-DFT-16QA
M-Inner_Full-PASS



NTNV-N78-3700-3800-PC2-30-100-L-DFT-16QA
M-Edge_1RB_Left-PASS



NTNV-N78-3700-3800-PC2-30-100-L-DFT-16QA
M-Edge_1RB_Right-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK
-Outer_Full-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK
-Inner_Full-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK
-Edge_1RB_Left-PASS



NTNV-N78-3700-3800-PC2-30-100-M-DFT-QPSK
-Edge_1RB_Right-PASS

NTNV-N78-3700-3800-PC2-30-100-M-DFT-16QA
M-Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-100-M-DFT-16QA
M-Inner_Full-PASSNTNV-N78-3700-3800-PC2-30-100-M-DFT-16QA
M-Edge_1RB_Left-PASSNTNV-N78-3700-3800-PC2-30-100-M-DFT-16QA
M-Edge_1RB_Right-PASSNTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-
Outer_Full-PASSNTNV-N78-3700-3800-PC2-30-100-H-DFT-QPSK-
Inner_Full-PASS