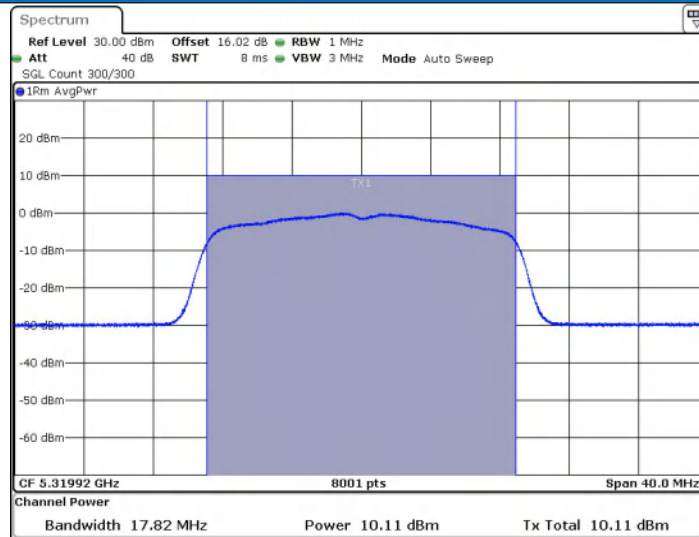
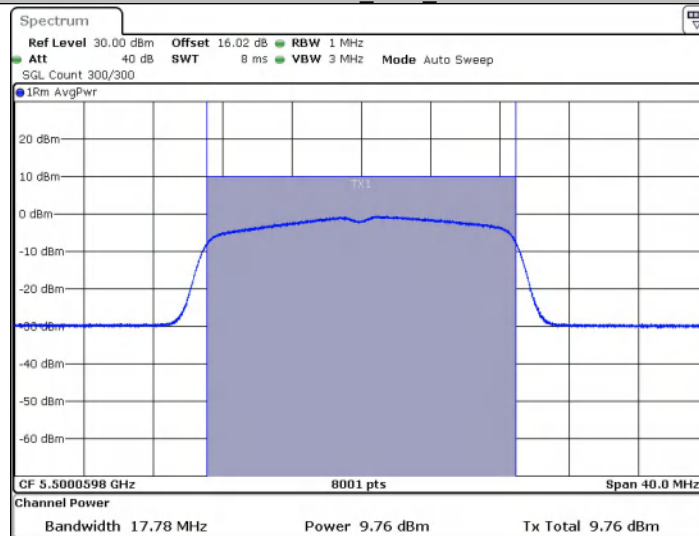




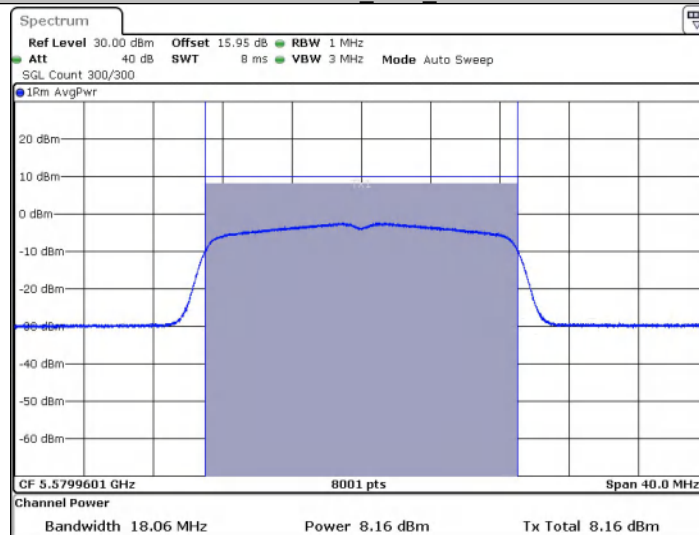
Date: 12 DEC 2025 17:34:10

11N20SISO_Ant1_5500



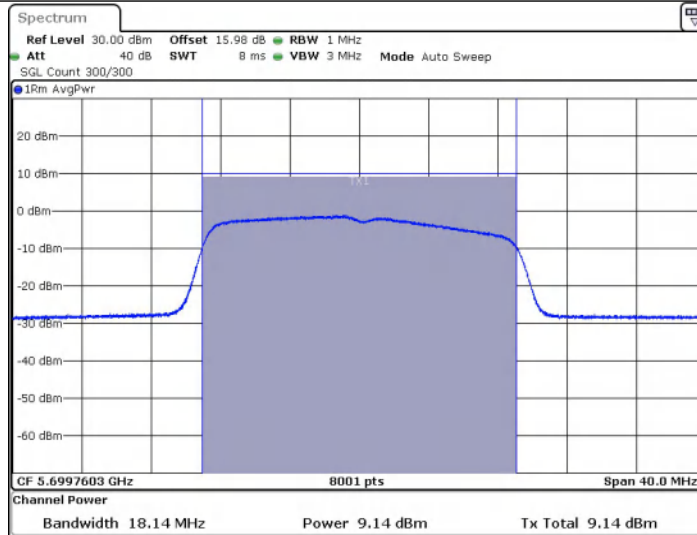
Date: 12 DEC 2025 17:36:45

11N20SISO_Ant1_5580



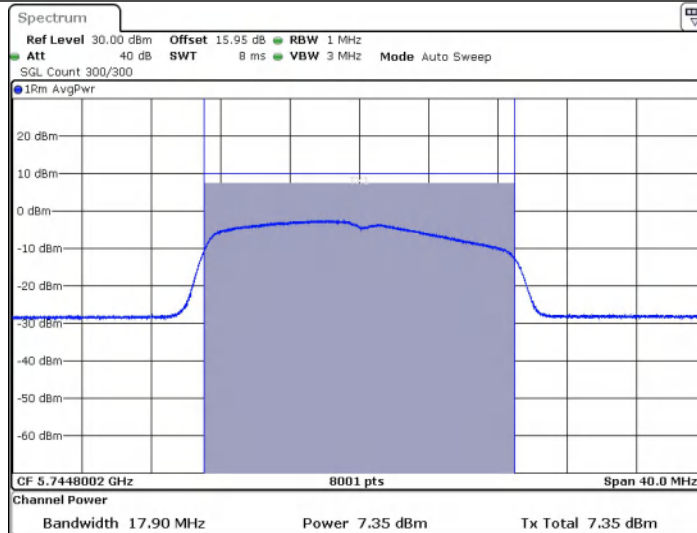
Date: 12 DEC 2025 17:38:21

11N20SISO_Ant1_5700



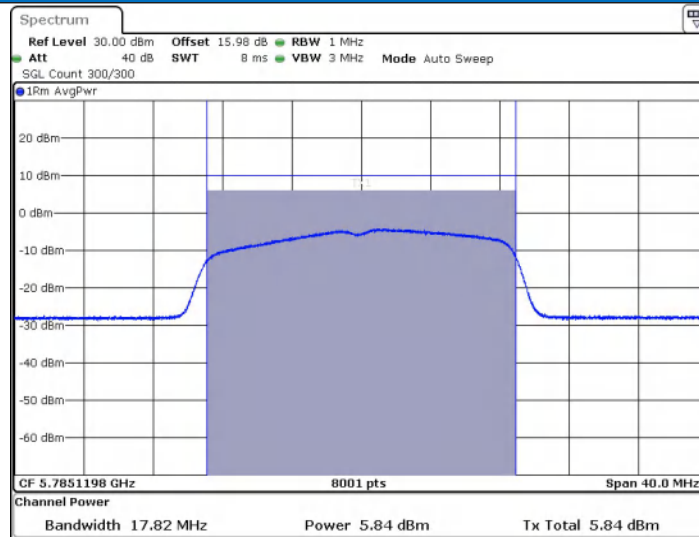
Date: 13 DEC 2025 15:48:01

11N20SISO_Ant1_5745



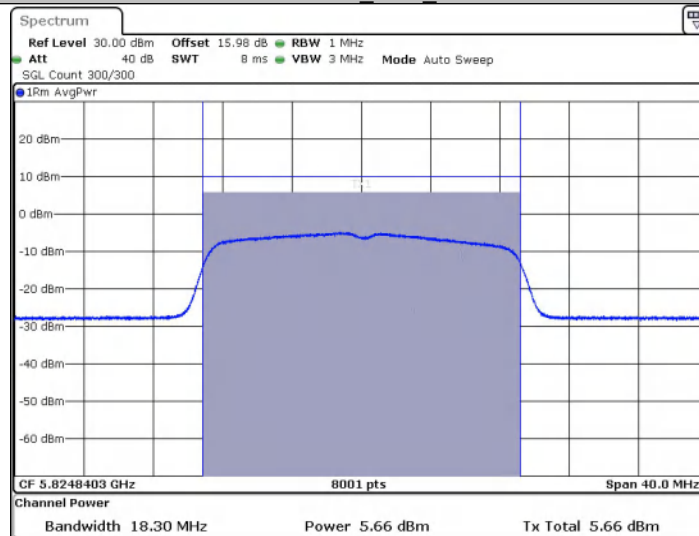
Date: 12 DEC 2025 17:41:13

11N20SISO_Ant1_5785



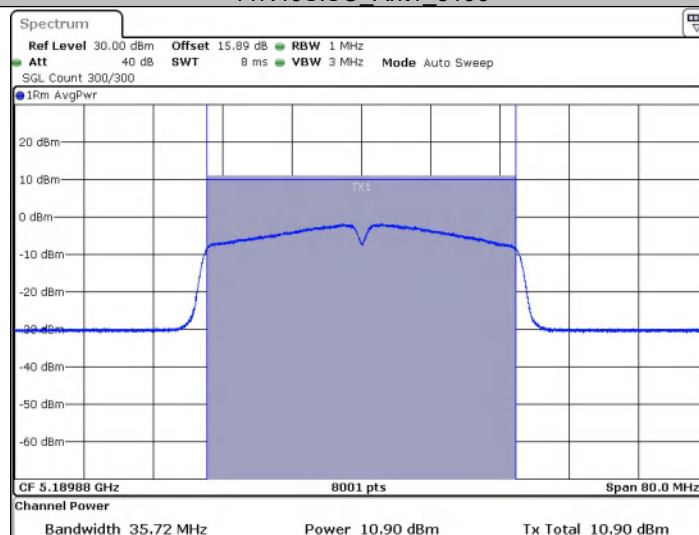
Date: 12 DEC 2025 17:42:53

11N20SISO_Ant1_5825



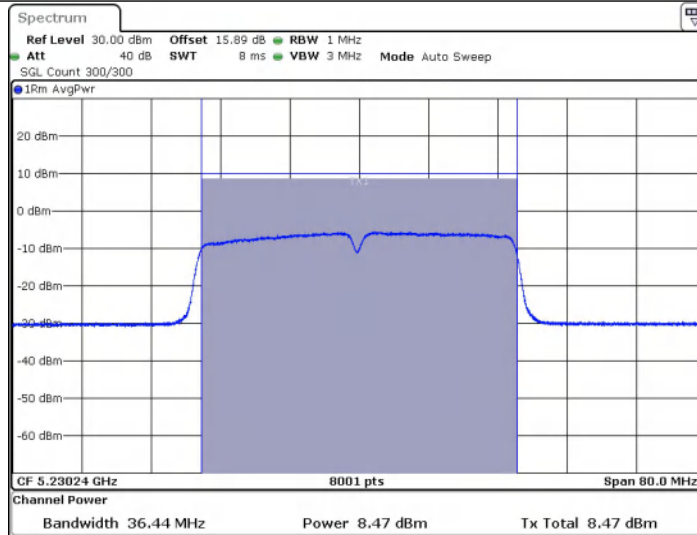
Date: 13 DEC 2025 15:56:35

11N40SISO_Ant1_5190



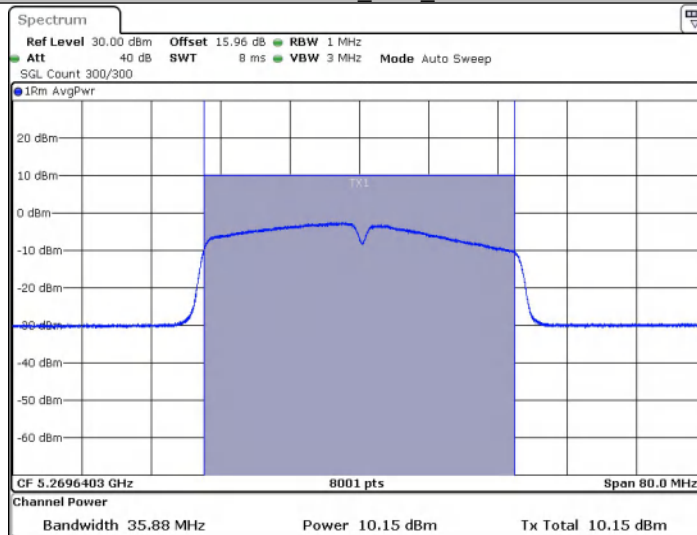
Date: 12 DEC 2025 17:45:50

11N40SISO_Ant1_5230



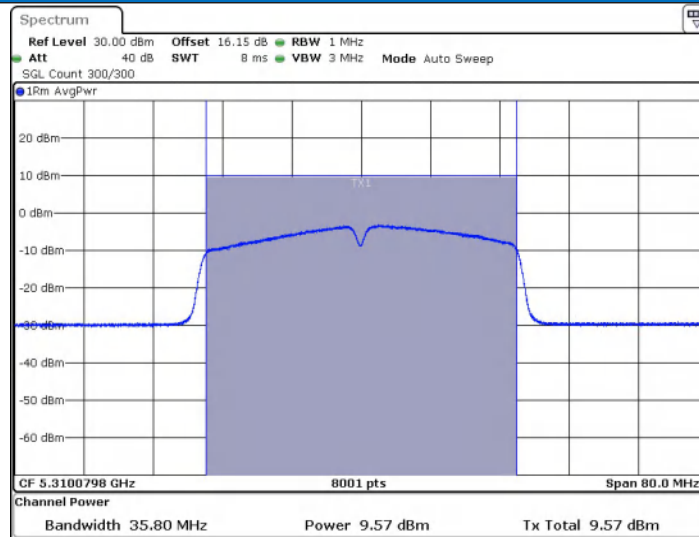
Date: 12 DEC 2025 17:47:56

11N40SISO_Ant1_5270



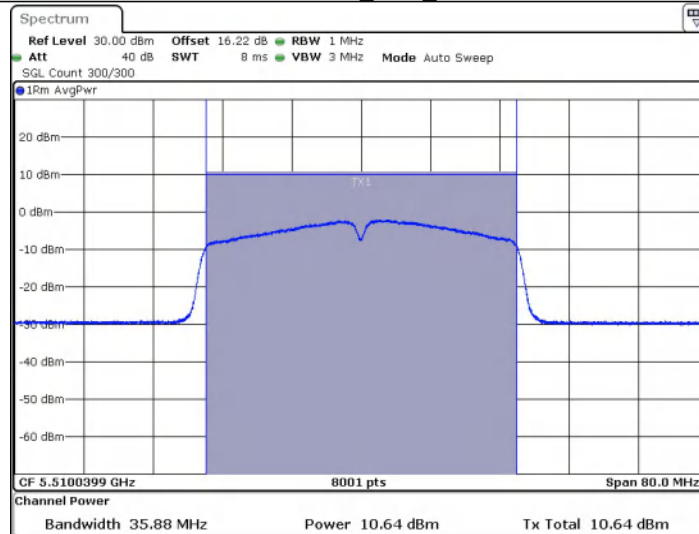
Date: 12 DEC 2025 17:49:22

11N40SISO_Ant1_5310



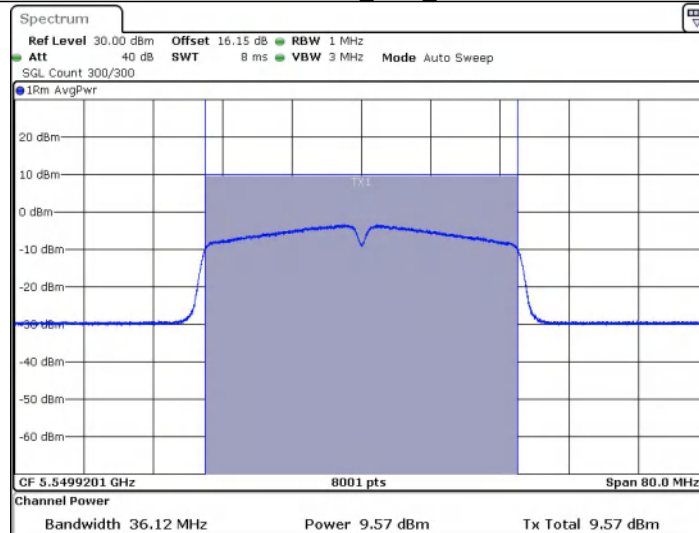
Date: 12 DEC 2025 17:50:39

11N40SISO_Ant1_5510



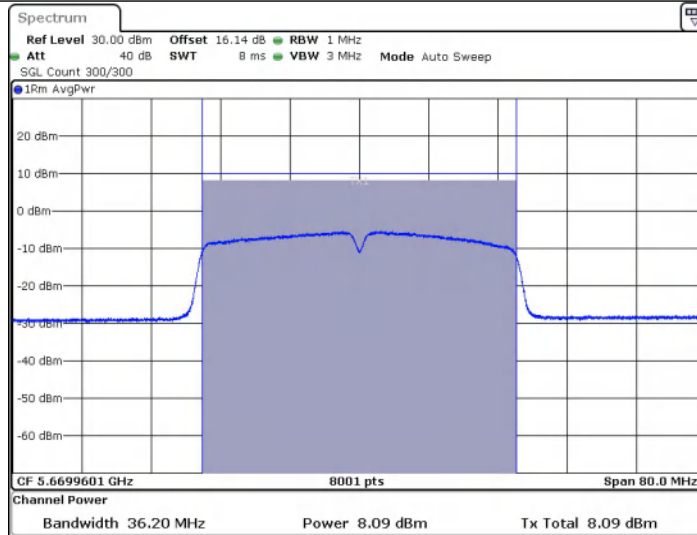
Date: 12 DEC 2025 17:54:09

11N40SISO_Ant1_5550



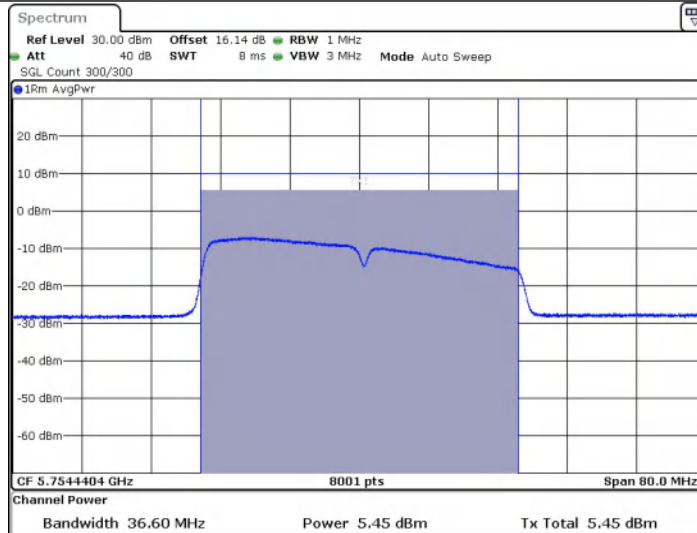
Date: 13 DEC 2025 13:47:27

11N40SISO_Ant1_5670



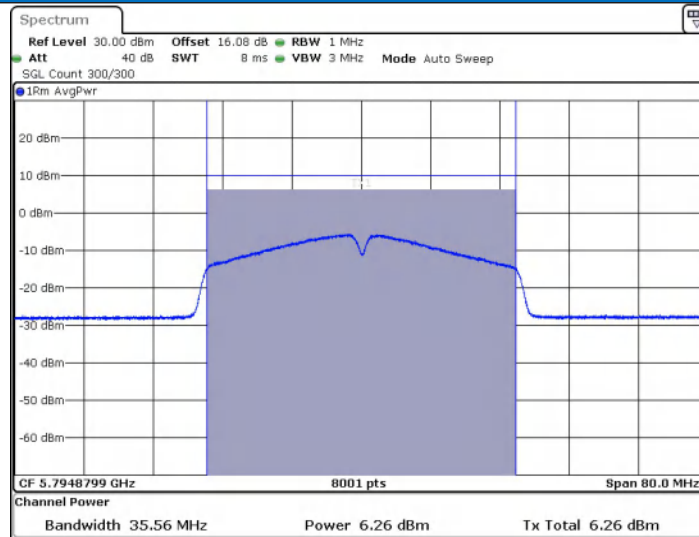
Date: 13 DEC 2025 13:48:47

11N40SISO_Ant1_5755



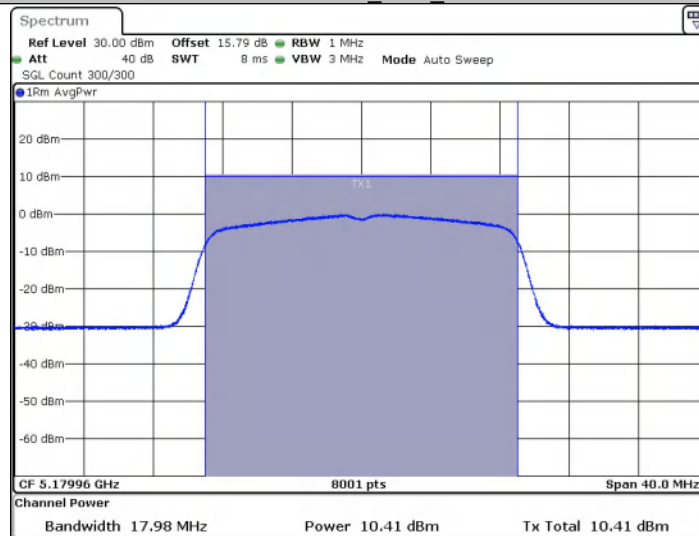
Date: 13 DEC 2025 13:51:57

11N40SISO_Ant1_5795



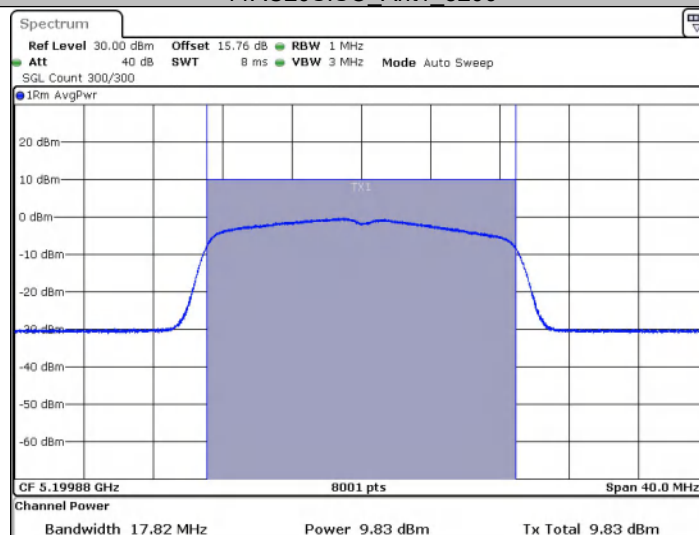
Date: 13 DEC 2025 13:53:43

11AC20SISO_Ant1_5180



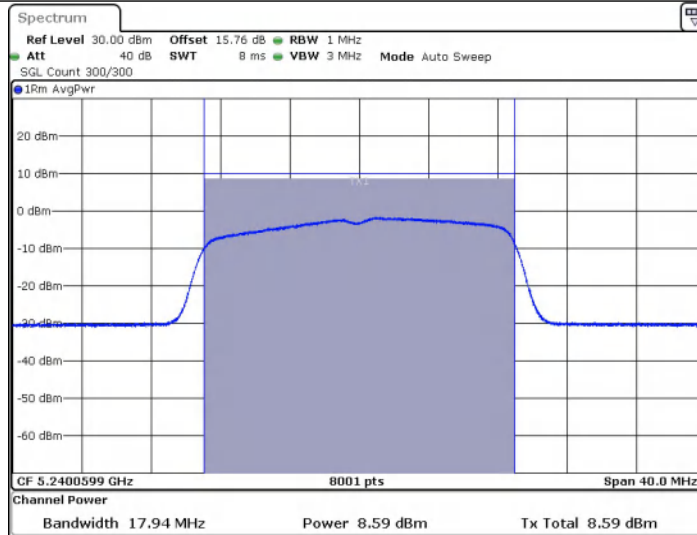
Date: 13 DEC 2025 13:55:20

11AC20SISO_Ant1_5200



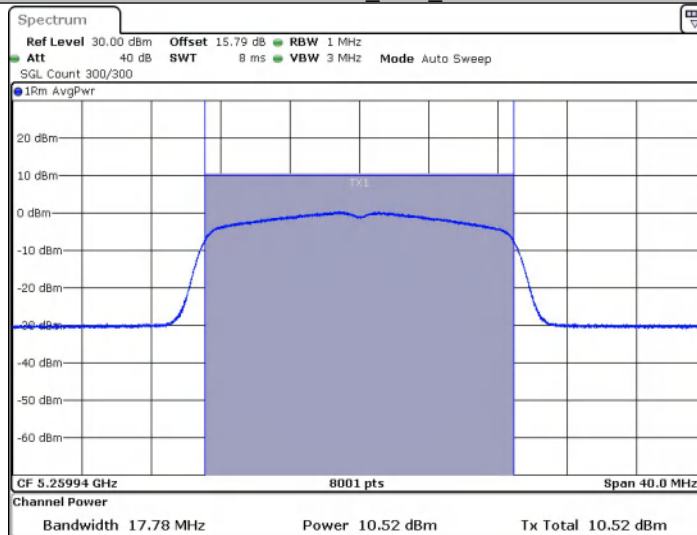
Date: 13 DEC 2025 13:57:18

11AC20SISO_Ant1_5240



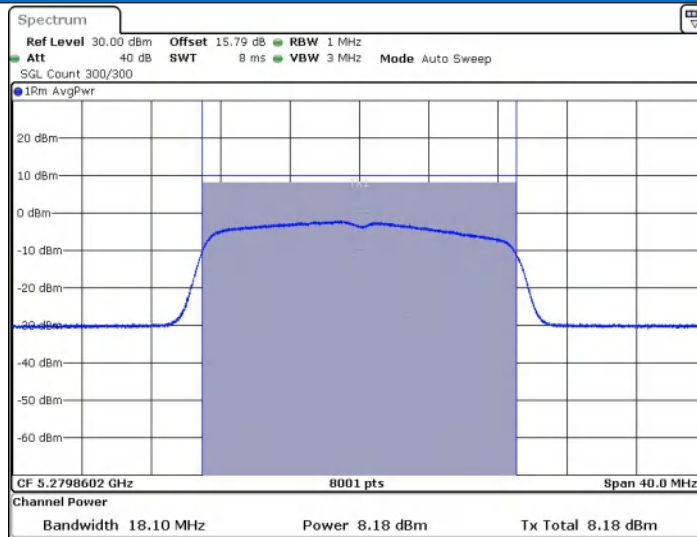
Date: 13 DEC 2025 13:58:27

11AC20SISO_Ant1_5260



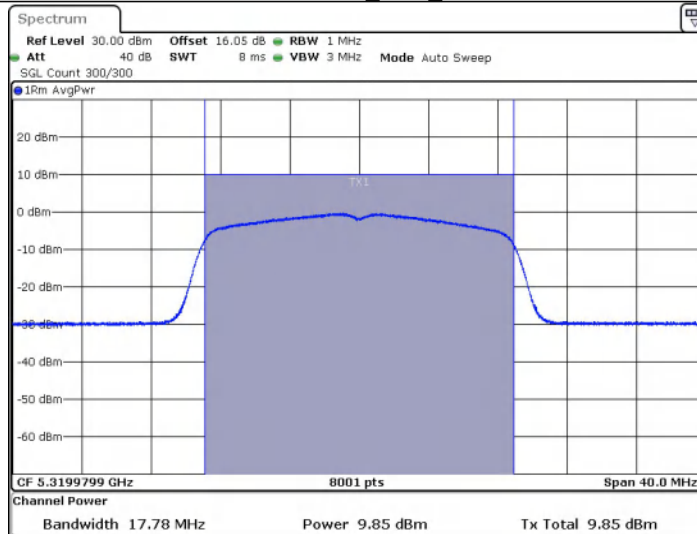
Date: 13 DEC 2025 13:59:48

11AC20SISO_Ant1_5280



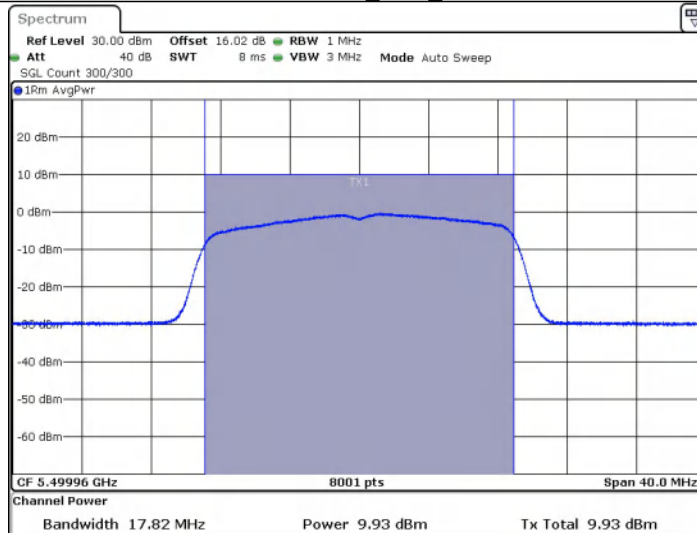
Date: 13 DEC 2025 14:01:46

11AC20SISO_Ant1_5320



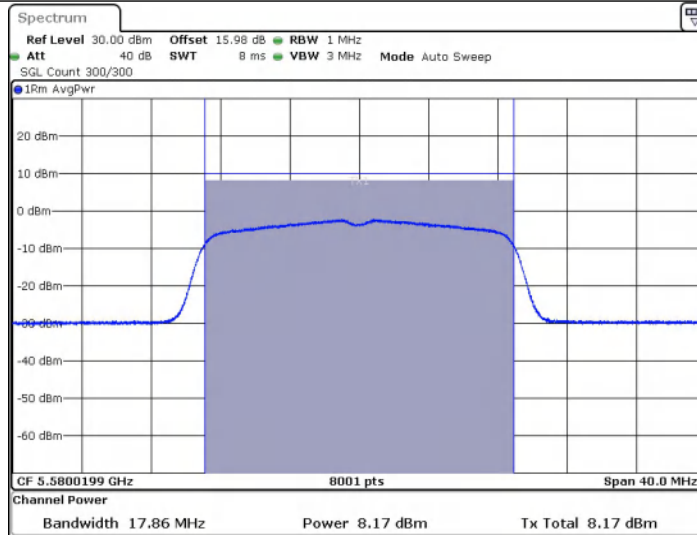
Date: 13 DEC 2025 14:03:01

11AC20SISO_Ant1_5500



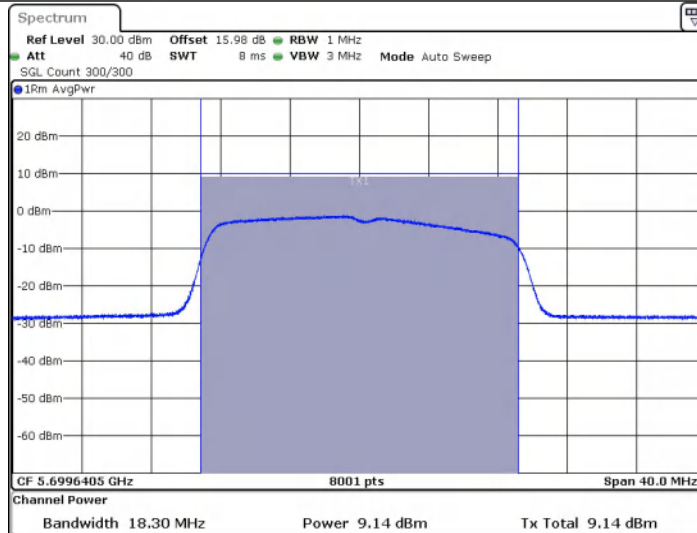
Date: 13 DEC 2025 14:04:47

11AC20SISO_Ant1_5580



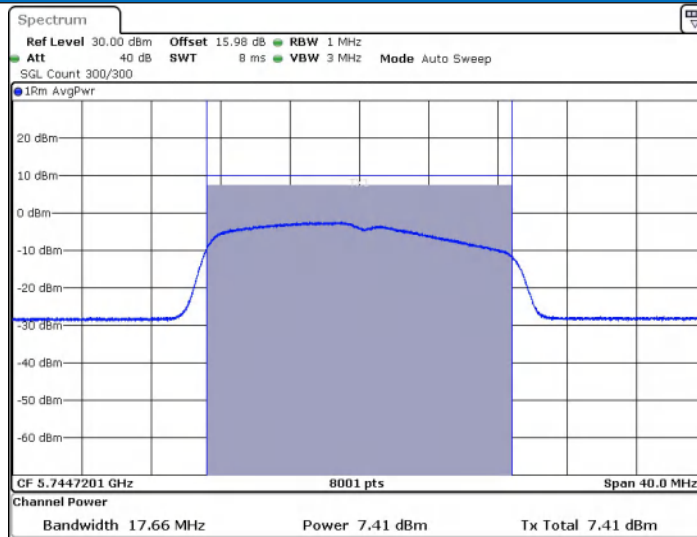
Date: 13 DEC 2025 14:06:28

11AC20SISO_Ant1_5700



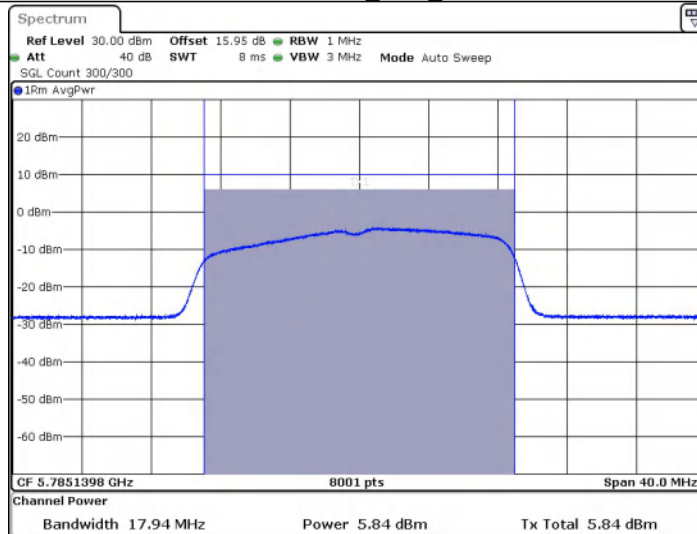
Date: 13 DEC 2025 15:49:42

11AC20SISO_Ant1_5745



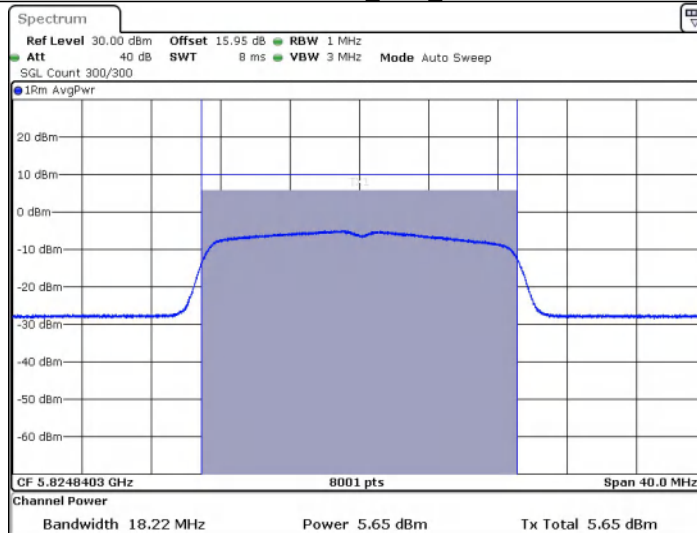
Date: 13 DEC 2025 14:09:53

11AC20SISO_Ant1_5785



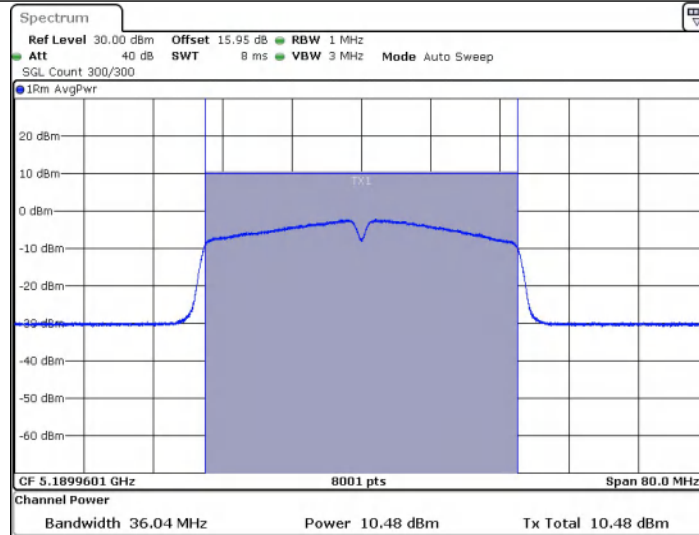
Date: 13 DEC 2025 14:11:34

11AC20SISO_Ant1_5825



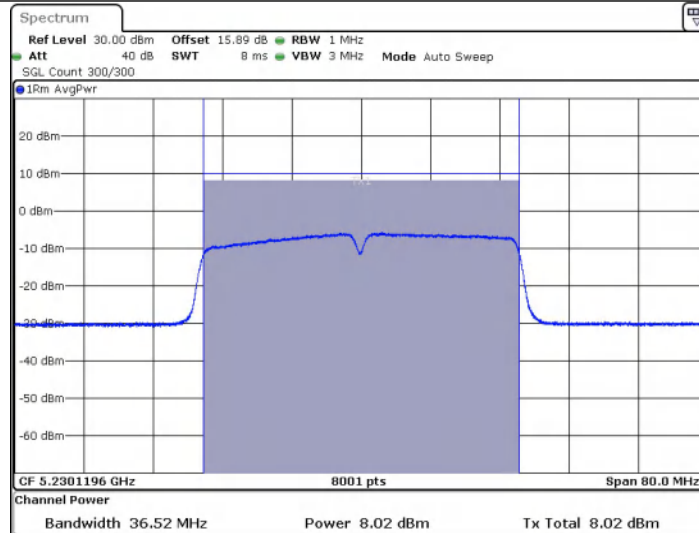
Date: 13 DEC 2025 15:58:50

11AC40SISO_Ant1_5190



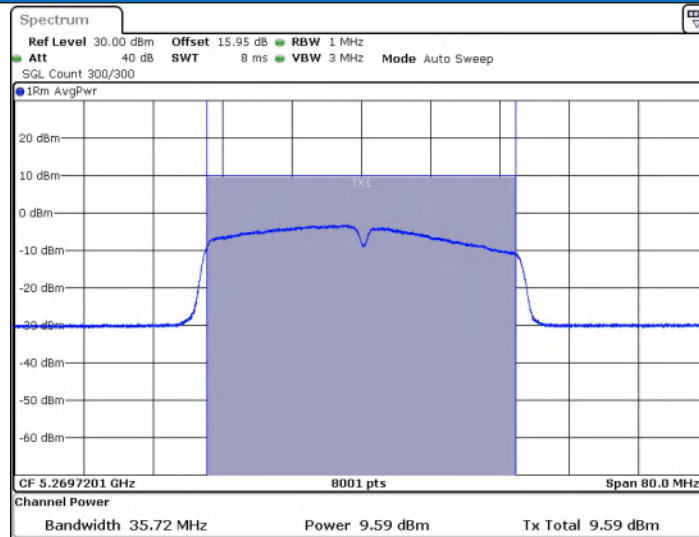
Date: 13 DEC 2025 14:15:16

11AC40SISO_Ant1_5230



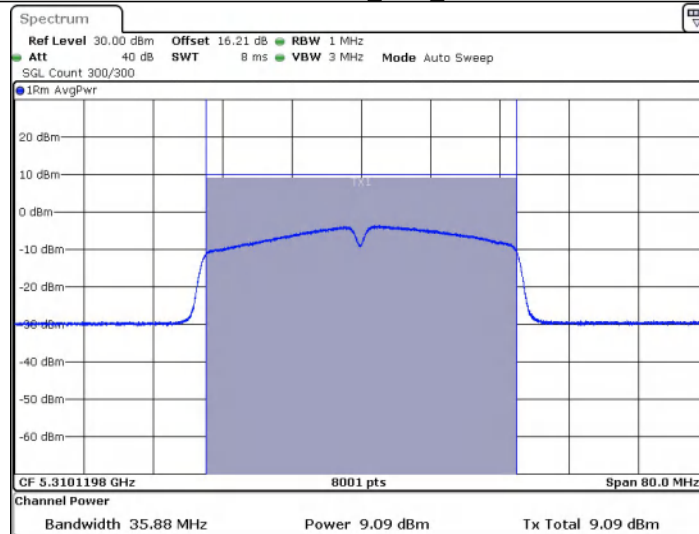
Date: 13 DEC 2025 14:17:15

11AC40SISO_Ant1_5270



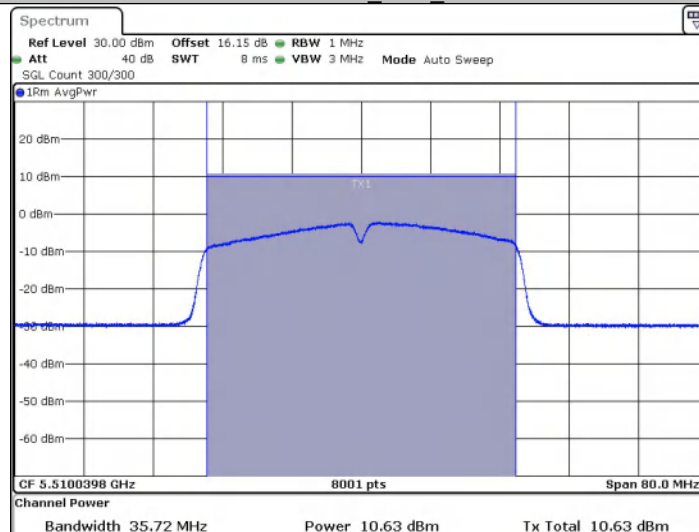
Date: 13 DEC 2025 14:18:37

11AC40SISO_Ant1_5310



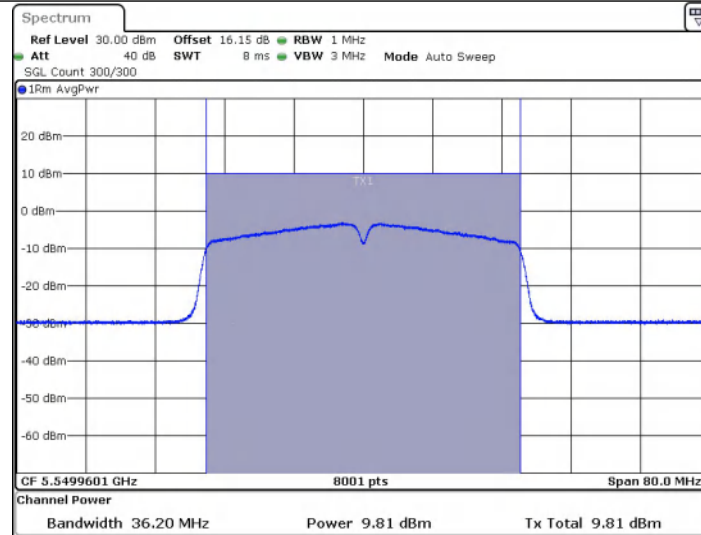
Date: 13 DEC 2025 16:04:59

11AC40SISO_Ant1_5510

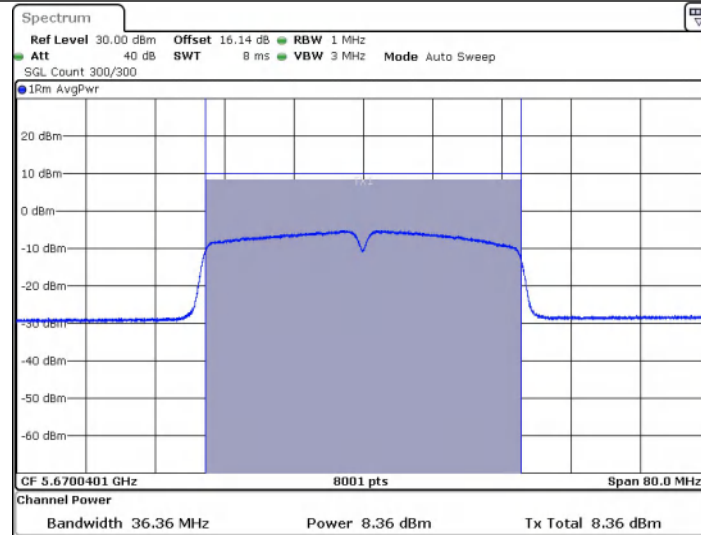


Date: 13 DEC 2025 14:22:02

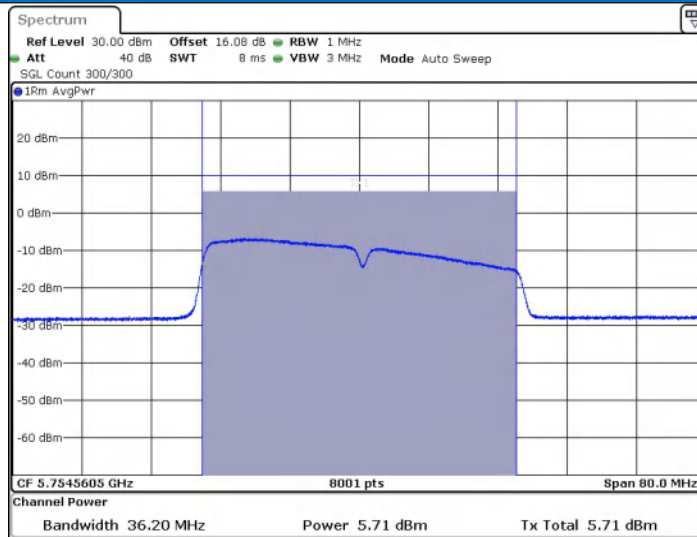
11AC40SISO_Ant1_5550



11AC40SISO_Ant1_5670

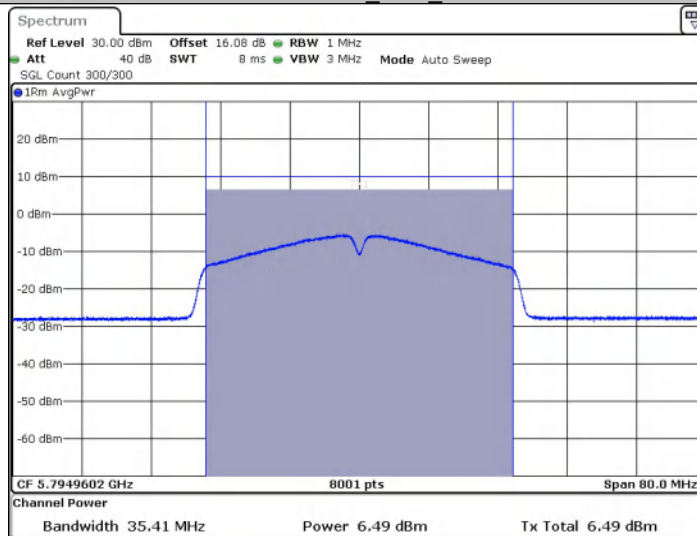


11AC40SISO_Ant1_5755



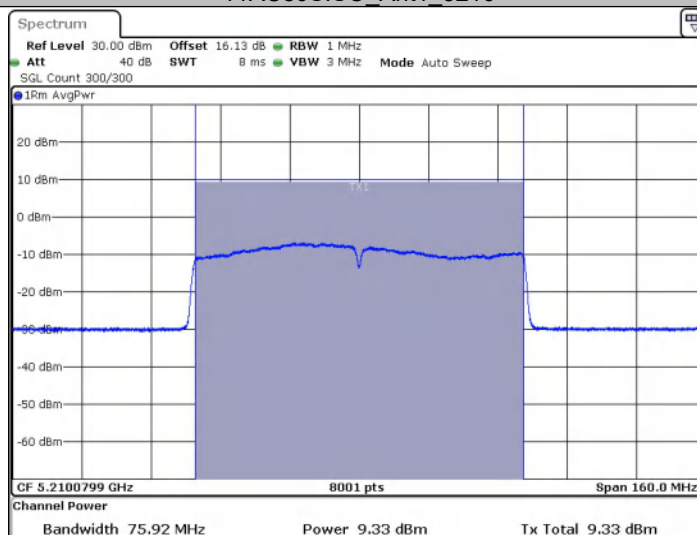
Date: 13 DEC 2025 14:27:57

11AC40SISO_Ant1_5795



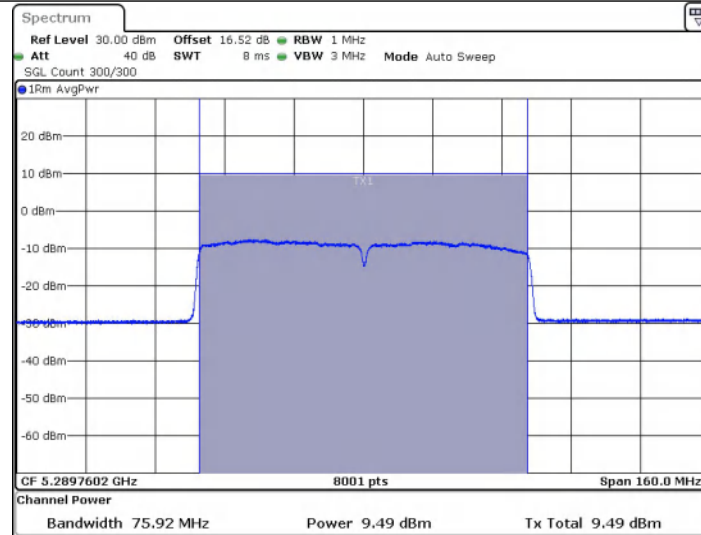
Date: 13 DEC 2025 14:30:06

11AC80SISO_Ant1_5210

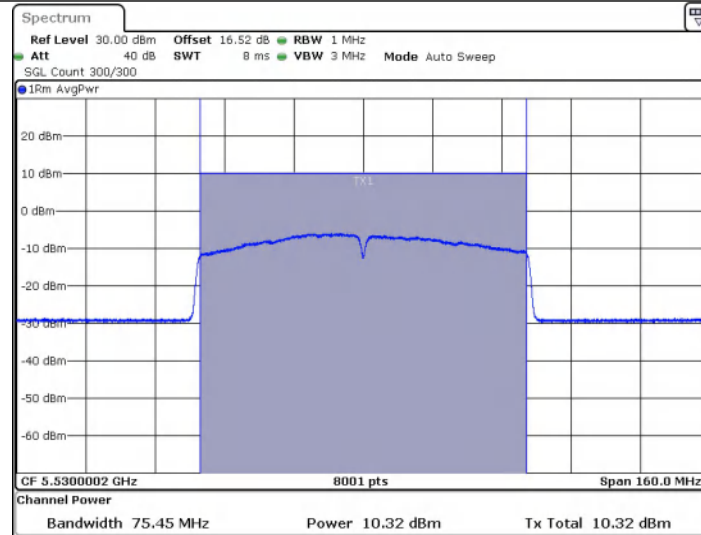


Date: 13 DEC 2025 14:32:16

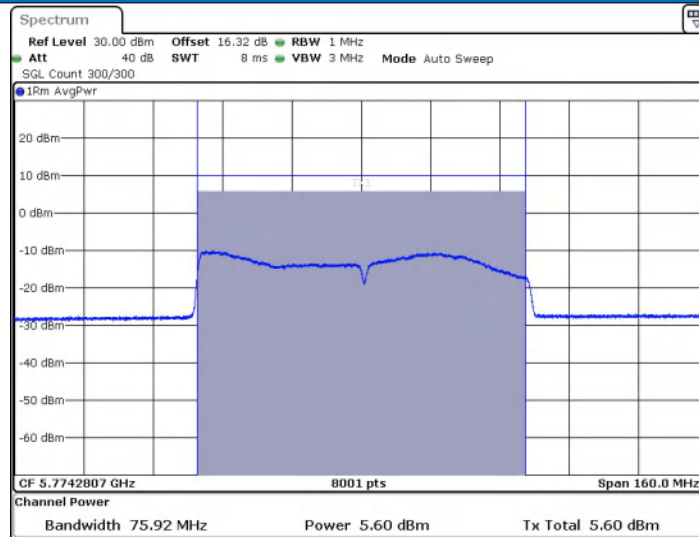
11AC80SISO_Ant1_5290



11AC80SISO_Ant1_5530

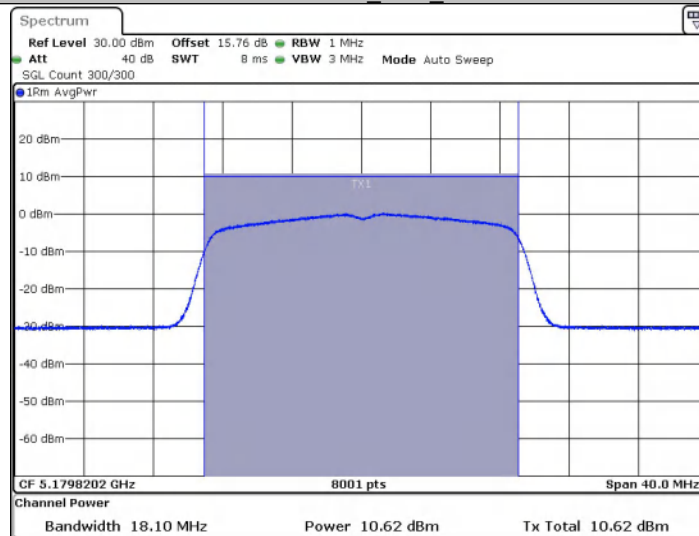


11AC80SISO_Ant1_5775



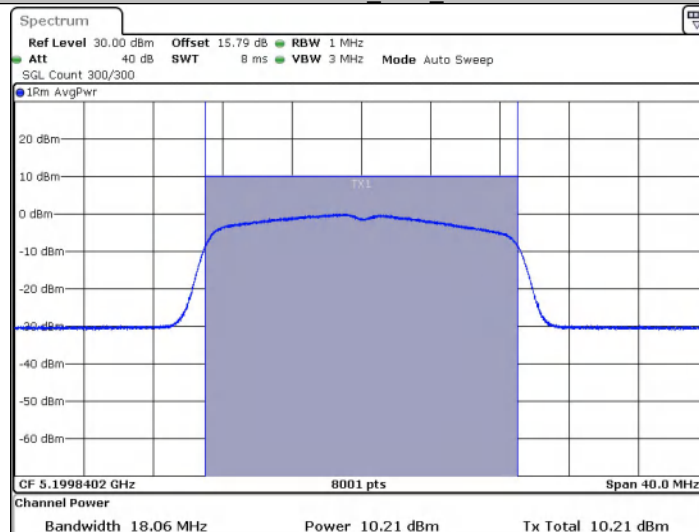
Date: 13 DEC 2025 16:10:57

11AX20SISO_Ant1_5180



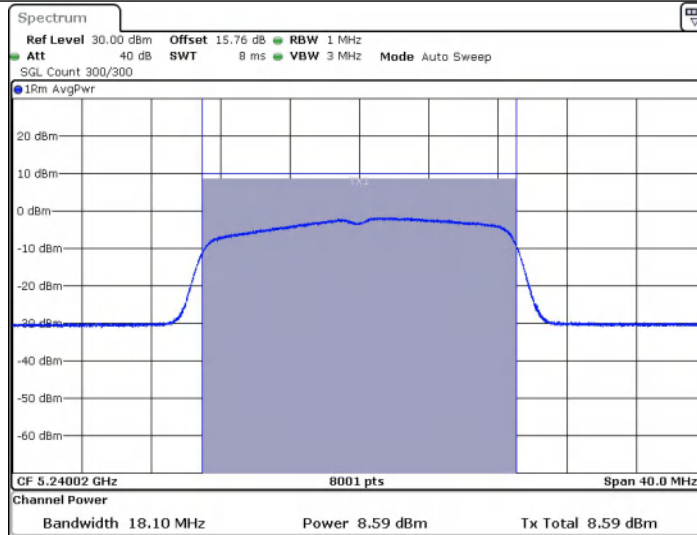
Date: 13 DEC 2025 14:42:48

11AX20SISO_Ant1_5200



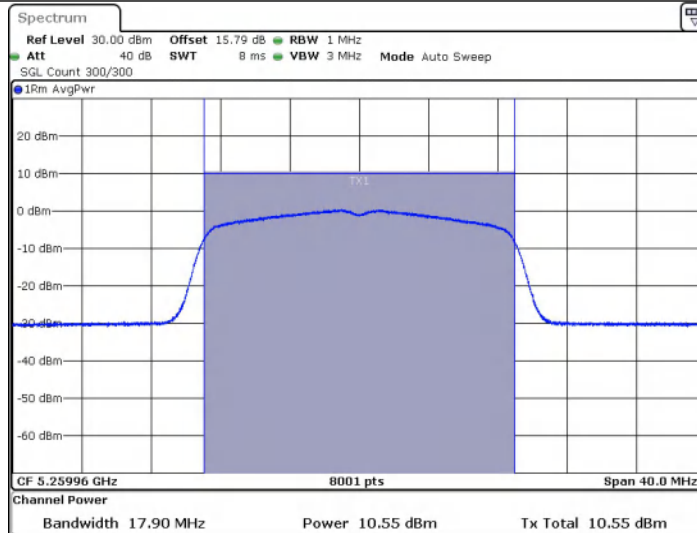
Date: 13 DEC 2025 14:44:52

11AX20SISO_Ant1_5240



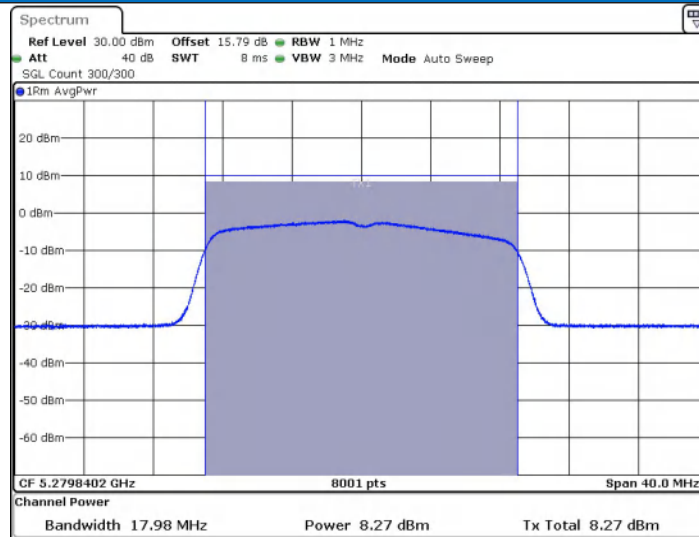
Date: 13 DEC 2025 14:46:24

11AX20SISO_Ant1_5260



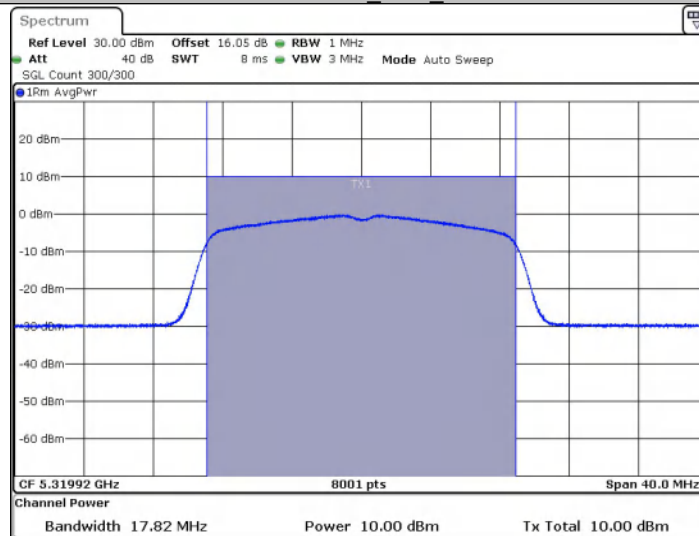
Date: 13 DEC 2025 14:47:46

11AX20SISO_Ant1_5280



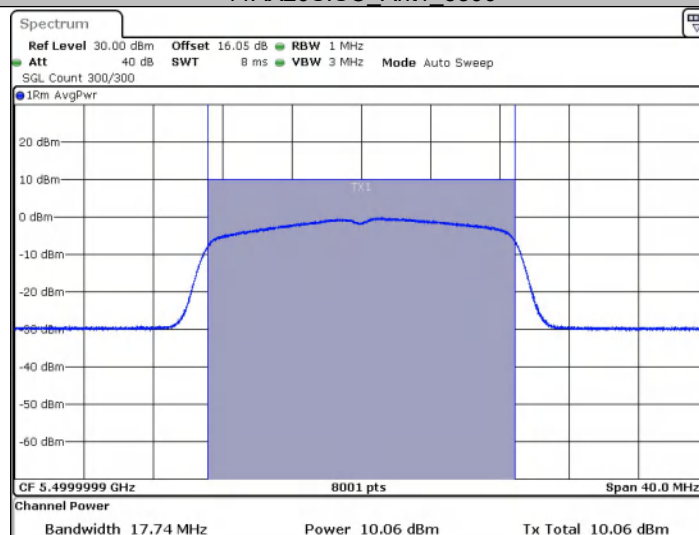
Date: 13 DEC 2025 14:48:57

11AX20SISO_Ant1_5320



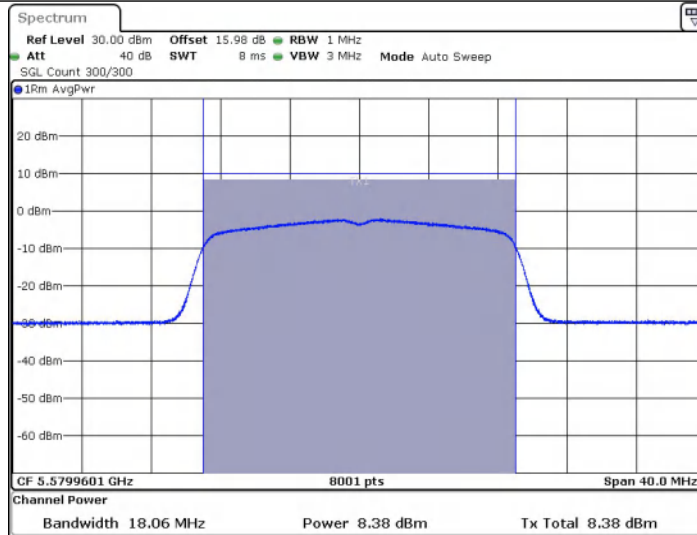
Date: 13 DEC 2025 14:50:09

11AX20SISO_Ant1_5500



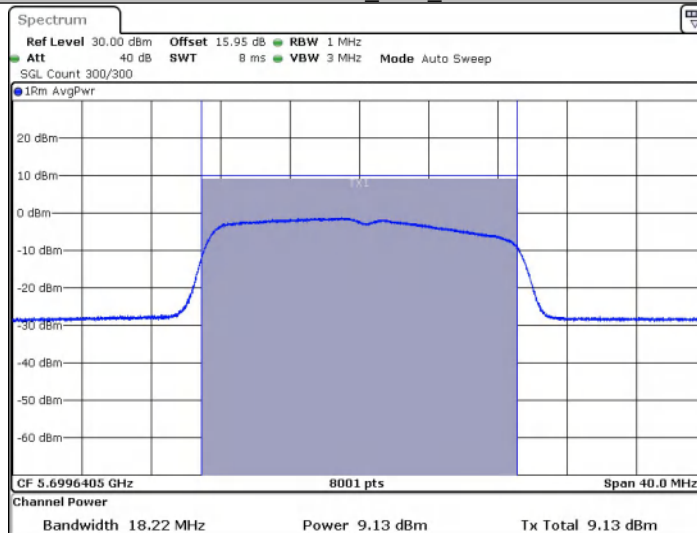
Date: 13 DEC 2025 14:52:11

11AX20SISO_Ant1_5580



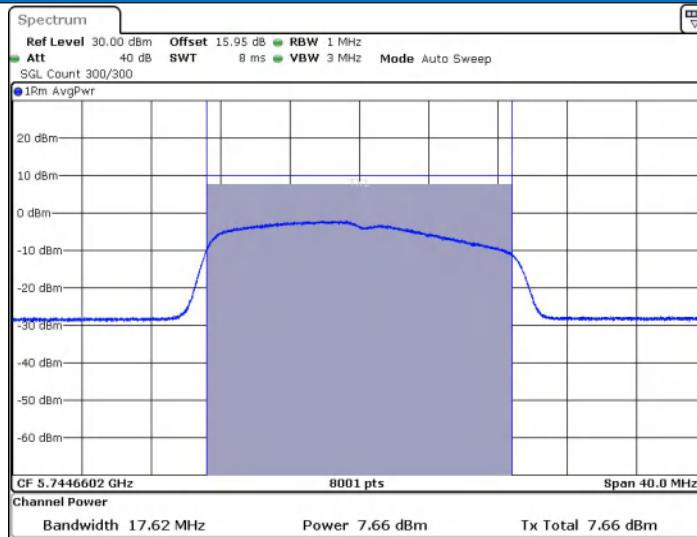
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11AX20SISO_Ant1_5700



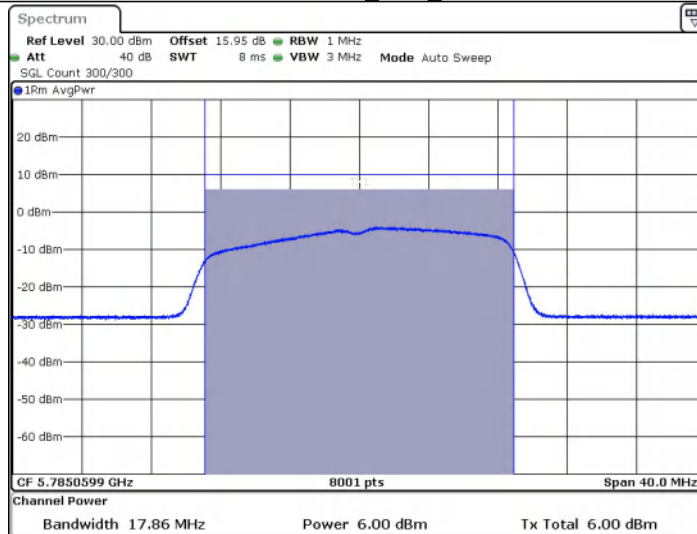
Date: 13 DEC 2025 15:51:46

11AX20SISO_Ant1_5745



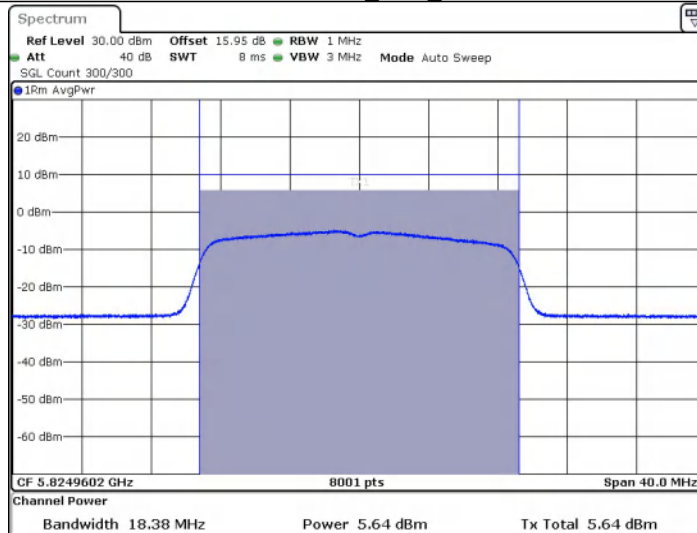
Date: 13 DEC 2025 14:57:02

11AX20SISO_Ant1_5785



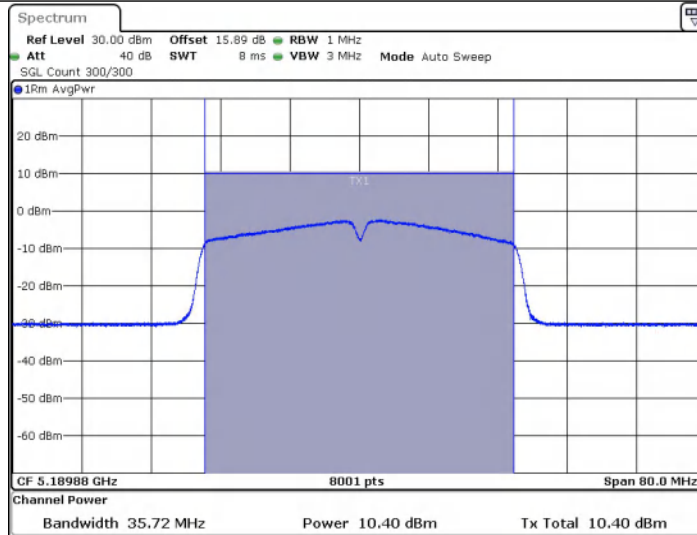
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11AX20SISO_Ant1_5825

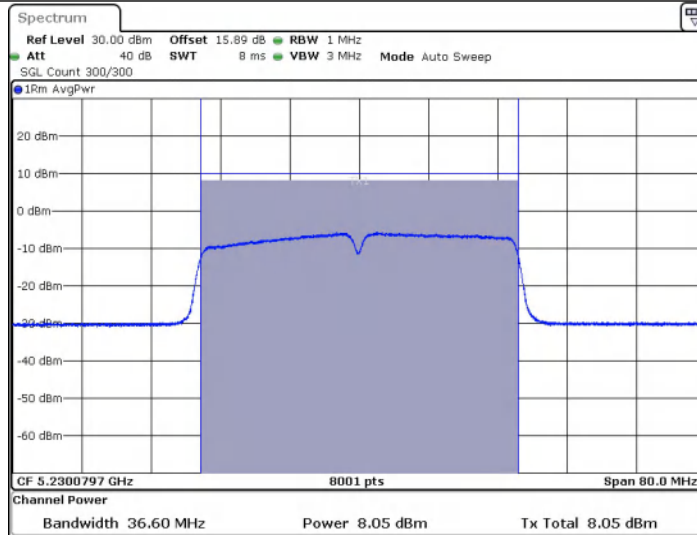


Date: 13 DEC 2025 16:00:43

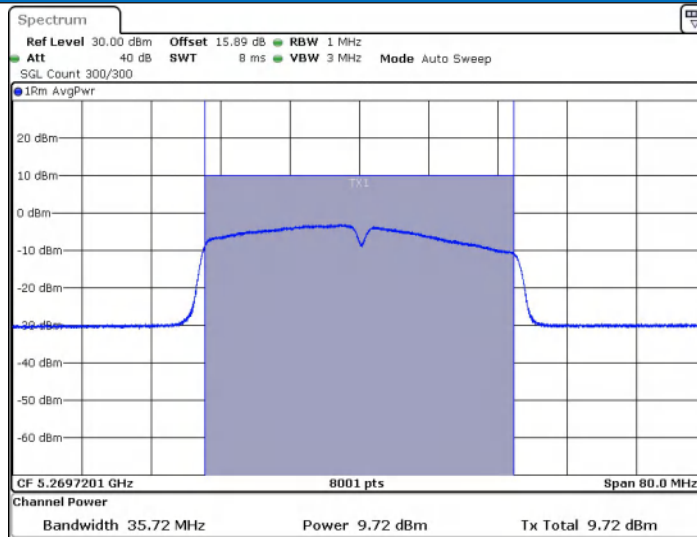
11AX40SISO_Ant1_5190



11AX40SISO_Ant1_5230

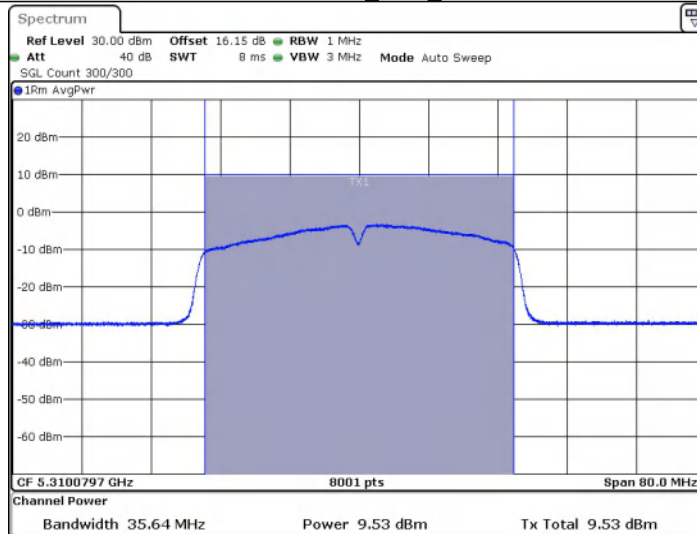


11AX40SISO_Ant1_5270



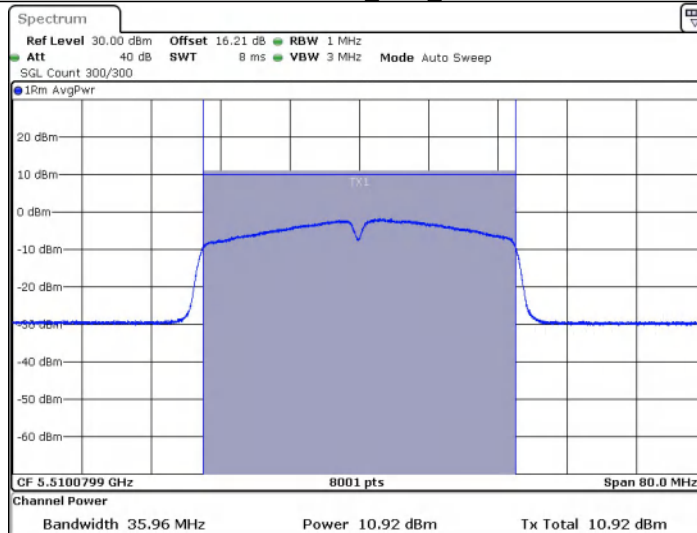
Date: 13 DEC 2025 15:05:54

11AX40SISO_Ant1_5310



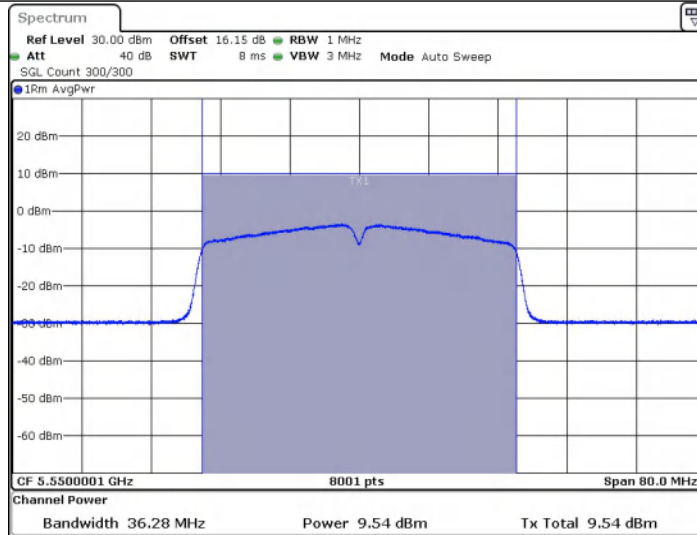
Date: 13 DEC 2025 15:09:06

11AX40SISO_Ant1_5510



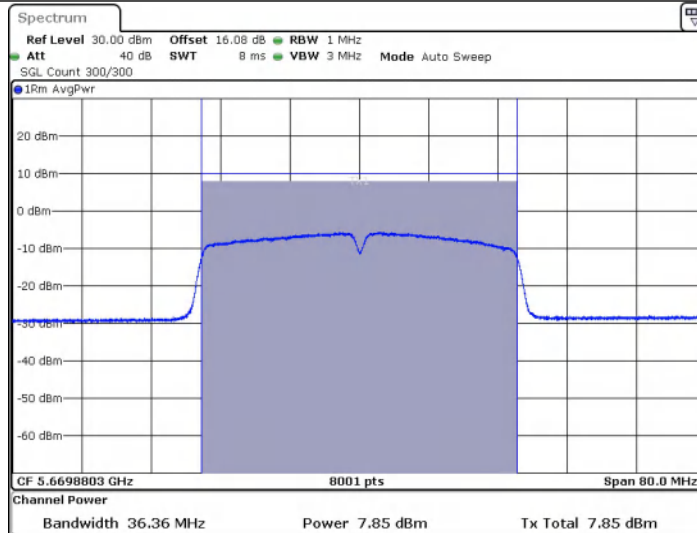
Date: 13 DEC 2025 15:10:54

11AX40SISO_Ant1_5550



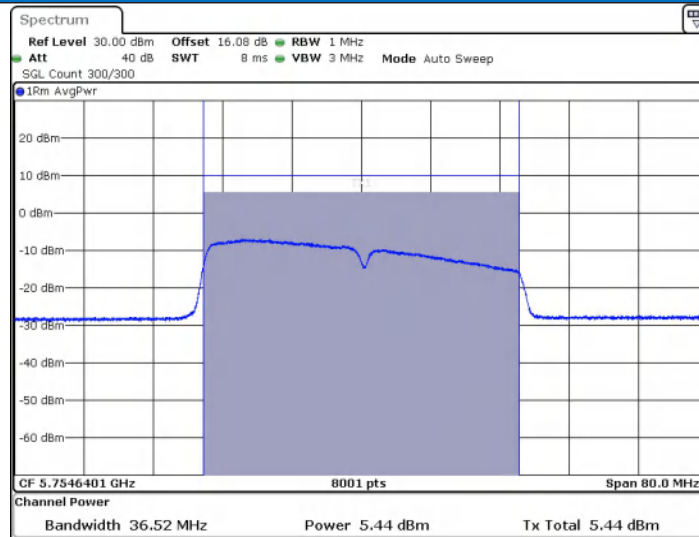
Date: 13 DEC 2025 15:13:08

11AX40SISO_Ant1_5670



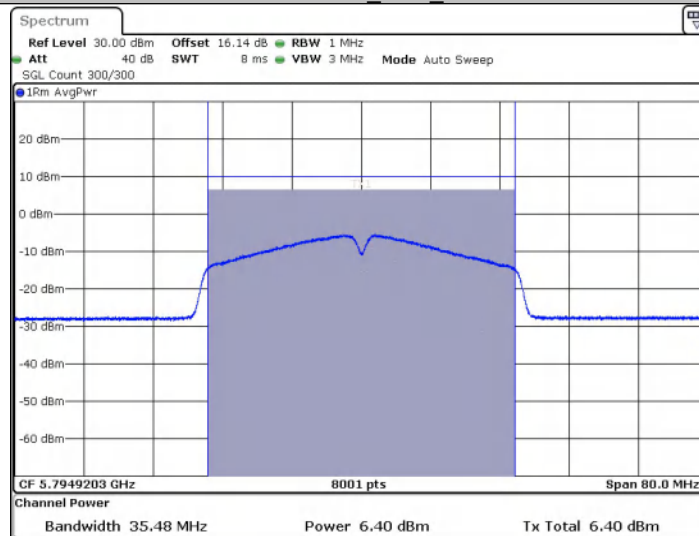
Date: 13 DEC 2025 15:25:38

11AX40SISO_Ant1_5755



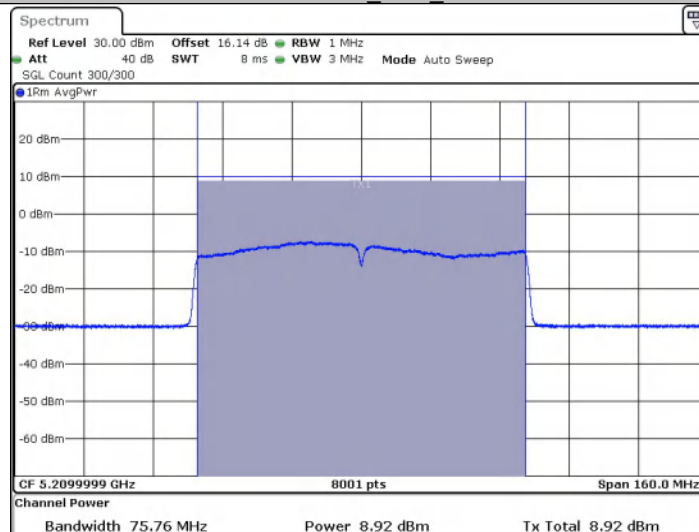
Date: 13 DEC 2025 15:27:11

11AX40SISO_Ant1_5795



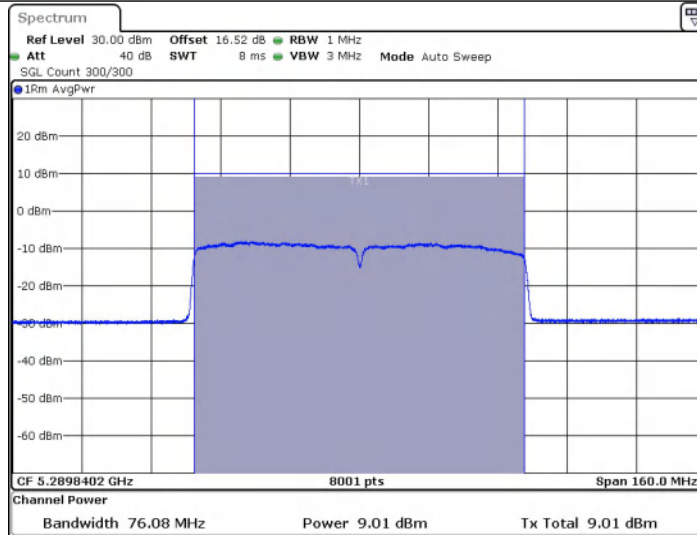
Date: 13 DEC 2025 15:28:47

11AX80SISO_Ant1_5210

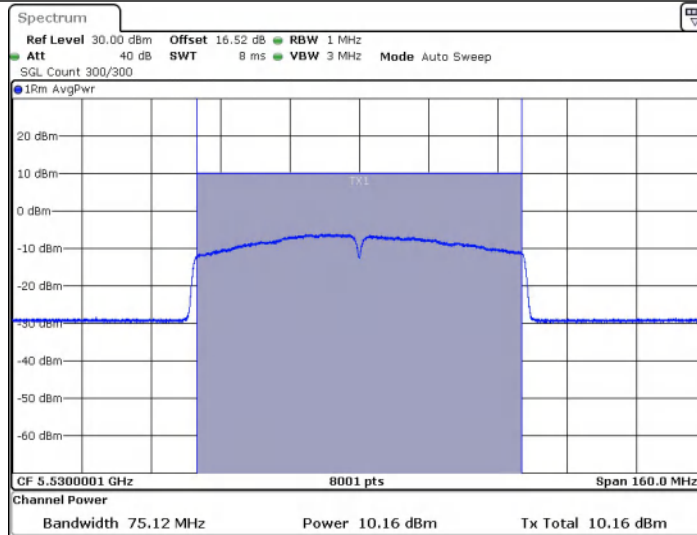


Date: 13 DEC 2025 15:30:38

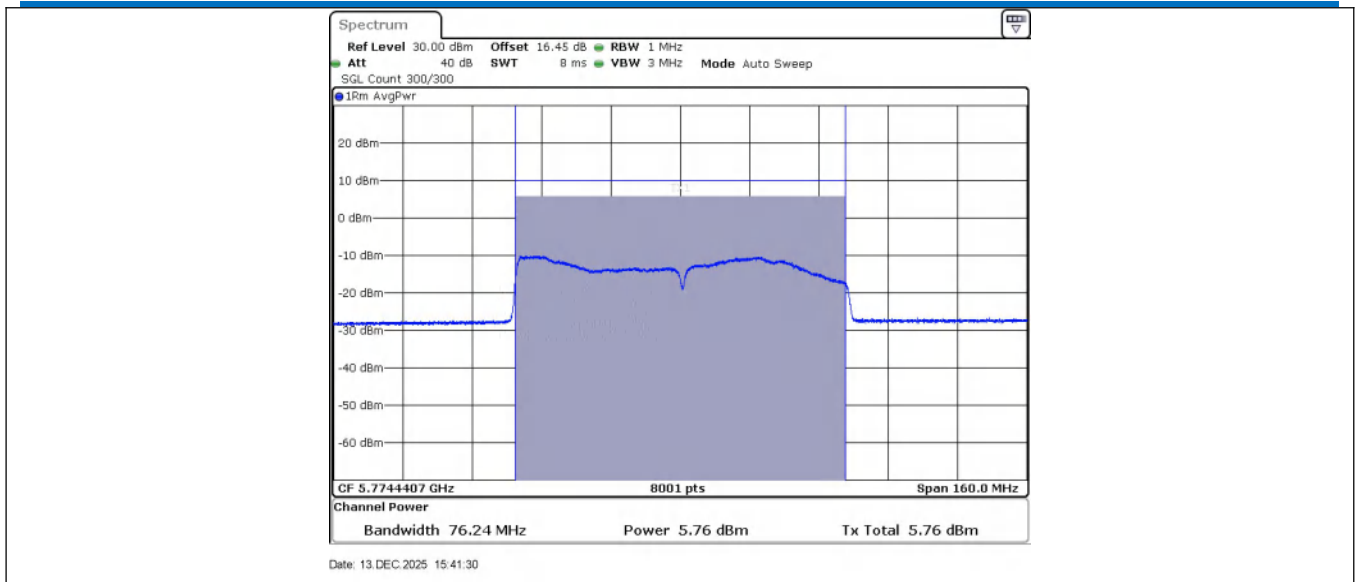
11AX80SISO_Ant1_5290



11AX80SISO_Ant1_5530



11AX80SISO_Ant1_5775



Remark:

Av.Power=Meas.Level+10 log (1/duty cycle)

E.i.r.p=Av.Power+G,

G = antenna gain in dBi.

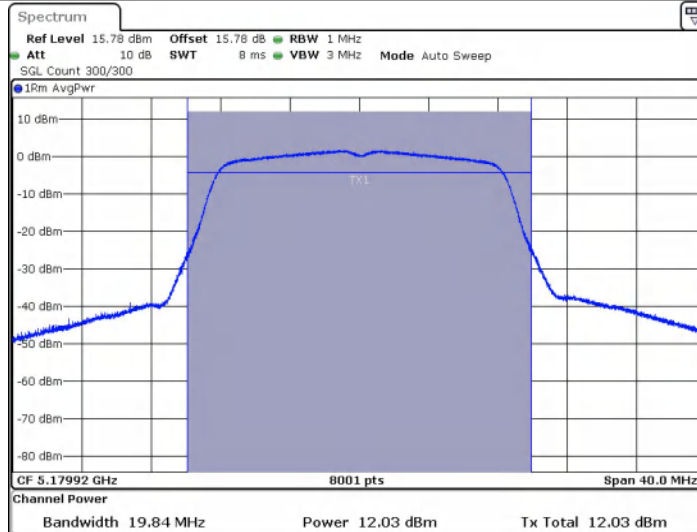
Ant2:

Test Mode	Antenna	Freq(MHz)	Result [dBm]	Limit [dBm]	Verdict
11A	Ant2	5180	12.03	≤23.8	PASS
		5200	9.91	≤23.8	PASS
		5240	9.05	≤23.8	PASS
		5260	13.30	≤23.8	PASS
		5280	11.05	≤23.8	PASS
		5320	12.59	≤23.8	PASS
		5500	11.17	≤23.8	PASS
		5580	10.08	≤23.8	PASS
		5700	10.03	≤23.8	PASS
		5745	9.55	≤29.8	PASS
		5785	9.93	≤29.8	PASS
		5825	11.10	≤29.8	PASS
11N20SISO	Ant2	5180	8.68	≤23.8	PASS
		5200	8.38	≤23.8	PASS
		5240	6.99	≤23.8	PASS
		5260	8.69	≤23.8	PASS
		5280	7.34	≤23.8	PASS
		5320	7.71	≤23.8	PASS
		5500	8.46	≤23.8	PASS
		5580	7.32	≤23.8	PASS
		5700	6.56	≤23.8	PASS
		5745	7.64	≤29.8	PASS
		5785	6.80	≤29.8	PASS
		5825	6.34	≤29.8	PASS
11N40SISO	Ant2	5190	8.77	≤23.8	PASS
		5230	6.76	≤23.8	PASS
		5270	8.26	≤23.8	PASS
		5310	7.40	≤23.8	PASS
		5510	9.21	≤23.8	PASS
		5550	8.96	≤23.8	PASS
		5670	7.61	≤23.8	PASS
		5755	7.32	≤29.8	PASS
		5795	7.37	≤29.8	PASS
11AC20SIS	Ant2	5180	8.67	≤23.8	PASS

	O		5200	8.47	≤23.8	PASS
			5240	6.97	≤23.8	PASS
			5260	8.70	≤23.8	PASS
			5280	7.49	≤23.8	PASS
			5320	7.73	≤23.8	PASS
			5500	8.47	≤23.8	PASS
			5580	7.44	≤23.8	PASS
			5700	6.52	≤23.8	PASS
			5745	7.78	≤29.8	PASS
			5785	6.76	≤29.8	PASS
			5825	6.38	≤29.8	PASS
	11AC40SIS O	Ant2	5190	8.73	≤23.8	PASS
			5230	6.69	≤23.8	PASS
			5270	8.39	≤23.8	PASS
			5310	7.32	≤23.8	PASS
			5510	9.10	≤23.8	PASS
			5550	8.89	≤23.8	PASS
			5670	7.68	≤23.8	PASS
			5755	7.25	≤29.8	PASS
			5795	7.35	≤29.8	PASS
	11AC80SIS O	Ant2	5210	7.97	≤23.8	PASS
			5290	8.03	≤23.8	PASS
			5530	9.14	≤23.8	PASS
			5775	7.41	≤29.8	PASS
	11AX20SIS O	Ant2	5180	8.81	≤23.8	PASS
			5200	8.46	≤23.8	PASS
			5240	6.96	≤23.8	PASS
			5260	8.70	≤23.8	PASS
			5280	7.42	≤23.8	PASS
			5320	7.85	≤23.8	PASS
			5500	8.56	≤23.8	PASS
			5580	7.51	≤23.8	PASS
			5700	6.65	≤23.8	PASS
			5745	7.67	≤29.8	PASS
			5785	6.87	≤29.8	PASS
			5825	6.39	≤29.8	PASS
	11AX40SIS	Ant2	5190	8.73	≤23.8	PASS

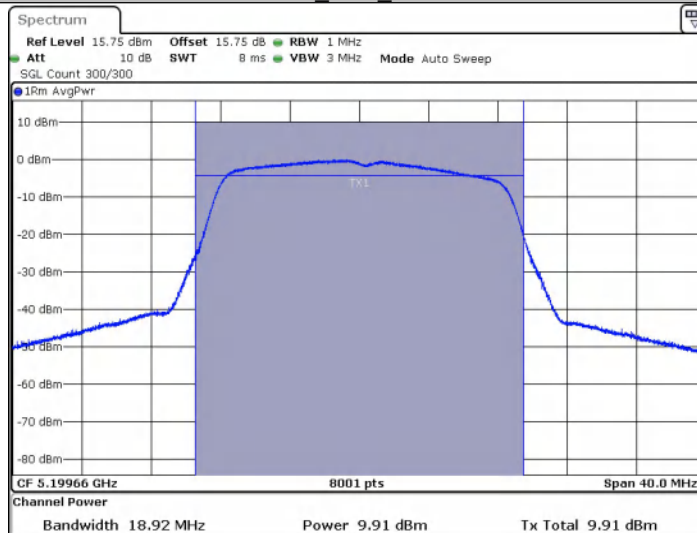
	O		5230	6.66	≤23.8	PASS
			5270	8.29	≤23.8	PASS
			5310	7.41	≤23.8	PASS
			5510	9.26	≤23.8	PASS
			5550	9.12	≤23.8	PASS
			5670	7.75	≤23.8	PASS
			5755	7.44	≤29.8	PASS
			5795	7.30	≤29.8	PASS
	11AX80SIS O	Ant2	5210	7.96	≤23.8	PASS
			5290	7.90	≤23.8	PASS
			5530	9.21	≤23.8	PASS
			5775	5.79	≤29.8	PASS

11A_Ant2_5180



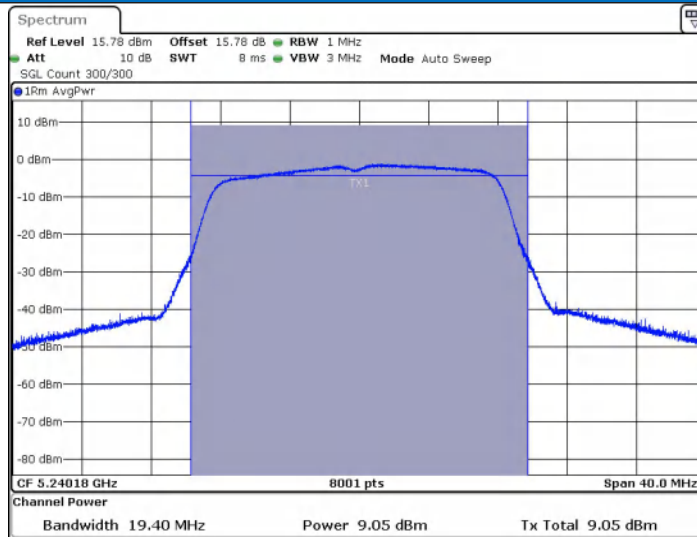
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11A_Ant2_5200



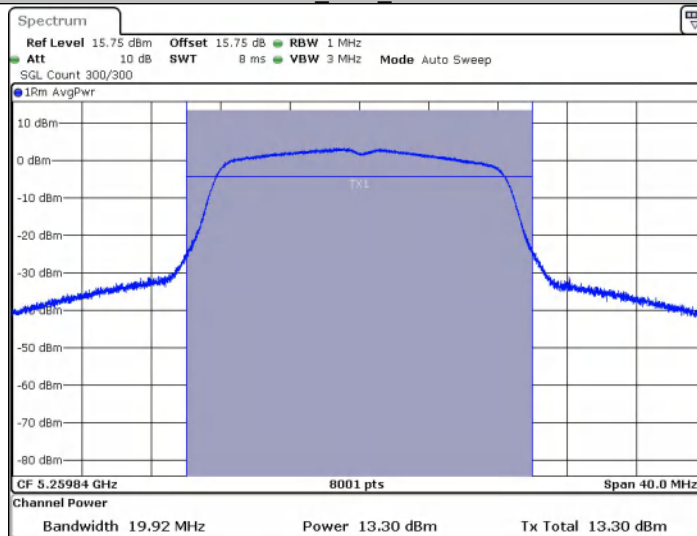
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11A_Ant2_5240



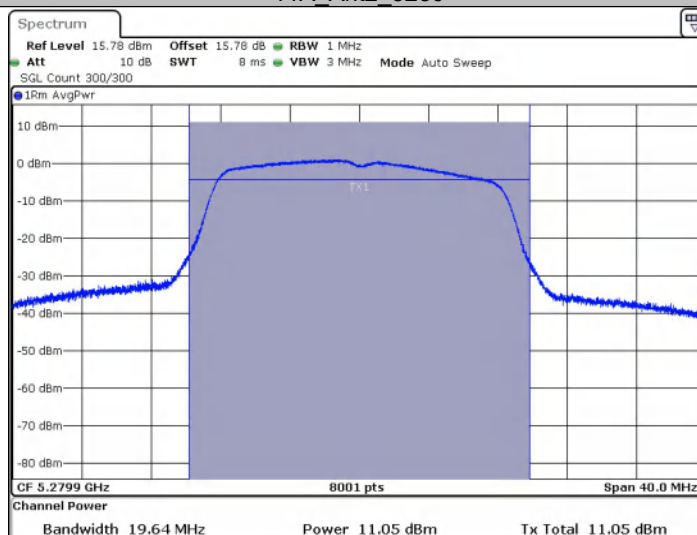
Date: 15 DEC 2025 15:07:55

11A_Ant2_5260



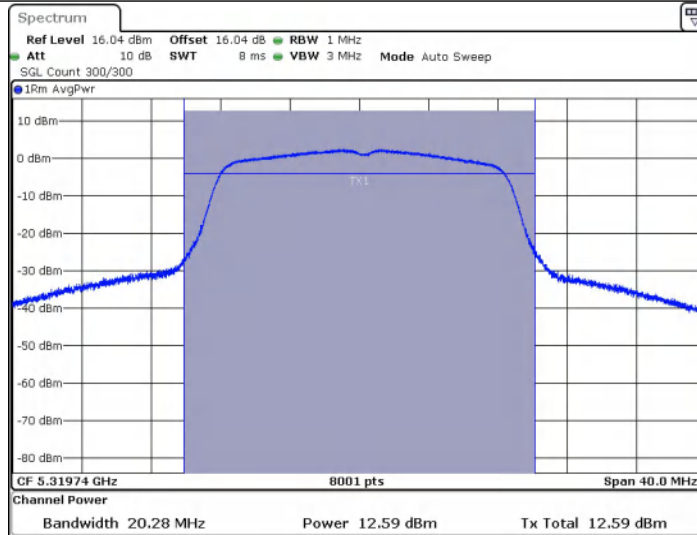
Date: 15 DEC 2025 15:09:07

11A_Ant2_5280



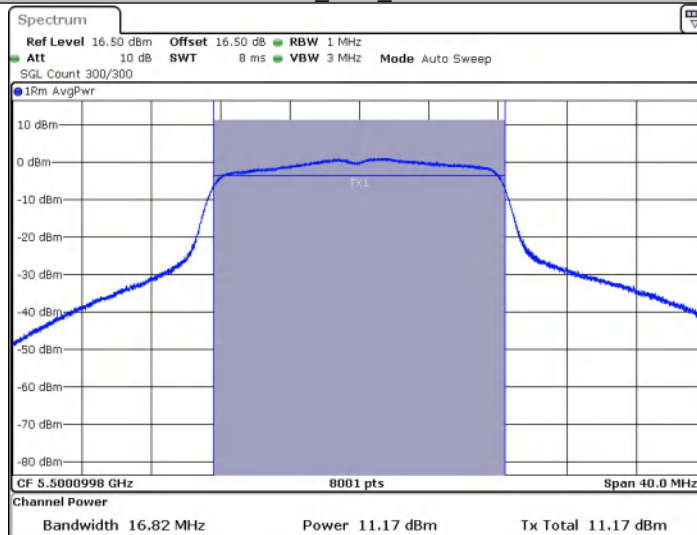
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11A_Ant2_5320



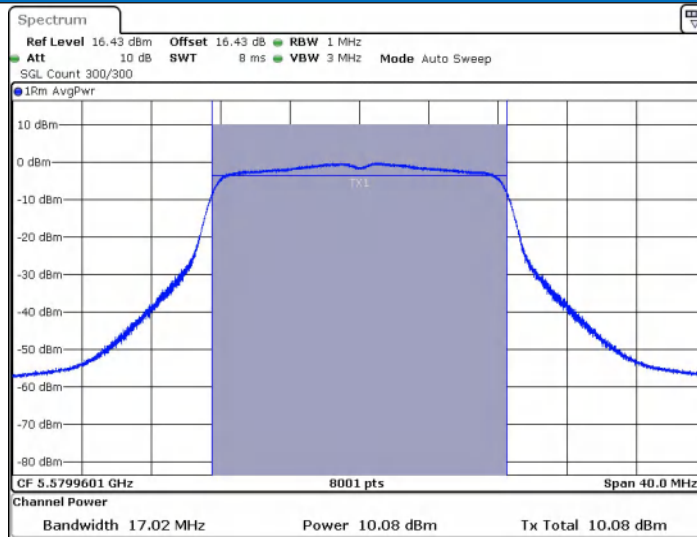
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11A_Ant2_5500



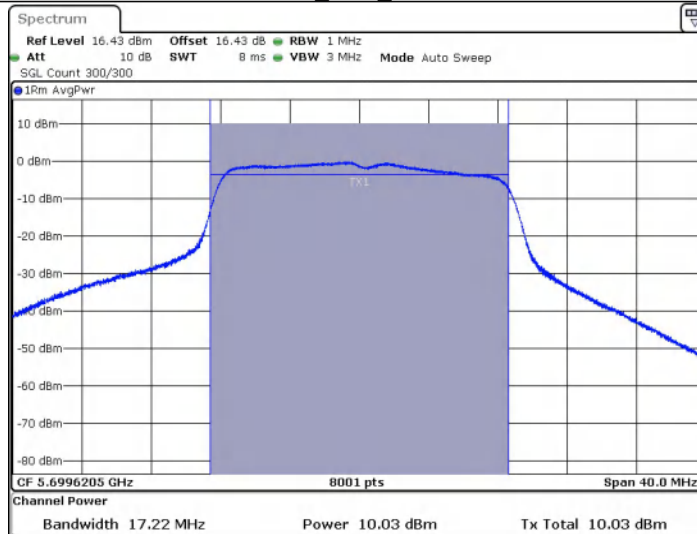
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11A_Ant2_5580



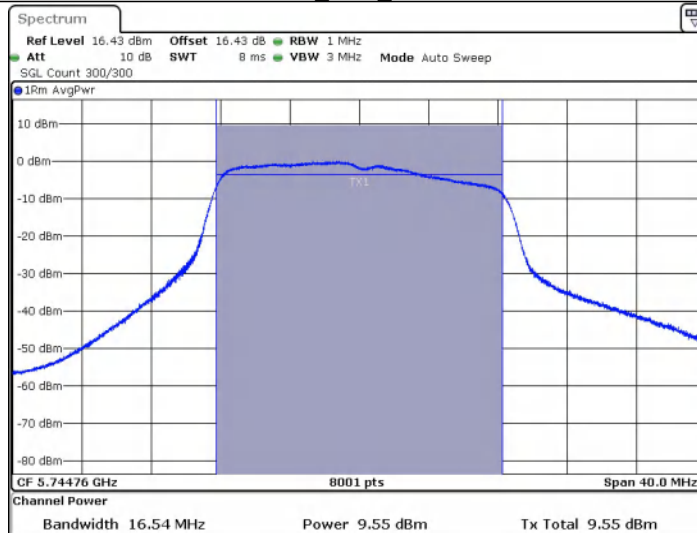
Date: 17.DEC.2025 14:44:47

11A_Ant2_5700



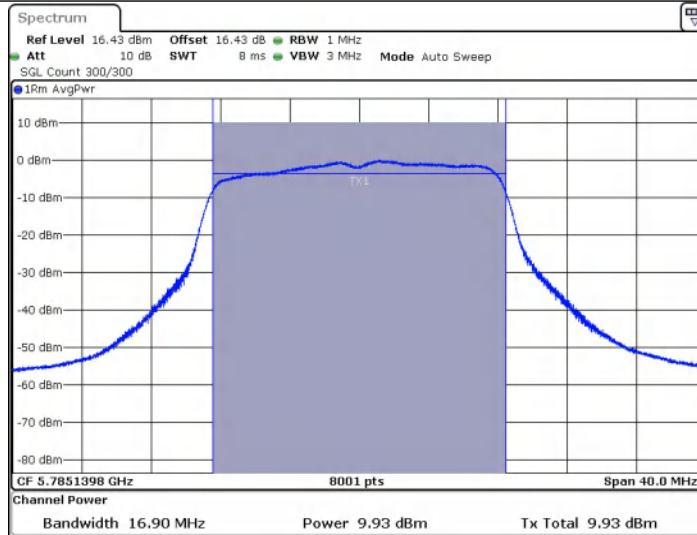
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11A_Ant2_5745



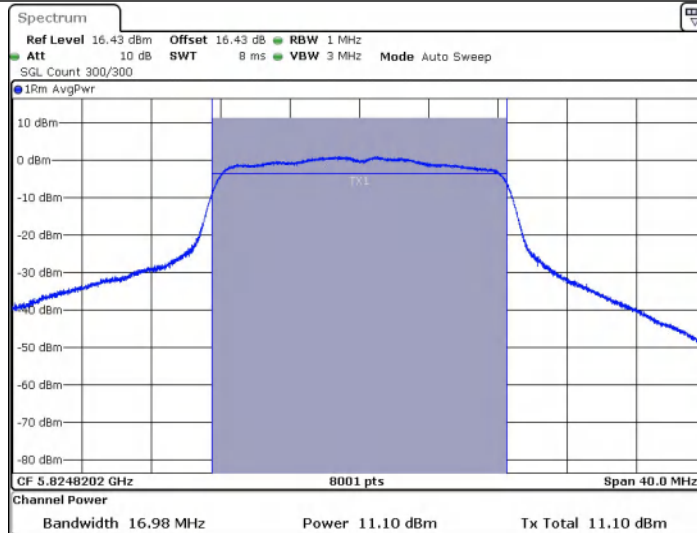
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11A_Ant2_5785



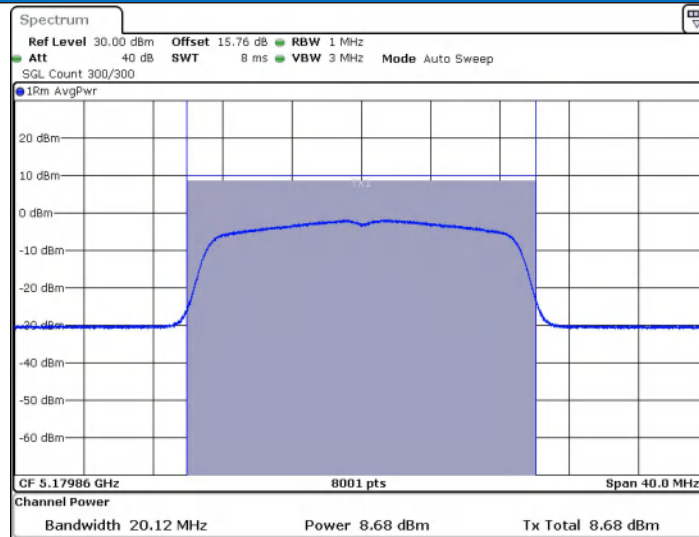
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11A_Ant2_5825



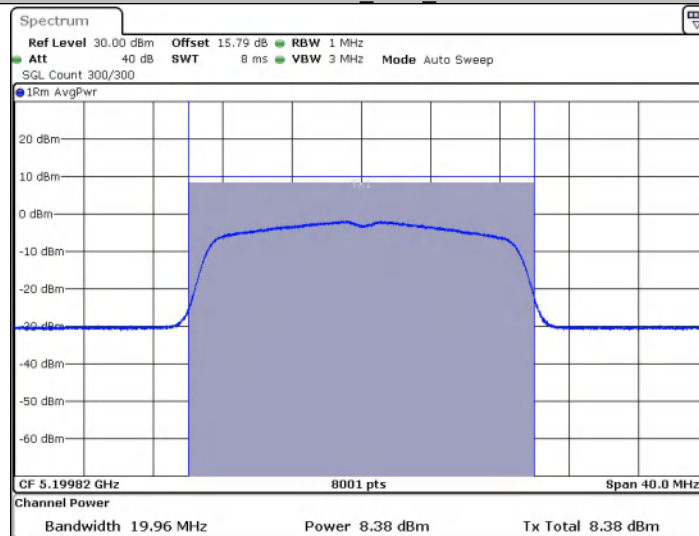
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11N20SISO_Ant2_5180



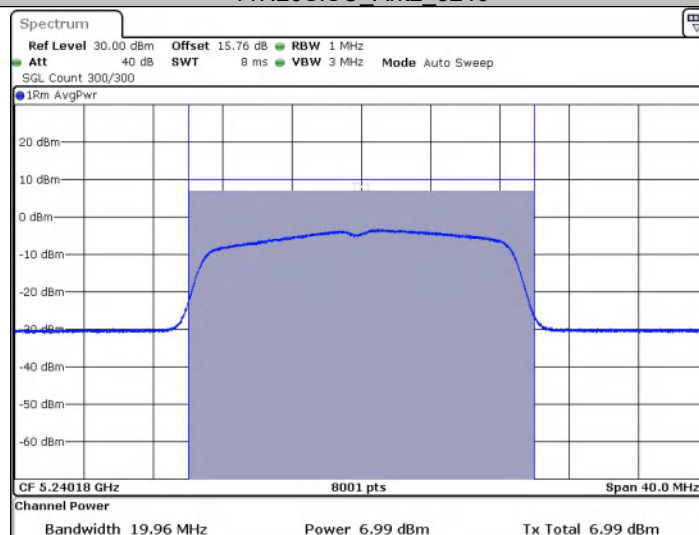
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11N20SISO_Ant2_5200



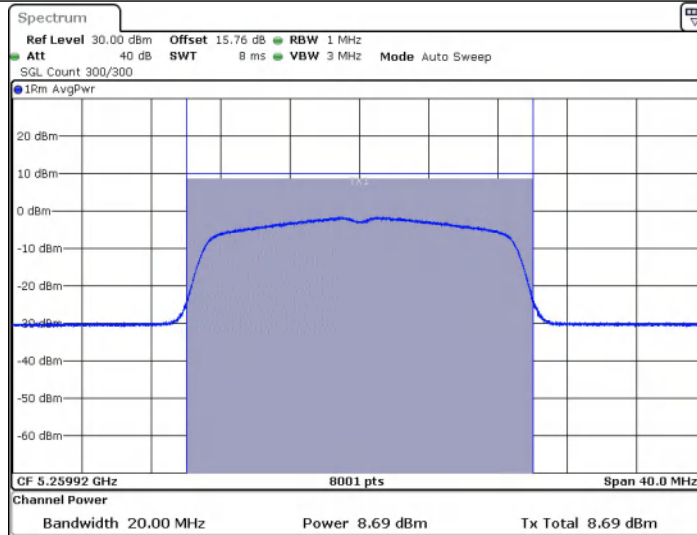
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11N20SISO_Ant2_5240

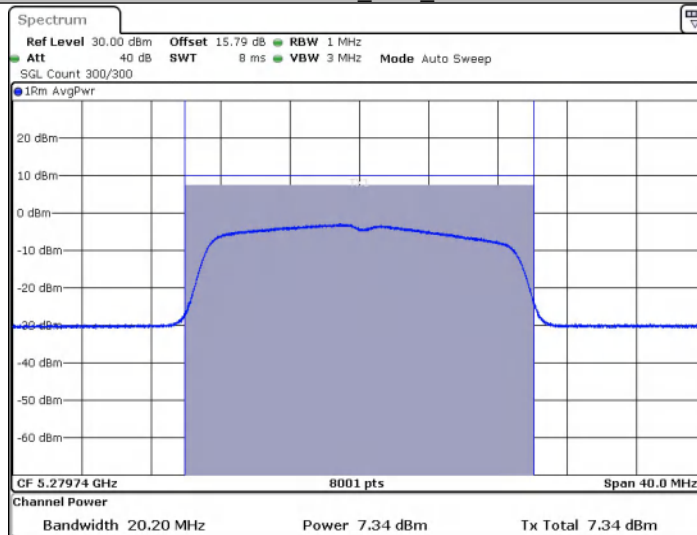


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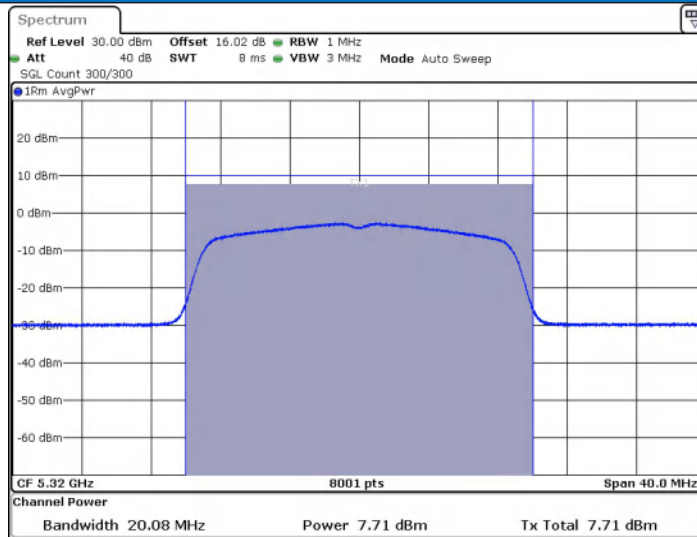
11N20SISO_Ant2_5260



11N20SISO_Ant2_5280

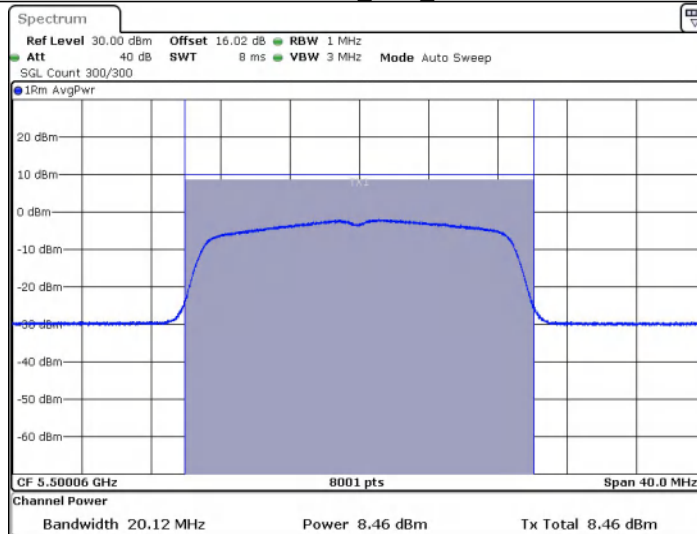


11N20SISO_Ant2_5320



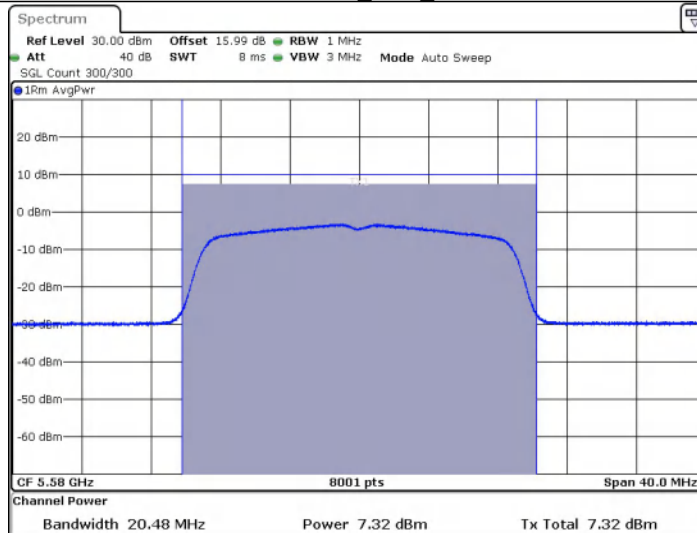
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11N20SISO_Ant2_5500



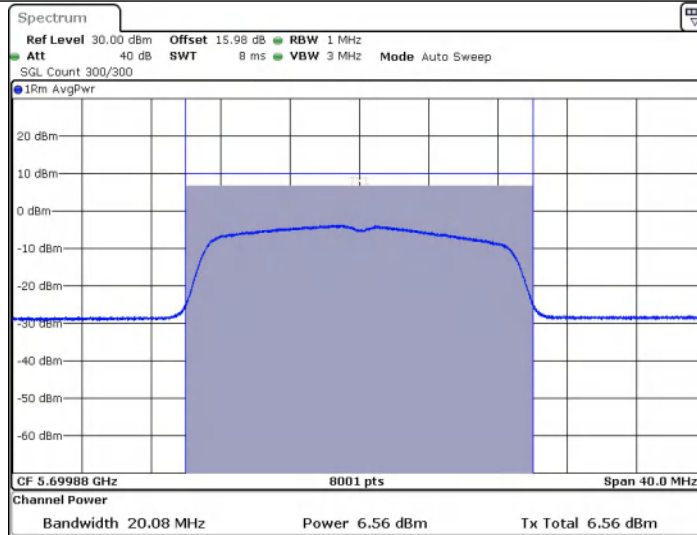
Date: 11 DEC 2025 15:09:04

11N20SISO_Ant2_5580



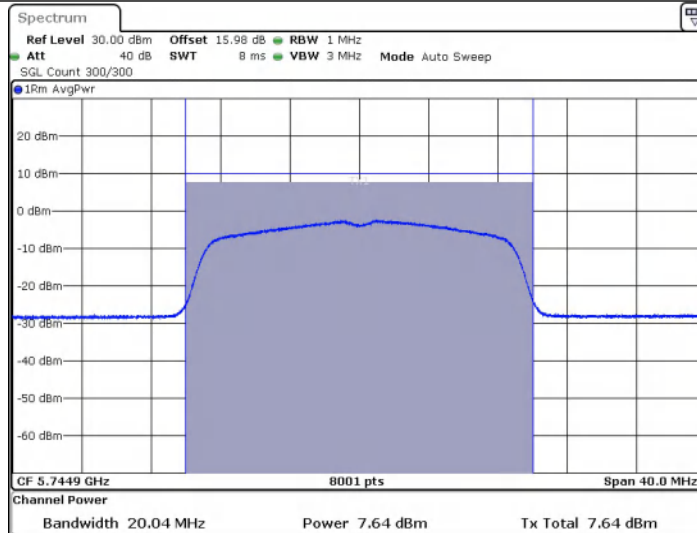
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11N20SISO_Ant2_5700



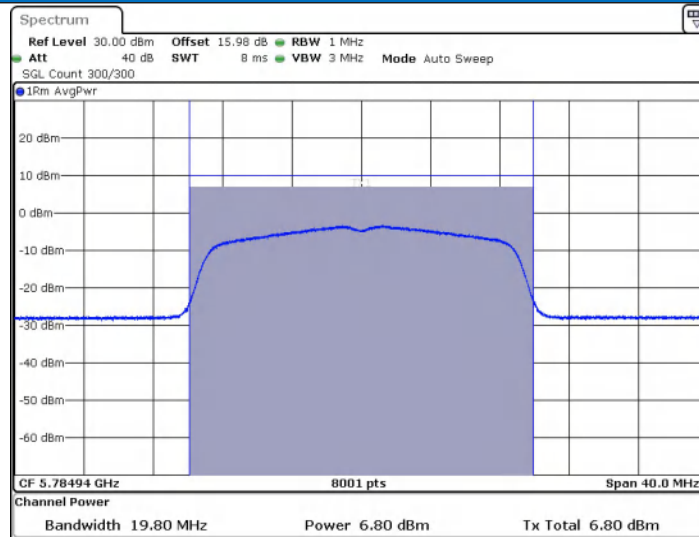
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11N20SISO_Ant2_5745



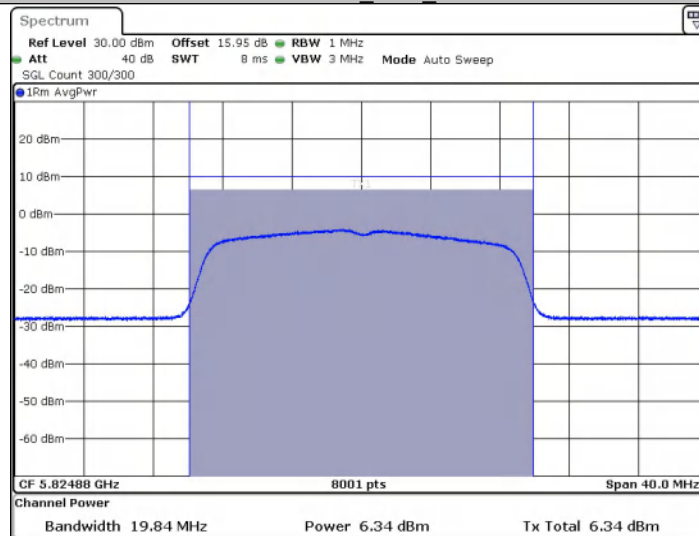
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11N20SISO_Ant2_5785



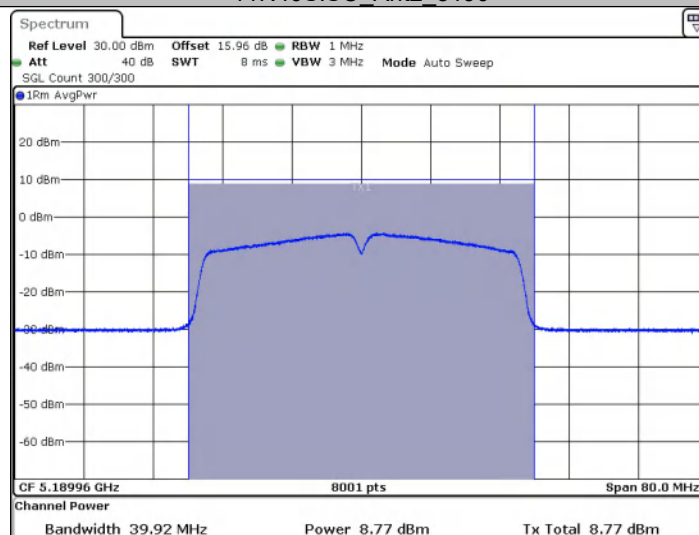
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11N20SISO_Ant2_5825



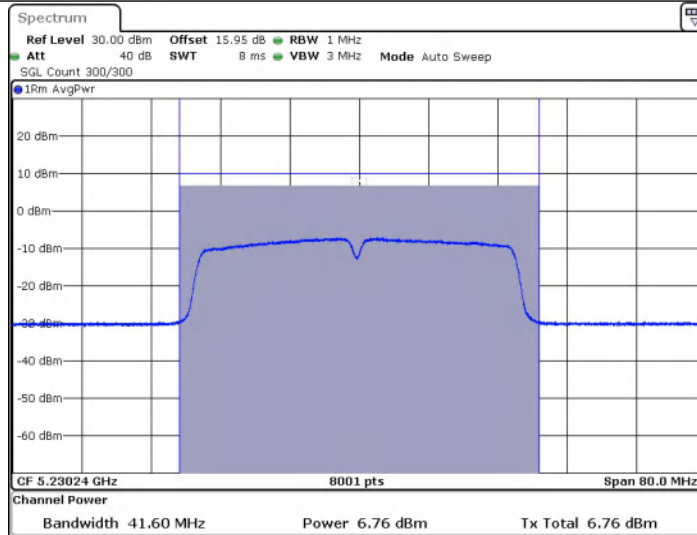
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11N40SISO_Ant2_5190



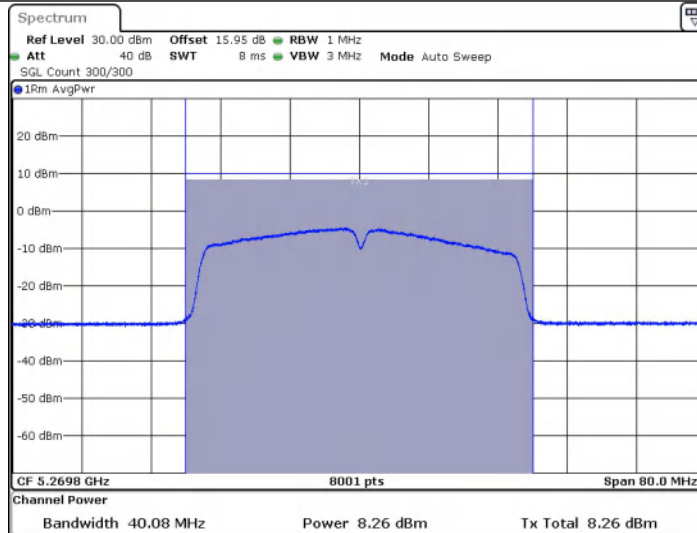
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11N40SISO_Ant2_5230



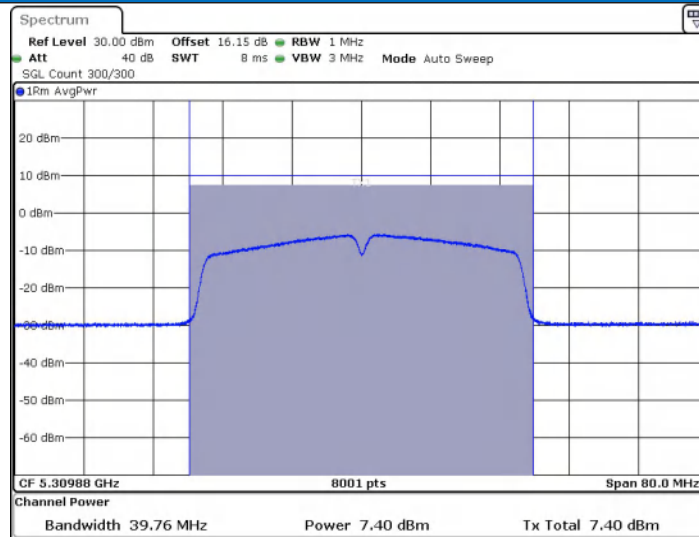
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11N40SISO_Ant2_5270



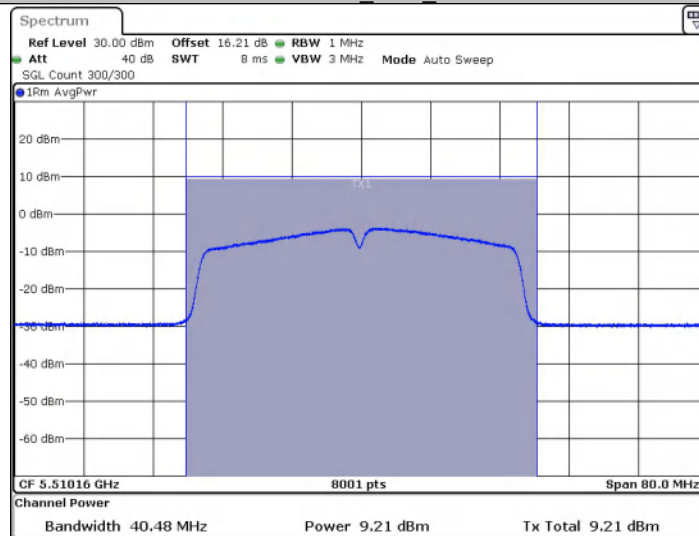
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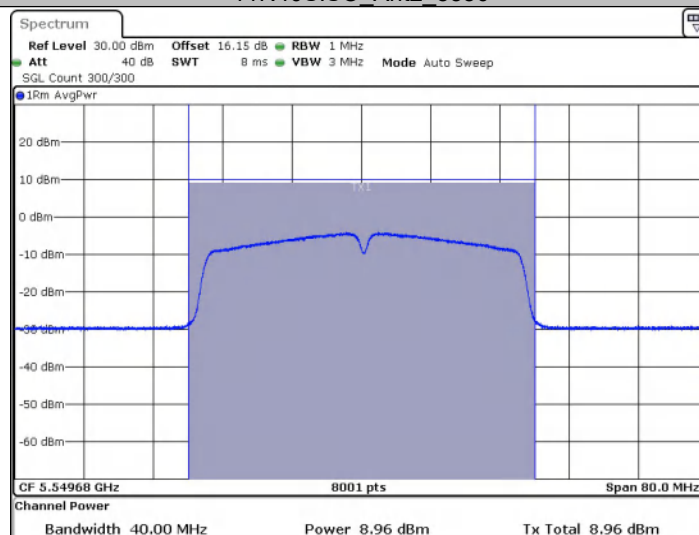
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11N40SISO_Ant2_5510



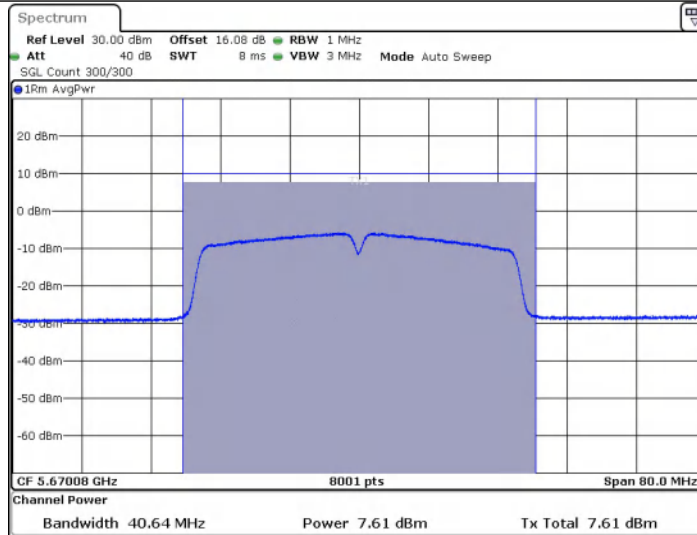
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11N40SISO_Ant2_5550

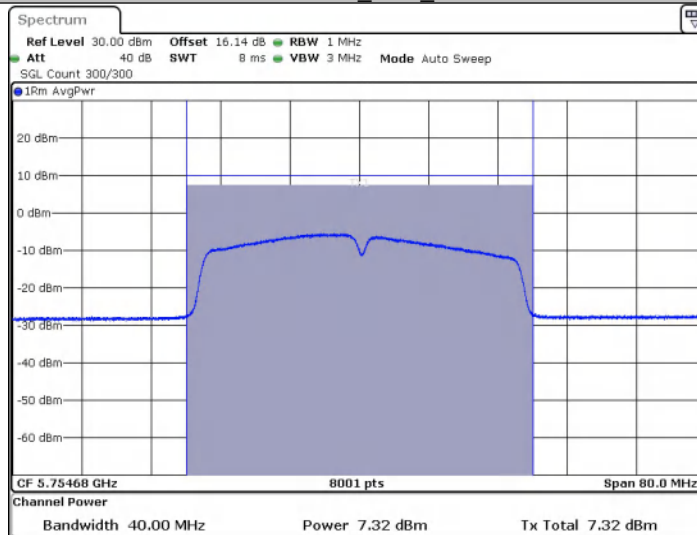


Date: 11 DEC 2025 15:31:04

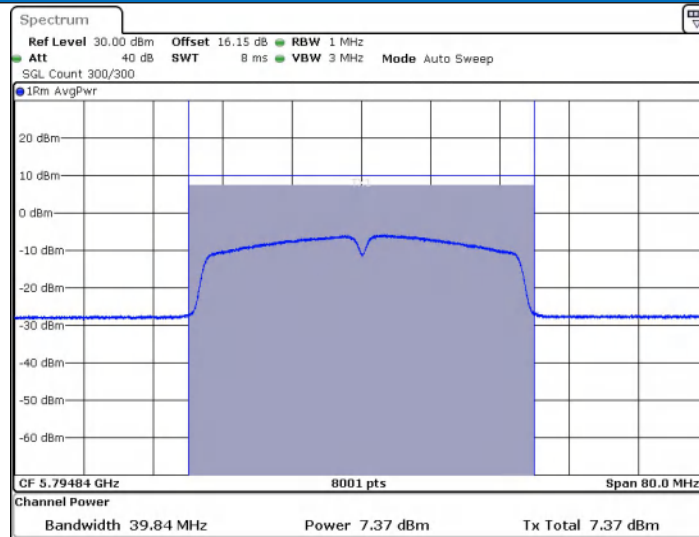
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11N40SISO_Ant2_5755

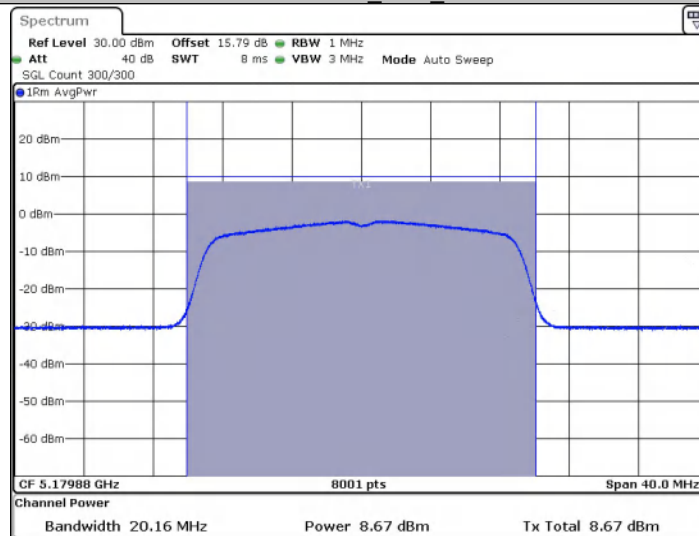


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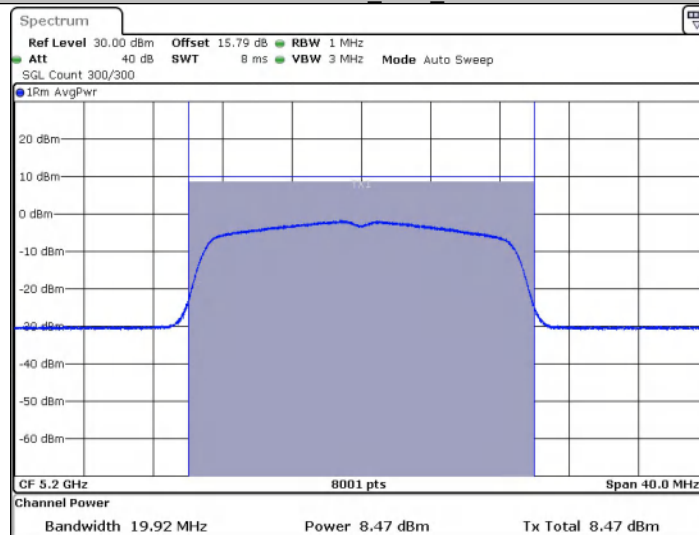
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11AC20SISO_Ant2_5180



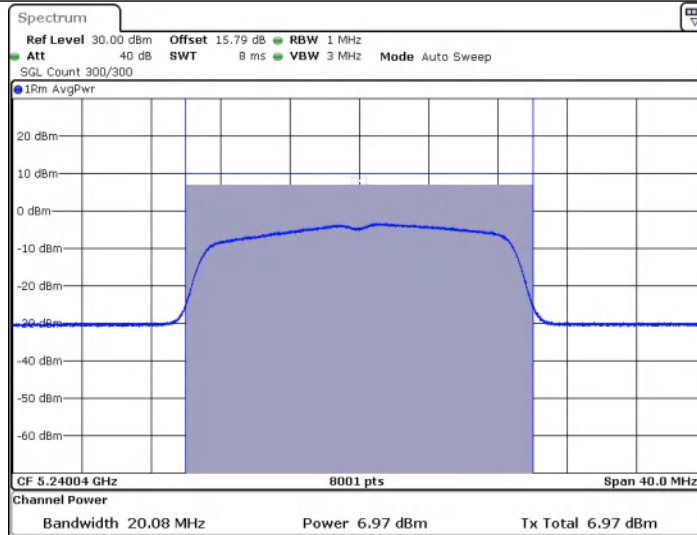
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11AC20SISO_Ant2_5200

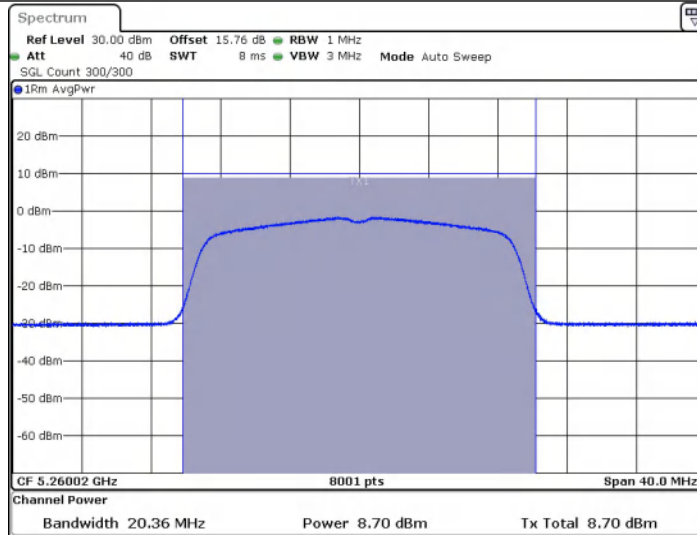


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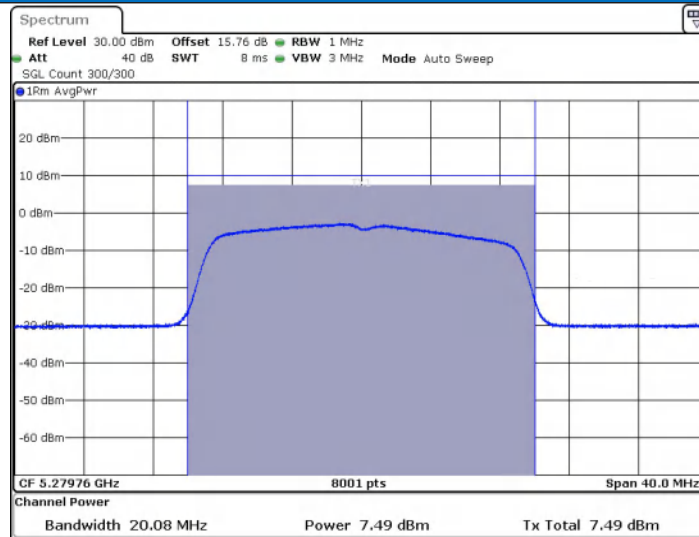
11AC20SISO_Ant2_5240



11AC20SISO_Ant2_5260

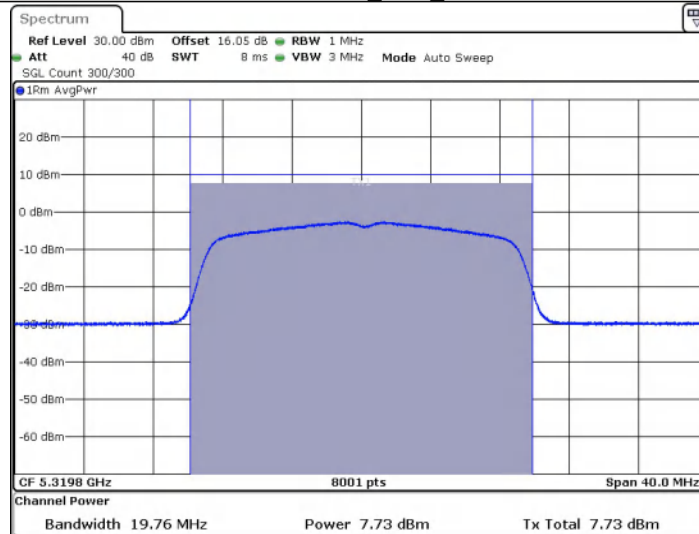


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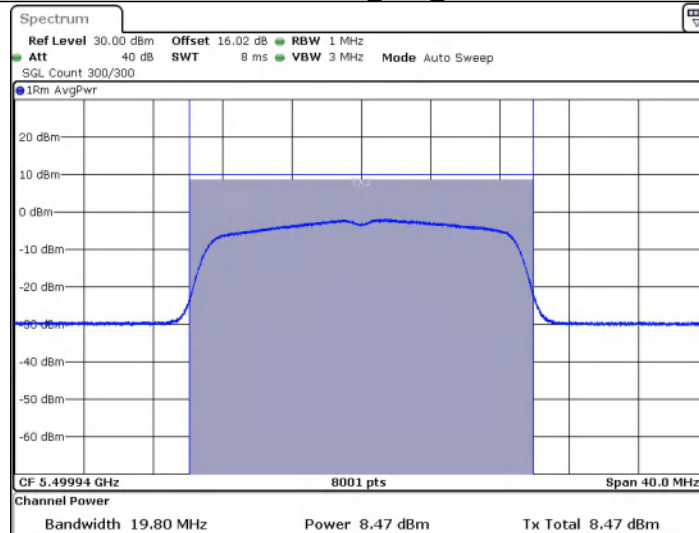
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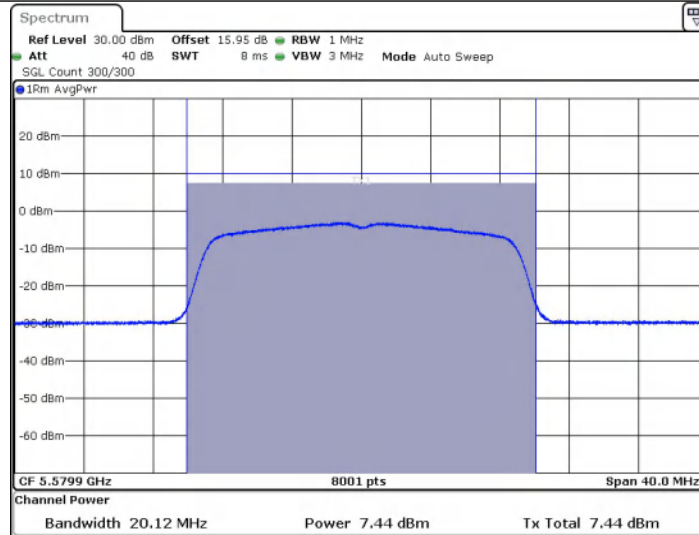
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11AC20SISO_Ant2_5500

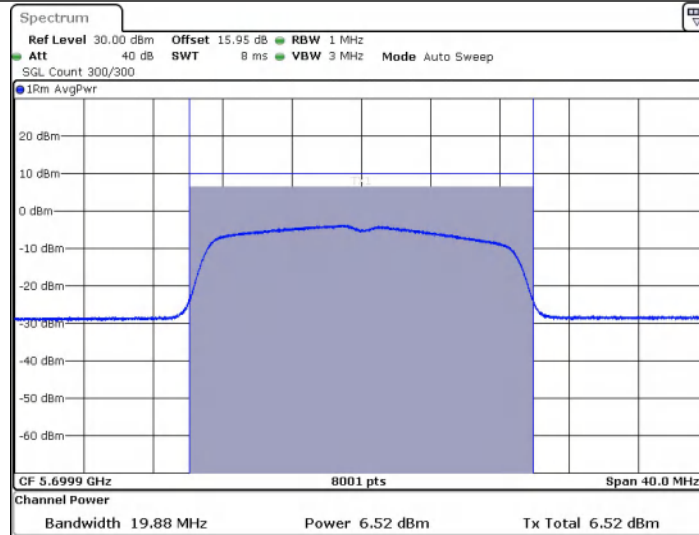


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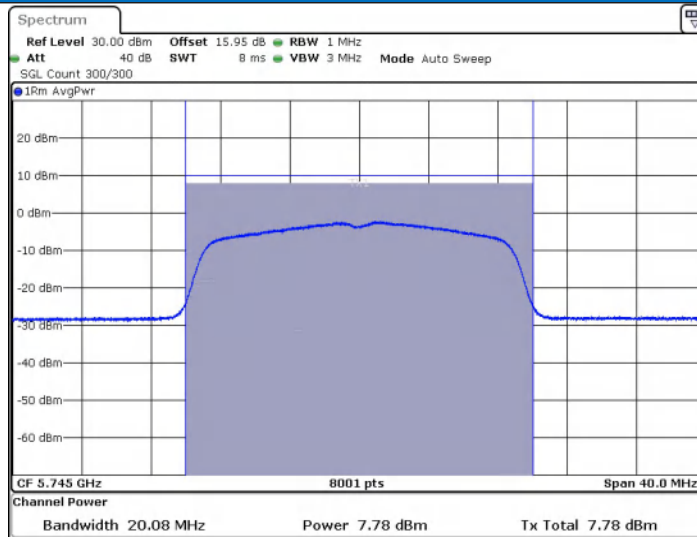
11AC20SISO_Ant2_5580



11AC20SISO_Ant2_5700

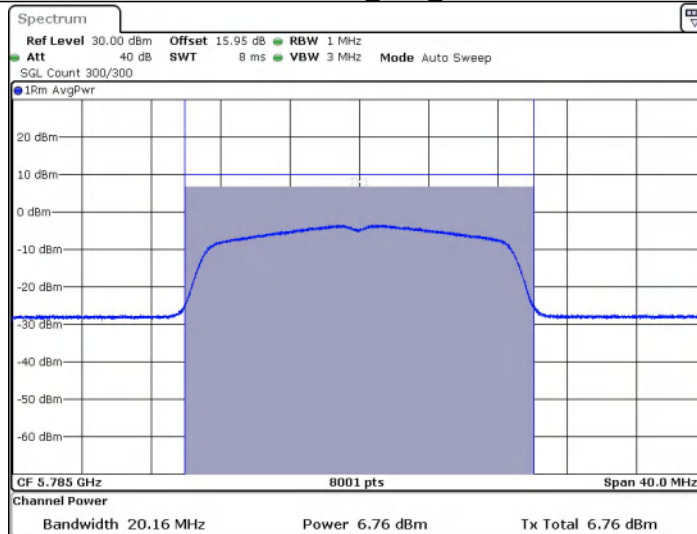


11AC20SISO_Ant2_5745



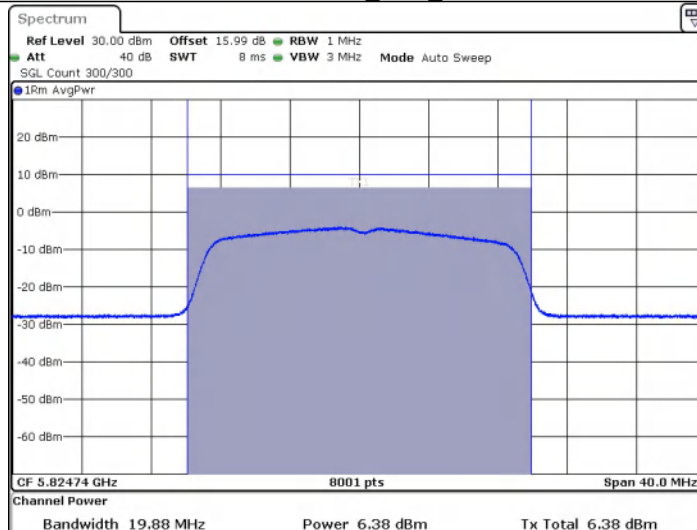
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11AC20SISO_Ant2_5785



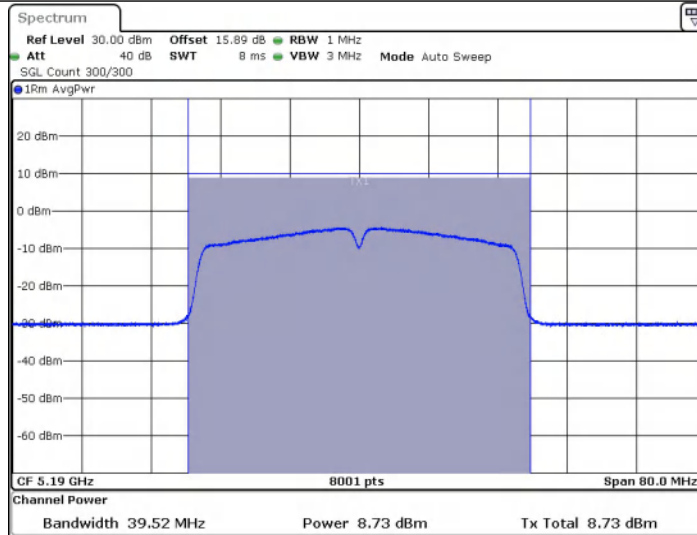
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11AC20SISO_Ant2_5825



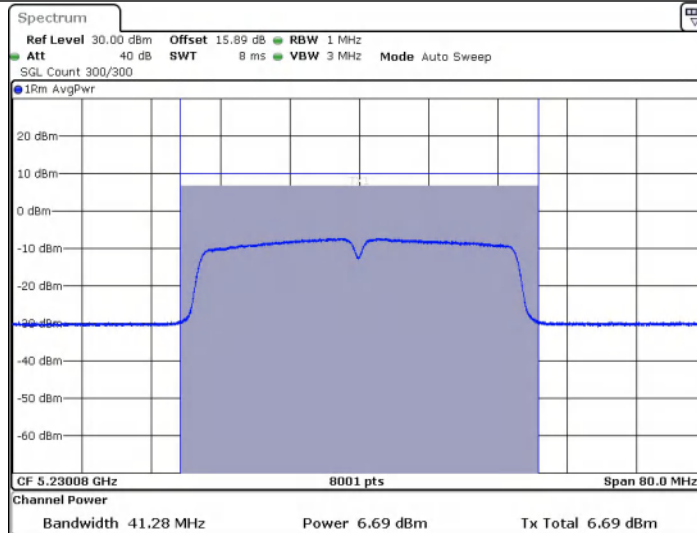
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11AC40SISO_Ant2_5190



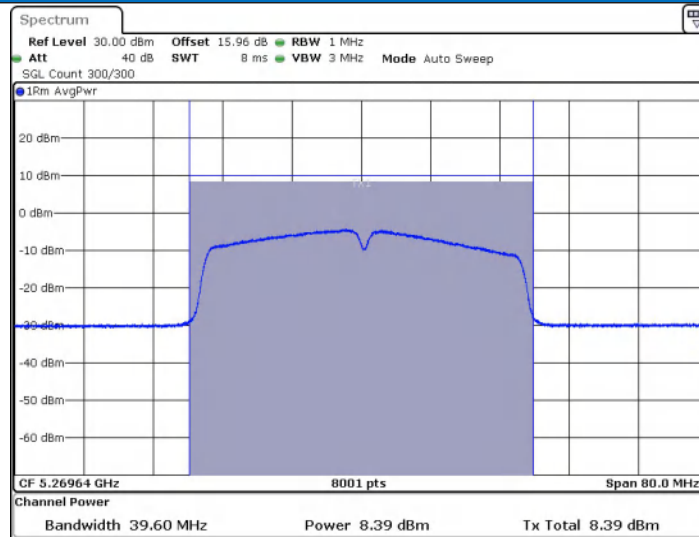
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11AC40SISO_Ant2_5230



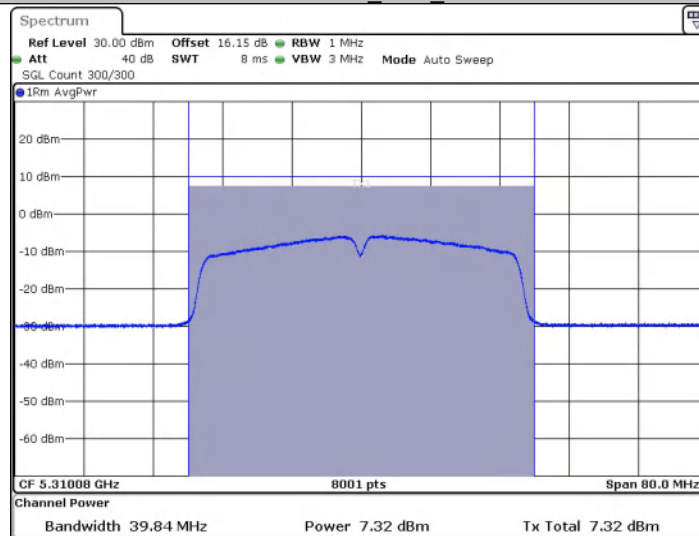
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11AC40SISO_Ant2_5270



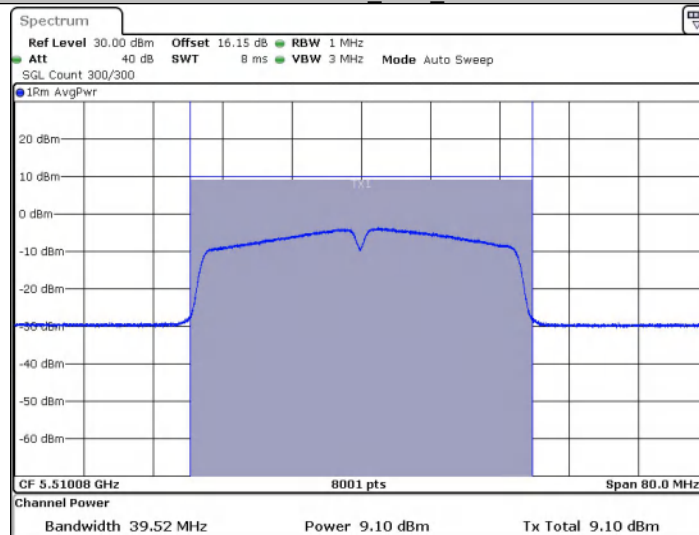
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11AC40SISO_Ant2_5310



Date: 11 DEC 2025 16:16:44

11AC40SISO_Ant2_5510



Date: 11 DEC 2025 16:17:41