

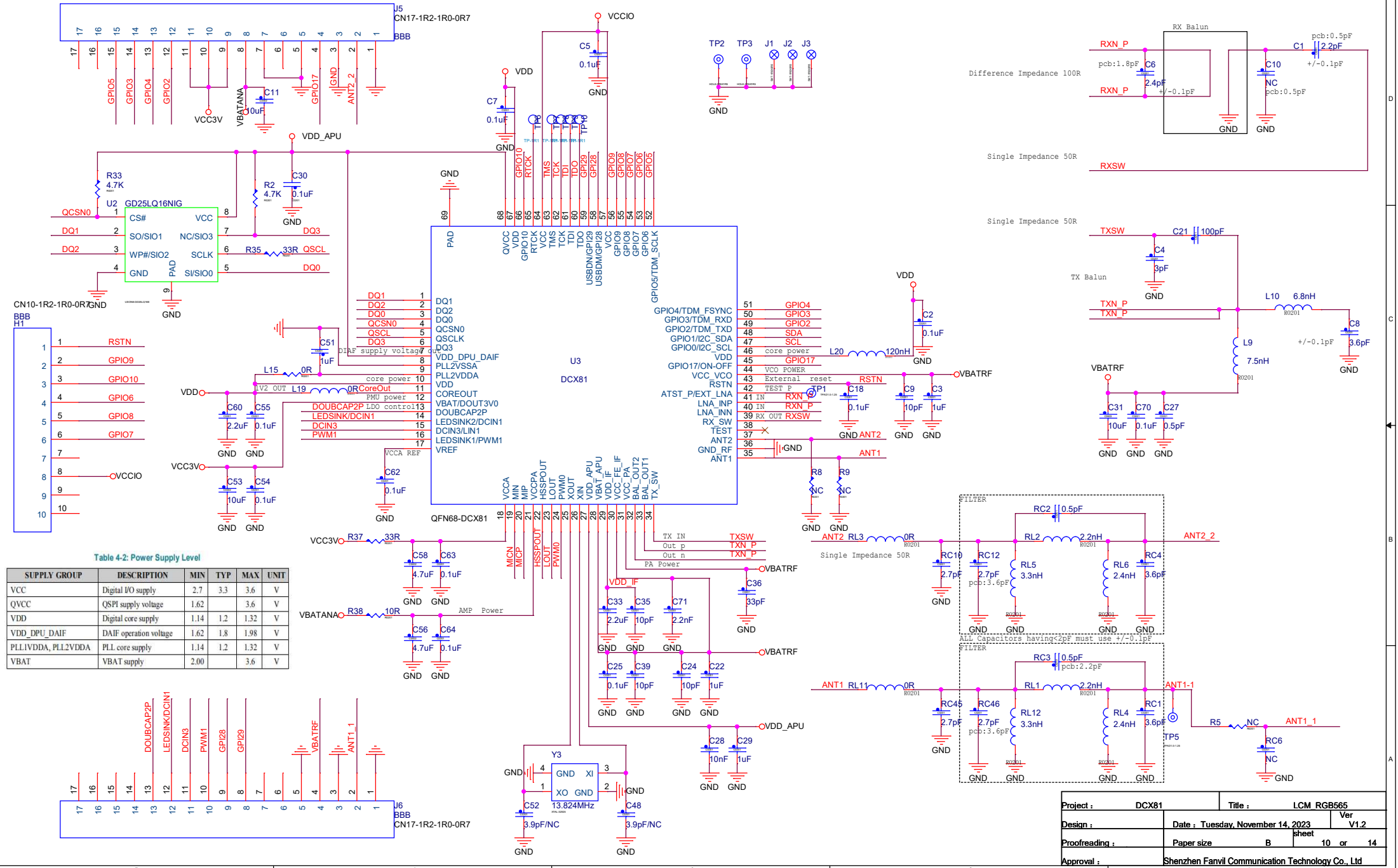


Table 4-2: Power Supply Level				
SUPPLY GROUP	DESCRIPTION	MIN	TYP	MAX
VCC	Digital I/O supply	2.7	3.3	3.6
QVCC	QSPI supply voltage	1.62		3.6
VDD	Digital core supply	1.14	1.2	1.32
VDD_DPU_DAIF	DAIF operation voltage	1.62	1.8	1.98
PLL1VDDA, PLL2VDDA	PLL core supply	1.14	1.2	1.32
VBAT	VBAT supply	2.00		3.6

Project :	DCX81	Title :	LCM RGB565
Design :	Date : Tuesday, November 14, 2023	Ver	V1.2
Proofreading :	Paper size	B	10 or 14
Approval :	Shenzhen Fanvil Communication Technology Co., Ltd		