

Appendix for 2A_n71A

Product Name: 5G CPE
Model No: C150

Appendix A: Average Power Output for NSA

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	21.59	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Inner_Full	22.43	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.93	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	20.89	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	21.45	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Inner_Full	21.93	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.83	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	20.71	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	21.97	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Inner_Full	23.52	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	22.12	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	22.07	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	22.00	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Inner_Full	22.97	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.85	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	22.51	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	22.06	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Inner_Full	23.50	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	22.16	PC3	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.86	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	22.09	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Inner_Full	23.12	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	22.19	PC3	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.74	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	21.30	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Inner_Full	22.01	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	22.00	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.47	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	21.19	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Inner_Full	21.50	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	22.22	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.21	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	22.05	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Inner_Full	23.61	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	22.00	PC3	PASS

DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	22.39	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	22.09	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Inner_Full	23.05	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.80	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	22.50	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	22.20	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Inner_Full	23.69	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.83	PC3	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	22.09	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	22.14	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Inner_Full	23.13	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.90	PC3	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	22.54	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	21.98	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Inner_Full	22.28	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	22.06	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.95	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	21.89	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Inner_Full	21.79	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	21.88	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	22.27	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	22.18	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Inner_Full	23.67	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.96	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.73	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	22.19	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Inner_Full	23.08	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.85	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.77	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	22.15	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Inner_Full	23.26	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	22.25	PC3	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.94	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	22.26	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Inner_Full	22.73	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.86	PC3	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	22.03	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	21.98	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Inner_Full	23.70	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	22.00	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	22.46	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	21.91	PC3	PASS

DC_2A_n71A	15	5+20	CP-16QAM	M+L	Inner_Full	22.69	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	22.04	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	22.42	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	22.07	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Inner_Full	23.64	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.81	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.81	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	22.19	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Inner_Full	23.16	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	22.15	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.42	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	22.15	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Inner_Full	23.12	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.99	PC3	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	22.21	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	22.12	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Inner_Full	22.65	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	22.65	PC3	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.92	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

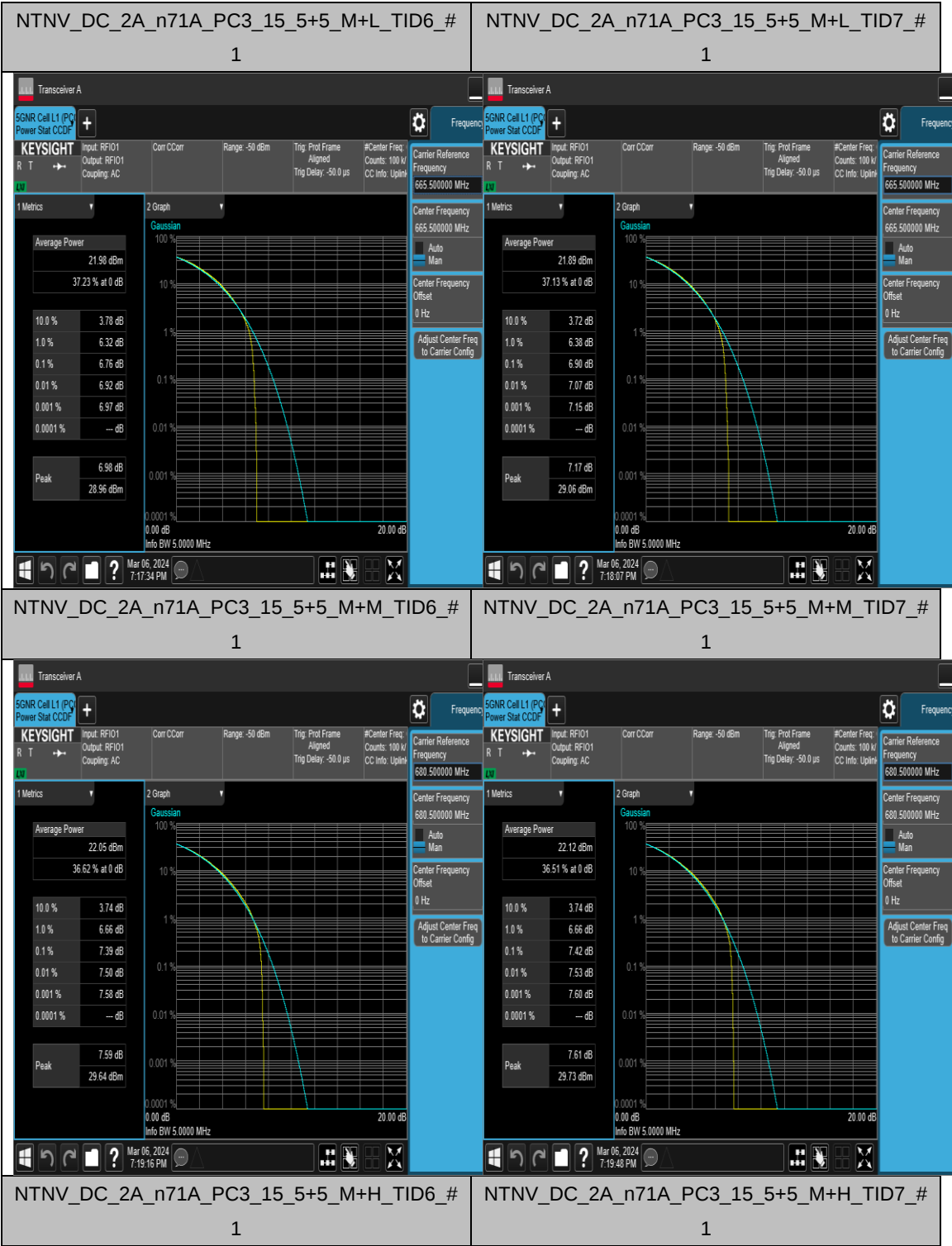
Peak-to-Average Ratio(CCDF)

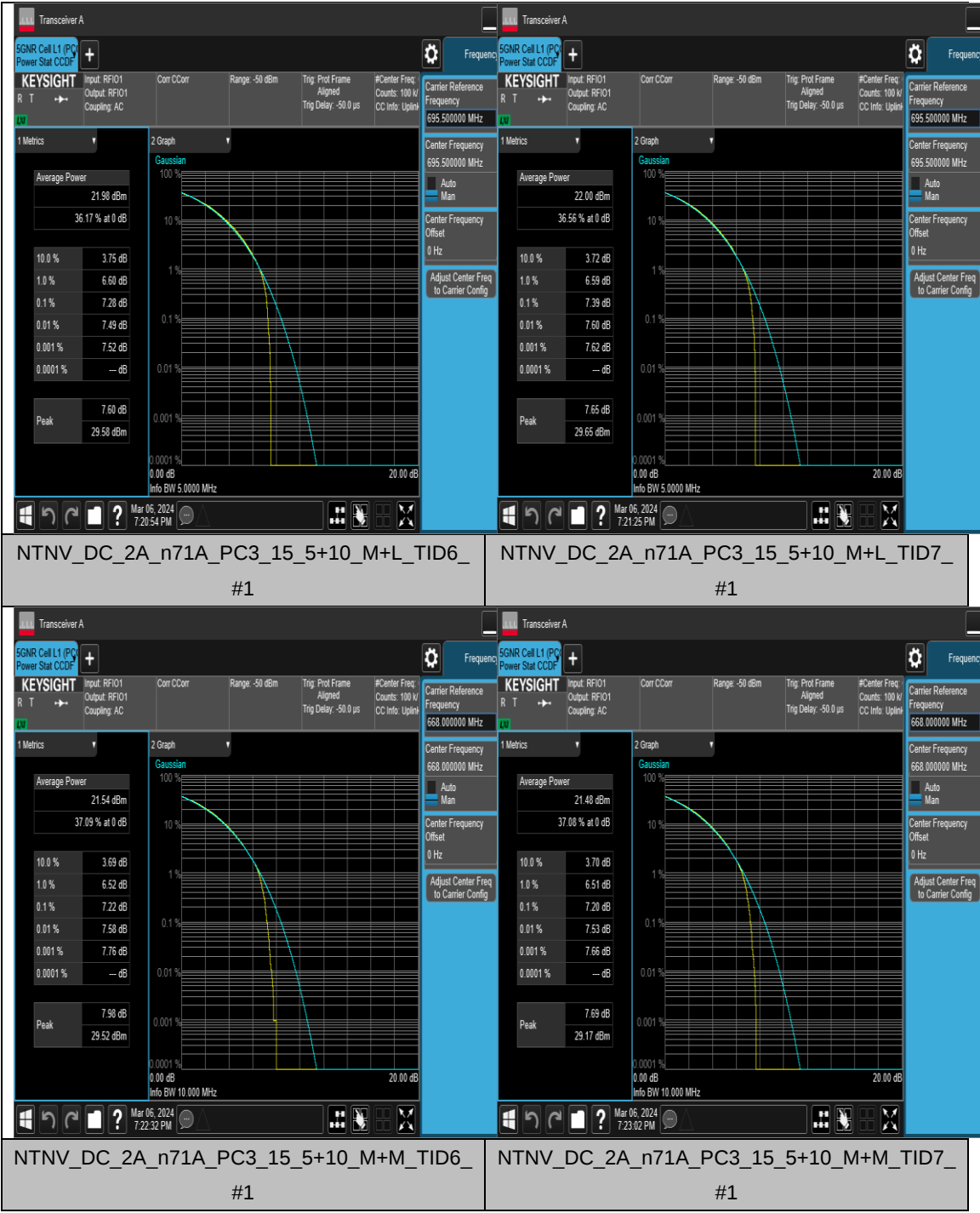
Test Result

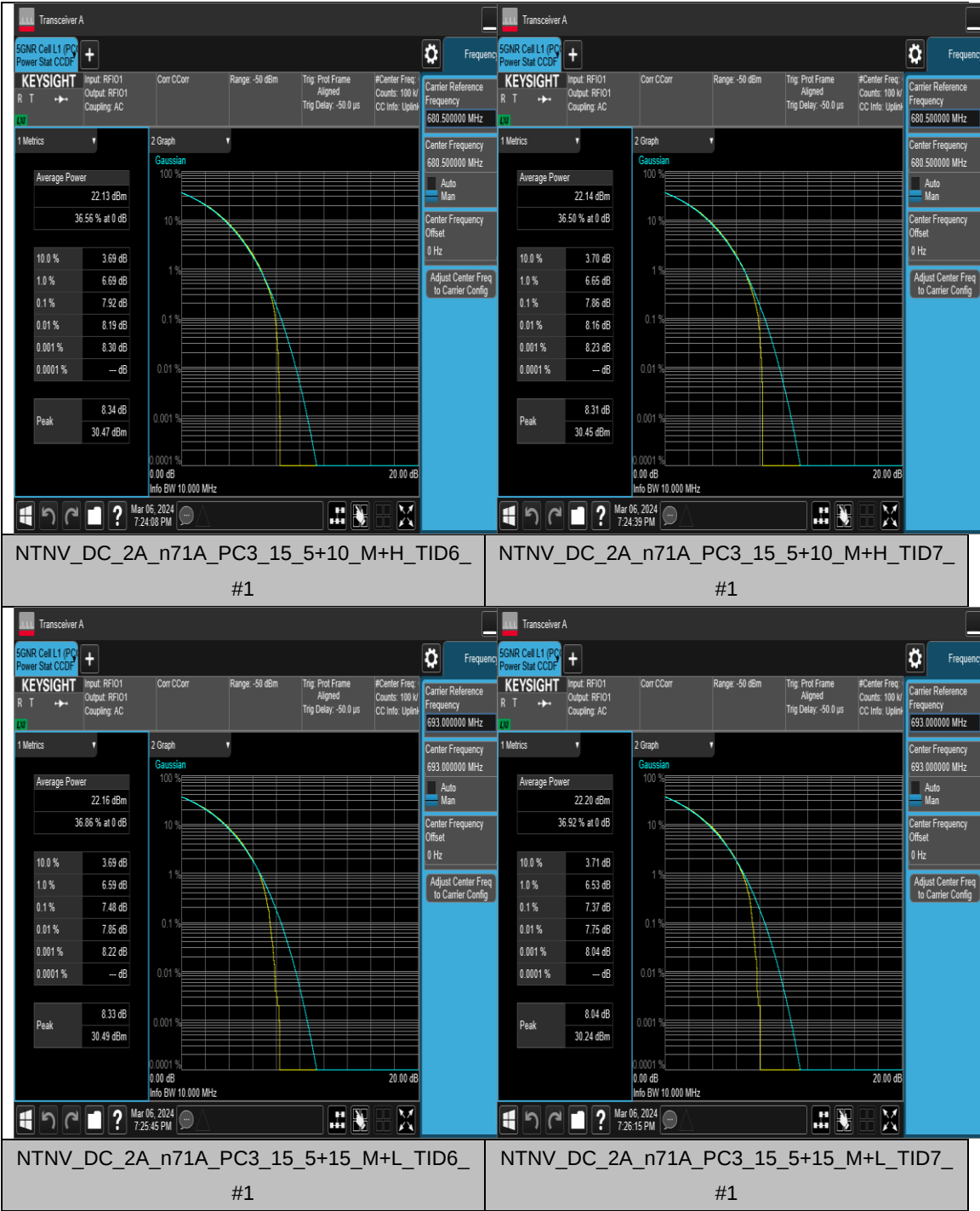
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 1A	15	5+5	CP-QPSK	M+L	Outer_Full	6.76	≤13	PASS
DC_2A_n7 1A	15	5+5	CP-16QAM	M+L	Outer_Full	6.90	≤13	PASS
DC_2A_n7 1A	15	5+5	CP-QPSK	M+M	Outer_Full	7.39	≤13	PASS
DC_2A_n7 1A	15	5+5	CP-16QAM	M+M	Outer_Full	7.42	≤13	PASS
DC_2A_n7 1A	15	5+5	CP-QPSK	M+H	Outer_Full	7.28	≤13	PASS
DC_2A_n7 1A	15	5+5	CP-16QAM	M+H	Outer_Full	7.39	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-QPSK	M+L	Outer_Full	7.22	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-16QAM	M+L	Outer_Full	7.20	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-QPSK	M+M	Outer_Full	7.92	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-16QAM	M+M	Outer_Full	7.86	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-QPSK	M+H	Outer_Full	7.48	≤13	PASS
DC_2A_n7 1A	15	5+10	CP-16QAM	M+H	Outer_Full	7.37	≤13	PASS
DC_2A_n7 1A	15	5+15	CP-QPSK	M+L	Outer_Full	7.21	≤13	PASS
DC_2A_n7 1A	15	5+15	CP-16QAM	M+L	Outer_Full	7.24	≤13	PASS
DC_2A_n7 1A	15	5+15	CP-QPSK	M+M	Outer_Full	7.33	≤13	PASS
DC_2A_n7 1A	15	5+15	CP-16QAM	M+M	Outer_Full	7.38	≤13	PASS
DC_2A_n7 1A	15	5+15	CP-QPSK	M+H	Outer_Full	7.73	≤13	PASS

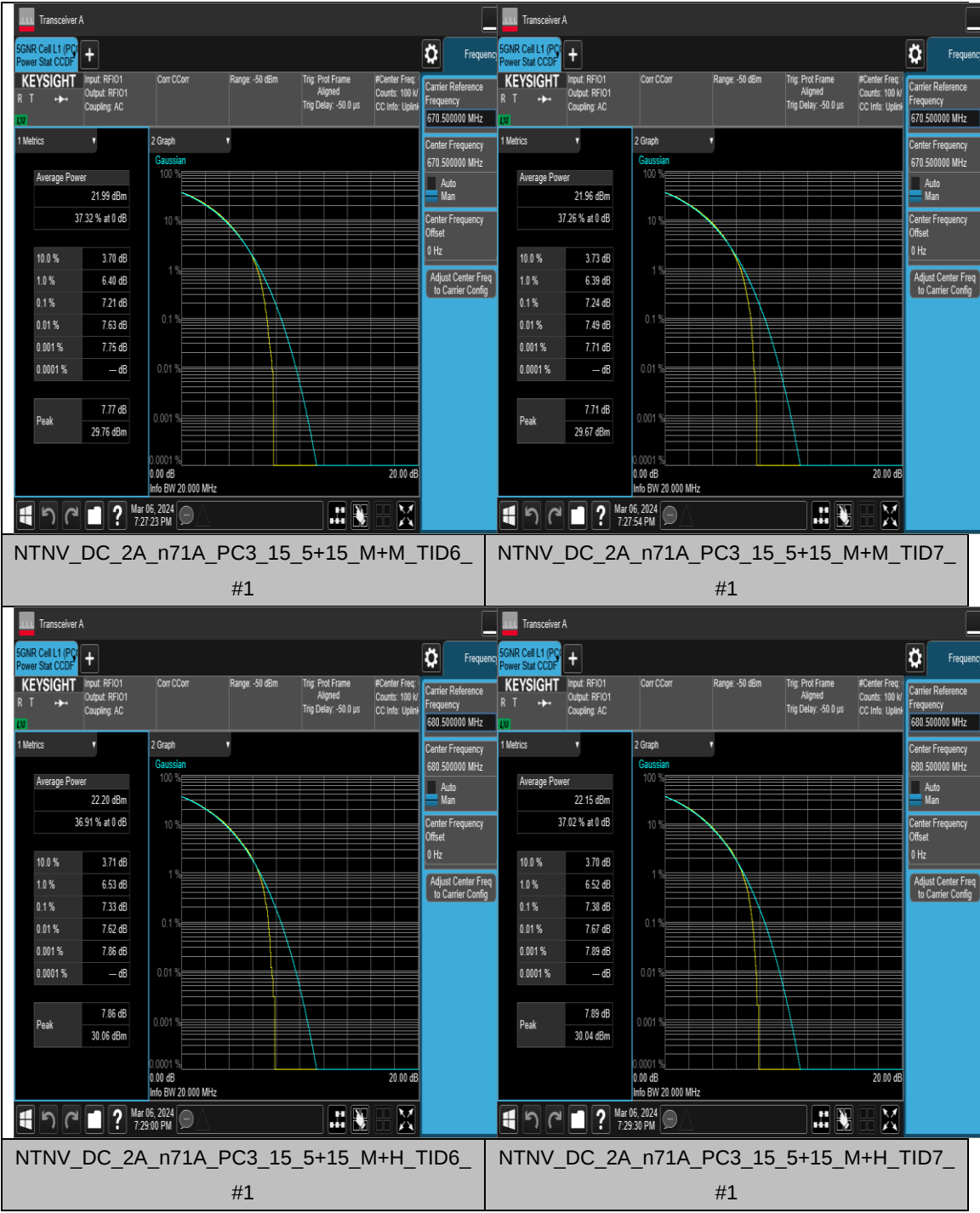
DC_2A_n7 1A	15	5+15	CP-16QAM	M+H	Outer_Full	7.66	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-QPSK	M+L	Outer_Full	7.47	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-16QAM	M+L	Outer_Full	7.39	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-QPSK	M+M	Outer_Full	7.52	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-16QAM	M+M	Outer_Full	7.54	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-QPSK	M+H	Outer_Full	7.67	≤13	PASS
DC_2A_n7 1A	15	5+20	CP-16QAM	M+H	Outer_Full	7.62	≤13	PASS

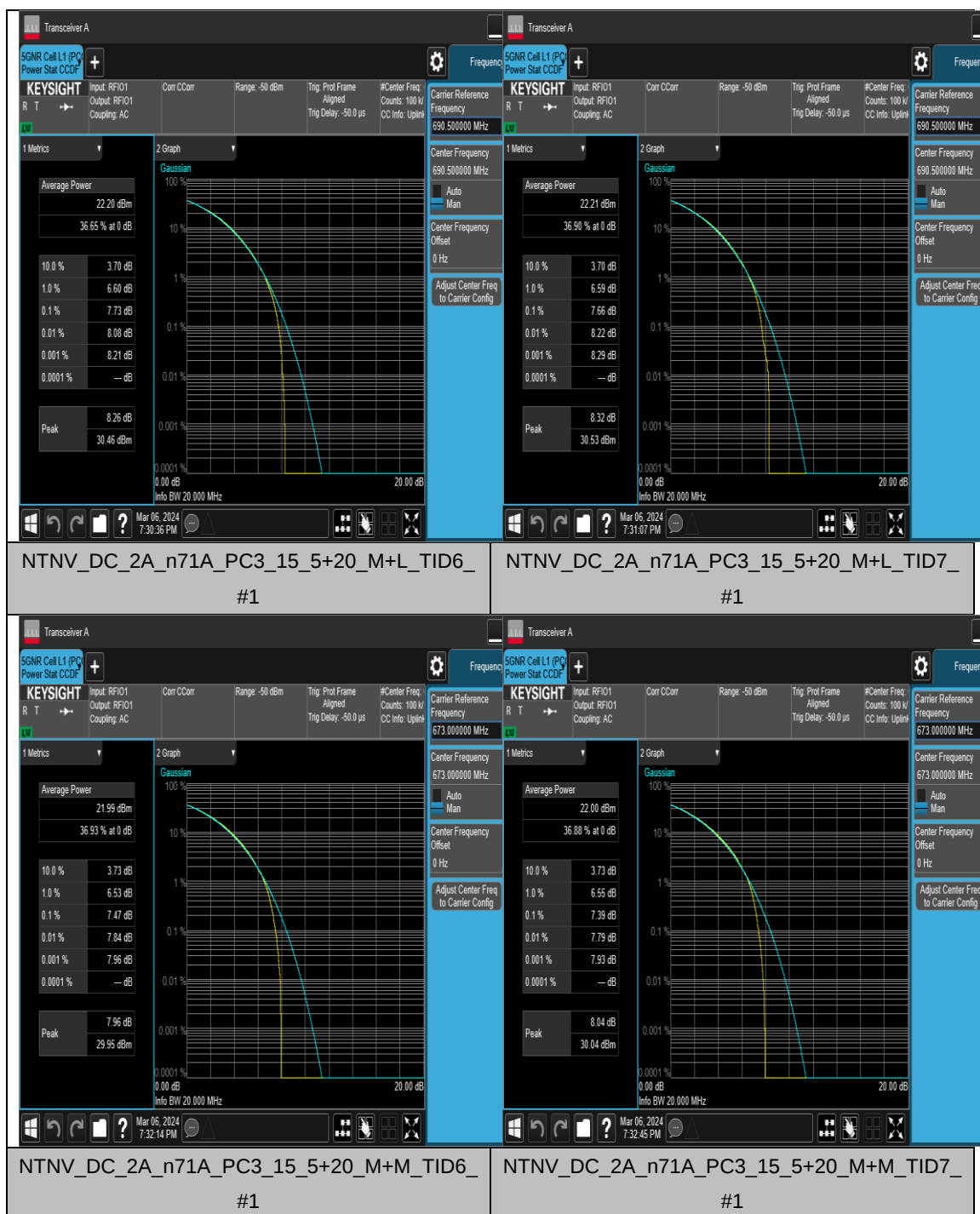
Test Graphs

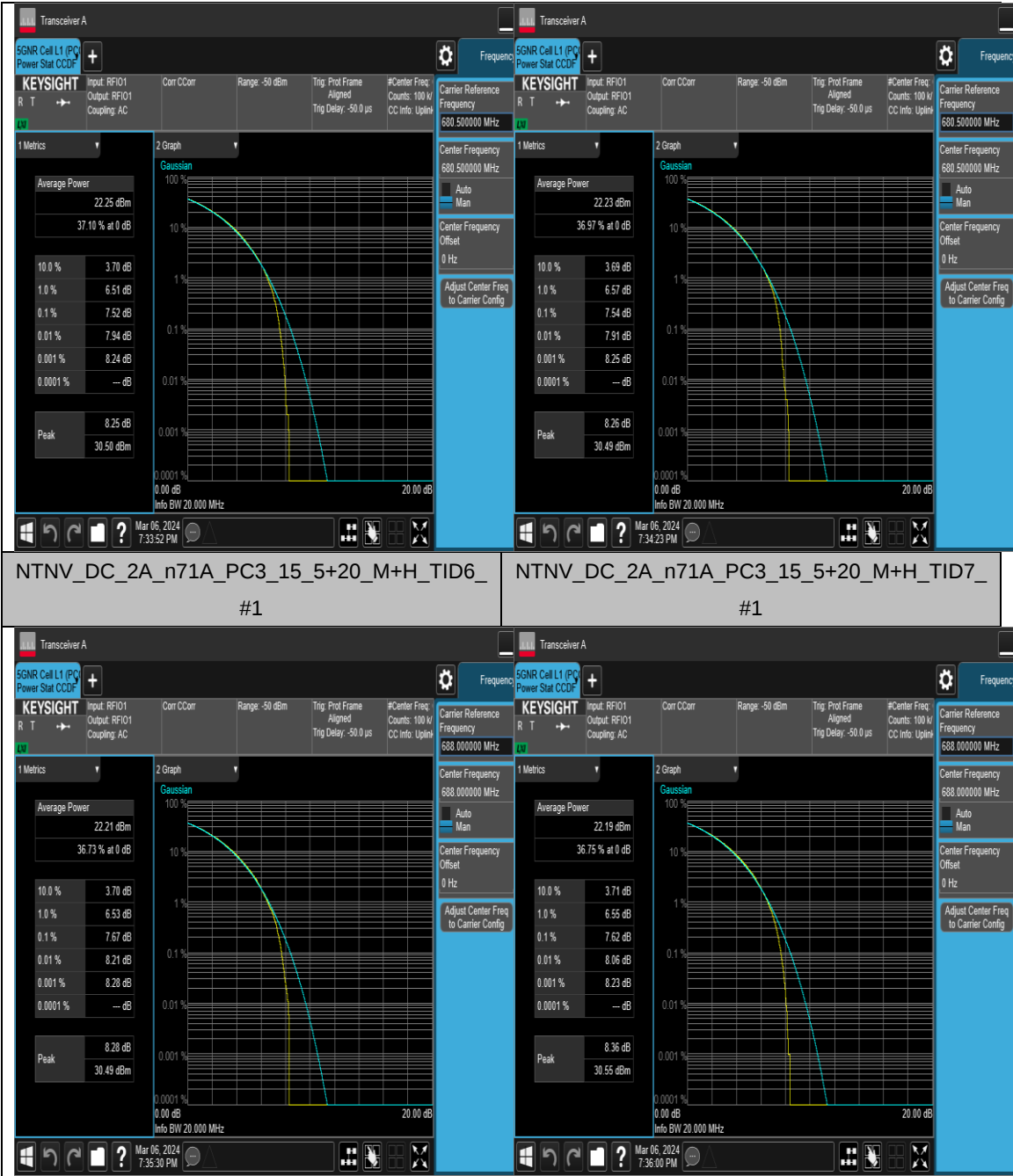












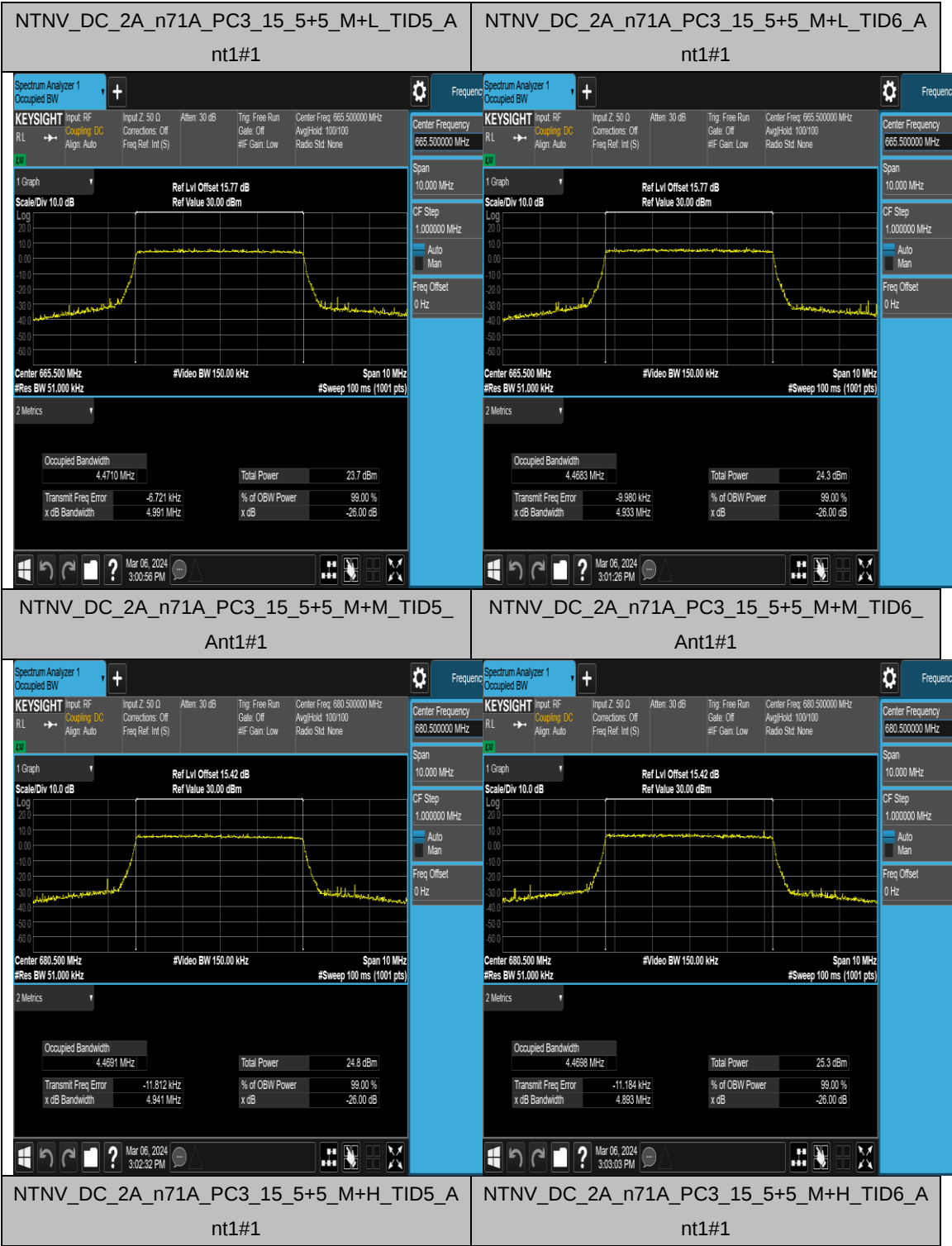
Appendix C: 26dB Bandwidth and Occupied Bandwidth for

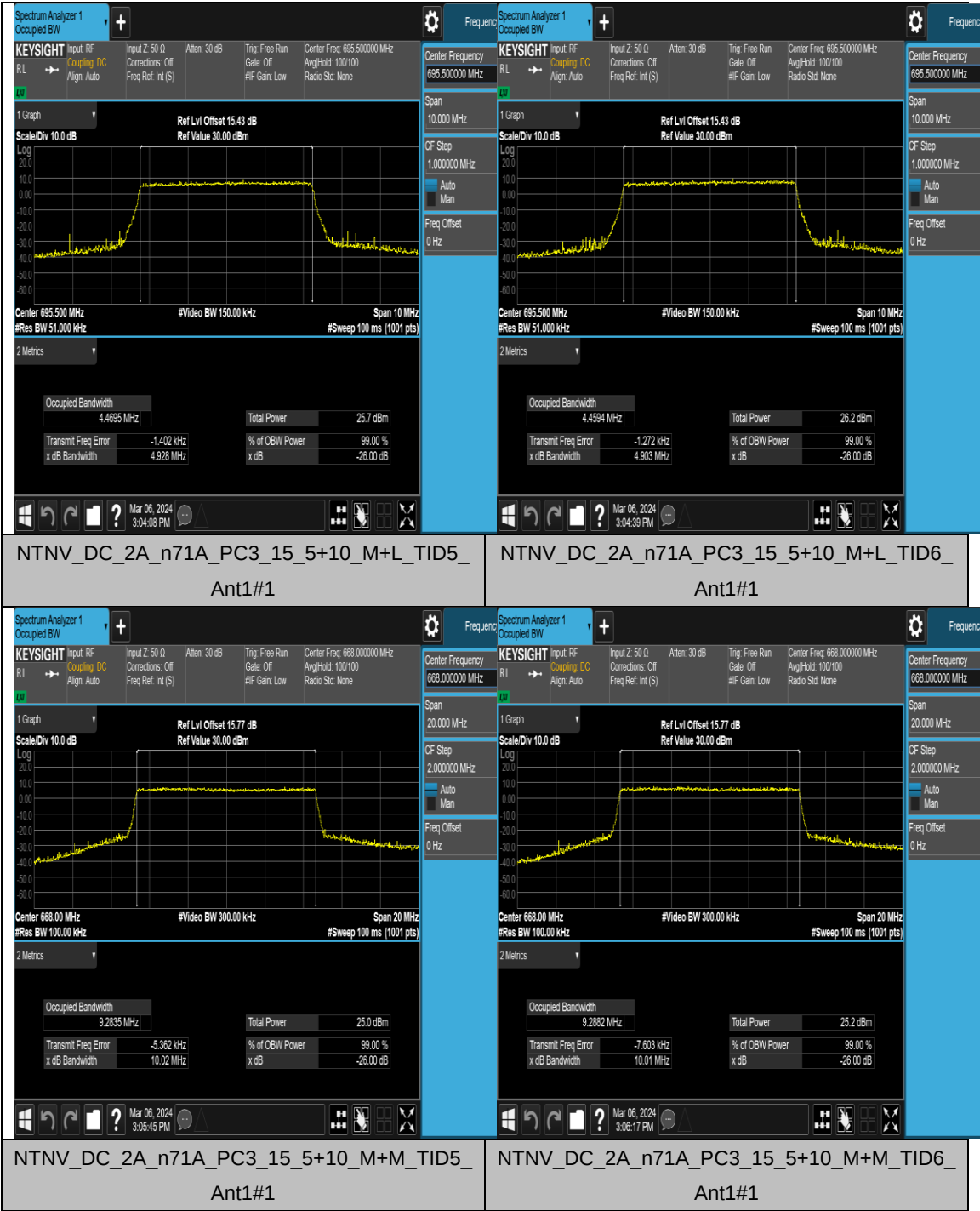
NSA

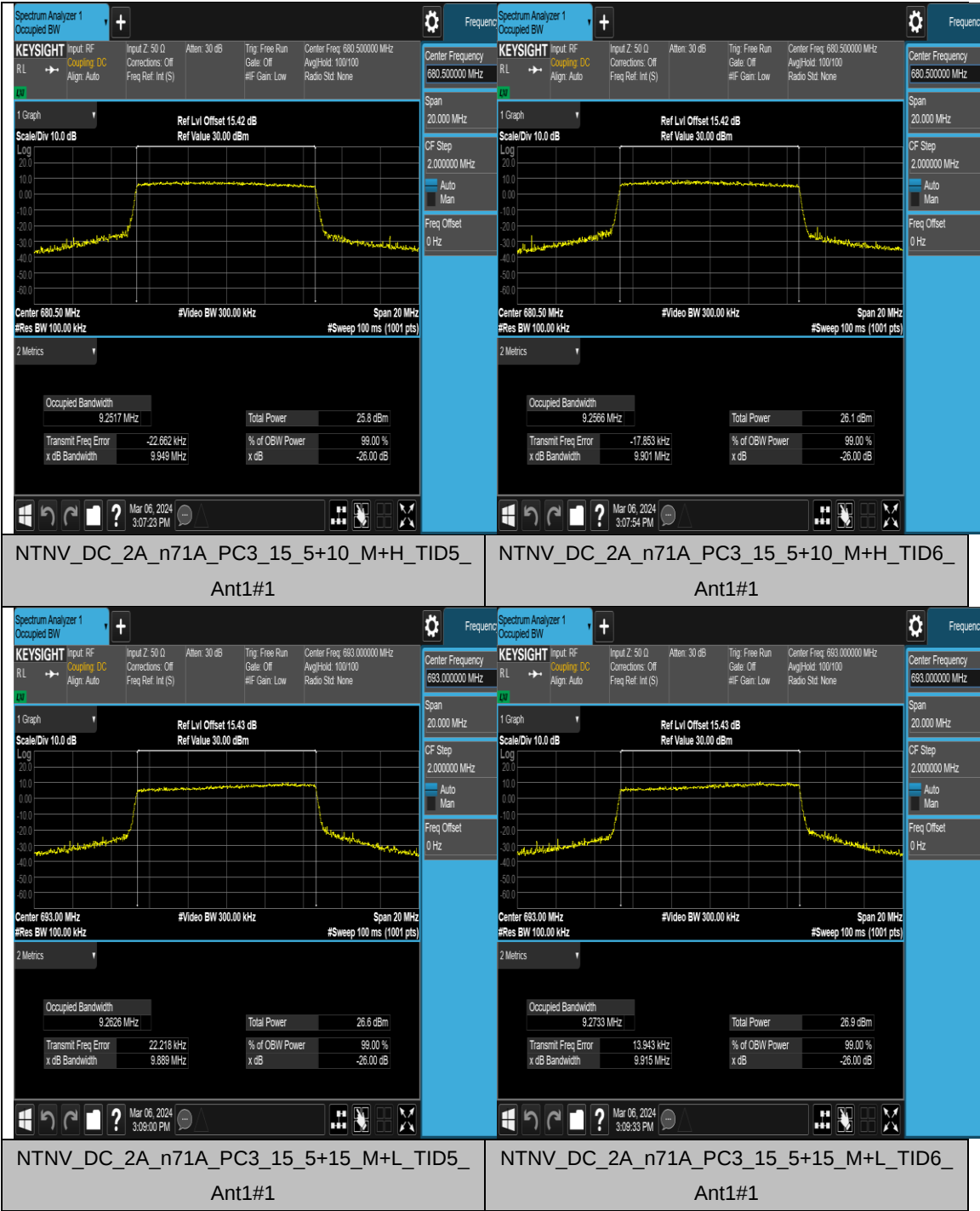
Test Result

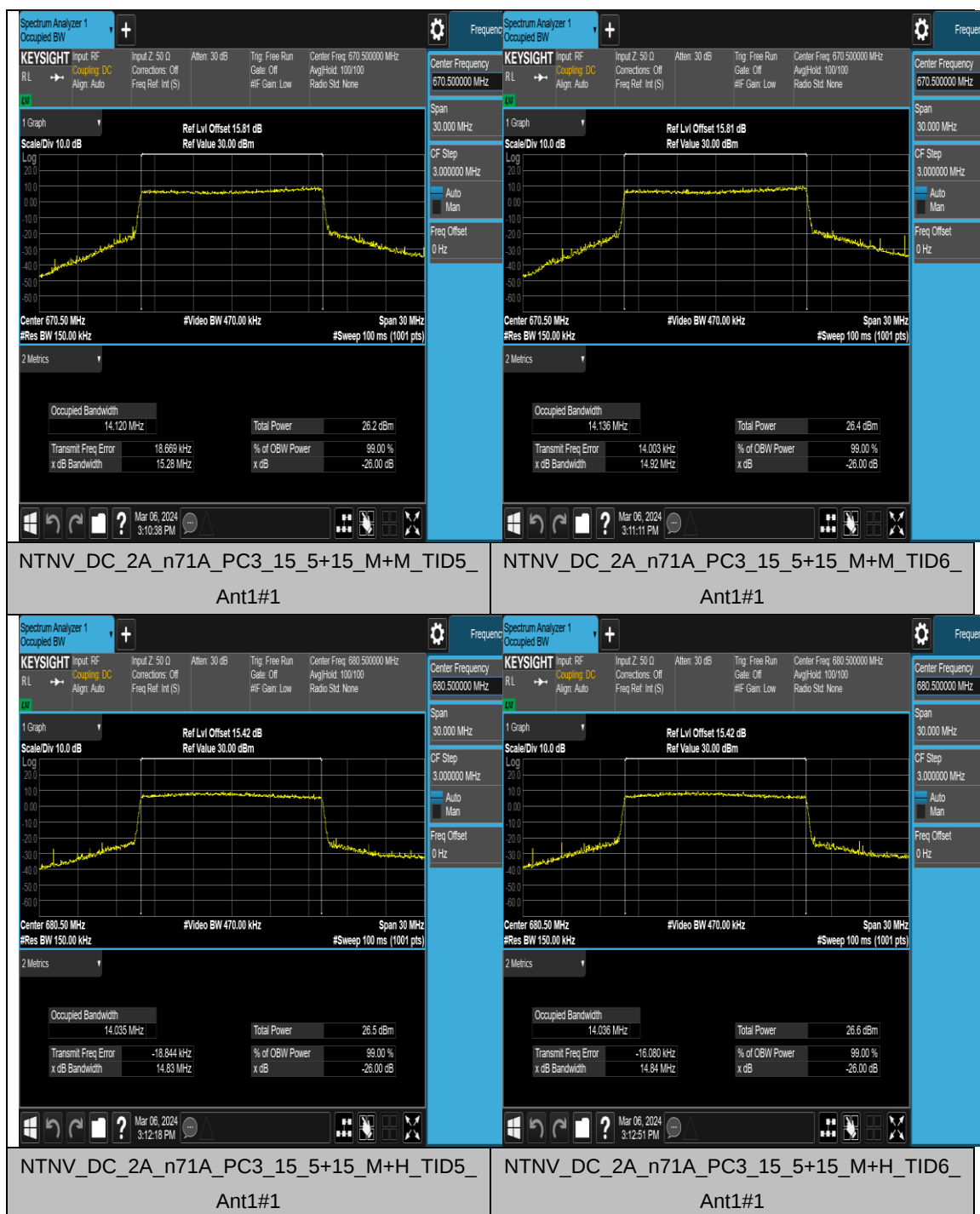
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4710	4.991	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4683	4.933	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4691	4.941	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4698	4.893	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4695	4.928	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4594	4.903	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2835	10.02	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2882	10.01	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2517	9.949	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2566	9.901	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2626	9.889	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2733	9.915	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	14.120	15.28	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	14.136	14.92	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	14.035	14.83	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	14.036	14.84	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	14.132	14.91	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	14.128	14.85	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	18.884	19.77	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	18.863	19.74	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	18.807	19.78	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	18.807	19.72	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	18.948	19.91	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	18.965	19.89	PASS

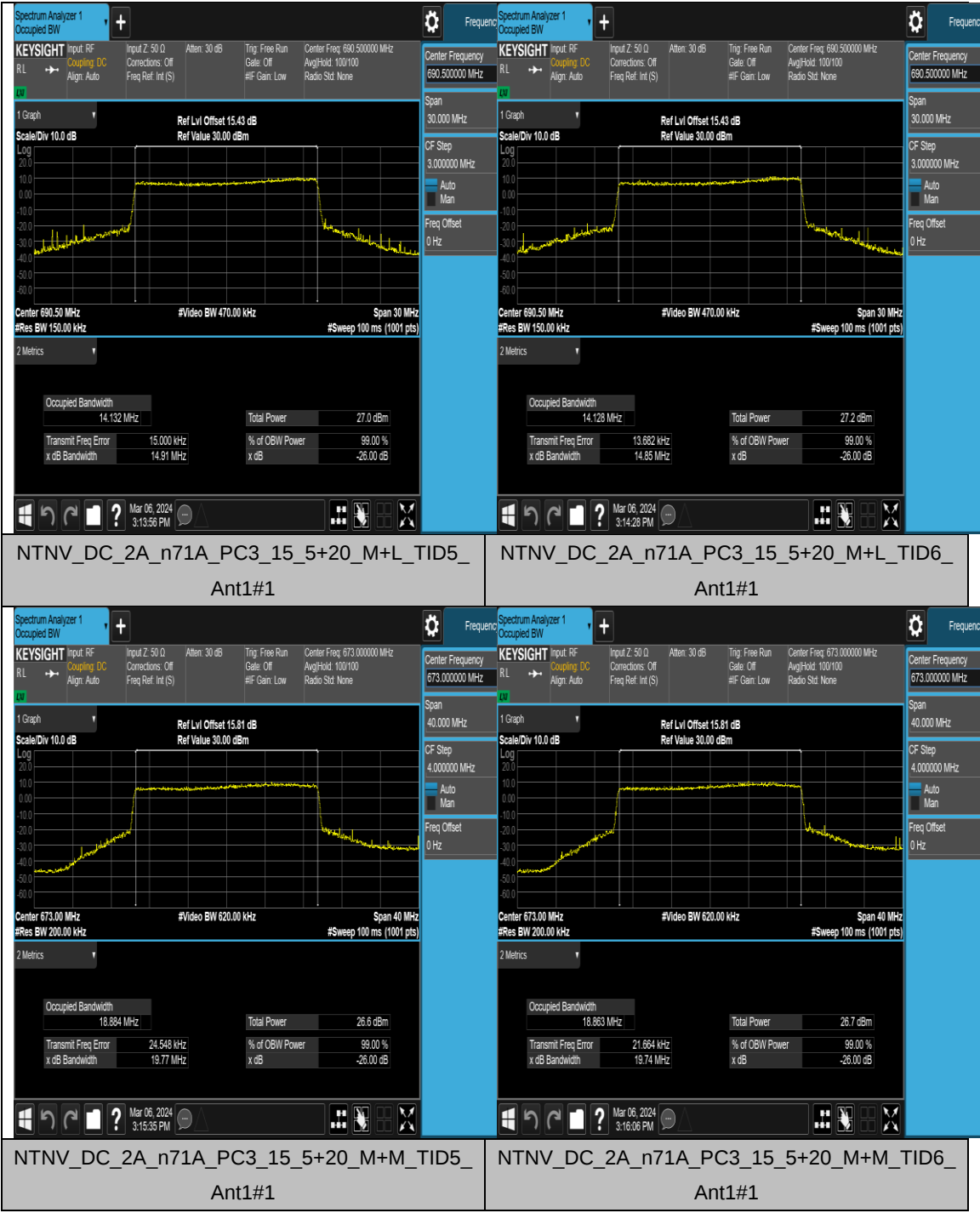
Test Graphs

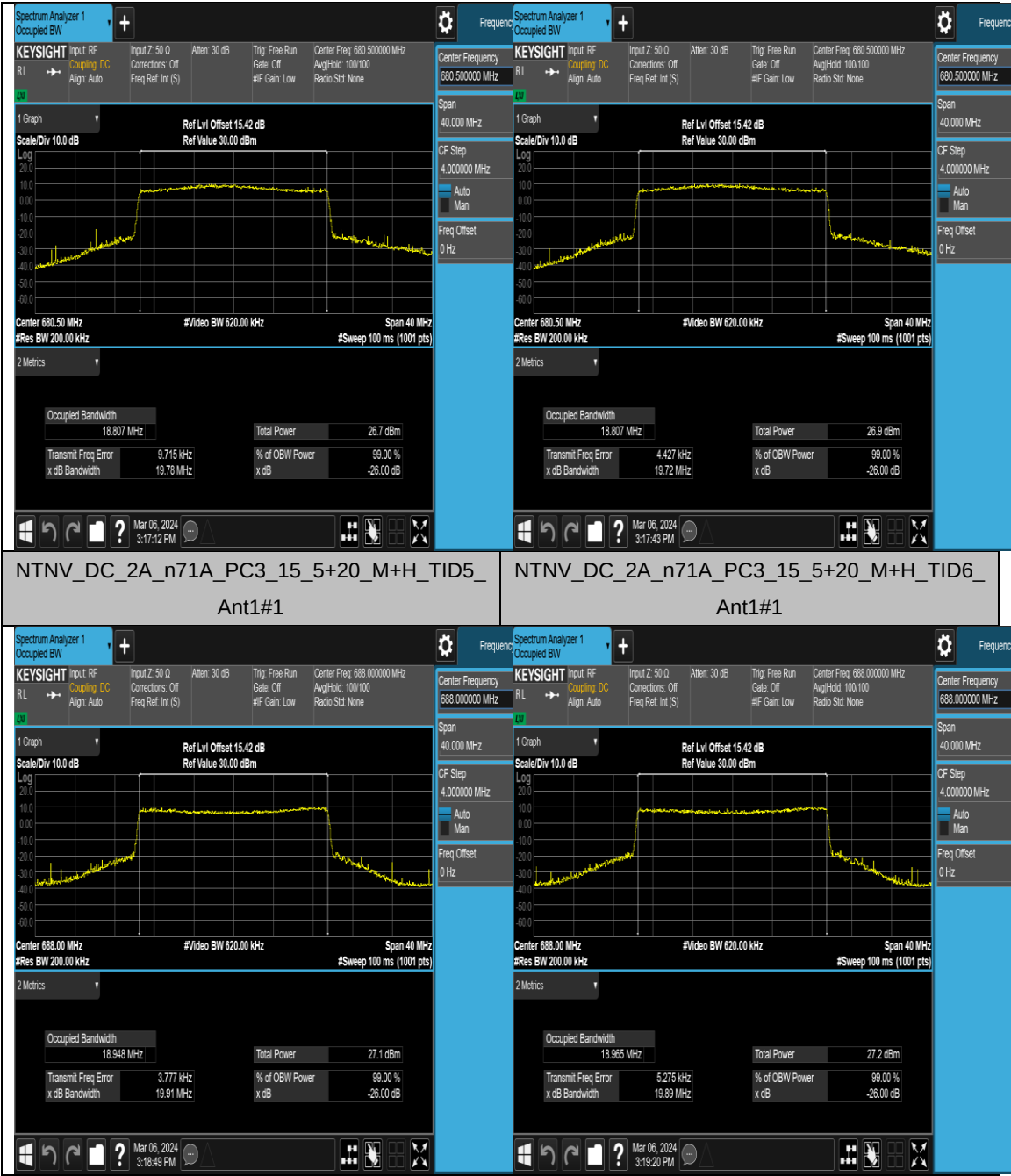












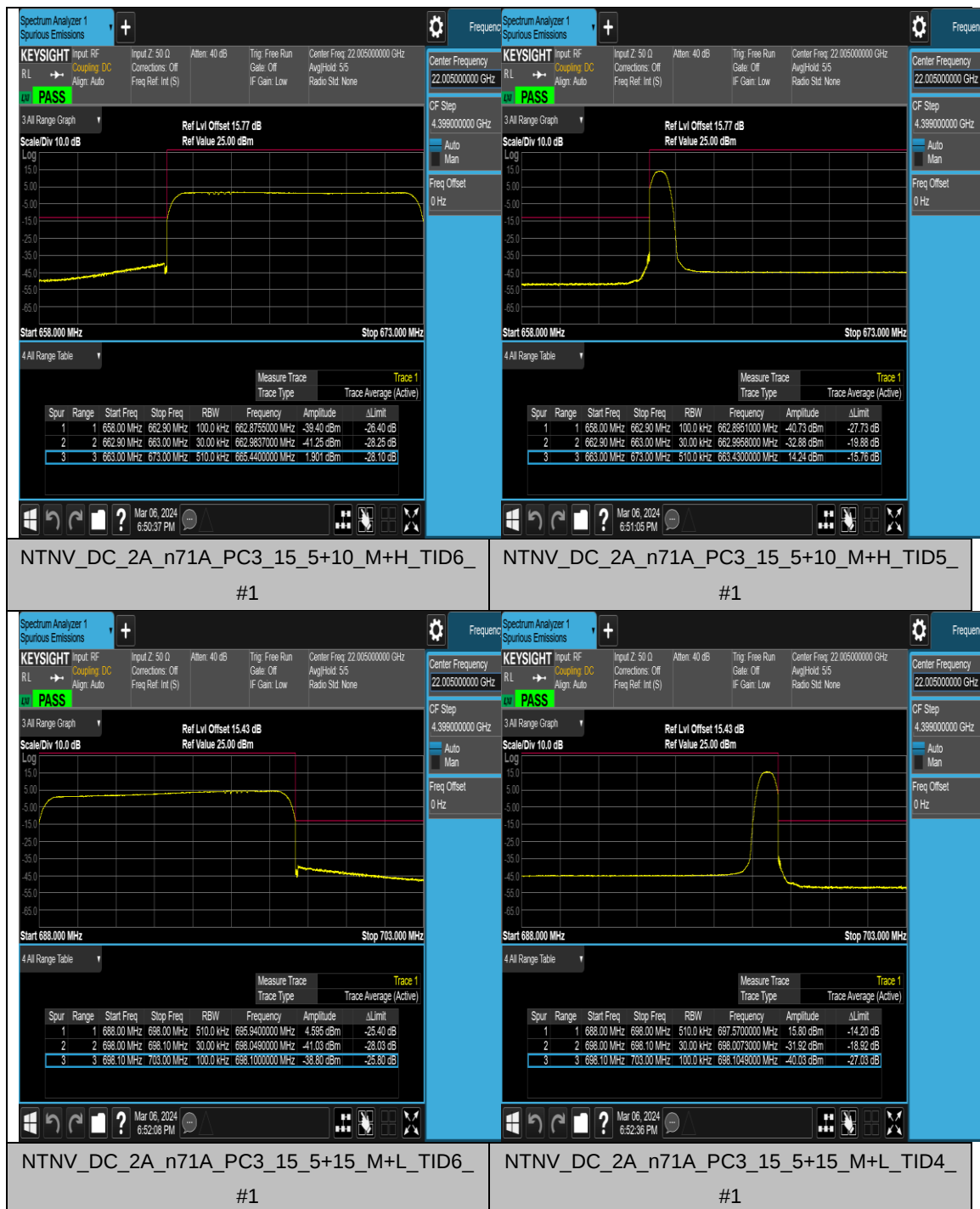
Appendix D: Band Edge for NSA

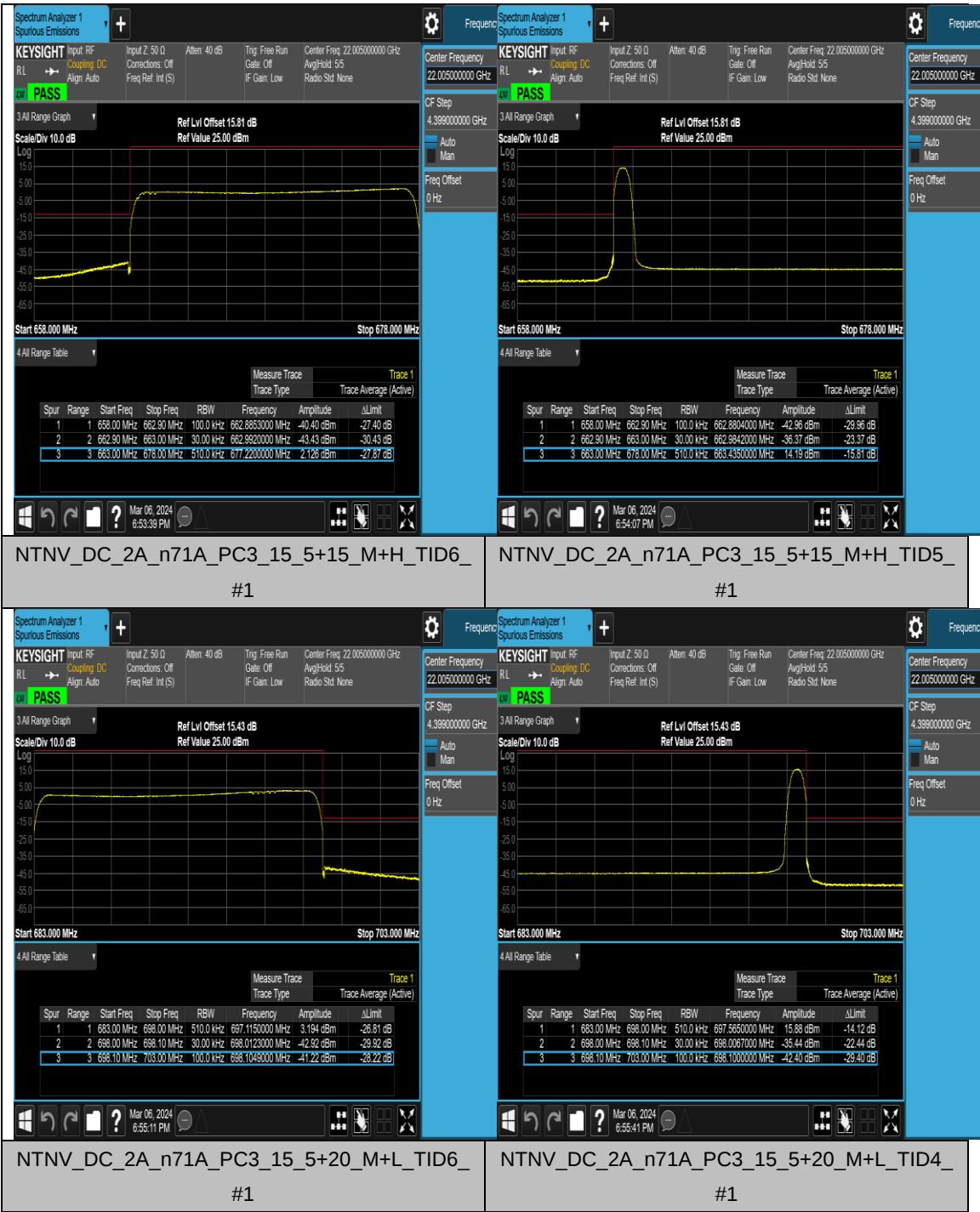
Test Result

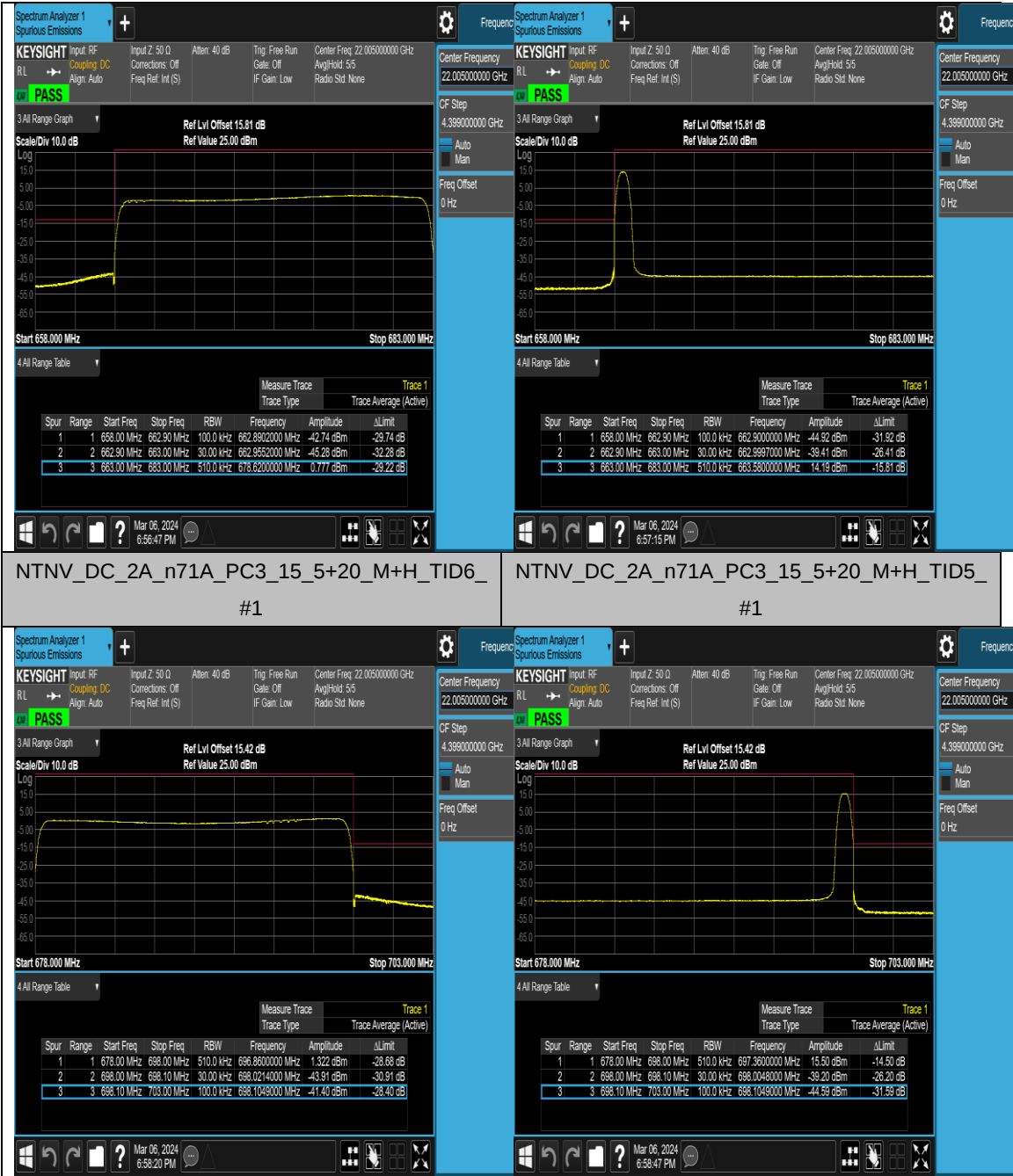
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

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DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-33	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-23	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_2A_n71A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS