Appendix for 71A_n66A

Product Name: 5G CPE
Model No: C150

Appendix A: Average Power Output for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	21.44	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Inner_Full	22.87	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	21.60	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	21.44	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	21.52	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Inner_Full	22.42	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	21.41	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	21.55	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	21.21	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Inner_Full	22.74	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	21.23	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	21.32	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	21.19	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Inner_Full	22.29	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	21.34	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	21.17	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	21.37	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Inner_Full	22.88	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	21.30	PC3	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	21.46	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	21.43	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Inner_Full	22.54	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	21.03	PC3	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	21.47	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	21.44	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Inner_Full	23.05	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	21.32	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	21.47	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	21.40	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Inner_Full	22.50	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	21.36	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	21.47	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	21.31	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Inner_Full	22.71	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	21.30	PC3	PASS

DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	21.53	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	21.31	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Inner_Full	22.36	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	21.36	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	21.21	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	21.43	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Inner_Full	22.92	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	21.36	PC3	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	21.48	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	21.48	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Inner_Full	22.54	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	21.41	PC3	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	21.28	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	21.49	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Inner_Full	22.92	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	21.41	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	21.31	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	21.44	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Inner_Full	22.35	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	21.51	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	21.25	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	21.32	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Inner_Full	22.89	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	21.38	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	21.46	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	21.42	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Inner_Full	22.28	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	21.20	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	21.30	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	21.46	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Inner_Full	23.07	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	21.47	PC3	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	21.52	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	21.42	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Inner_Full	22.45	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	21.24	PC3	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	21.15	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	21.34	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Inner_Full	22.79	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	21.29	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	21.20	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	21.30	PC3	PASS

DC_71A_n66A	15	5+20	CP-16QAM	M+L	Inner_Full	22.33	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	21.25	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	21.49	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	21.29	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Inner_Full	22.70	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	21.39	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	21.39	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	21.40	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Inner_Full	22.31	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	21.44	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	21.57	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	21.39	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Inner_Full	23.10	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	21.56	PC3	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	21.47	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	21.36	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Inner_Full	22.52	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	21.60	PC3	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	21.79	PC3	PASS

Appendix B: Peak-to-Average Ratio for NSA

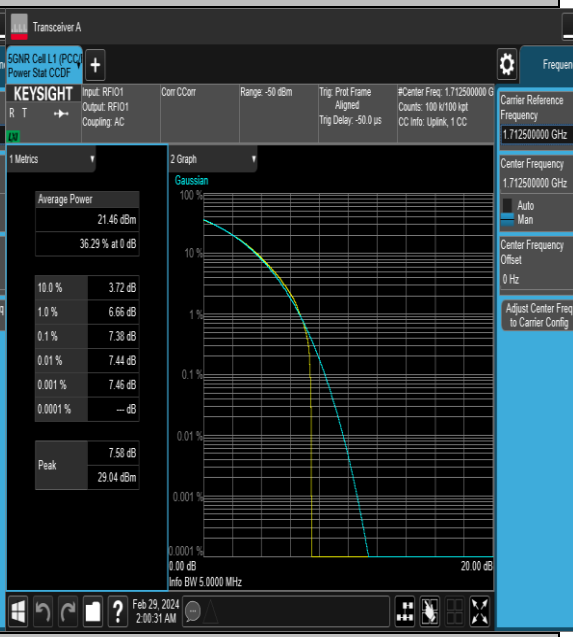
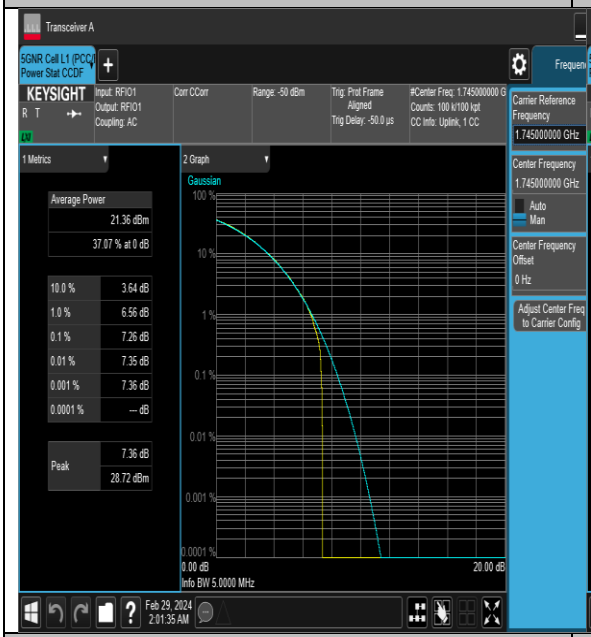
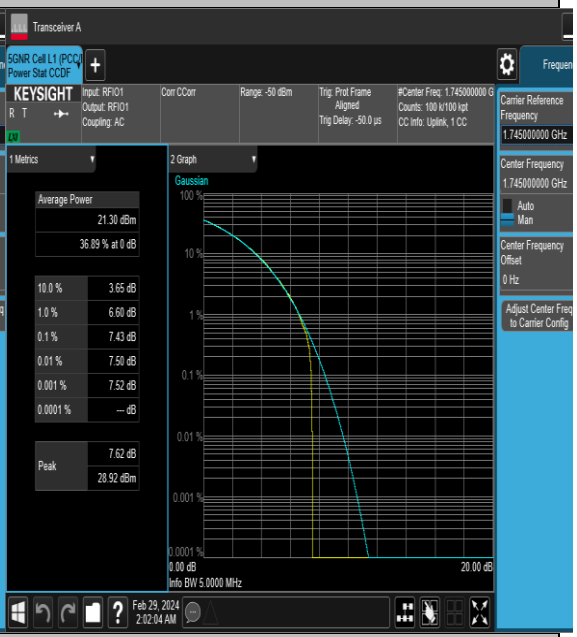
Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	7.26	≤13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	7.38	≤13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	7.26	≤13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	7.43	≤13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	6.67	≤13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	6.75	≤13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	7.60	≤13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	7.61	≤13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	7.32	≤13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	7.37	≤13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	7.30	≤13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	7.23	≤13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	7.44	≤13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	7.39	≤13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	7.19	≤13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	7.19	≤13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	7.27	≤13	PASS

DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	7.25	≤13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	7.28	≤13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	7.23	≤13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	7.04	≤13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	6.99	≤13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	7.25	≤13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	7.21	≤13	PASS

Test Graphs

NTNV_DC_71A_n66A_PC3_15_5+5_M+L_TID6_#1	NTNV_DC_71A_n66A_PC3_15_5+5_M+L_TID7_#1
	
NTNV_DC_71A_n66A_PC3_15_5+5_M+M_TID6_#1	NTNV_DC_71A_n66A_PC3_15_5+5_M+M_TID7_#1
	
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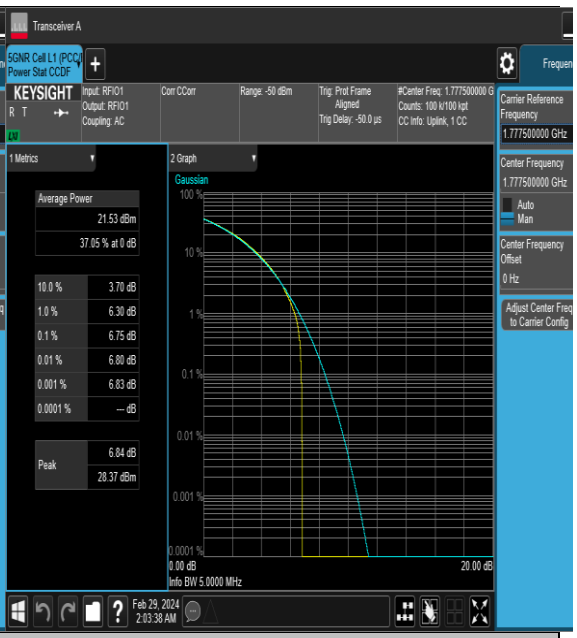
NTNV_DC_71A_n66A_PC3_15_5+10_M+L_TID6_#1



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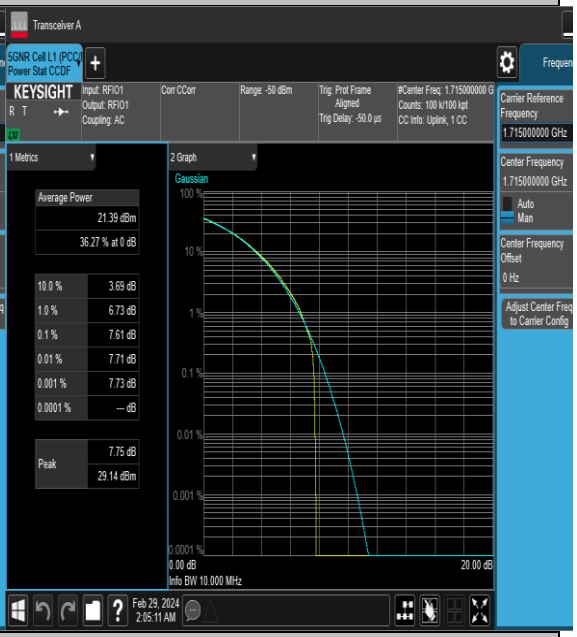

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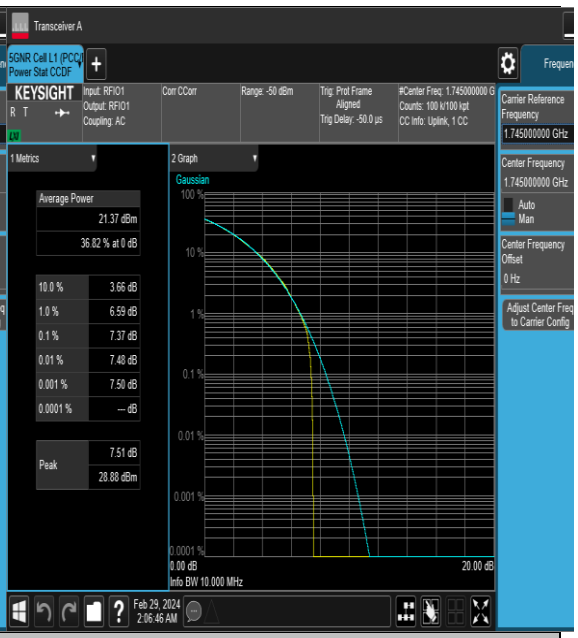

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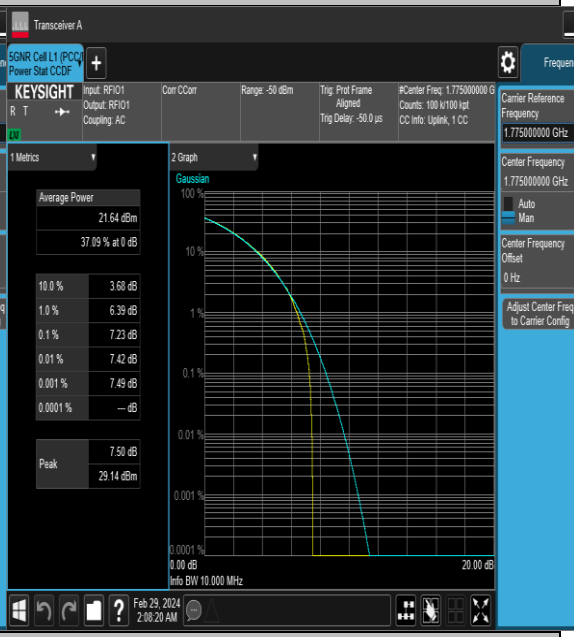
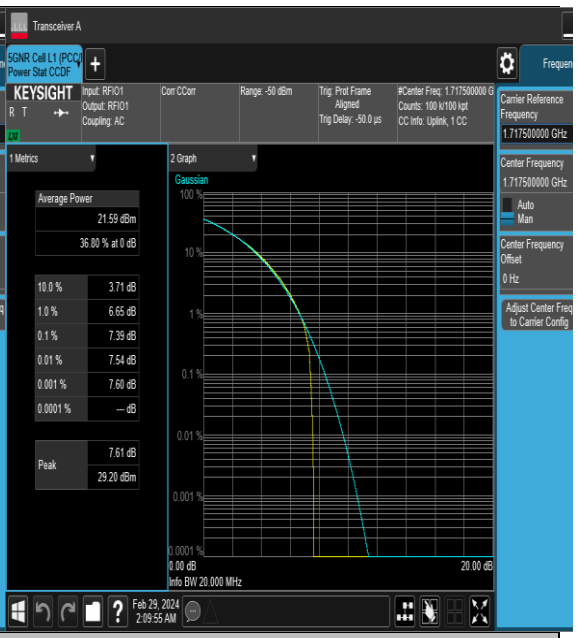
NTNV_DC_71A_n66A_PC3_15_5+15_M+L_TID6_#1



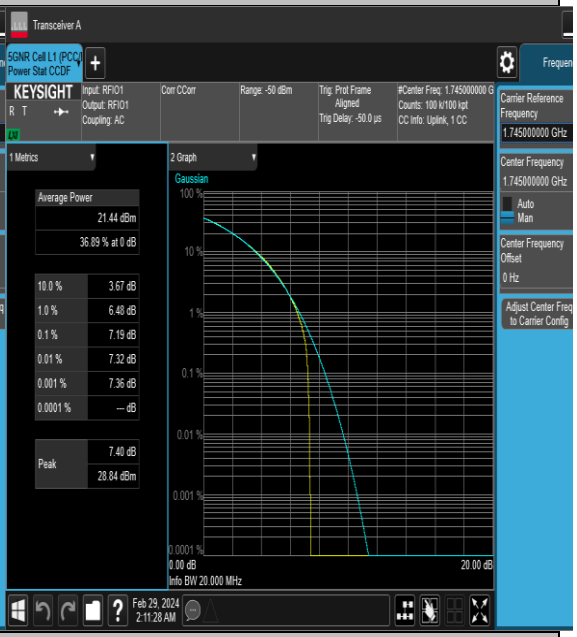
NTNV_DC_71A_n66A_PC3_15_5+15_M+L_TID7_#1

NTNV_DC_71A_n66A_PC3_15_5+15_M+M_TID6_#1



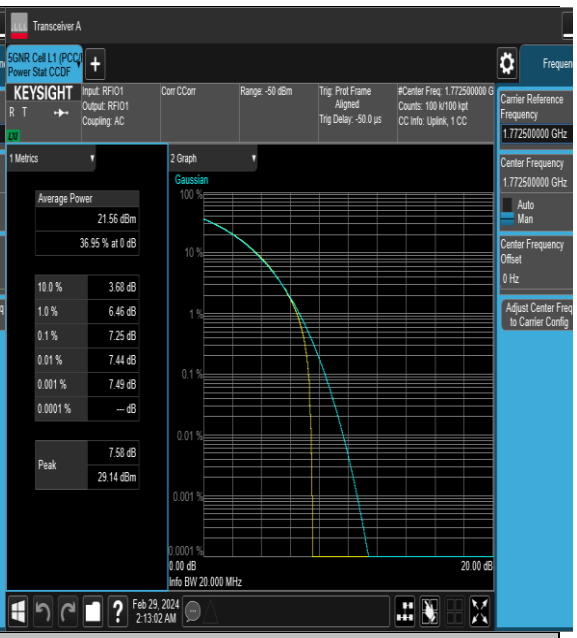
NTNV_DC_71A_n66A_PC3_15_5+15_M+M_TID7_#1

NTNV_DC_71A_n66A_PC3_15_5+15_M+H_TID6_#1



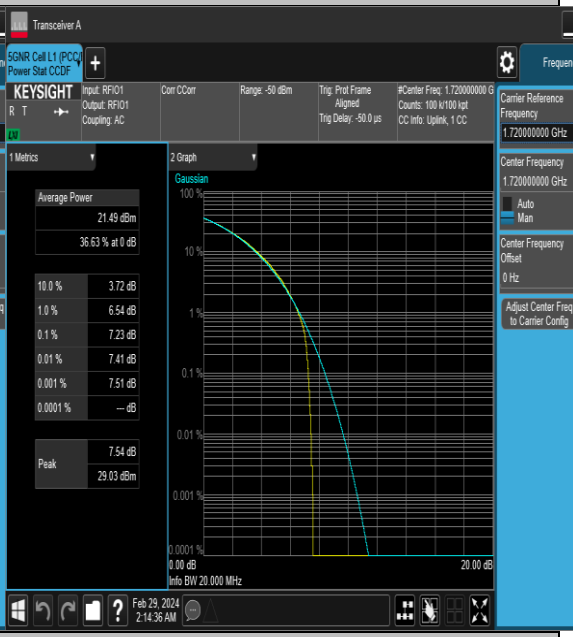
NTNV_DC_71A_n66A_PC3_15_5+15_M+H_TID7_#1

NTNV_DC_71A_n66A_PC3_15_5+20_M+L_TID6_#1



NTNV_DC_71A_n66A_PC3_15_5+20_M+L_TID7_#1

NTNV_DC_71A_n66A_PC3_15_5+20_M+M_TID6_#1



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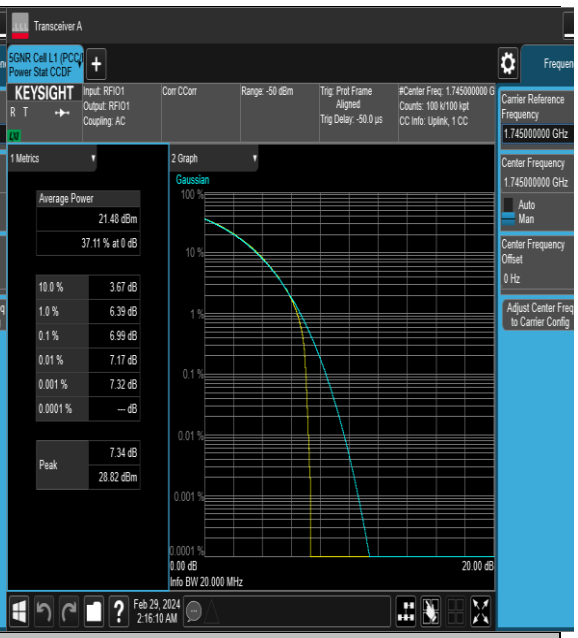

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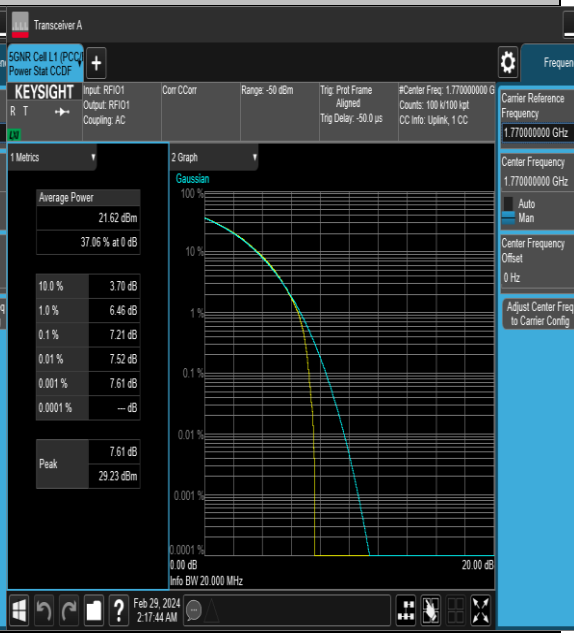

Appendix C: 26dB Bandwidth and Occupied Bandwidth for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	4.4666	4.944	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	4.4645	4.928	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	4.4655	4.914	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	4.4723	4.924	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	4.4655	4.920	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	4.4624	4.885	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	9.2733	9.958	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	9.2882	9.972	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	9.2611	9.952	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	9.2808	9.884	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	9.2677	9.931	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	9.2798	9.854	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	14.107	14.93	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	14.114	14.89	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	14.098	14.86	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	14.092	14.92	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	14.063	14.90	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	14.116	14.86	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	18.914	19.86	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	18.921	19.82	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	18.853	19.81	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	18.889	19.79	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	18.868	19.76	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	18.880	19.76	PASS

Test Graphs

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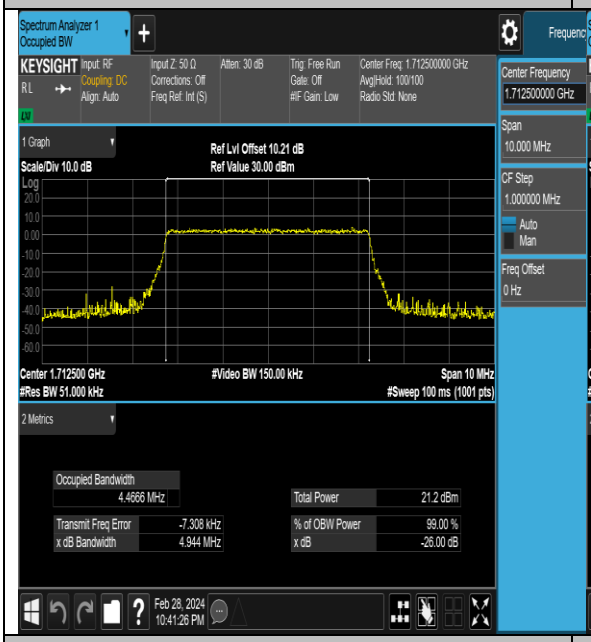

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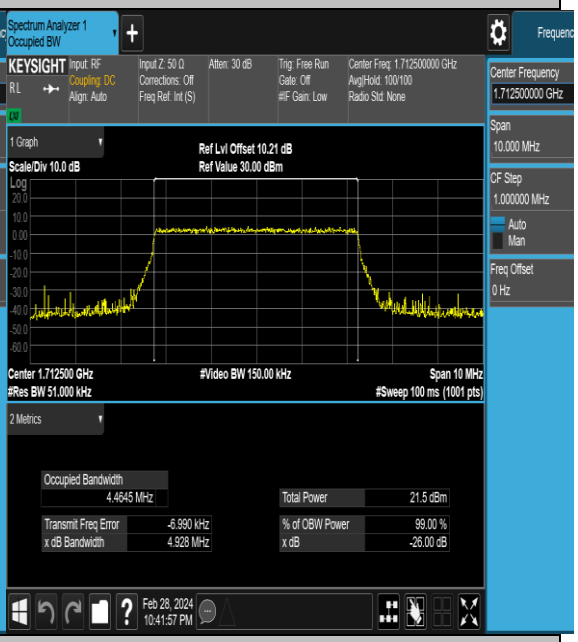

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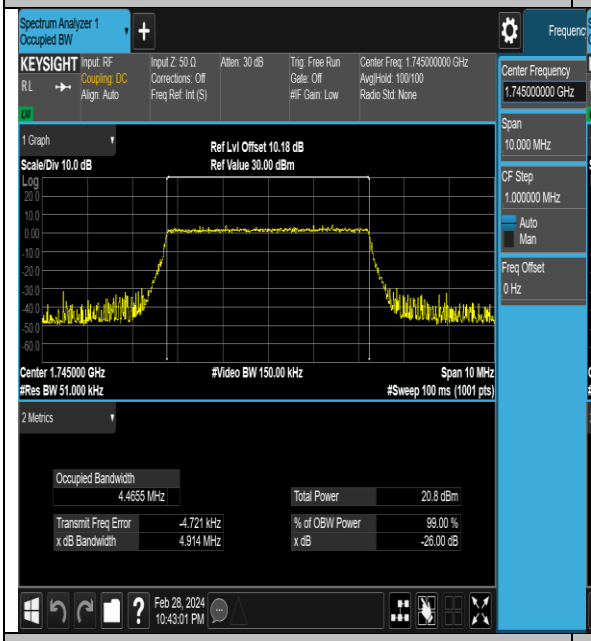

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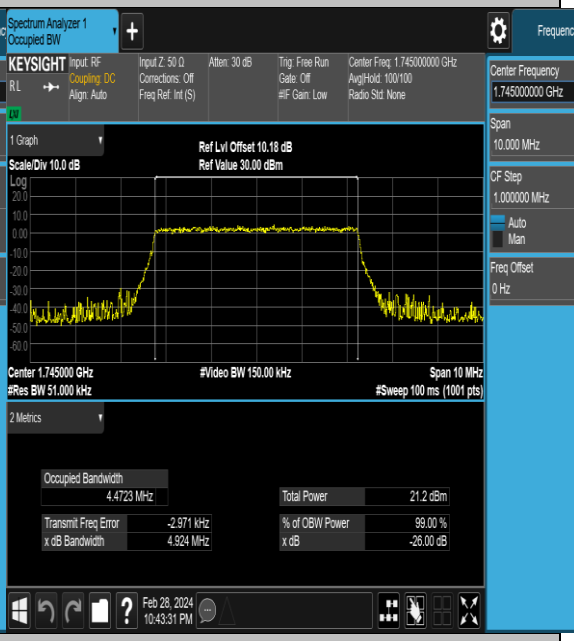

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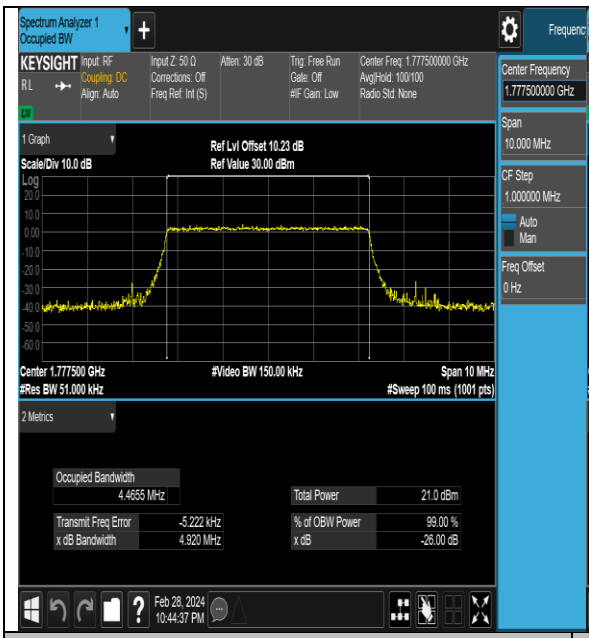

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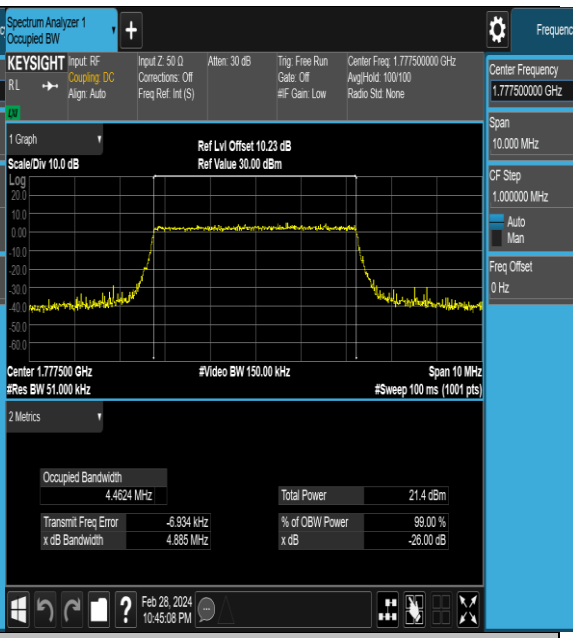

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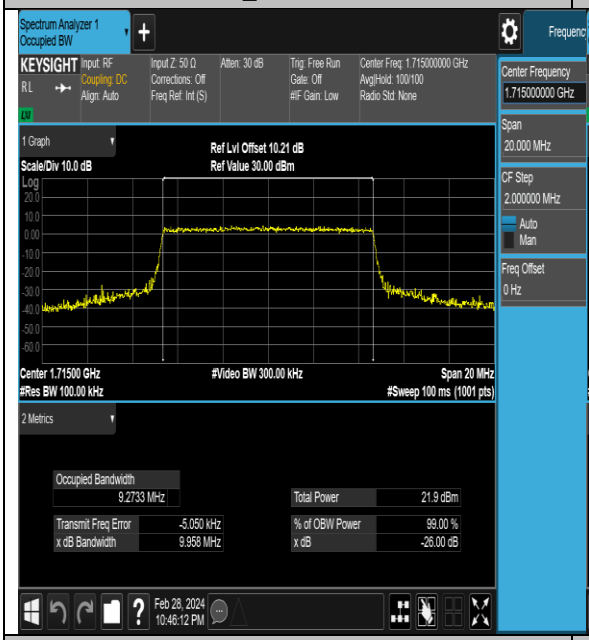

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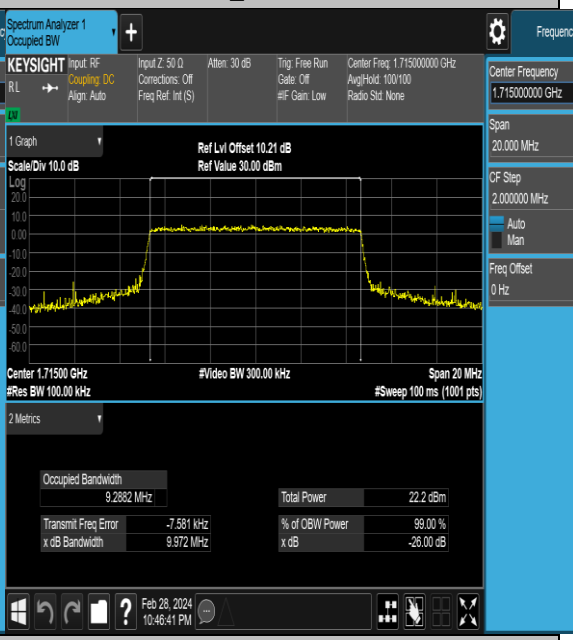

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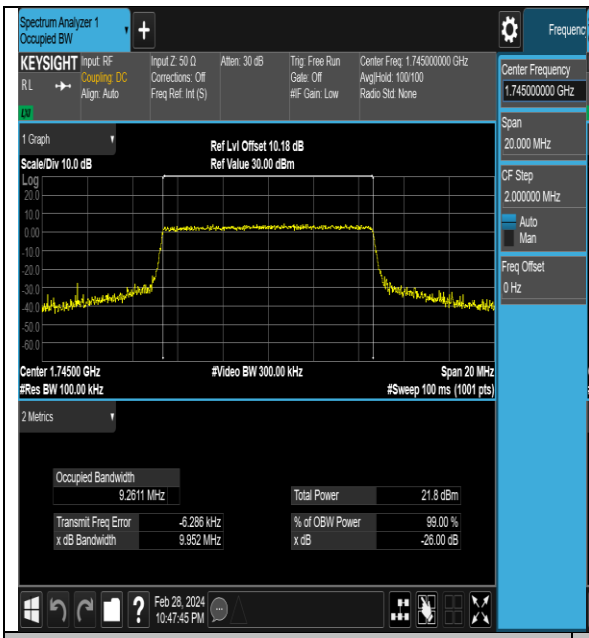

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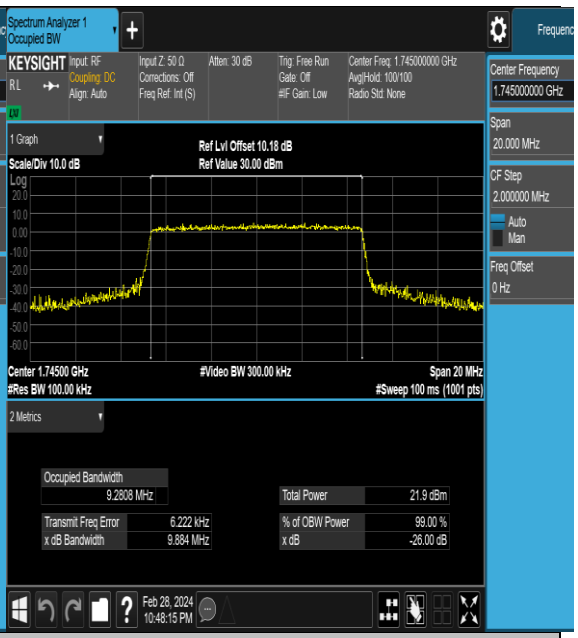

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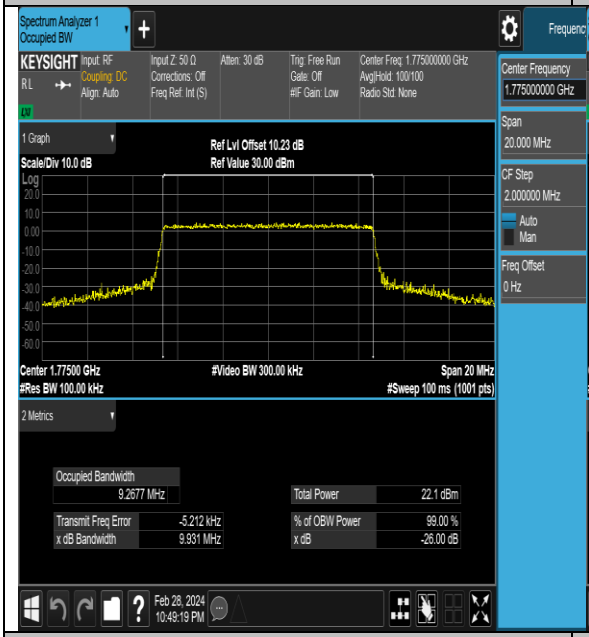

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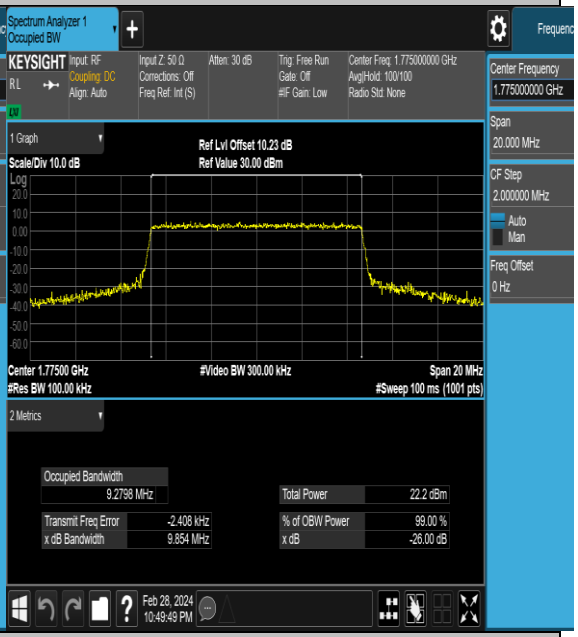

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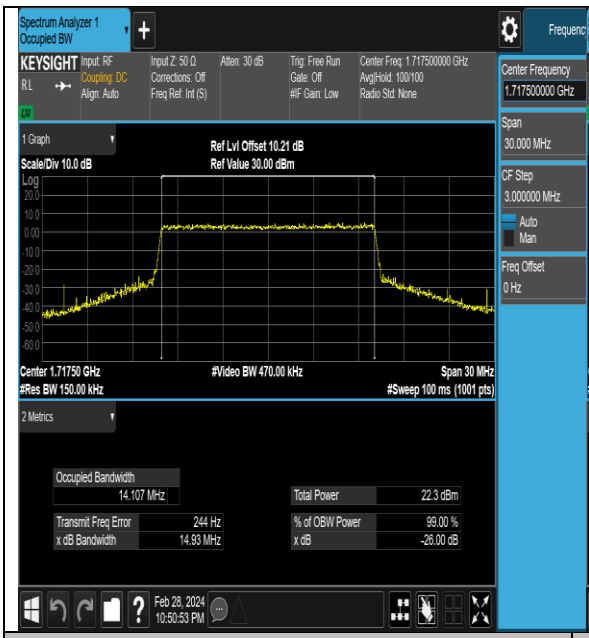

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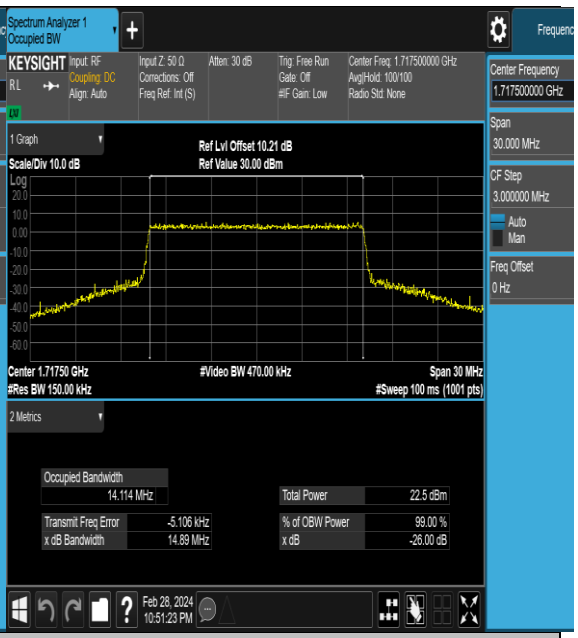

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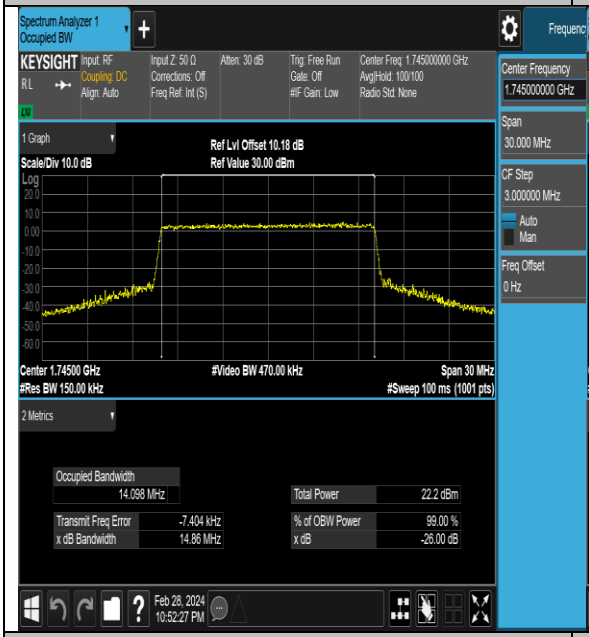

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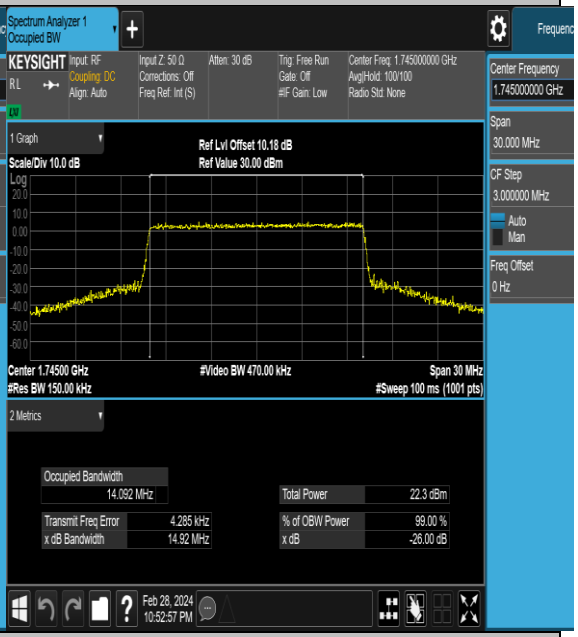

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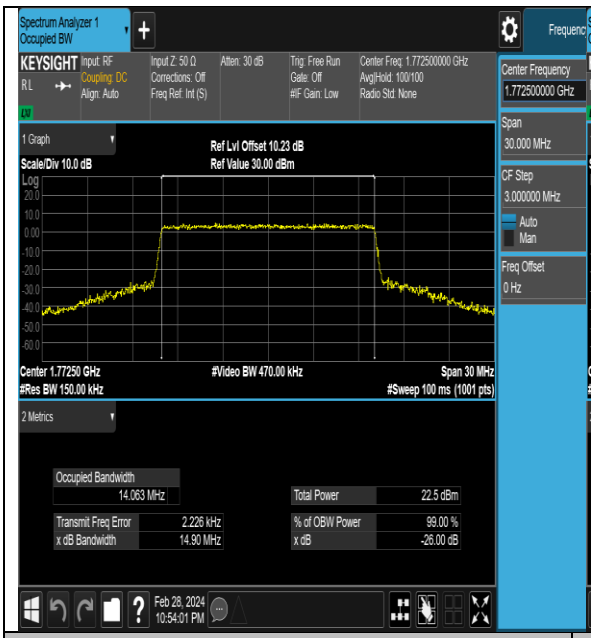

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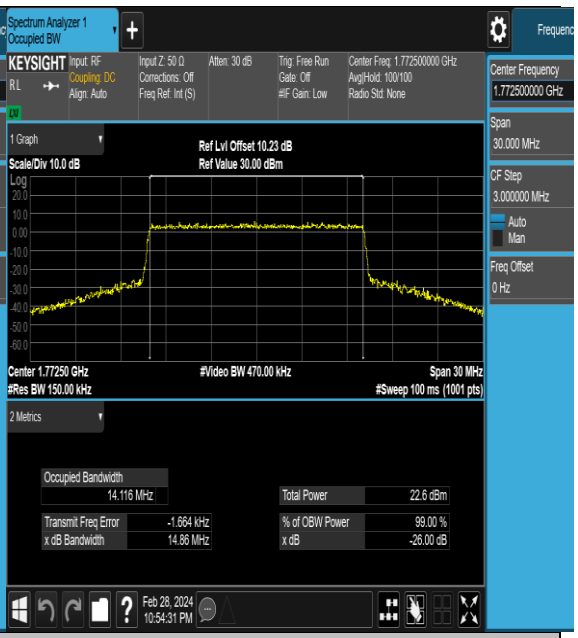

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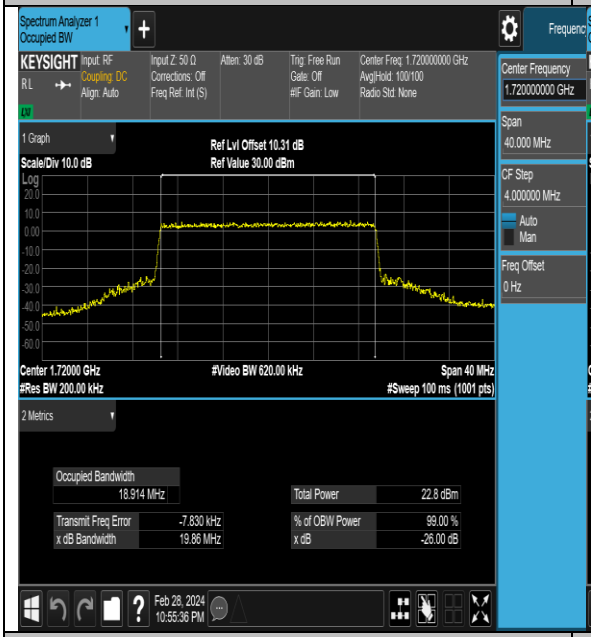

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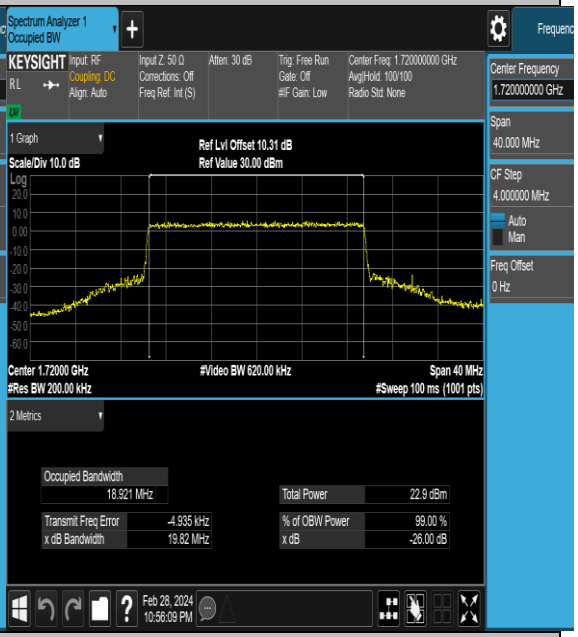

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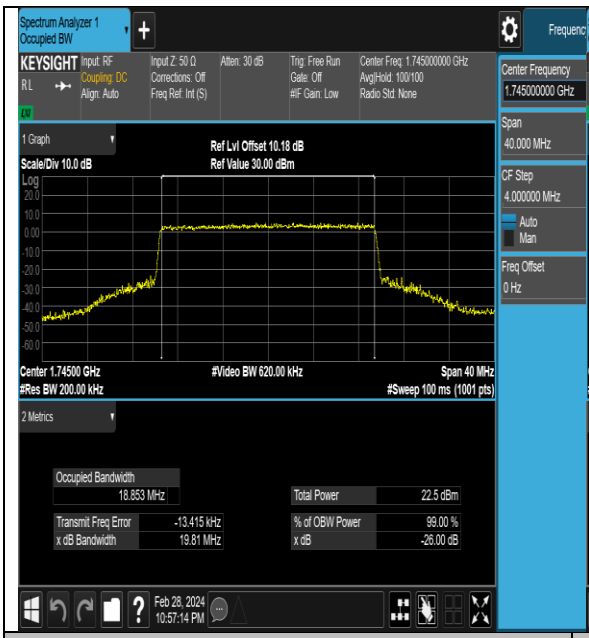

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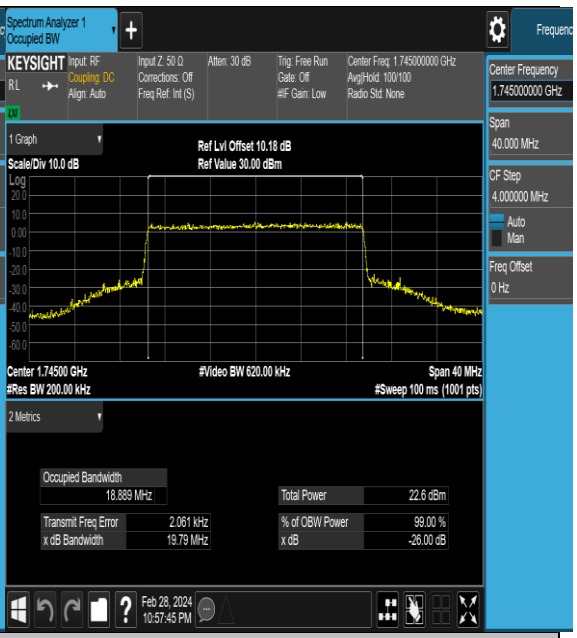

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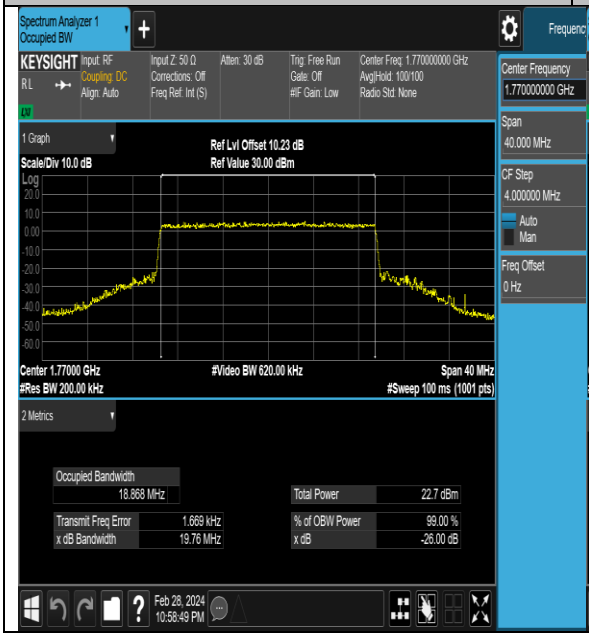
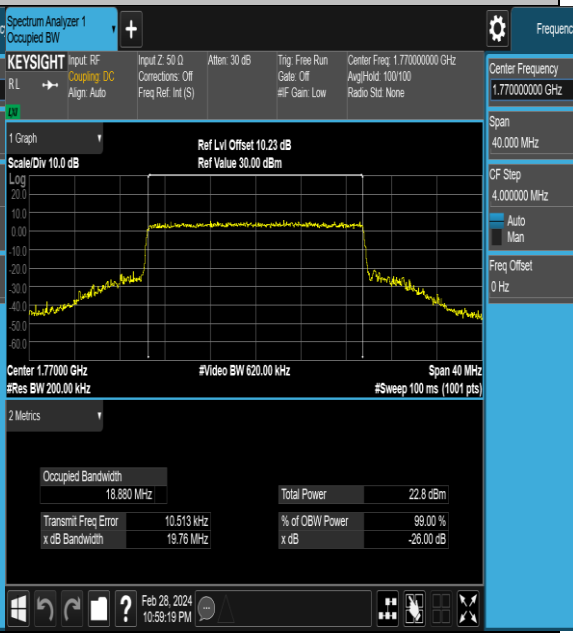




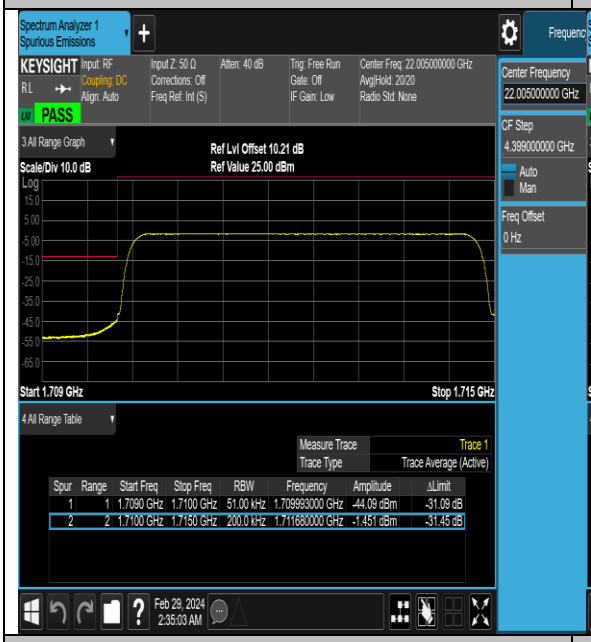
Appendix D: Band Edge for NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs

NTNV_DC_71A_n66A_PC3_15_5+5_M+L_TID6_#1



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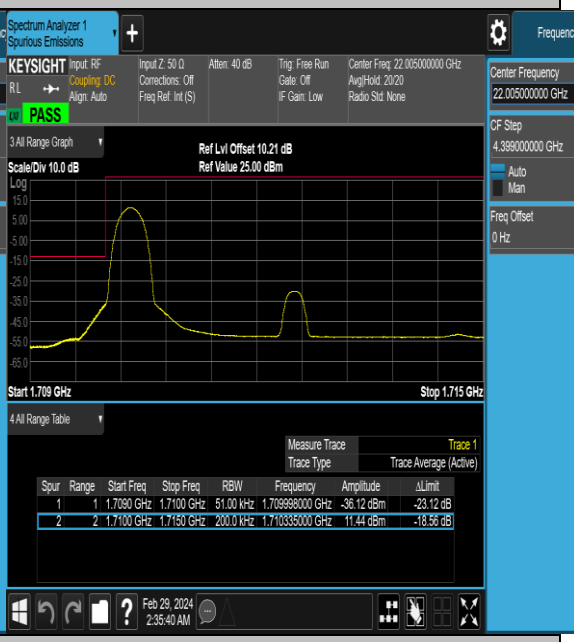

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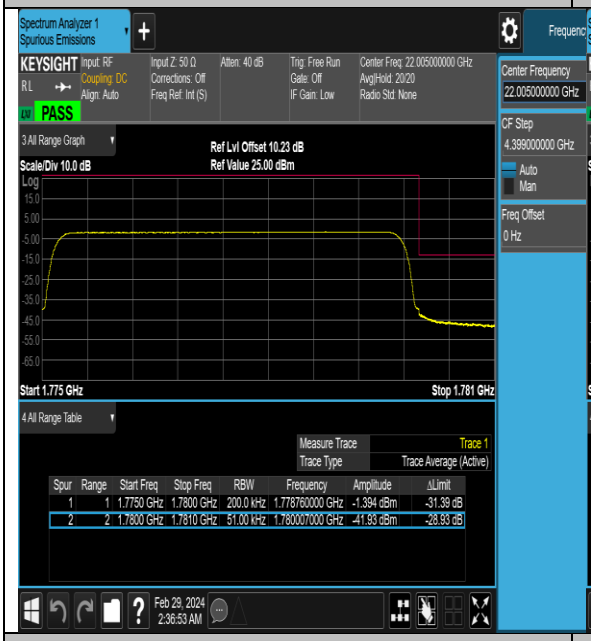

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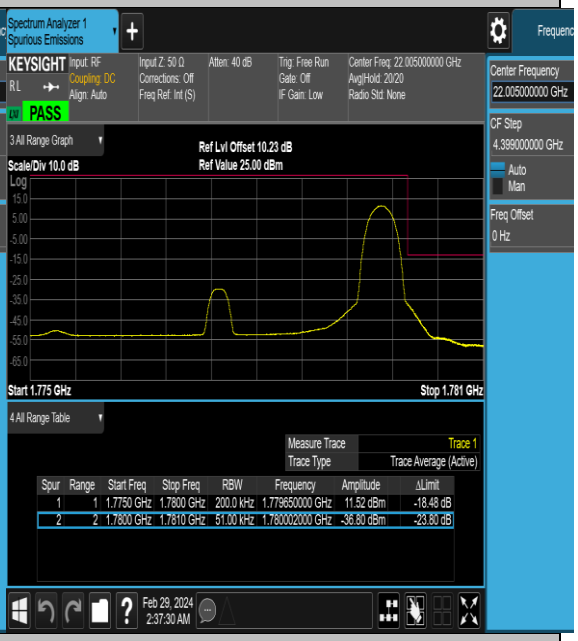

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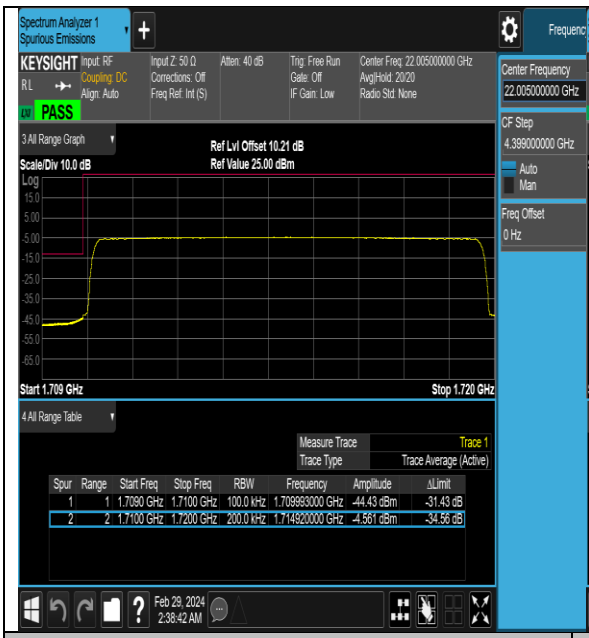

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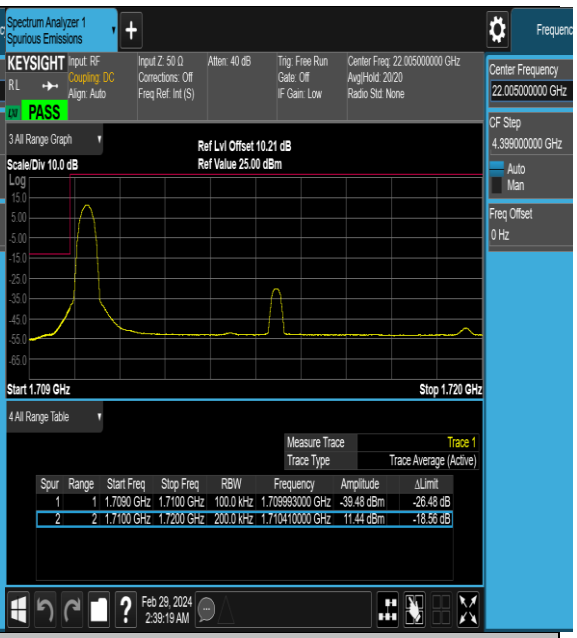

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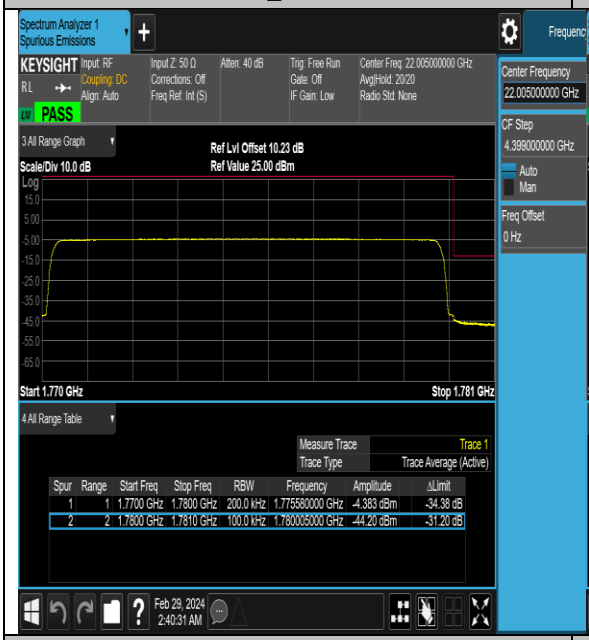

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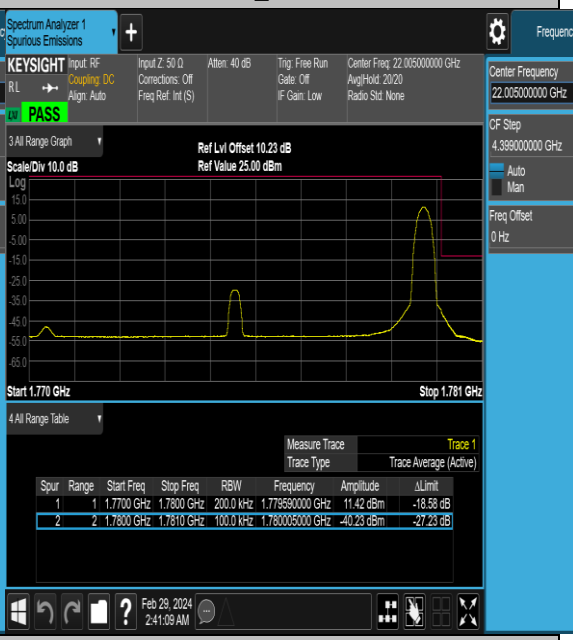

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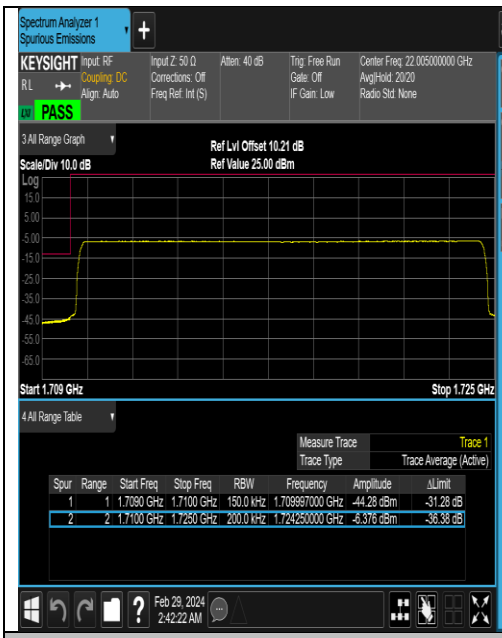

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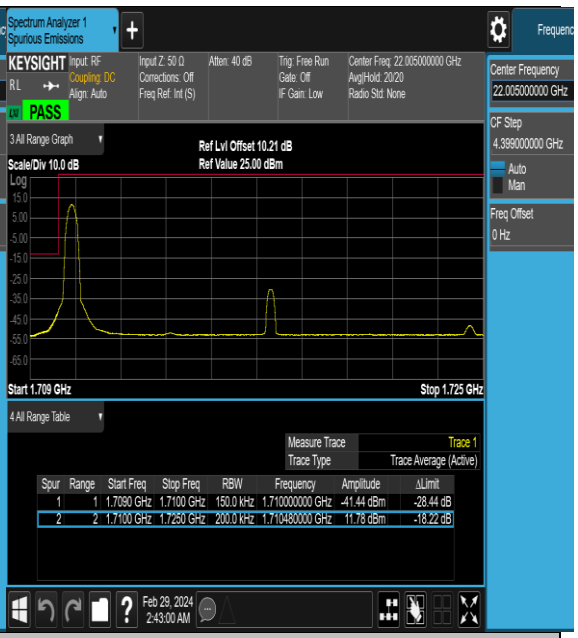

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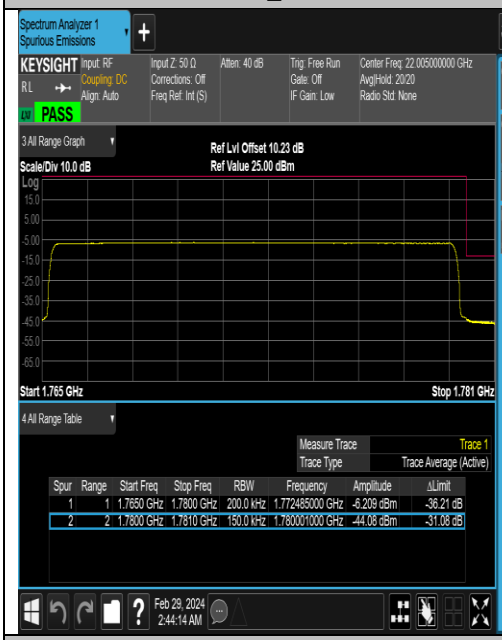

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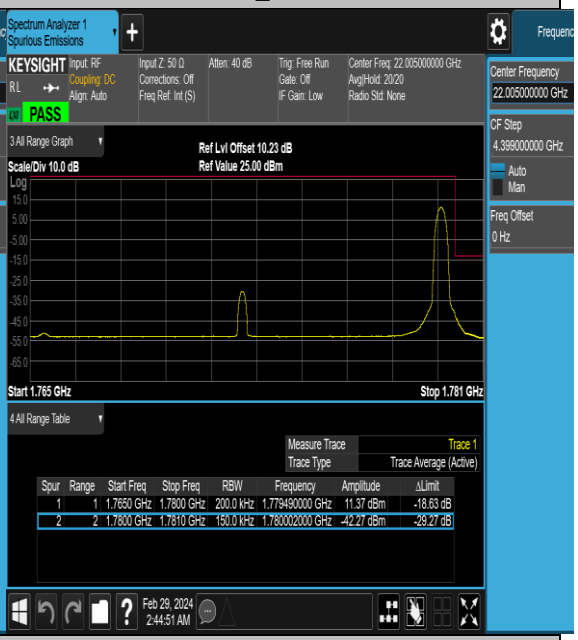

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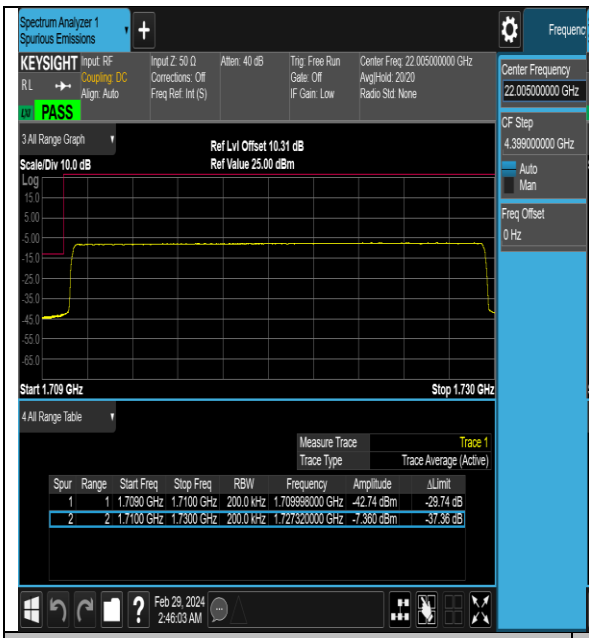

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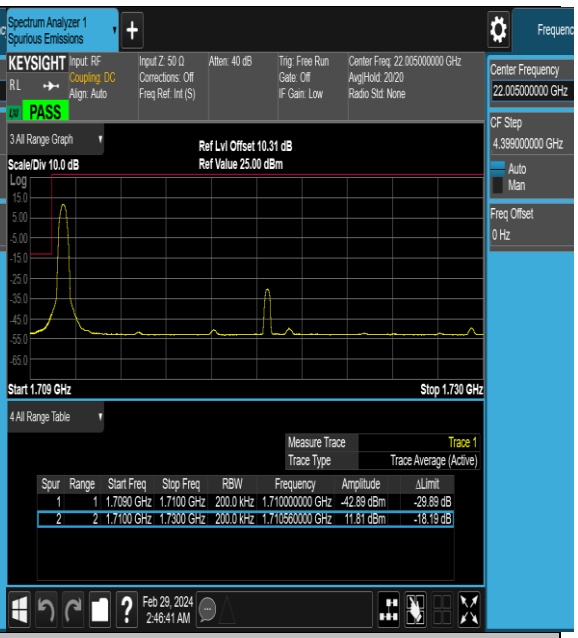

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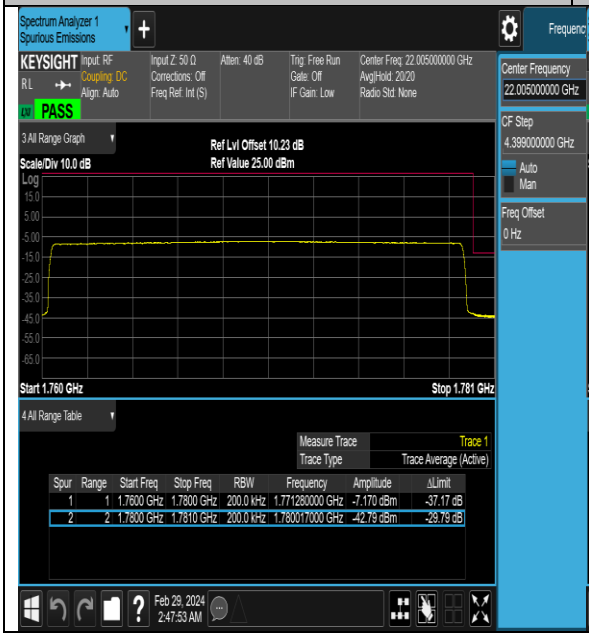

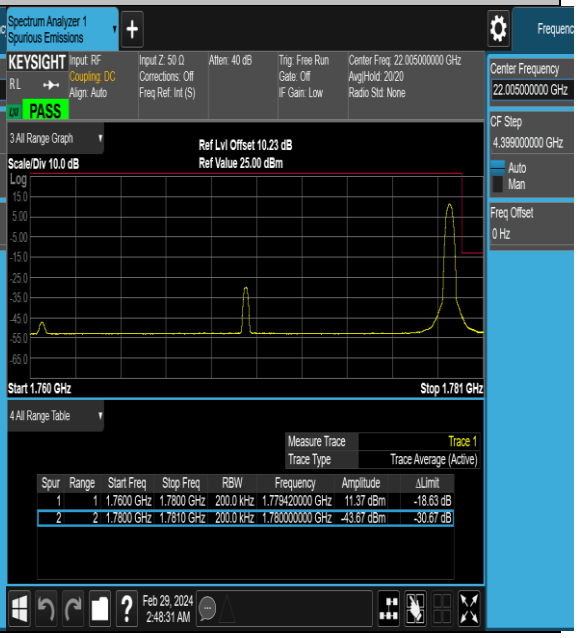

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Channel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+5	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+10	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+15	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_71A_n66A	15	5+20	CP-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS